TODO: add skipping rounds

Specification of the signature-free Sailfish consensus algorithm at a high level of abstraction.

We use a number of abstractions and simplifying assumptions in order to expose the high-level principles of the algorithm clearly and in order to make model-checking of interesting configuations tractable:

- 1) Nodes read and write a global DAG. When a node transitions into a new round, it is provided with an arbitrary quorum of vertices from the previous round (except that, after GST, some additional assumptions apply).
- 2) We do not model timeouts. Instead, we assume that, every round r after GST, each correct node votes for the previous leader.
- 3) We model Byzantine nodes explicitly by assigning them an algorithm too. This algorithm should allow the worst attacks possible, but, while the author thinks this is true, there is no formal guarantee that this is the case. A more realistic model would allow Byzantine nodes to send completely arbitrary messages at any time, but this would make model-checking with TLC too hard.
- 4) We do not explicitly model committing based on 2f + 1 first RBC messages.
- 5) There are no weak edges.

EXTENDS Digraph, Integers, FiniteSets, Sequences

CONSTANTS

N The set of all nodes

- , F The set of Byzantine nodes
- , R The set of rounds
- , $IsQuorum(_)$ Whether a set is a quorum (i.e. cardinality $\ge n$ -f)
- , $IsBlocking(_)$ Whether a set is a blocking set (i.e. cardinality $\ge f + 1$)
- , $Leader(_)$ operator mapping each round to its leader
- , GST the first round in which the system is synchronous

ASSUME $\exists n \in R : R = 1 \dots n$ useful rounds start at 1

For our purpose of checking safety and liveness of Sailfish, we do not need to model blocks of transactions. Instead, DAG vertices just consist of a node and a round.

```
V \stackrel{\triangle}{=} N \times R the set of possible DAG vertices Node(v) \stackrel{\triangle}{=} v[1] Round(v) \stackrel{\triangle}{=} v[2]
```

Next we define how we order DAG vertices when we commit a leader vertice

```
LeaderVertice(r) \stackrel{\Delta}{=} \langle Leader(r), r \rangle
```

```
RECURSIVE OrderSet(\_) arbitrarily order the members of the set S OrderSet(S) \stackrel{\triangle}{=} \text{IF } S = \{\} \text{ THEN } \langle \rangle \text{ ELSE } LET e \stackrel{\triangle}{=} \text{ CHOOSE } e \in S: \text{TRUE}
```

```
Append(OrderSet(S \setminus \{e\}), e)
 NOTE: CHOOSE is deterministic in TLA+,
 i.e. Choose e \in S : true is always the same e if S is the same
RECURSIVE CollectLeaders(\_, \_, \_)
CollectLeaders(vs, r, dag) \stackrel{\triangle}{=} IF vs = \{\} THEN \langle \rangle ELSE
     Let children \stackrel{\triangle}{=} \text{Union } \{Children(v, dag) : v \in vs}\}
          IF LeaderVertice(r) \in vs
            THEN Append(
                CollectLeaders(Children(LeaderVertice(r), dag), r-1, dag),
                LeaderVertice(r)
            ELSE CollectLeaders(children, r-1, dag)
RECURSIVE OrderVertices(_, _)
OrderVertices(dag, leaderVertices) \stackrel{\Delta}{=}
    IF leaderVertices = \langle \rangle THEN \langle \rangle ELSE
    LET l \stackrel{\triangle}{=} Head(leaderVertices)
           toOrder \stackrel{\triangle}{=} Descendants(\{l\}, dag)
           prefix \triangleq OrderSet(toOrder)
           \begin{array}{ll} remaining Vertices & \triangleq Vertices(dag) \setminus (toOrder \cup \{l\}) \\ remaining Edges & \triangleq \{e \in Edges(dag) : \{e[1], e[2]\} \subseteq remaining Vertices\} \end{array}
           remainingDAG \triangleq \langle remainingVertices, remainingEdges \rangle
           prefix \circ \langle l \rangle \circ OrderVertices(remainingDAG, Tail(leaderVertices))
CommitLeader(v, dag) \triangleq
     LET leaderVertices \triangleq CollectLeaders(\{v\}, Round(v), dag)
          Order Vertices (dag, leader Vertices)
Now we specify the algorithm in the PlusCal language.
  --algorithm Sailfish {
     variables
          vs = \{\}, the vertices of the DAG
          es = \{\}; the edges of the DAG
    \begin{array}{c} \textbf{define} \ \{ \\ dag \ \stackrel{\triangle}{=} \ \langle vs, \ es \rangle \end{array}
          NoVoteQuorum(r, delivered) \triangleq
               LET No Vote \triangleq \{v \in delivered : Leader Vertice(r-1) \notin Children(v, dag)\}
               IN IsQuorum(\{Node(v) : v \in NoVote\})
      }
     process ( correctNode \in N \setminus F )
          variables
               round = 0, current round; 0 means the node has not started execution
               log = \langle \rangle; delivered log
l0:
         while (TRUE) {
```

```
if ( round = 0 ) { start the first round r = 1
           round := 1;
           vs := vs \cup \{\langle self, 1 \rangle\}
      else { start a round r > 1
           with (r \in \{r \in R : r > round\})
           with ( delivered \in SUBSET \{ v \in vs : Round(v) = r - 1 \} ) {
               await IsQuorum(\{Node(v) : v \in delivered\});
               await LeaderVertice(r-1) \in delivered \Rightarrow
                         \lor LeaderVertice(r-2) \in Children(LeaderVertice(r-1), dag)
                         \vee NoVoteQuorum(r-1, delivered);
               if ( Leader(r) = self )
                              \vee LeaderVertice(r-1) \in delivered
                              \vee NoVoteQuorum(r, delivered);
               round := r;
               with ( newV = \langle self, round \rangle ) {
                   vs := vs \cup \{newV\};
                   es := es \cup \{\langle newV, pv \rangle : pv \in delivered\};
                };
                commit if there is a quorum of votes for the leader of r-2:
               if (round > 1)
                   with ( votesForLeader = \{pv \in delivered : \langle pv, LeaderVertice(round - 2) \rangle \in es\} )
                   if ( IsBlocking(\{Node(pv): pv \in votesForLeader\}) )
                        log := CommitLeader(LeaderVertice(round - 2), dag)
           }
       }
   }
}
```

Next comes our model of Byzantine nodes. Because the real protocol disseminates DAG vertices using reliable broadcast, Byzantine nodes cannot equivocate and cannot deviate much from the protocol (lest their messages be ignored). Also note that creating a round-r vertice commutes to the left of actions of rounds greater than r and to the right of actions of rounds smaller than R, so we can, without loss of generality, schedule Byzantine nodes in the same "round-by-round" manner as other nodes.

```
process ( byzantineNode \in F )
    variables round_- = 0;
{

l0: while ( TRUE ) {
    round_- := round_- + 1;
    maybe add a vertices to the DAG:
    either with ( newV = \langle self, round_- \rangle ) {
        if ( round_- = 1 )
            vs := vs \cup \{newV\}
        else
        with ( delivered \in SUBSET \{ v \in vs : Round(v) = round_- - 1 \} ) {
```

```
\mathbf{await} \ IsQuorum(\{Node(v): v \in delivered\});\\ vs := vs \cup \{newV\};\\ es := es \cup \{\langle newV, \ pv\rangle: pv \in delivered\}\\ \}\\ \} \ \mathbf{or} \ \mathbf{skip};\\ \}\\ \}
```

Next we define the safety and liveness properties

Compatible $(s1, s2) \stackrel{\triangle}{=}$ whether the sequence s1 is a prefix of the sequence s2, or vice versa LET $Min(n1, n2) \stackrel{\triangle}{=}$ IF $n1 \geq n2$ THEN n2 ELSE n1IN $\forall i \in 1... Min(Len(s1), Len(s2)) : <math>s1[i] = s2[i]$

Agreement $\stackrel{\triangle}{=} \forall n1, n2 \in N \setminus F : Compatible(log[n1], log[n2])$

Liveness
$$\stackrel{\triangle}{=} \forall r \in R : r \ge GST \land Leader(r) \notin F \Rightarrow \exists B \in \text{SUBSET } (N \setminus F) : \land IsBlocking(B) \land \forall n \in B : round[n] \ge r + 2 \Rightarrow \exists i \in \text{DOMAIN } log[n] : log[n][i] = LeaderVertice(r)$$

Finally we make a few auxiliary definitions used for model-checking with TLC

The round of a node, whether Byzantine or not $Round_{-}(n) \stackrel{\triangle}{=} \text{IF } n \in F \text{ THEN } round_{-}[n] \text{ ELSE } round[n]$

Basic typing invariant:

```
TypeOK \triangleq \\ \land \forall v \in vs : Node(v) \in N \land Round(v) \in Nat \setminus \{0\} \\ \land \forall e \in es : \\ \land e = \langle e[1], e[2] \rangle \\ \land \{e[1], e[2]\} \subseteq vs \\ \land Round(e[1]) > Round(e[2])
```

 $\land \ \forall \, n \in N : Round_{-}(n) \in Nat$

Synchrony assumption: for each round r from GST onwards, if the leader of r is correct then every correct node votes for the round-r leader vertex in round r+1

```
Synchrony \triangleq \forall r \in R : r \geq GST \land Leader(r) \notin F \Rightarrow \forall n \in N \setminus F :
LET \ v \triangleq \langle n, r+1 \rangle
IN \quad v \in vs \Rightarrow LeaderVertice(r) \in Children(v, dag)
```

We add the synchrony assumption to the specification

```
\begin{array}{ll} SyncNext & \triangleq (\exists \, self \in N \setminus F : correctNode(self) \land Synchrony') \\ & \lor (\exists \, self \in F : byzantineNode(self)) \\ SyncSpec & \triangleq \, Init \land \Box [SyncNext]_{vars} \end{array}
```

Next we define a constraint to stop the model-checker.

```
\begin{array}{l} \mathit{Max}(S) \, \stackrel{\triangle}{=} \, \text{ choose } x \in S : \forall \, y \in S : y \, \leq x \\ \mathit{StateConstraint} \, \stackrel{\triangle}{=} \, \forall \, n \in N : \mathit{Round}\_(n) \in 0 \ldots \mathit{Max}(R) \end{array}
```

Finally, we give some properties we expect to be violated (useful to get the model-checker to print interesting executions).

```
 \begin{array}{ll} Falsy1 & \triangleq \neg(\\ & \forall \, n \in N : Round\_(n) = Max(R) \\ ) \\ Falsy2 & \triangleq \neg(\\ & \exists \, n \in N \setminus F : Len(log[n]) > 1 \\ ) \\ \end{array}
```