

Specification of the *Sailfish* consensus algorithm at a high level of abstraction.

We use a number of abstractions and simplifying assumptions in order to expose the high-level principles of the algorithm clearly and in order to make model-checking of interesting configurations tractable :

- 1) Nodes read and write a global *DAG*. Each round, each node gets to see an arbitrary quorum of vertices from the previous round (and, after *GST*, this quorum must include all correct vertices).
- 2) We do not model timeouts. Instead, before *GST*, nodes can non-deterministically increase their round number (including skipping rounds entirely); after *GST*, correct nodes can only increment their round number and only do so after acting upon a superset of the correct vertices of the previous round.
- 3) We do not model the *DAG* ordering procedure. Instead, we check that for every two committed vertices, there is a path in the *DAG* from the one with the higher round to the one with the lower round. Moreover, we define committed vertices using the global *DAG* and it is plausible that local *DAG* views would contain fewer committed vertices; so there is a potential for missing safety or liveness violations because of this.
- 4) We model *Byzantine* nodes explicitly by assigning them an algorithm. This algorithm should allow *Byzantine* nodes to do the worst possible, but there is no guarantee that this is the case. A more realistic model would allow *Byzantine* nodes to send completely arbitrary messages at any time, but this would make model-checking too hard.
- 5) We do model committing based on $2f + 1$ first *RBC* messages.

This version of the algorithm does not use “*no_vote*” messages.

EXTENDS *DomainModel*

CONSTANT

GST the first synchronous round (all later rounds are synchronous)

--algorithm *Sailfish*{

variables

$vs = \{\}$, the vertices of the *DAG*

$es = \{\}$; the edges of the *DAG*

define {

$LeaderVertice(r) \triangleq \langle Leader(r), r \rangle$

$ValidVerticeQuorums(r) \triangleq$

Quorums of valid vertices of round r

$\{ VQ \in \text{SUBSET } vs : \text{LET } NQ \triangleq \{ Node(v) : v \in VQ \} \text{IN}$

$\wedge NQ \in \text{Quorum}$

$\wedge \forall v \in VQ :$

$\wedge Round(v) = r$

the leader vertice, if included, must be valid (*i.e.* if it does not point to the previous leader vertice, t

$\wedge \vee \neg(r > 0 \wedge v = LeaderVertice(r) \wedge \langle v, LeaderVertice(r-1) \rangle \notin es)$

$\vee \exists VQ2 \in \text{SUBSET } VQ :$

$\wedge VQ2 \in \text{Quorum}$

$\wedge \forall v2 \in VQ2 : \langle v2, LeaderVertice(r-1) \rangle \notin es \}$

$VertexQuorums(r) \triangleq$
 Quorums of vertices of round r ; included leader vertex may be invalid (*i.e.* does not point to the previous leader)
 $\{ VQ \in \text{SUBSET } vs : \text{LET } NQ \triangleq \{ Node(v) : v \in VQ \} \text{IN}$
 $\quad \wedge NQ \in Quorum$
 $\quad \wedge \forall v \in VQ : Round(v) = r \}$
 $\}$
process ($correctNode \in N \setminus F$)
variables $round = 0$; current round
 $\{$
 l0: **while** (TRUE)
 either with ($v = \langle self, round \rangle$) $\{$
 add a new vertex to the *DAG* and go to the next round
 $vs := vs \cup \{v\}$;
 if ($round > 0$)
 with ($VQ \in ValidVertexQuorums(round - 1)$) $\{$
 from *GST* onwards, each node receives all correct vertices of the previous round:
 when $round \geq GST \Rightarrow (N \setminus F) \subseteq \{ Node(v2) : v2 \in VQ \}$;
 if ($Leader(round) = self$) $\{$
 we must either include the previous leader vertex,
 or a quorum of vertices not voting for the previous leader vertex
 when
 $\vee LeaderVertex(round - 1) \in VQ$
 $\vee \exists Q \in Quorum : \forall n \in Q \setminus \{self\} : \text{LET } vn \triangleq \langle n, round \rangle \text{IN}$
 $\wedge vn \in vs$
 $\wedge \langle vn, LeaderVertex(round - 1) \rangle \notin es$;
 $\}$;
 $es := es \cup \{ \langle v, pv \rangle : pv \in VQ \}$; add the edges
 $\}$;
 $round := round + 1$
 $\}$
 or with ($r \in \{ r \in R : r > round \}$) $\{$
 go to a higher round
 when $r \leq GST$; from *GST* onwards, correct nodes do not skip rounds
 $round := r$
 $\}$
 $\}$

Next comes our model of *Byzantine* nodes. Because the real protocol disseminates *DAG* vertices using reliable broadcast, *Byzantine* nodes cannot equivocate and cannot deviate much from the protocol (lest their messages be ignored). Also note that creating a round- r vertex commutes to the left of actions of rounds greater than r and to the right of actions of rounds smaller than R , so we can, without loss of generality, schedule *Byzantine* nodes in the same “round-by-round” manner as other nodes.

process ($byzantineNode \in F$)
variables $round_- = 0$;
 $\{$

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l0:  while ( TRUE ) {
      maybe add a vertices to the DAG:
      either with (  $v = \langle self, round_- \rangle$  ) {
         $vs := vs \cup \{v\}$ ;
        if (  $round_- > 0$  )
          with (  $vq \in VerticeQuorums(round_- - 1)$  ) {
             $es := es \cup \{\langle v, pv \rangle : pv \in vq\}$ 
          }
        } or skip;
      go to the next round:
       $round_- := round_- + 1$ 
    }
  }
}

```

Next we define the safety and liveness properties

$$\begin{aligned}
Committed(v) &\triangleq \\
&\wedge v \in vs \\
&\wedge Node(v) = Leader(Round(v)) \\
&\wedge \exists Bl \in Blocking : Bl \subseteq \{Node(pv) : pv \in Parents(v, es)\} \\
&\wedge \vee Round(v) = 0 \\
&\quad \vee LeaderVertice(Round(v) - 1) \in Children(v, es) \\
&\quad \vee \exists Q \in Quorum : \forall n \in Q : LET \, vn \triangleq \langle n, Round(v) \rangle IN \\
&\quad \quad \wedge vn \in vs \\
&\quad \quad \wedge \langle vn, LeaderVertice(Round(v) - 1) \rangle \notin es
\end{aligned}$$

$$\begin{aligned}
Safety &\triangleq \forall v1, v2 \in vs : \\
&\wedge Committed(v1) \\
&\wedge Committed(v2) \\
&\wedge Round(v1) \leq Round(v2) \\
&\Rightarrow Reachable(v2, v1, es)
\end{aligned}$$

$$\begin{aligned}
Liveness &\triangleq \forall r \in R : \\
&\wedge r \geq GST \\
&\wedge Leader(r) \notin F \\
&\quad \text{all correct } round - (r + 1) \text{ vertices are created:} \\
&\wedge \forall n \in N \setminus F : round[n] > r + 1 \\
&\Rightarrow Committed(LeaderVertice(r))
\end{aligned}$$

Finally we make a few auxiliary definitions used for model-checking with *TLC*

$$\begin{aligned}
Quorum1 &\triangleq \{Q \in \text{SUBSET } N : Cardinality(Q) \geq Cardinality(N) - Cardinality(F)\} \\
Blocking1 &\triangleq \{Q \in \text{SUBSET } N : Cardinality(Q) > Cardinality(F)\}
\end{aligned}$$

The round of a node, whether *Byzantine* or not

$$Round_ (n) \triangleq \text{IF } n \in F \text{ THEN } round_ [n] \text{ ELSE } round[n]$$

Basic typing invariant:

$$\begin{aligned}
TypeOK &\triangleq \\
&\wedge \forall v \in vs : Node(v) \in N \wedge Round(v) \in Nat \\
&\wedge \forall e \in es : \\
&\quad \wedge e = \langle e[1], e[2] \rangle \\
&\quad \wedge \{e[1], e[2]\} \subseteq vs \\
&\quad \wedge Round(e[1]) > Round(e[2]) \\
&\wedge \forall n \in N : Round_ (n) \in Nat
\end{aligned}$$

Sequentialization constraints, which enforce a particular ordering of the actions. Because of how actions commute, the set of reachable states remains unchanged. Essentially, we schedule all nodes “round-by-round” and in lock-steps, with the leader last. This speeds up model-checking a lot.

Note that we must always schedule the leader last because, due to its use of other nodes’s vertices, its action does not commute to the left of the actions of other nodes.

$$\begin{aligned}
SeqConstraints(n) &\triangleq \\
&\text{wait for all nodes to finish previous rounds:} \\
&\wedge (Round_ (n) > 0 \Rightarrow \forall n2 \in N : Round_ (n2) \geq Round_ (n)) \\
&\text{wait for all nodes with lower index to leave the round (leader index is always last):} \\
&\wedge \forall n2 \in N : NodeIndexLeaderLast(n2, Round_ (n)) < NodeIndexLeaderLast(n, Round_ (n)) \Rightarrow Round_ (n) > Round_ (n2) \\
SeqNext &\triangleq (\exists self \in N \setminus F : SeqConstraints(self) \wedge correctNode(self)) \\
&\quad \vee (\exists self \in F : SeqConstraints(self) \wedge byzantineNode(self)) \\
SeqSpec &\triangleq Init \wedge \Box [SeqNext]_{vars}
\end{aligned}$$

Example assignment of leaders to rounds:

$$ModLeader(r) \triangleq NodeSeq[(r \% Cardinality(N)) + 1]$$

Constraint to stop the model checker:

$$\begin{aligned}
StateConstraint &\triangleq \\
LET Max(S) &\triangleq CHOOSE x \in S : \forall y \in S : y \leq x IN \\
&\forall n \in N : Round_ (n) \in 0 \dots (Max(R) + 1)
\end{aligned}$$

Some properties we expect to be violated (useful to get the model-checker to print interesting executions):

$$Falsy1 \triangleq \neg(\\
\quad \wedge Committed(\langle Leader(1), 1 \rangle) \\
)$$

$$\begin{aligned}
Falsy2 &\triangleq \neg(\\
&\quad \wedge Committed(\langle Leader(0), 0 \rangle) \\
&\quad \wedge \neg Committed(\langle Leader(1), 1 \rangle) \\
&\quad \wedge \neg Committed(\langle Leader(2), 2 \rangle) \\
&\quad \wedge Committed(\langle Leader(3), 3 \rangle) \\
&)
\end{aligned}$$