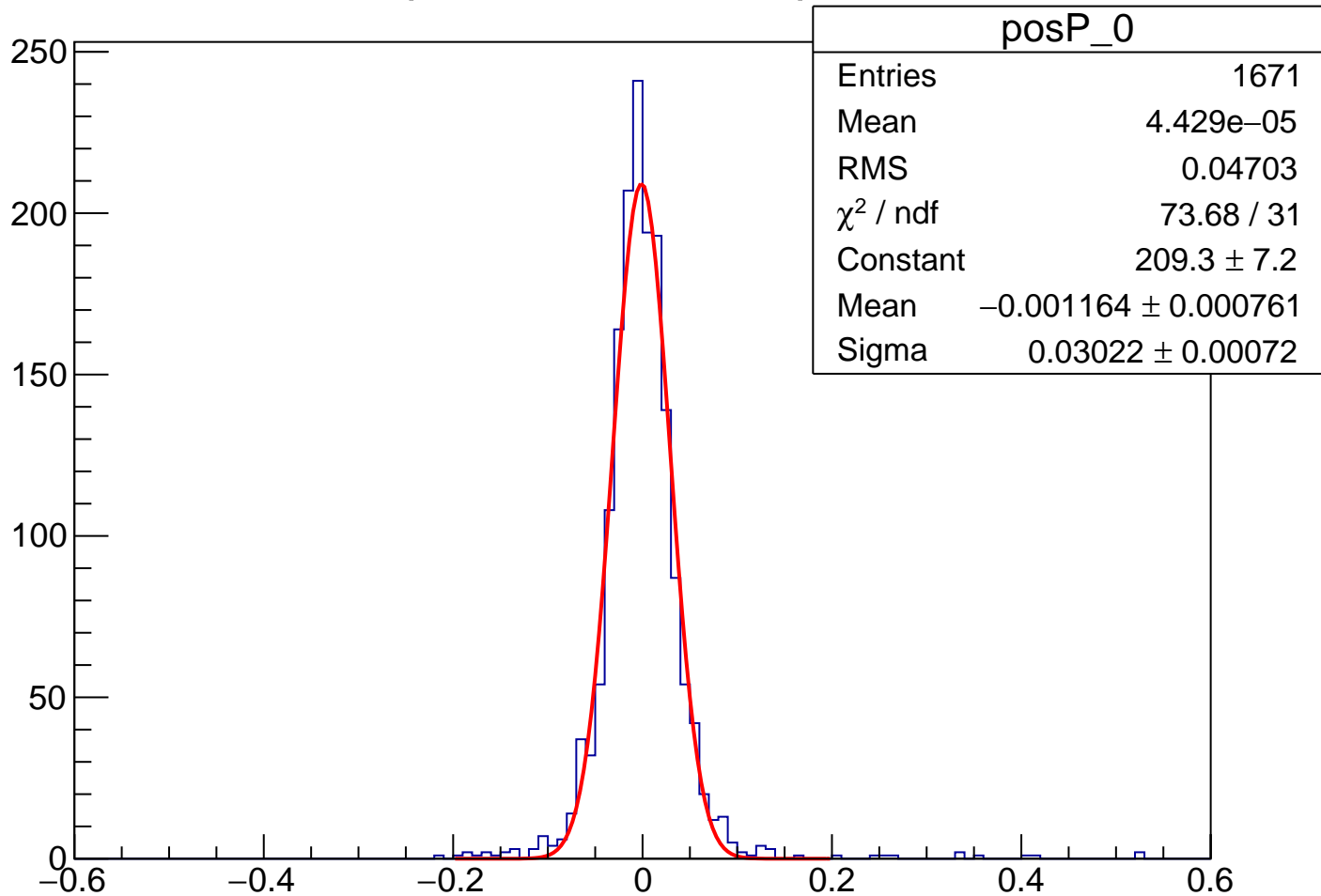
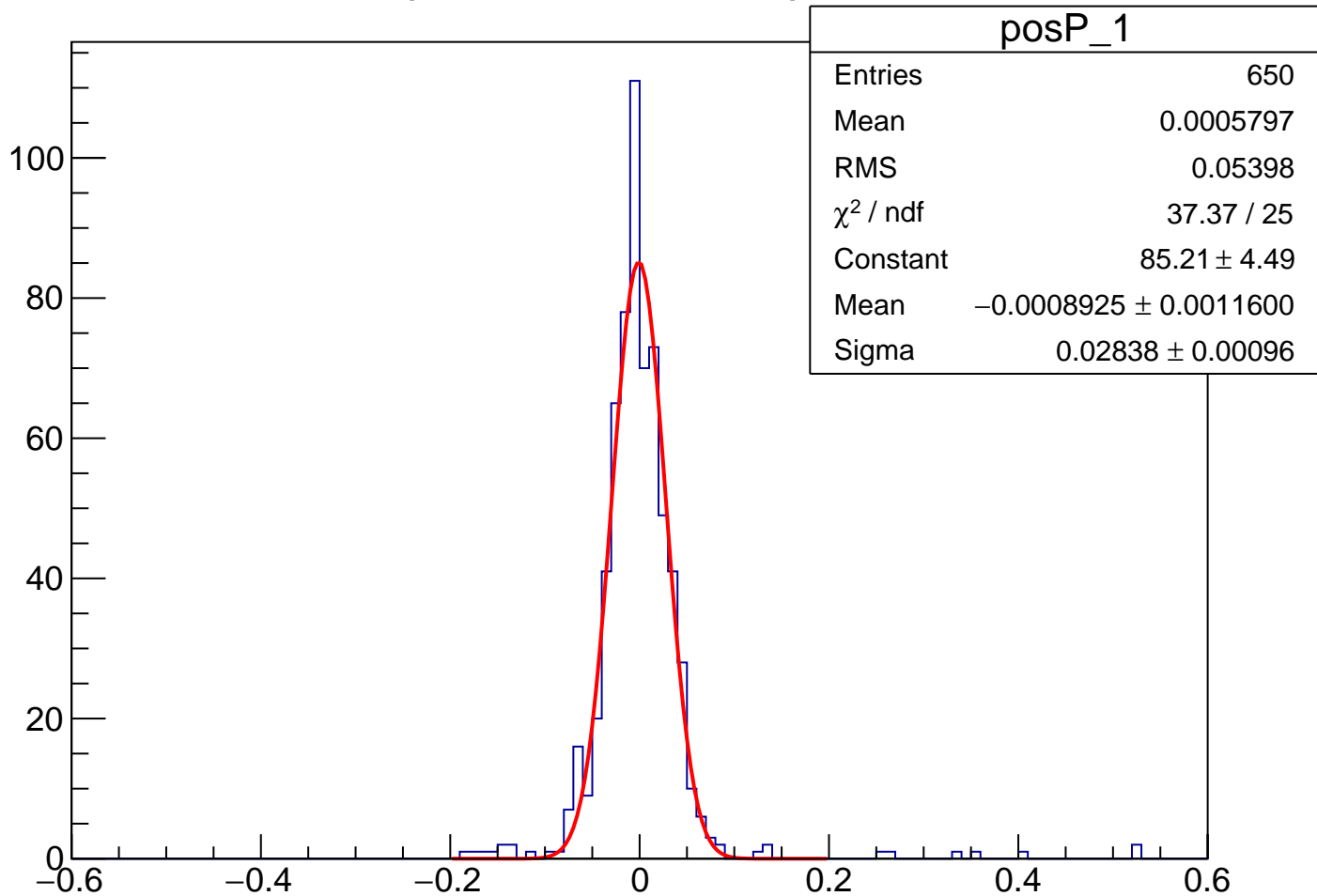


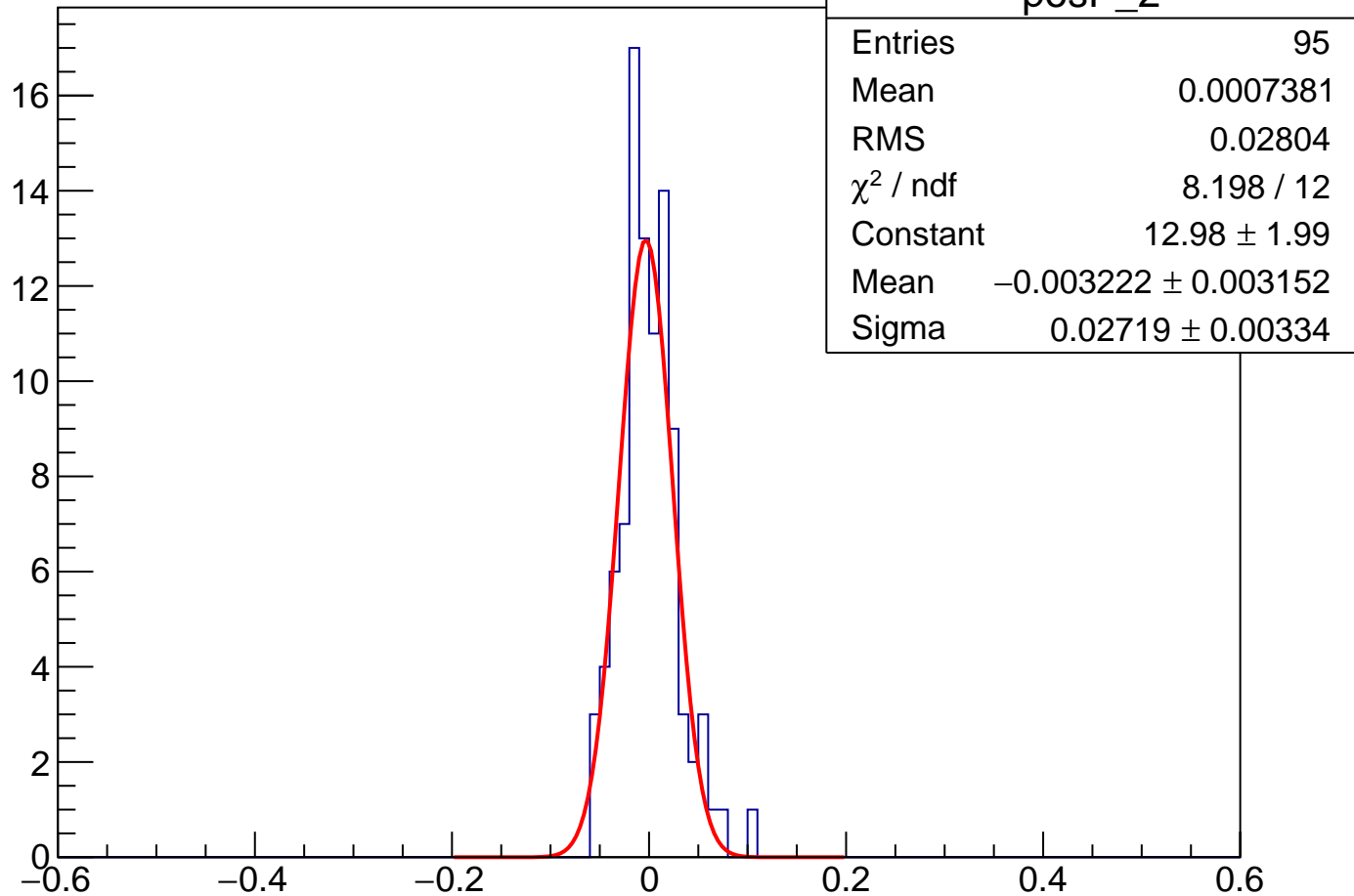
# ap\_24MeV\_loose\_posP



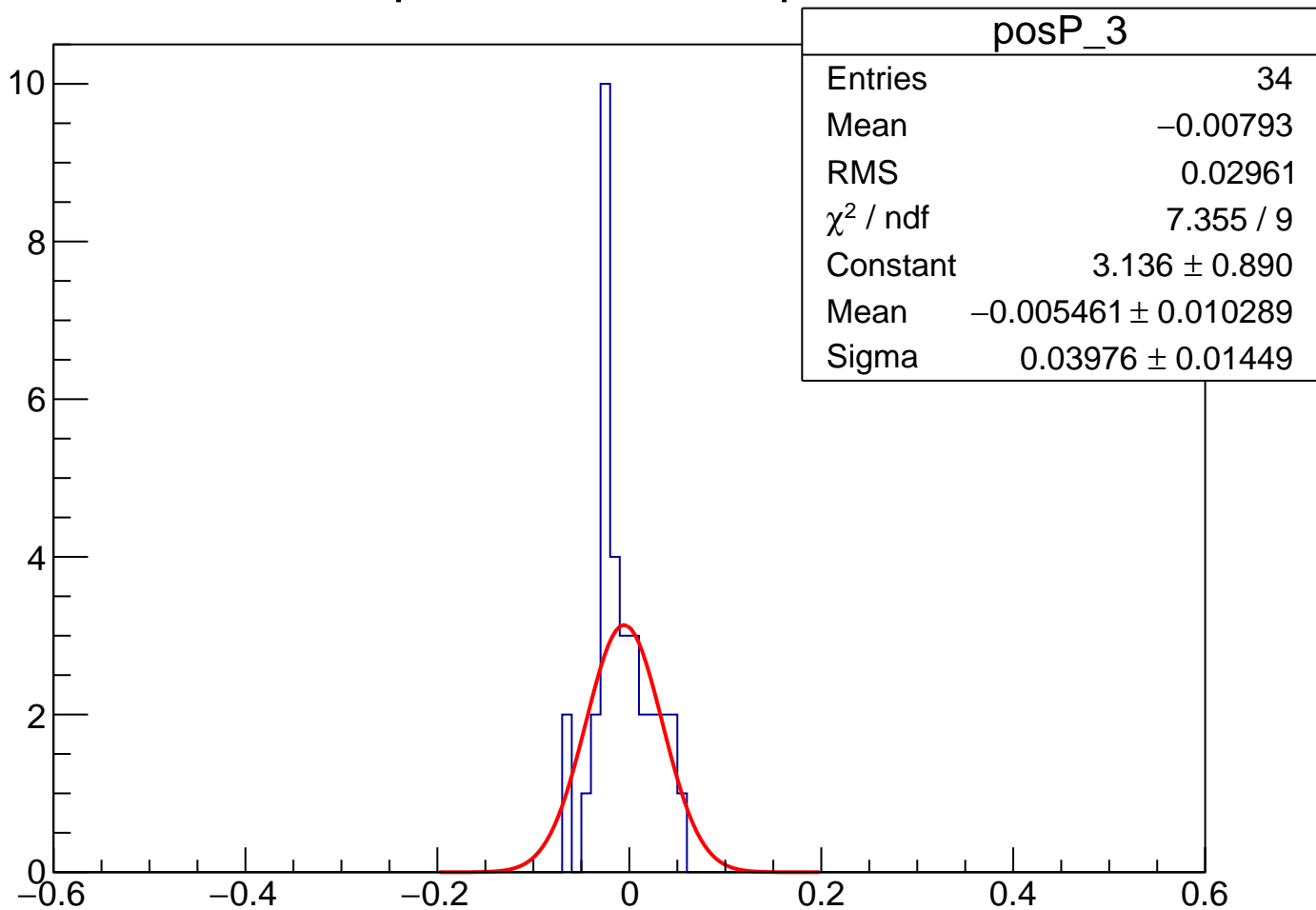
# ap\_24MeV\_L1L1\_posP



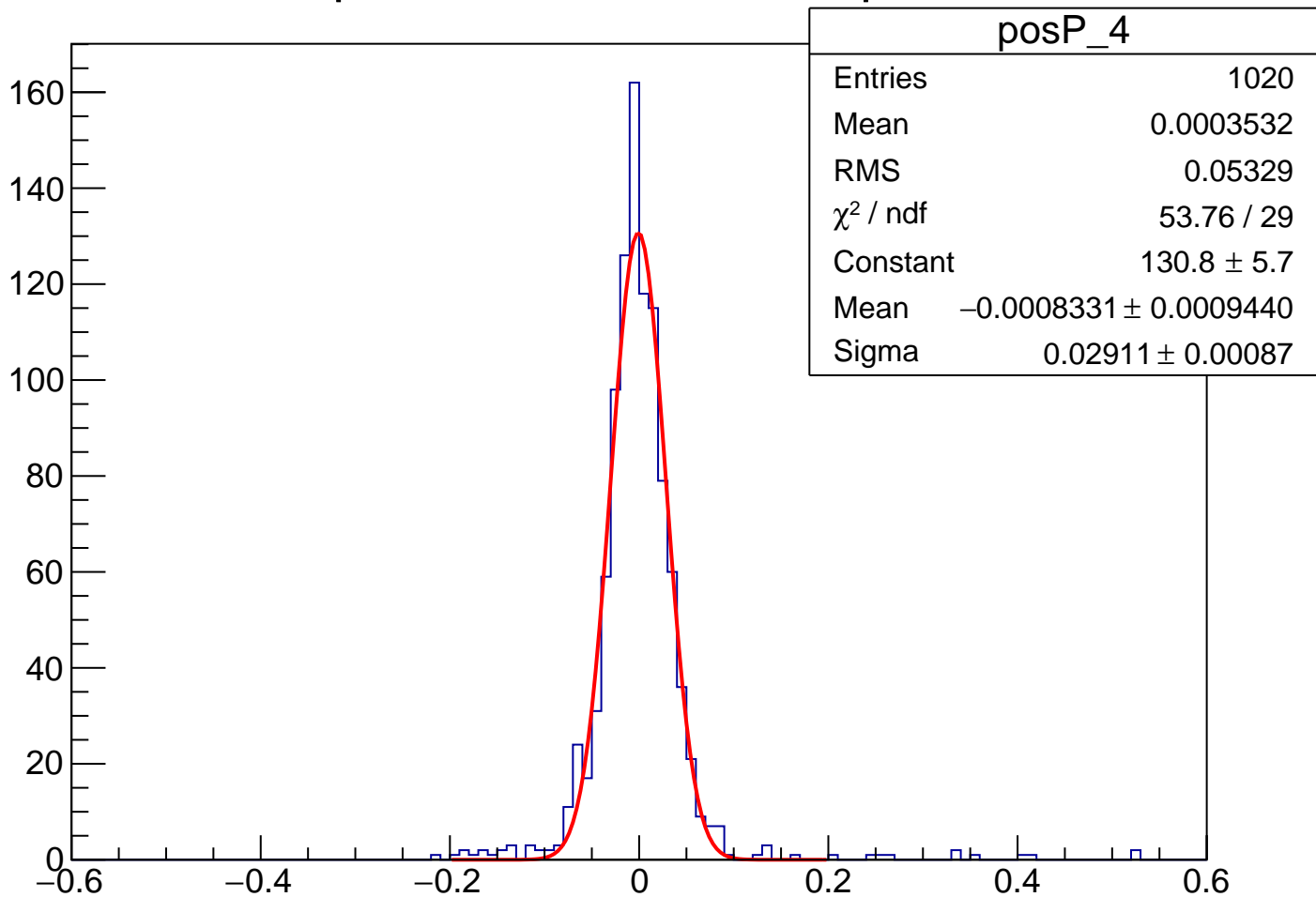
# ap\_24MeV\_L1L2\_posP



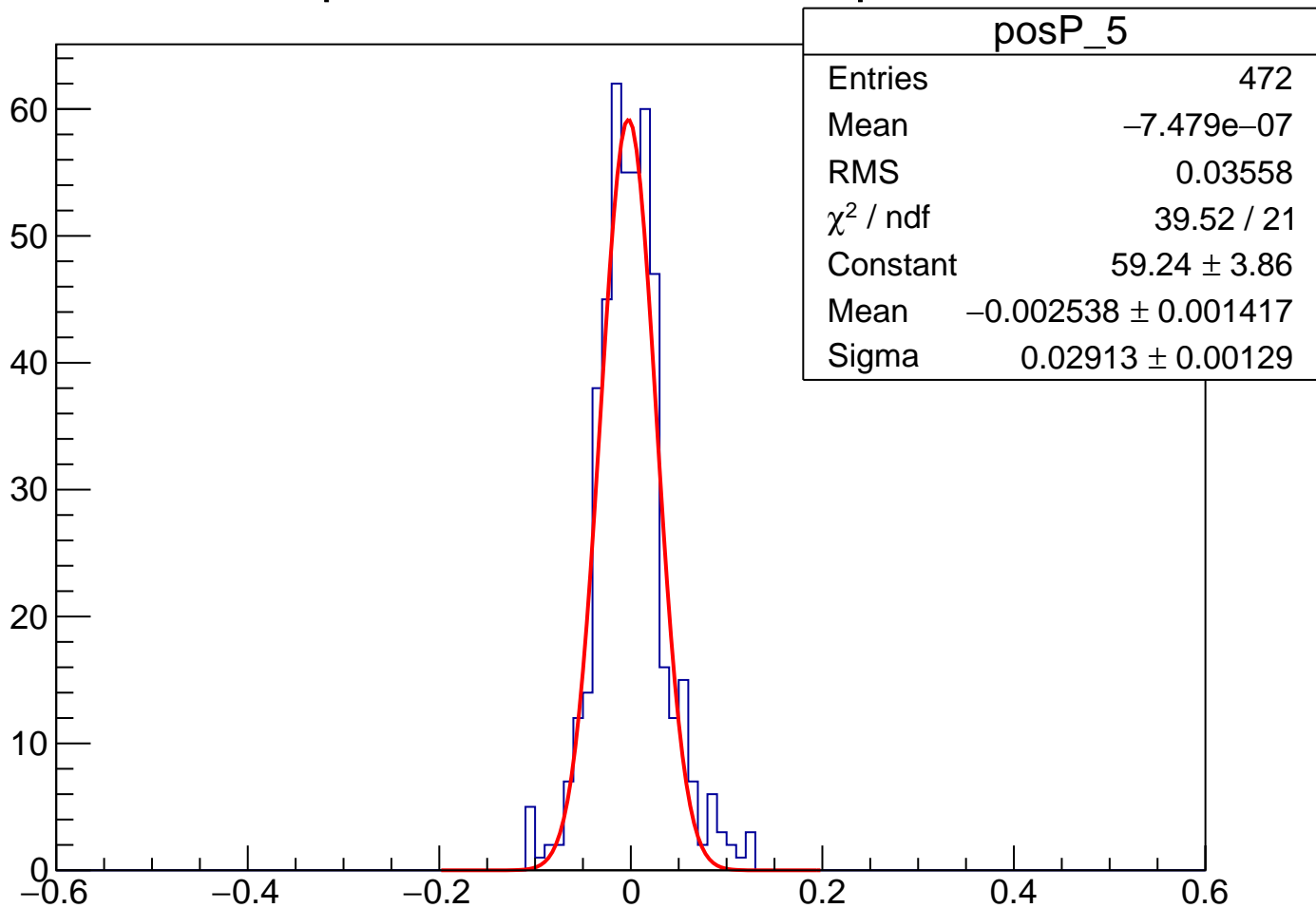
## ap\_24MeV\_L2L2\_posP



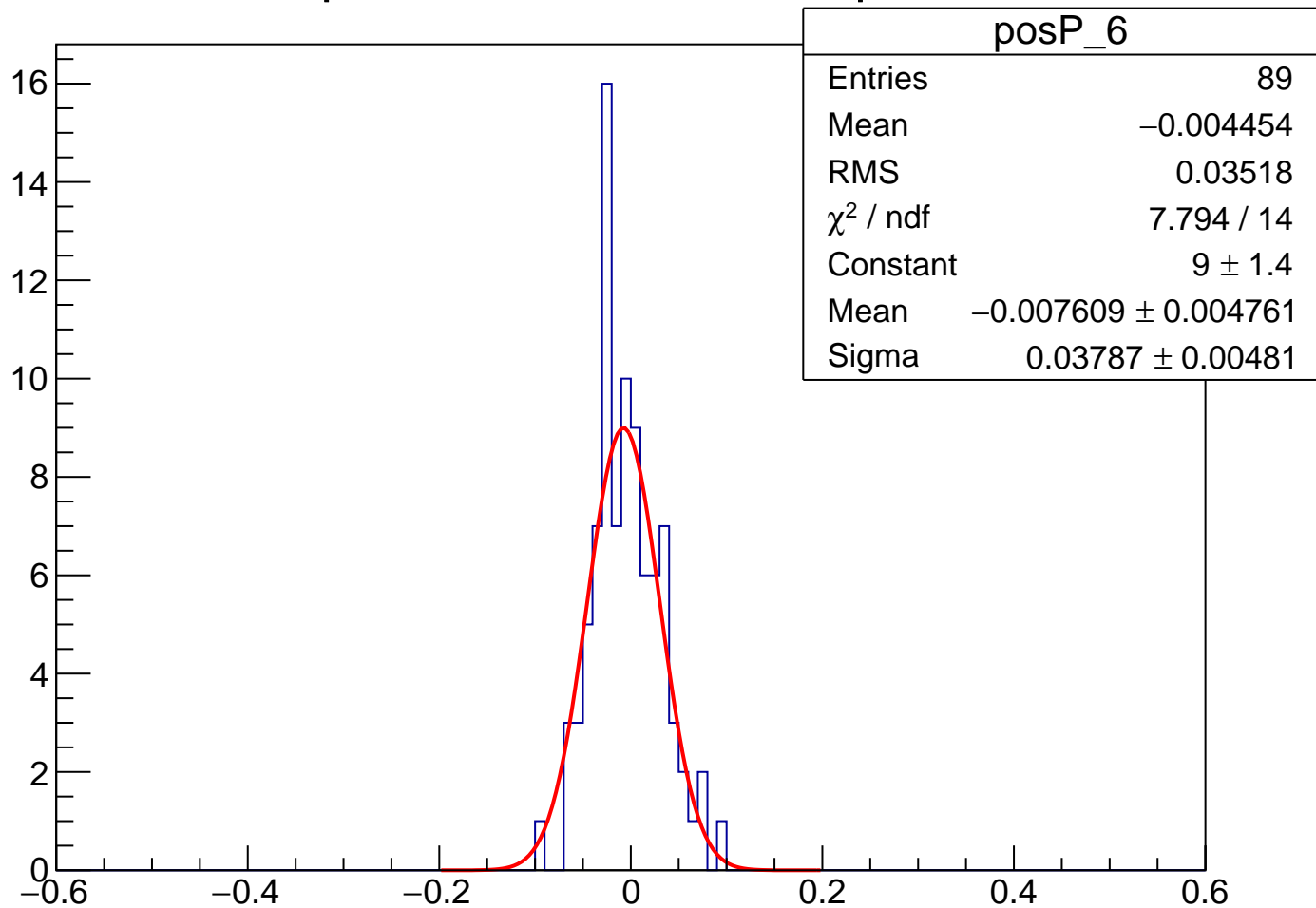
# ap\_24MeV\_L1L1\_loose\_posP



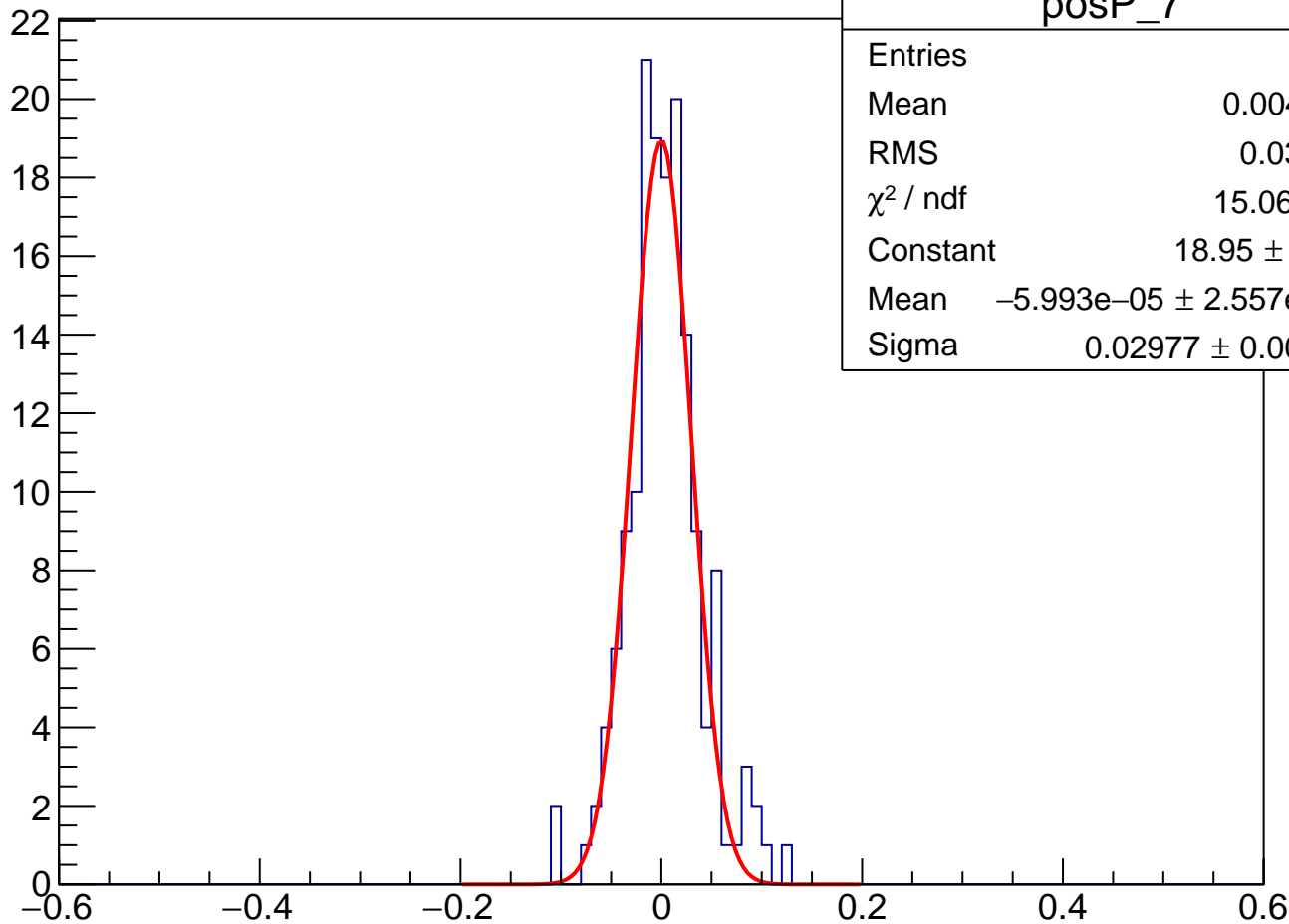
# ap\_24MeV\_L1L2\_loose\_posP



# ap\_24MeV\_L2L2\_loose\_posP



# ap\_24MeV\_L1L2\_loose\_extrap\_posP





# ap\_24MeV\_L2L2\_loose\_extrap\_posP

