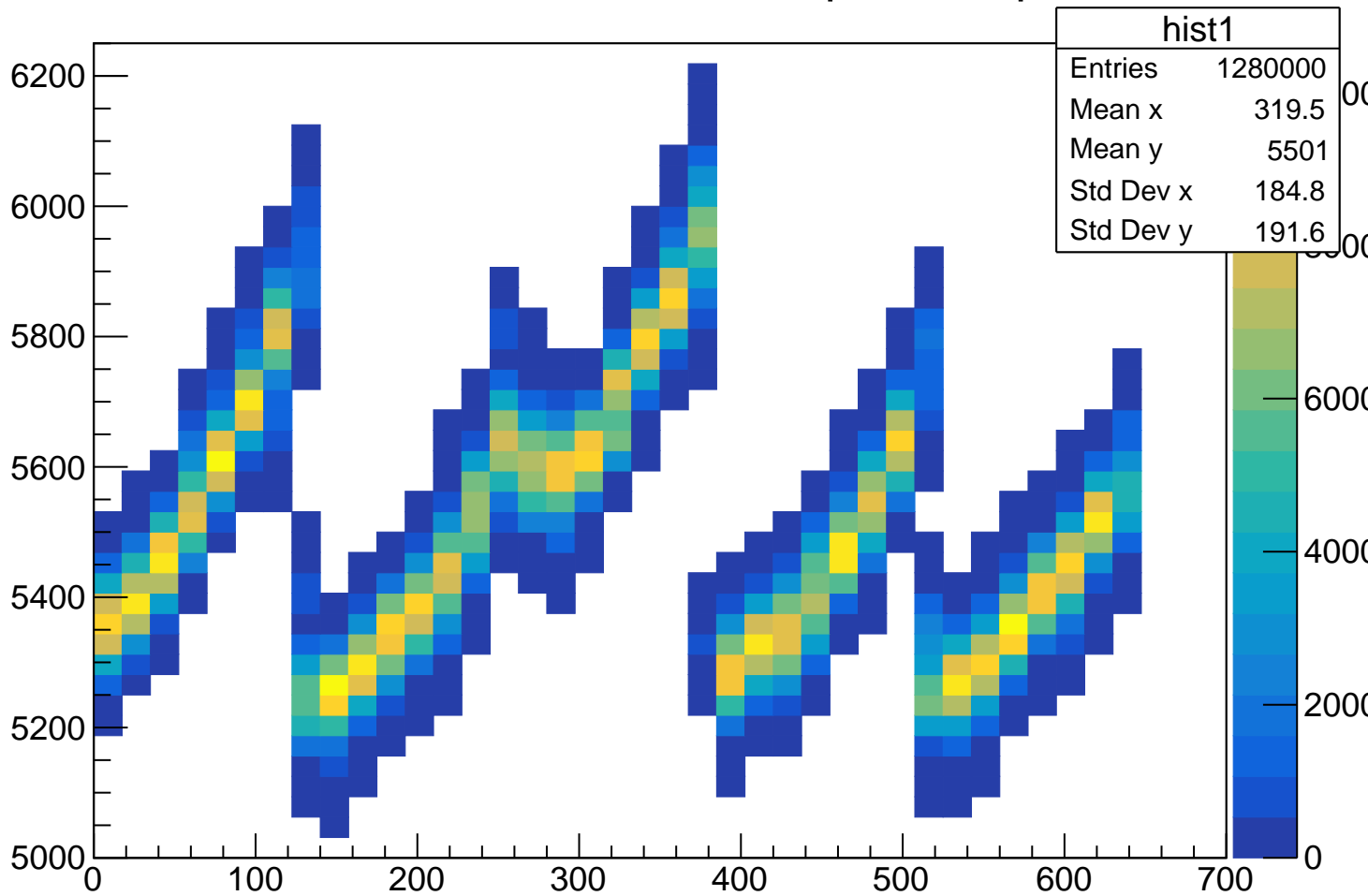
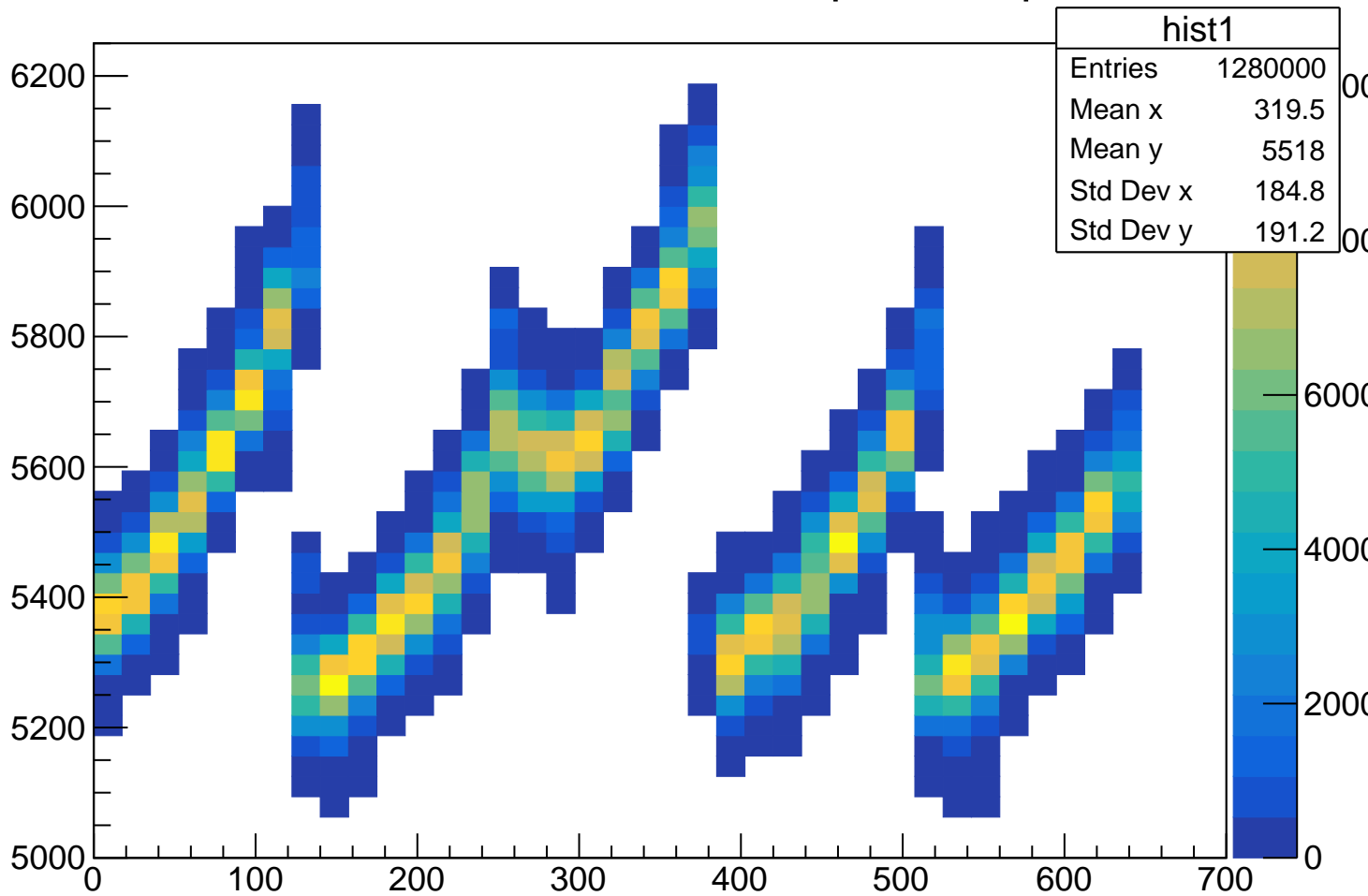


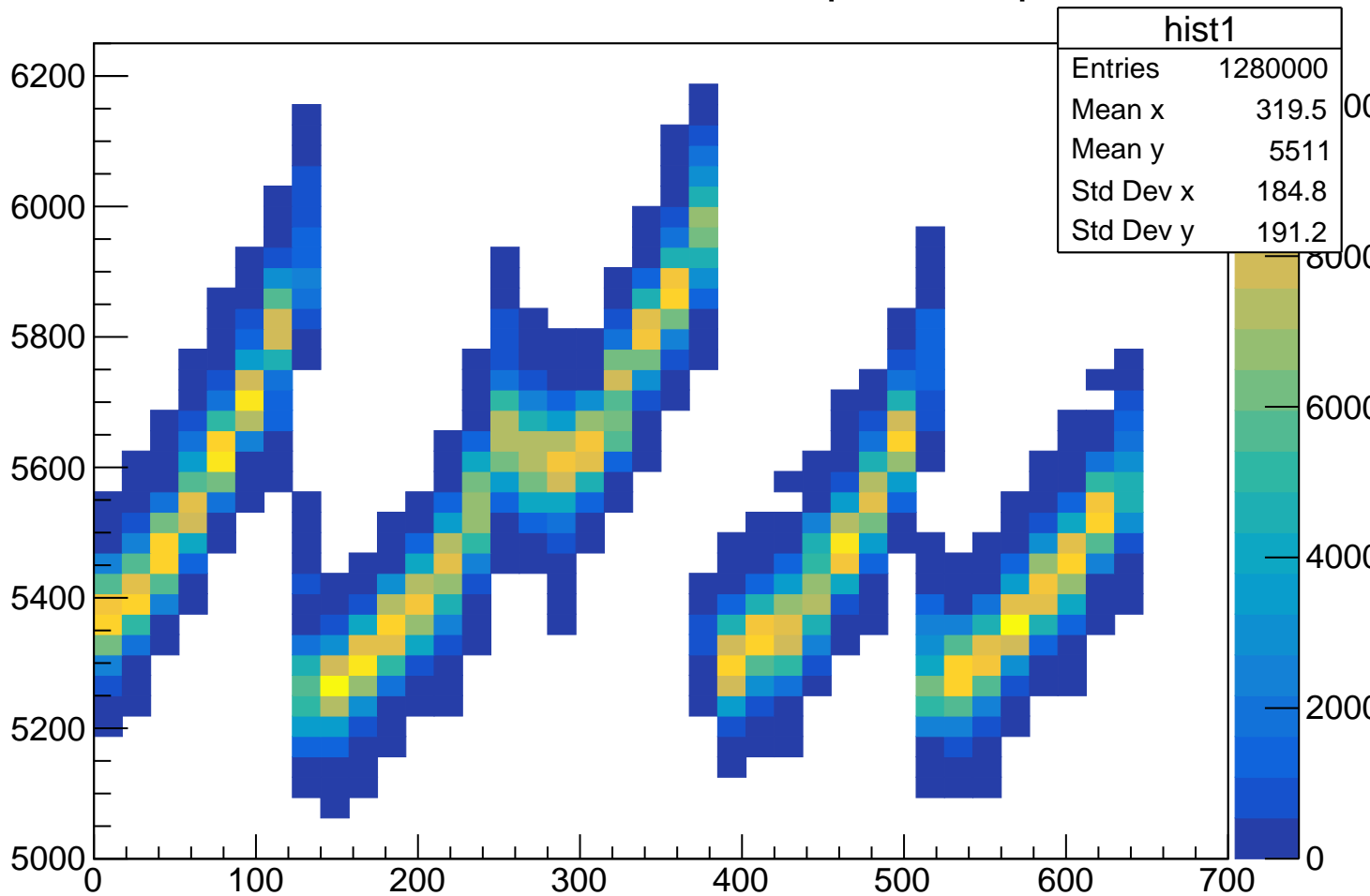
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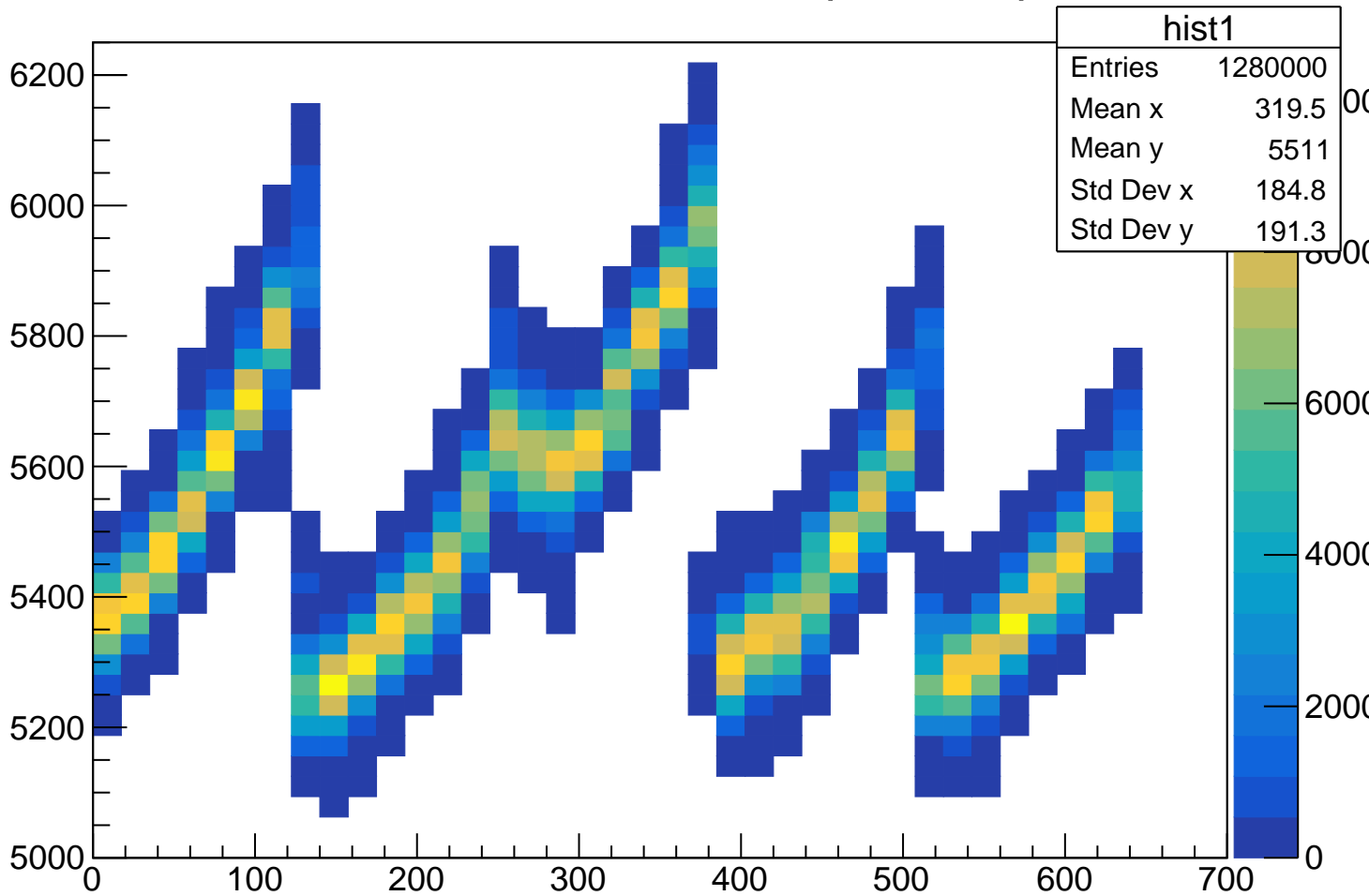
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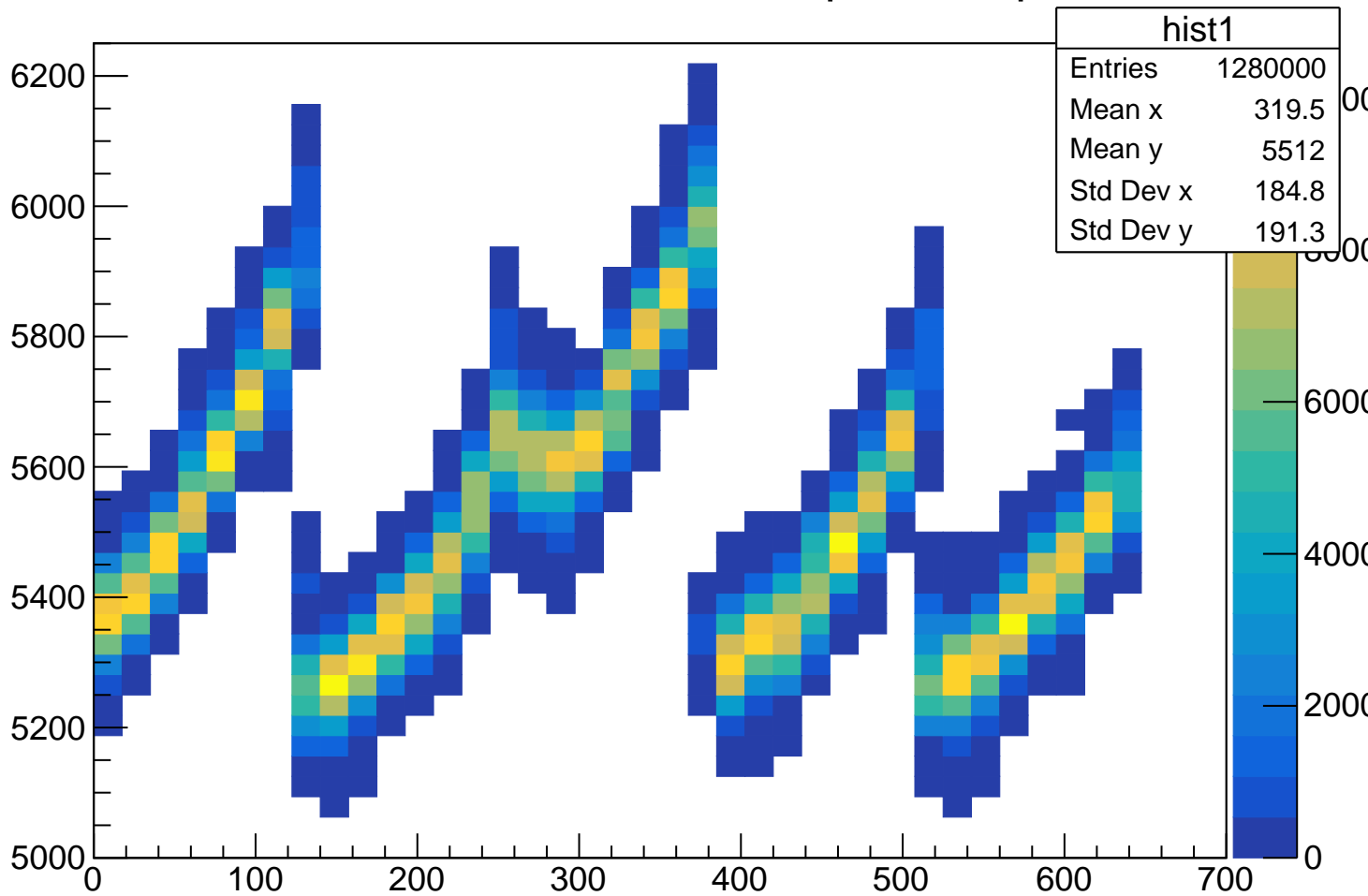
l6b\_axial\_slot\_f3\_h3\_avdd3pt0\_sample2



# l6b\_axial\_slot\_f3\_h3\_avdd3pt0\_sample3



l6b\_axial\_slot\_f3\_h3\_avdd3pt0\_sample4



l6b\_axial\_slot\_f3\_h3\_avdd3pt0\_sample5

