

**Release Test Strategy Template**

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# Product and Release Information

Brief description of the product and release, key customers, targets etc.

## Product Release Schedule

* Feature Complete (Most Features) date:
* WB Feature Complete date:
* Release Branch date:
* System testing start date:
* Restricted Submissions date:
* Code Freeze date:
* Final Build date:
* RTO date:
* GA date:

# Mandatory Reviewers and Approvers:

|  |  |  |  |
| --- | --- | --- | --- |
| Reviewer | Role | Approved (Y/N) | Comments |
|  |  |  |  |

***Example: Mandatory Reviewers should include: Design, peers, Scrum Master***

***Example: Approvers should include: Design lead, Test lead and Product Owner***

*NOTE: Currently the review will be started with Code Collaborator.*

# New Features in the Release

* + List new features in the release with Jira ID

## Feature Interaction

* List New Feature Interactions with Regression Domains
* List New feature RFC interactions with ANVL release test coverage improvement plan

## Feature Test Considerations

### Configuration or System Under Test (SUT) Requirements

* Refer to specific feature test plan which could be found in Jira

### Feature System Test Impacts

* Refer to specific feature test plan which could be found in Jira

# New Feature Regression Test

## Test plan is referred to what have done during feature testing for the new features

New feature test plans are reviewed with code collaborator and test result put in LTAF.

## New feature regression test plan

New feature test result should be accessed in LTAF:

<http://pek-lpgtest3.wrs.com/ltaf/feature_report.php>

# Vx7 Regression Test

Release regression includes CoreOS, Storage, MW, Connectivity, PPC arch, IA arch, ARM arch, UI, Networking, WB/OsTool, B&C, Compiler, Graphics, Hypervisor and CERT.

## CoreOS

### Configurations or SUT

List test matrix, for example

* PPC:

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **BSP Name** | **Compiler** | **up/smp** | **32/64** | **Enable/Disable Safety** | **Remove RTP etc layer VSB** |
| fsl\_p1p2 | gnu | smp | 32 | enable | no |
| fsl\_p1p2 | gnu | up | 32 | enable | no |
| fsl\_p1p2 | gnu | up | 32 | disable | no |
| fsl\_p1p2 | gnu | smp | 32 | disable | no |
| fsl\_p1p2 | gnu | smp | 32 | enable | yes |

* ARM:

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **BSP Name** | **Compiler** | **up/smp** | **32/64** | **Enable/Disable Safety** | **Remove RTP etc layer VSB** |
| fsl\_imx6\_imx6dl-sabreai | llvm | smp | 32 | enable | no |
| fsl\_imx6\_imx6dl-sabreai | llvm | up | 32 | enable | no |
| fsl\_imx6\_imx6dl-sabreai | llvm | smp | 32 | disable | no |

* IA:

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **BSP Name** | **Compiler** | **up/smp** | **32/64** | **Enable/Disable Safety** | **Remove RTP etc layer VSB** |
| itl\_generic | llvm | up | 64 | enable | no |
| itl\_generic | llvm | smp | 64 | enable | no |
| itl\_generic | llvm | up | 32 | enable | no |
| itl\_generic | llvm | smp | 32 | enable | no |

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description, for example

|  |
| --- |
| vxTest\_vxbus\_subsystem |
| vxTest\_vxbus\_gen |
| vxTest\_user\_wind\_shl\_romfs |
| vxTest\_user\_wind\_romfs |

List TC location, for example

$WIND\_BASE/pkgs/os/core/kernel-xxx/vxTest

List total TC number

## Storage

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## MW

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## Connectivity

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## PPC BSP

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## ARM BSP

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## IA BSP

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## UI

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## Networking

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## Workbench

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## B&C

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## Libc

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## Graphics

List test matrix

### Configurations or SUT

### Jira US

Link to US in Jira:

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## CERT

### Configurations or SUT

List test matrix

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## Multi Version

### Configurations or SUT

### Jira US

Link to US in Jira

### Test Cases or Test Suite

List TC description:

TC location:

TC number:

## Third-Party Library

### IOT

#### JSON

##### Configurations or SUT

##### Jira US

Link to US in Jira

##### Test Cases or Test Suite

List TC description:

TC location:

TC number:

### CORE\_DUMP\_RTP\_COMPRESS\_ZLIB

#### Test Cases or Test Suite

List TC description:

TC location:

TC number:

#### Configurations

### OpenCV

#### Configurations or SUT

#### Jira US

Link to US in Jira

#### Test Cases or Test Suite

List TC description:

TC location:

TC number:

### UNIX

#### Configurations or SUT

#### Jira US

Link to US in Jira

#### Test Cases or Test Suite

List TC description:

TC location:

TC number:

# Vx7 System Test

Testing of software and hardware conducted on a complete, integrated system to evaluate the system's compliance with its requirements. When possible, system testing should validate the product end-to-end as customers intend to use it. Expected results, messages, logs, alarms, records and outputs should be validated for every scenario, as a customer would see them, use them or be impacted by them.

System testing generally falls within the scope of black box testing, and as such, should require no knowledge of the inner design of the code or logic.

## System Integration Test

Build test on Linux hosts

Build test based on BSP to cover default, debug mode, inet4 only mode, micro configuration ( disable most of containers/layers) and fast build.

Build test based on CPU to cover default, debug mode, inet4 only mode, micro configuration (disable most of containers/layers) and fast build.

Build VIP, CDemo DKM, CPlus DKM, CPlus RTP, HelloWorld RTP based on vxsim prebuild VSB, and run them.

VIP profile test

Build and Load VIP image with development profile, development MINIMAL profile, development MINIMAL RTP profile, standalone profile, standalone MINIMAL profile, standalone MINIMAL RTP profile.

RTP and DKM test

Run RTP test to cover sample HelloWorld RTP, CPlus RTP, FloatPoint RTP, SystemCall RTP.

Test CDemo DKM with different compiler tool for VIP and DKM.

Test HelloWorld RTP with different compiler tool for VIP and RTP.

Basic CPP test

Build and run CPP (C Plus Plus) DKM application, that include envelope, operator, inherit and polymorphism.

QT test

Since QT did not work in SR0610, the related tests are stopped.

### Configurations or SUT

Target list:

The tests cover ARM UP/SMP 32bit/64bit, PPC UP/SMP 32bit/64bit, IA UP/SMP 32bit/64bit, vxsim 32bit/64bit. All test cases just select some BSP configurations, not full.

### Jira US

Link to US in Jira

### Test Cases or Suite

## Out of Box Experience (OOBE)

OOBE tests are not involved since the cases are not applicable in SR0630.

OOBE installation test

OOBE installation test focus on installation time and size.

Installation test: Try to installation the Vx7 with Customer view.

PartA:

Major focus on the Upgrade Case, the Customer already installed Vxworks product, then New upgrade package available, the customer do upgrade installation.

Following data provide a comparison that download from R1 ftp server and Windshare website.

Actually, the Customer will use the windshare to download all time, but till now no mirror windshare deployment in China, use R1 server simulate the windshare deployment case.

PartB:

New Customer become Vxworks System User, 2 Ways for Install the Software,

1) Only download the Installer, Install whole Vxworks product from Internet.

2) Download the FULL Installation package from Windriver windshare to local store, installed from local.

OOBE scenarios test

User Scenario test: Try to start use the Vxworks system after installation.

Basic documentation test: install spin and open readme to check all content and link work; Open WB and select “Help”->”Wind River Knowledge Library”->”VxWorks7” to confirm knowledge library can be opened.

Design test case to cover read and write host file from target.

Test build image time.

Test multi-thread build.

WorkFlow test

Covered the workflow testing from PM.

### Configurations or SUT

### Jira US

Link to US in Jira

### Test Cases or Suite

## Customer Use Case Test

VxSDK Test:

Create VIP/VSB RPM and WB branding RPM, generate installation package, install VxSDK package and confirm all branding correct, run network configuration test based on VIP/VSB of VxSDK package.

### Migration Test

-none

### Configurations or SUT

### Jira US

Link to US in Jira

### Test Cases or Suite

## Performance Test

### Benchmark Test

Kernel benchmarking test

Kernel benchmarking test includes math, mem, posix, rtpPosix, rtp, wind, pbzip, smp, raw, 9 suits.

The test case could be cache and dosfs enabled, or cache enabled and dosfs disabled, or cache and dosfs disabled. SMP configuration has 18 suits for every BSP, and UP configuration has 17 suits for every BSP.

Based on the benchmark performance result, the compare report will be generated between two DVD spins. The intention of performing such benchmarking is to ensure continued performance improvement without regression from release to release.

Footprint test

Footprint test use the image size (Text segment, Data segment, BSS segment size) to check if new release import some potential issues.

CERT benchmarking test

CERT benchmarking test includes math, mem, rtp, wind, smp, raw, 6 suits.

The test case could be cache and dosfs enabled, or cache enabled and dosfs disabled, or cache and dosfs disabled.

### Capacity Test

-none

### Overload Test

-none

### Configurations or SUT

### Jira US

Link to US in Jira

### Test Cases or Suite

## Resiliency Test

*-none*

### Robustness Test

-none

### Availability Test

*-none*

### Advance Life Cycle Test

*-none*

### Configurations or SUT

*-none*

### Jira US

*-none*

### Test Cases or Suite

*-none*

## Stability Test

Crashme Test

Crashme is from opensource, is used to stability test, and tests the operating environment's robustness by invoking random data as if it were a procedure. Crashme cover three levels test, Basic (1min), Med (5min), and Heavy (30mins).

### Configurations or SUT

Target list:

Crashme select one target of every ARCH (PPC, IA, ARM), for this release one PPC64 target should be tested.

Please see the matrix file for details.

### Jira US

Link to US in Jira

### Test Cases or Suite

TC location

<http://twiki.wrs.com/PBUeng/VxWorksSystemCrashme>

TC number

Total: 12

## Security Test

Security Profile Test:

User Management test

User Management Policy test

User Priviledge test

Loginbanner test

SecEventLoginOK test

SecEventLogout test

SecEventLoginFailed test

Security TCPlay test

### Configurations or SUT

Security profile covers ARM up, PPC up/smp 32bit/64bit, IA up/smp 32bit/64bit.

Please see the matrix file for details.

### Jira US

Link to US in Jira

### Test Cases or Suite

## Backward Compatibility Test

*-none*

### Configurations or SUT

*-none*

### Jira US

*-none*

### Test Cases or Suite

*-none*

## Upgrade Test

*-none*

### Configurations or SUT

*-none*

### Jira US

*-none*

### Test Cases or Suite

*-none*

## Documentation Test

-Covered by OOBE testing

### Configurations or SUT

-Covered by OOBE testing

### Jira US

### Test Cases or Suite

## Code Coverage

For VxWorks7 Release testing, Code coverage data will be generated via simics tool..

Report link : <http://pek-cc-pb08l.wrs.com/vxtest/vxtest1/LOG_VX7/Vx-7_CodeCoverage/report.html>

# Helix Regression Test

## Vx7 regression test on Helix spin

5A test framework is used to test on Helix spin

## Helix regression test on Helix spin

# Test Improvement

List major test improvemen on this release.

# Test Result

## LTAF Links

<http://pek-lpgtest3.wrs.com/ltaf/report.php>

## Defect Query Links

List release defect dashboard here:

# Not Tested & Test Limitations

## Not Tested

## Test Limitations

List test limitations here

# Risks and Dependencies

## Risk Summary

# Mitigation Plan

# References

1. Vx7 test strategy template:
2. VxWorks test case meta data guideline: <https://jive.windriver.com/docs/DOC-80297>
3. VxWorks test document Jive location: <https://jive.windriver.com/community/engineering/operation-system-common-platforms/teams/vxworks/vxworks-test>