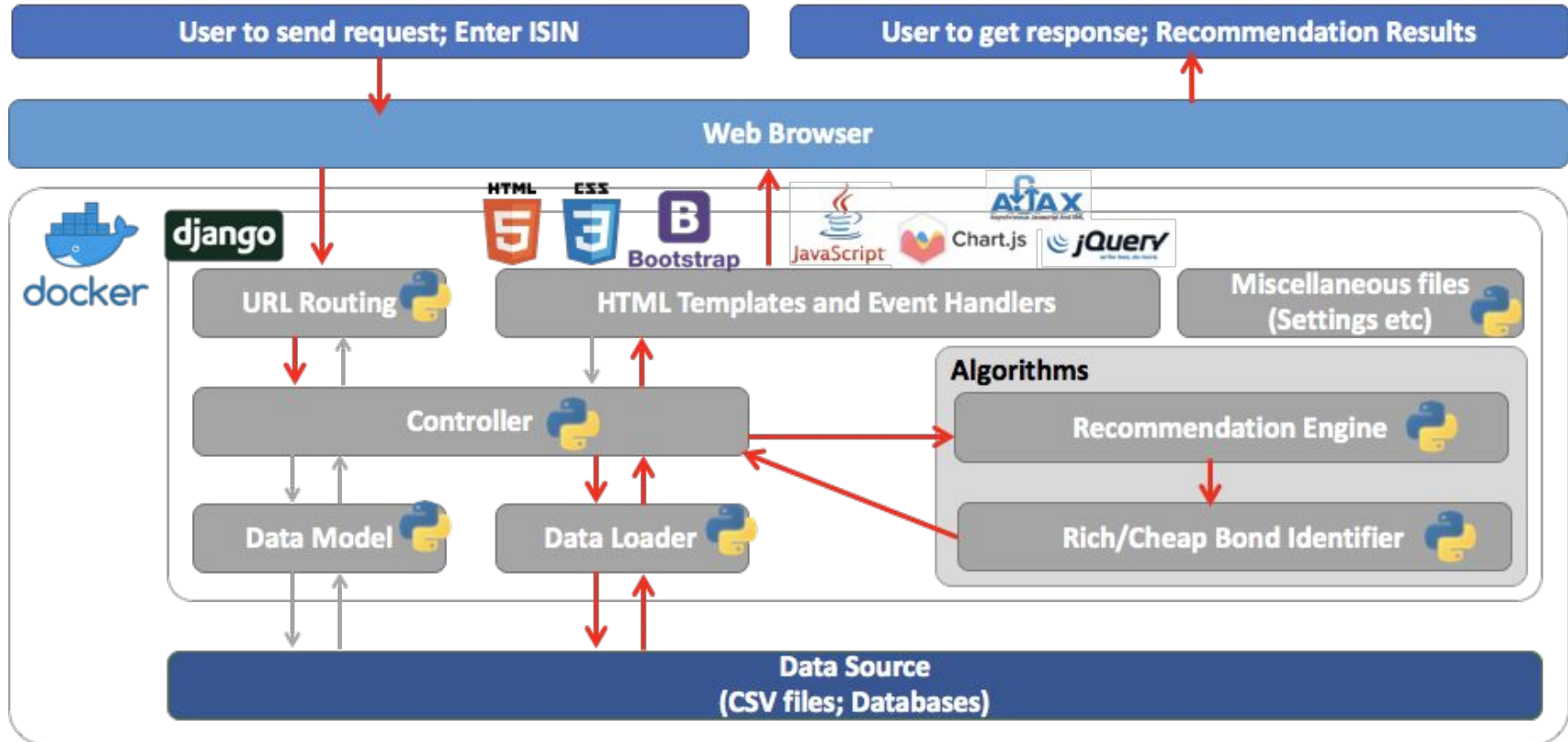


# High-level System Architecture



# High-level Algorithms Detail

