

NEXT GENERATION OF AI FOR POWER ELECTRONICS: **Explainable, Light, and Flexible**

Xinze Li, xinzel@uark.edu

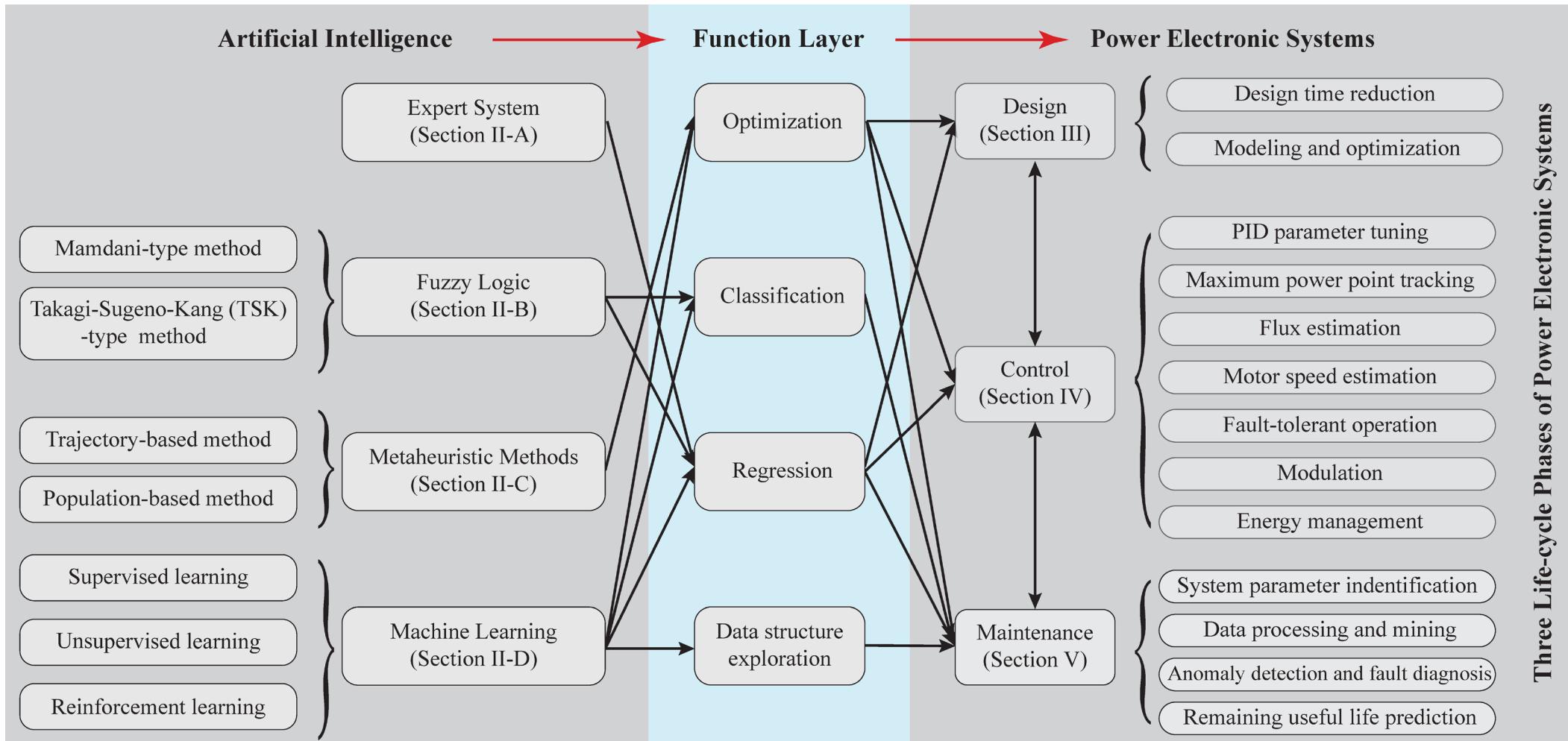


UNIVERSITY OF
ARKANSAS[®]

NEXT GENERATION OF AI FOR POWER ELECTRONICS

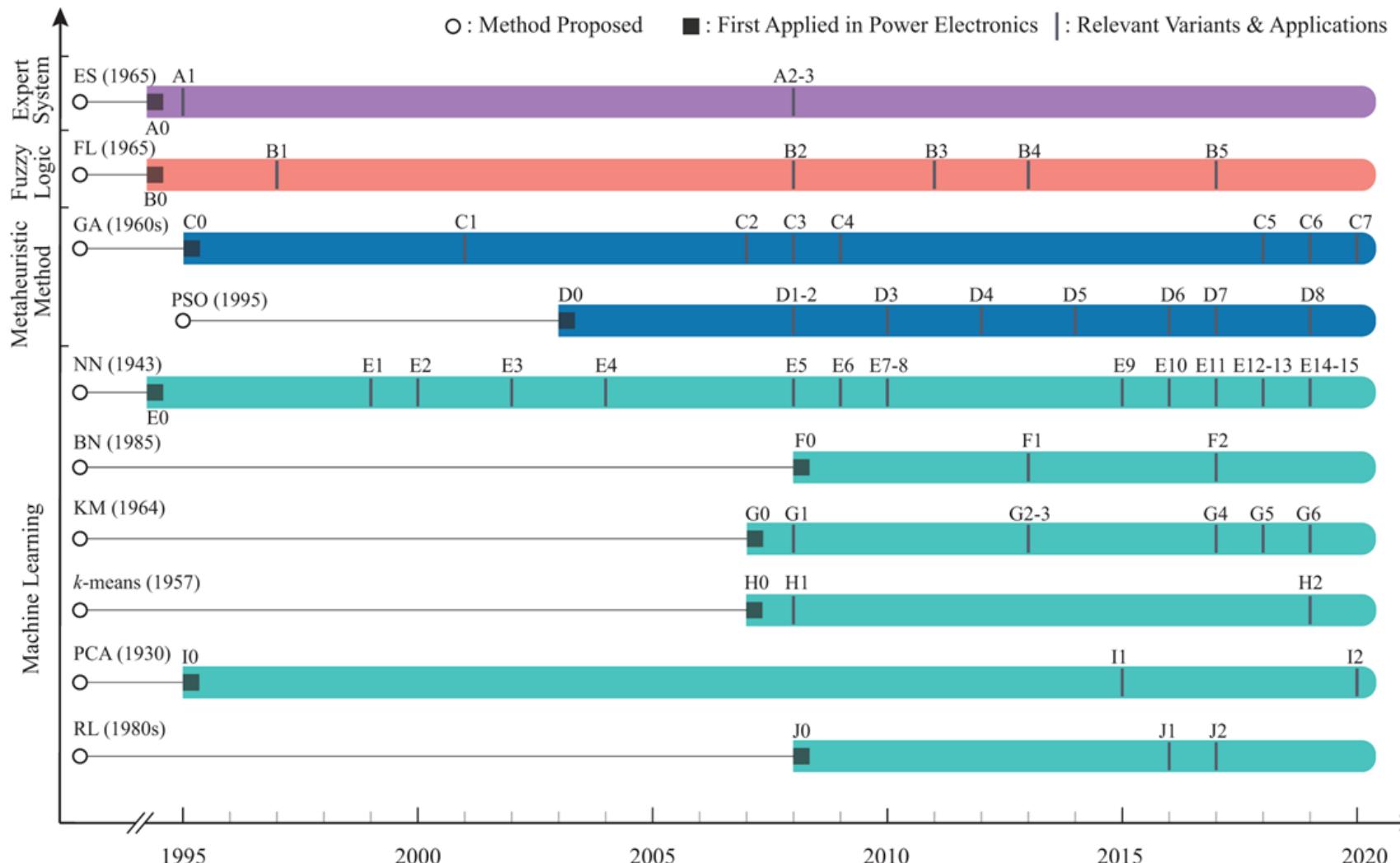
- I. Applications of AI in Power Electronics and its **Challenges**
- II. Physics-Informed Machine Learning (PIML) for Power Electronics
- III. Fundamentals of Physics-in-Architecture Neural Network (PANN)
and its Explainability in Power Electronics
- IV. PANN is Light in Two Aspects: Data-Light and Lightweight
- V. PANN is Flexible: “All You Need” is One PANN Model
- VI. Future Directions of PANN in Power Electronics

Applications of AI in Power Electronics



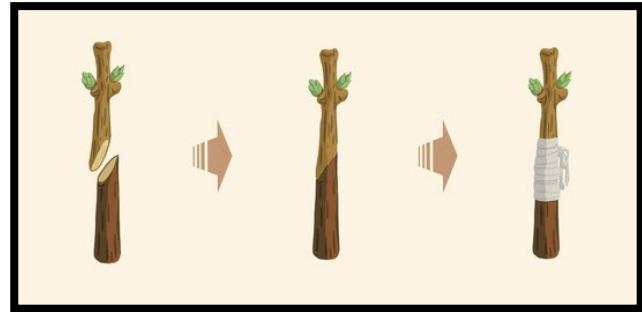
Source: Shuai Zhao, Frede Blaabjerg, and Huai Wang, "An overview of artificial intelligence applications for power electronics," *IEEE Transactions on Power Electronics*, vol. 36, no. 4, pp. 4633 – 4658, Apr. 2021.

Timeline of AI Algorithms Applied in Power Electronics

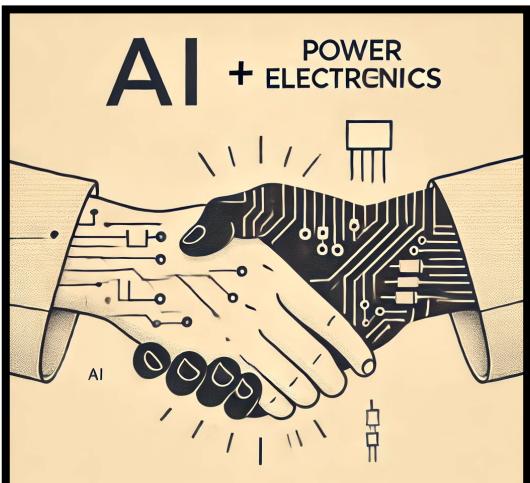


Source: Shuai Zhao, Frede Blaabjerg, and Huai Wang, "An overview of artificial intelligence applications for power electronics," *IEEE Transactions on Power Electronics*, vol. 36, no. 4, pp. 4633 – 4658, Apr. 2021.

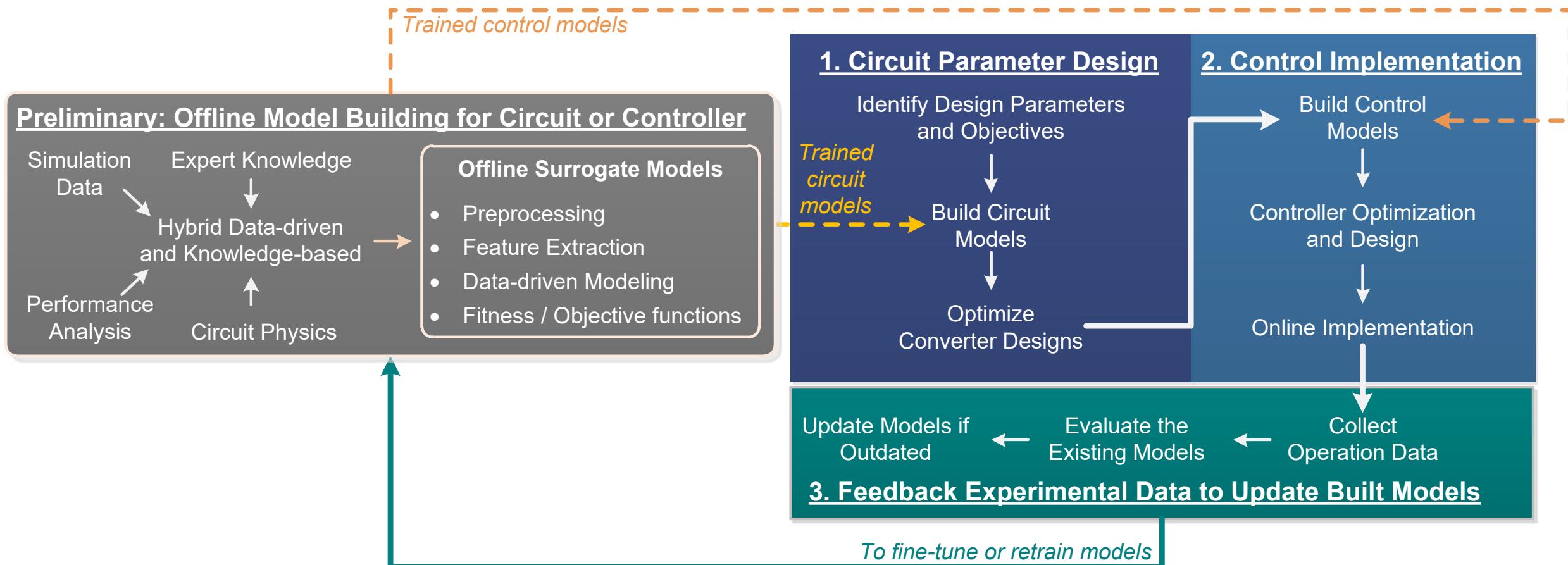
Just like **plant grafting**, where a specific branch is carefully chosen and integrated into a **compatible rootstock**



AI needs to be **adapted and enhanced** with power electronics expertise.

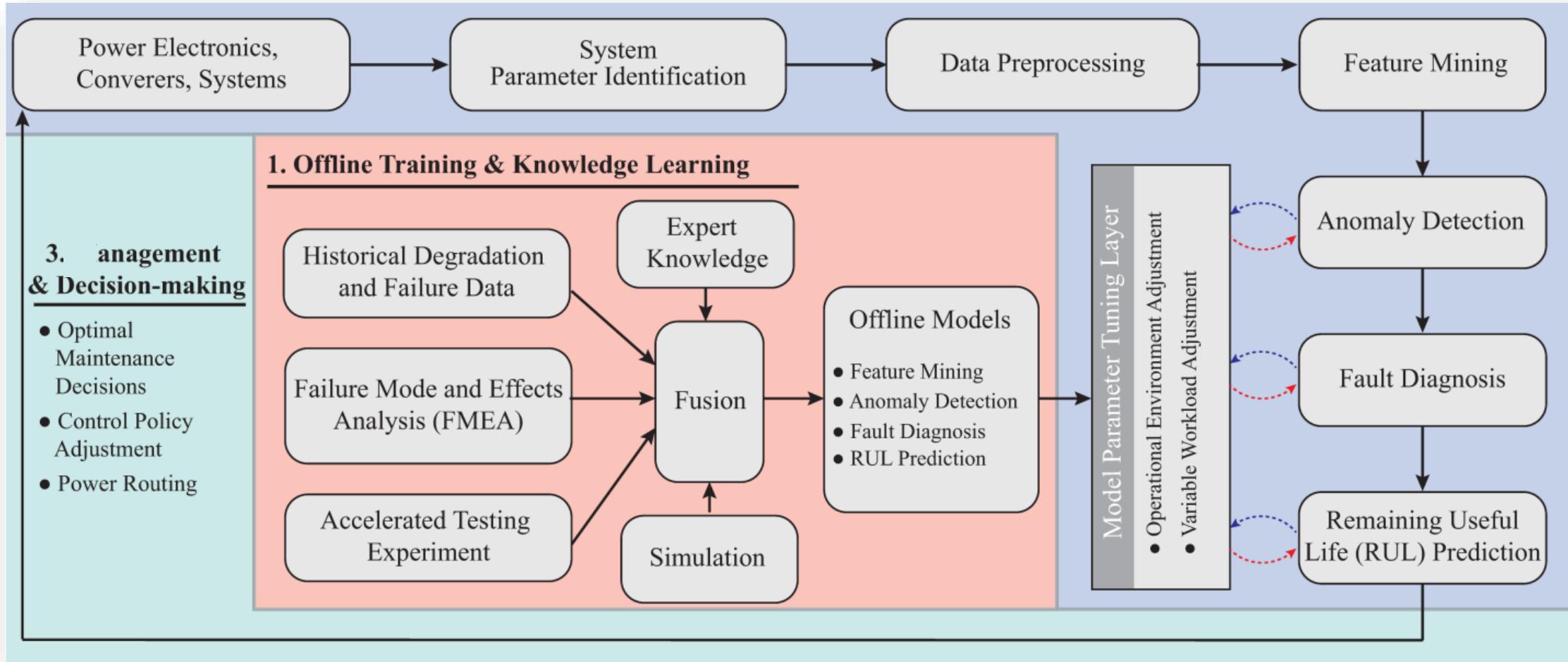


Existing Application 1: AI-Based Circuit and Control Design



Abstract diagram of circuit and control design through AI-based methods

Existing Application 2: Digital Twin Based Condition Monitoring



Source:

S. Zhao, Y. Peng, Y. Zhang and H. Wang, "Parameter Estimation of Power Electronic Converters With Physics-Informed Machine Learning," in *IEEE Transactions on Power Electronics*, vol. 37, no. 10, pp. 11567-11578, Oct. 2022.

Challenge 1: Power Electronics Requires **Data-Light and Computation-Light** AI

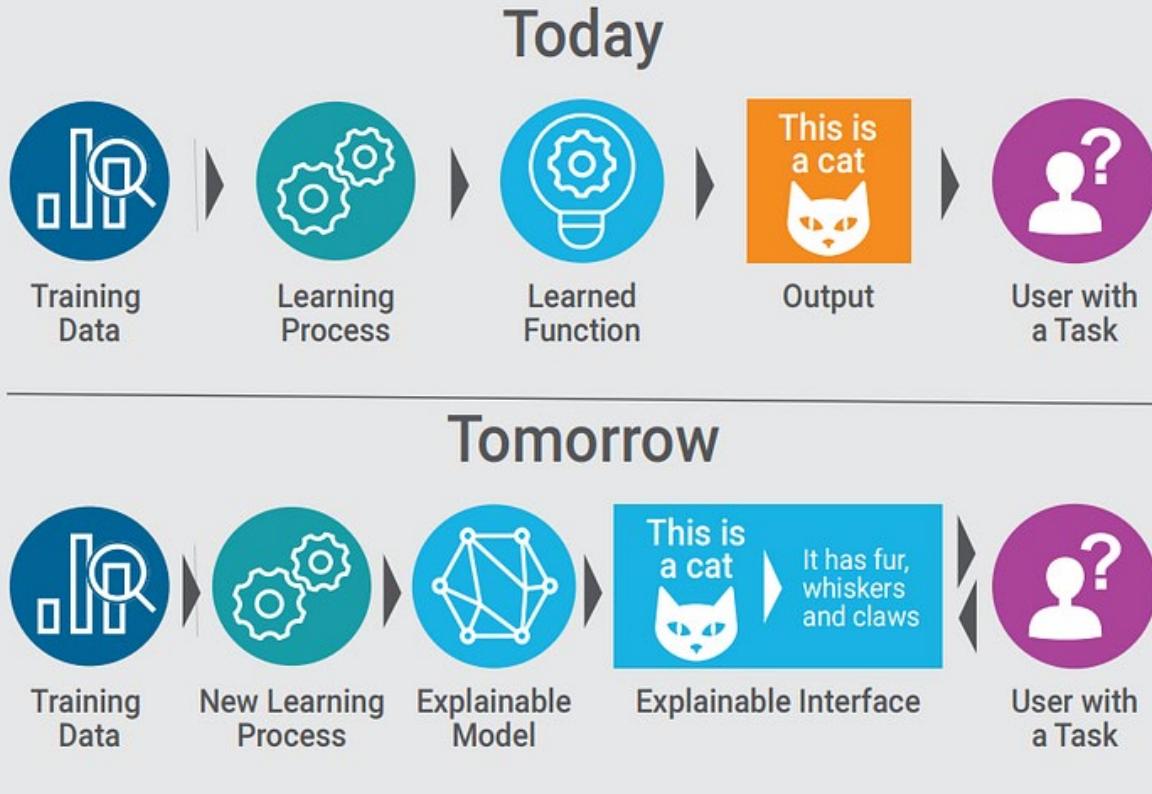
		Conventional fields	Power electronics
Data	“Big data” 	Small data 	
Accuracy	Medium 	High 	
Computation unit	Flexible 	Edge node with limited computation power 	

Characteristics of power electronics and other fields

Source: Aalborg University, “Researchers will design cognitive power electronics”,
<https://www.aau.dk/researchers-will-design-cognitive-power-electronics-n28286>

Challenge 2: Explainability of AI to Enhance Deployment Confidence

The goal of explainable AI



Leverage PE-GPT to Provide Power Electronics Insights into a Designed Buck Converter

User: For the task to design LC filter for the buck converter which has minimum current ripple, **why do you recommend this inductor value?**

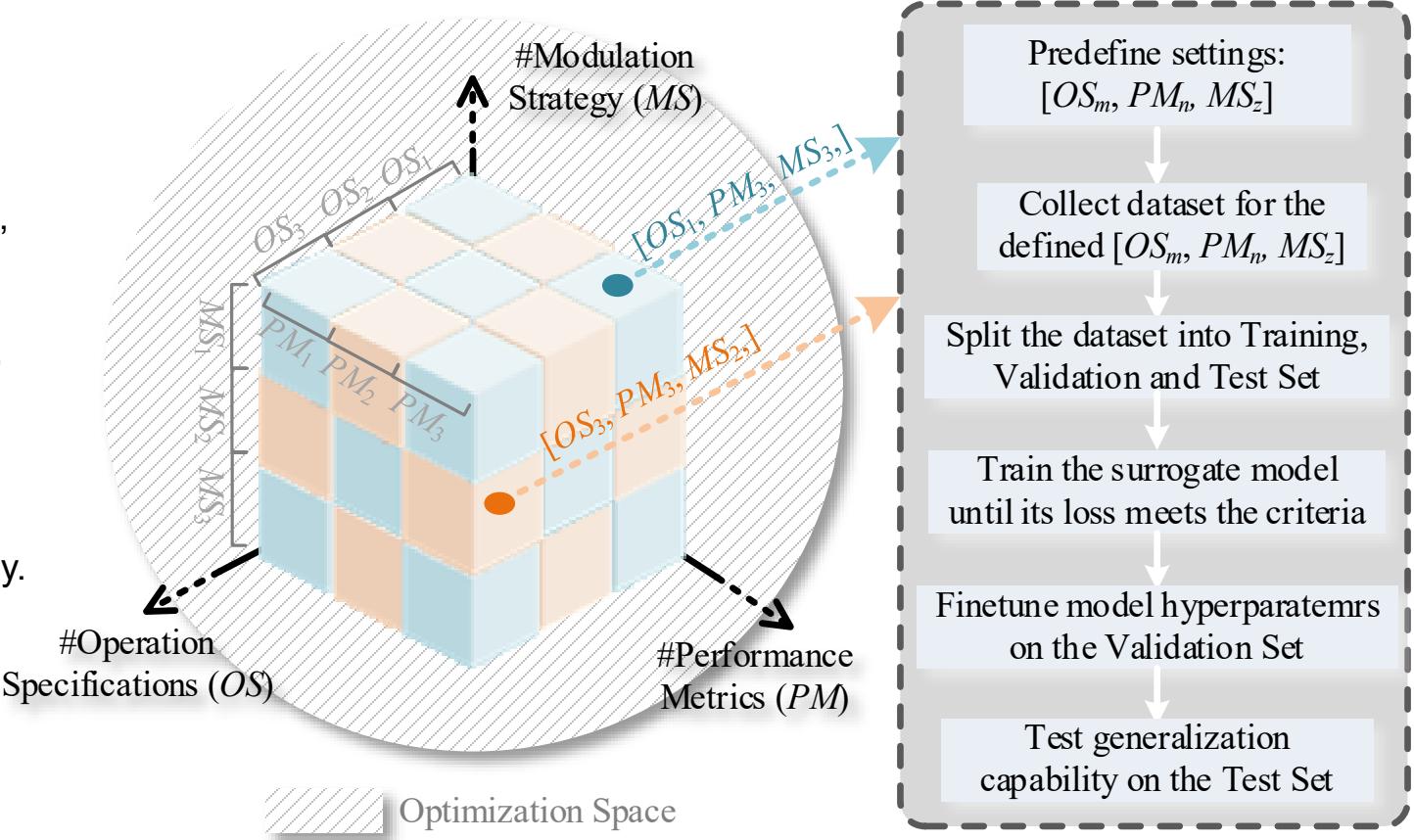


PE-GPT by F. Lin et al.

AI: The inductor helps to smooth out the ripple in the output current by **resisting sudden changes in current flow**. Lower ripple percentages typically require larger inductors.

Challenge 3: Existing AI has **NO Flexibility** for Diverse Scenarios

- Traditional data-driven is **fixed in operational settings**, limiting model flexibility when settings change.
- Building separate surrogate models for all settings **is data-intensive and computationally demanding**.
- Handling multiple settings need **$N_t = m \times n \times z$ models** (imaging only 3 variables), increasing complexity significantly.
- Topology, circuit parameters, control strategies, performance metrics, operation specifications, etc., could all change.



Source: F. Lin, X. Li, X. Zhang and H. Ma, "STAR: One-Stop Optimization for Dual-Active-Bridge Converter With Robustness to Operational Diversity," in *IEEE Journal of Emerging and Selected Topics in Power Electronics*, vol. 12, no. 3, pp. 2758-2773, June 2024.

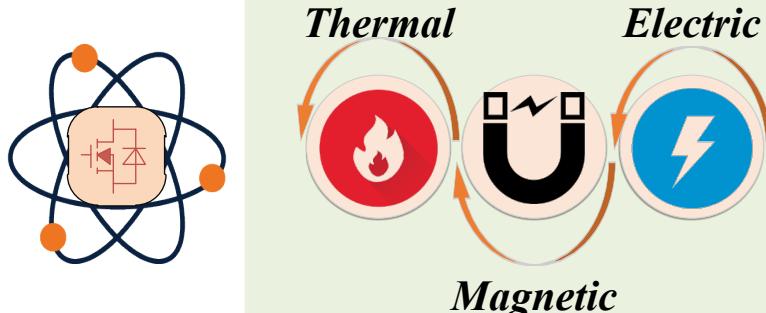
Number of models required for variable modulation strategy, operation specification, and performance metrics

NEXT GENERATION OF AI FOR POWER ELECTRONICS

- I. Applications of AI in Power Electronics and its Challenges
- II. Physics-Informed Machine Learning (PIML) for Power Electronics**
- III. Fundamentals of Physics-in-Architecture Neural Network (PANN)
and its Explainability in Power Electronics
- IV. PANN is Light in Two Aspects: Data-Light and Lightweight
- V. PANN is Flexible: “All You Need” is One PANN Model
- VI. Future Directions of PANN in Power Electronics

Concept of Physics-Informed Machine Learning (PIML):

Physics Laws Expressed as Partial Differential Equations (PDEs)



Circuit state-space equations:

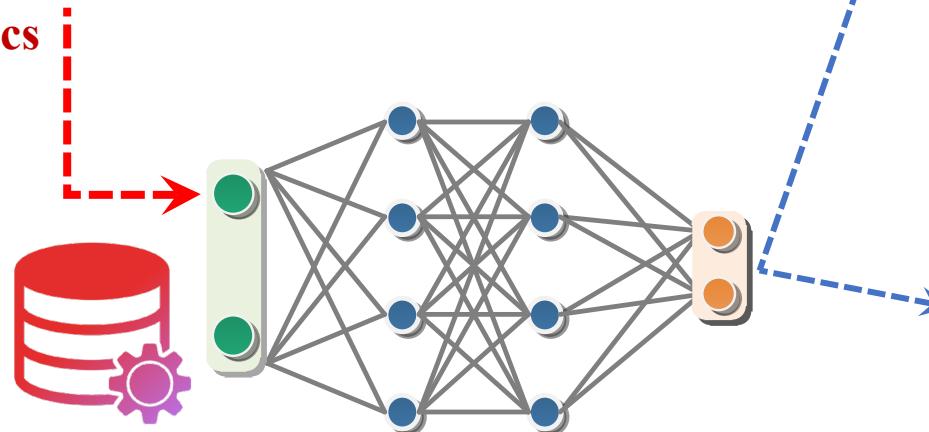
$$\frac{dx(t)}{dt} = Ax(t) + Bu(t)$$

Heat equation in isotropic medium:

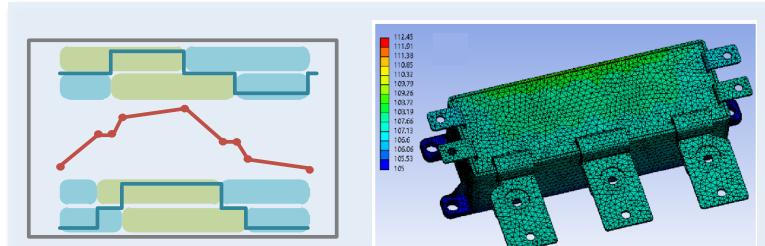
$$\frac{\partial T(t)}{\partial t} = \alpha \left(\frac{\partial^2 T}{\partial x^2} + \frac{\partial^2 T}{\partial y^2} + \frac{\partial^2 T}{\partial z^2} \right)$$

Integrate physics

Steer the learning of PIML towards **identifying physically consistent solutions**



Machine Learning Models

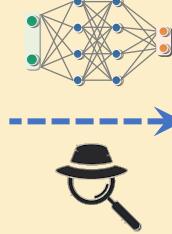


✓ **Ill-posed scenarios**

- Noisy or missing data
- Deficient physical models

✓ **Mesh-free**

Forward Problem (Solve PDEs)

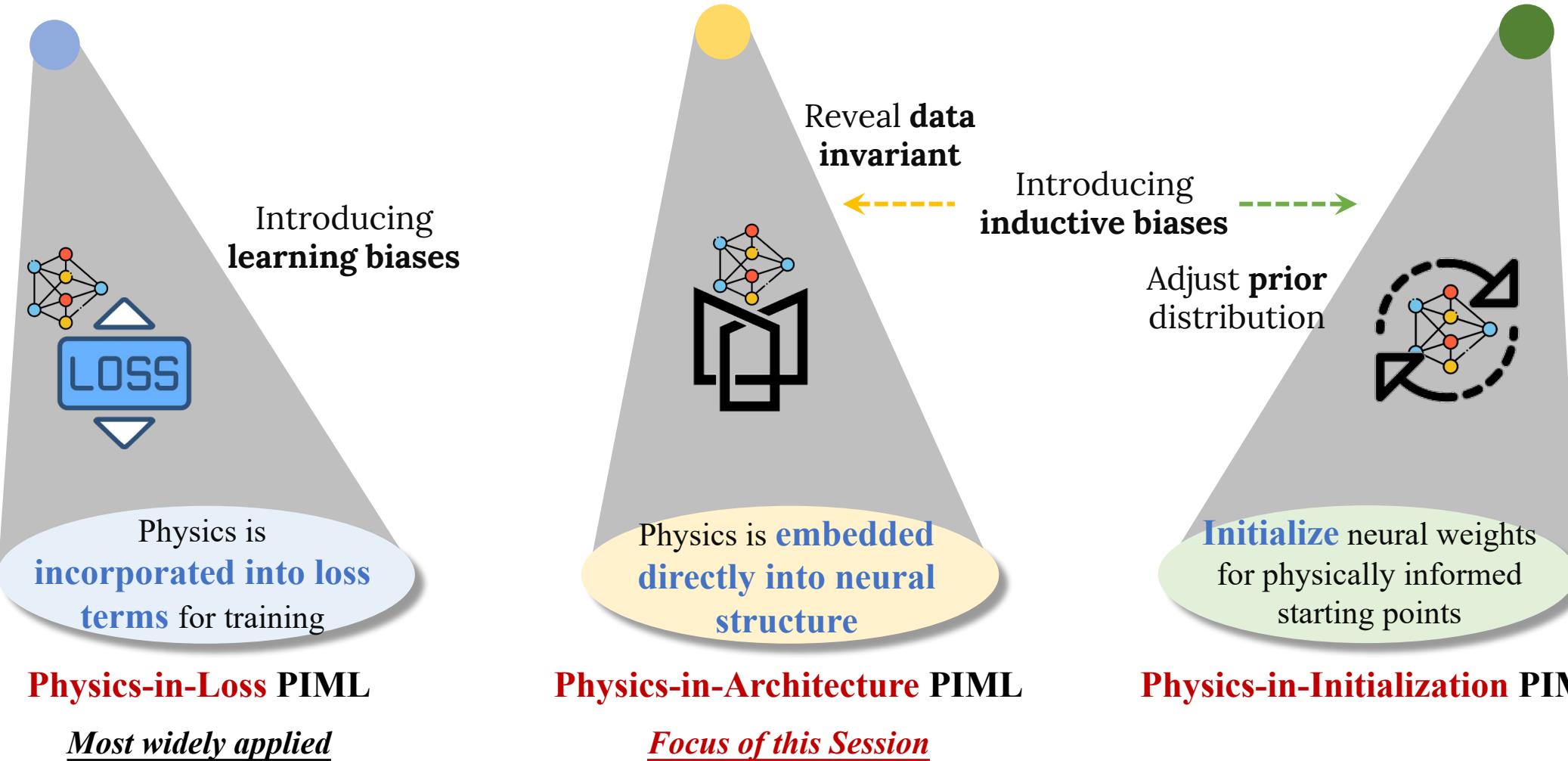


Identify parameters

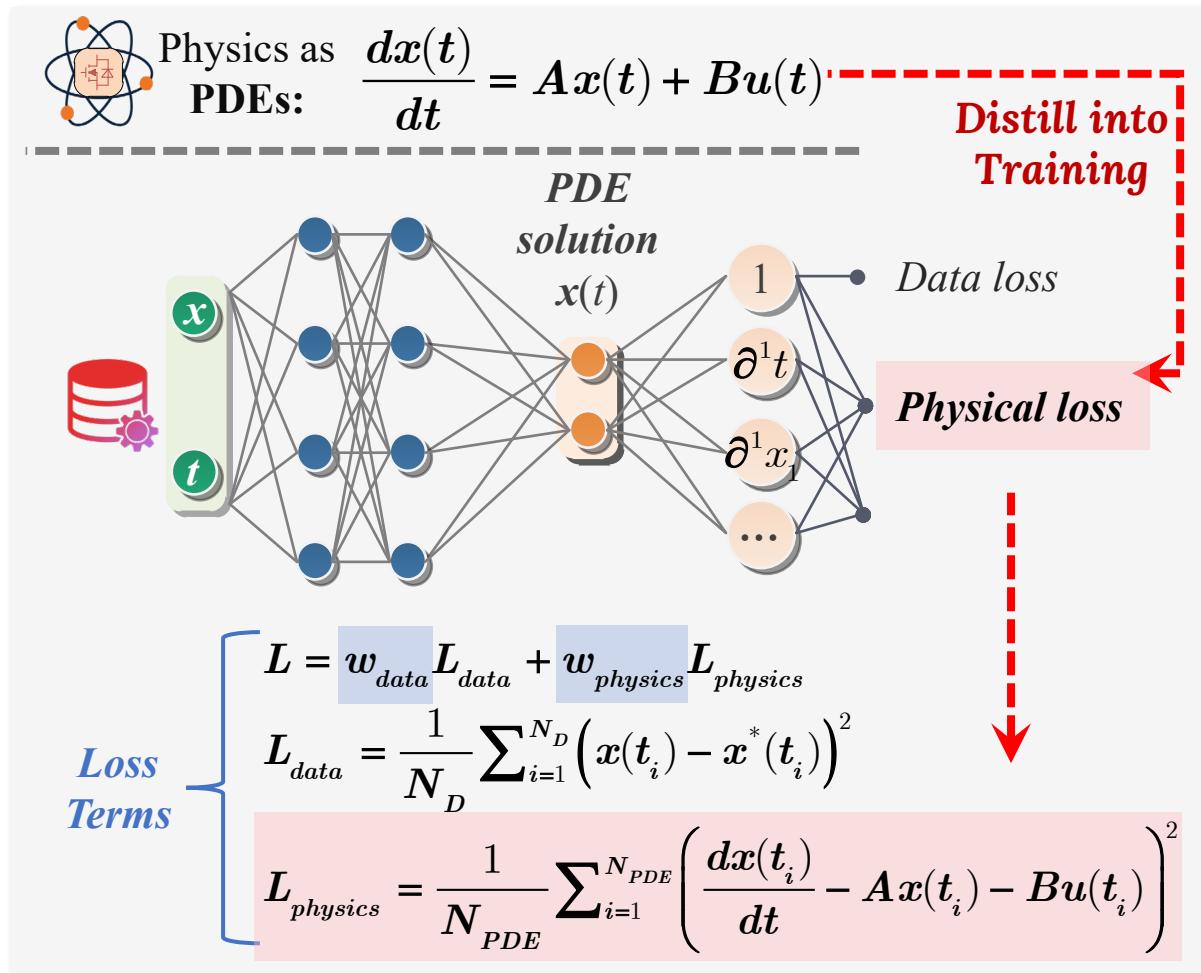
- ✓ **Easy mathematical formulation**
- ✓ **Rich support in AI fields**

Inverse Problem (Infer Parameters)

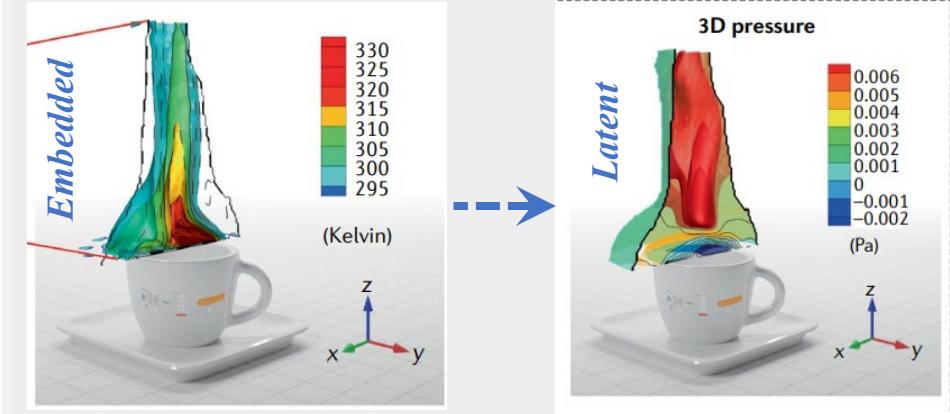
Main Types of PIML Approaches:



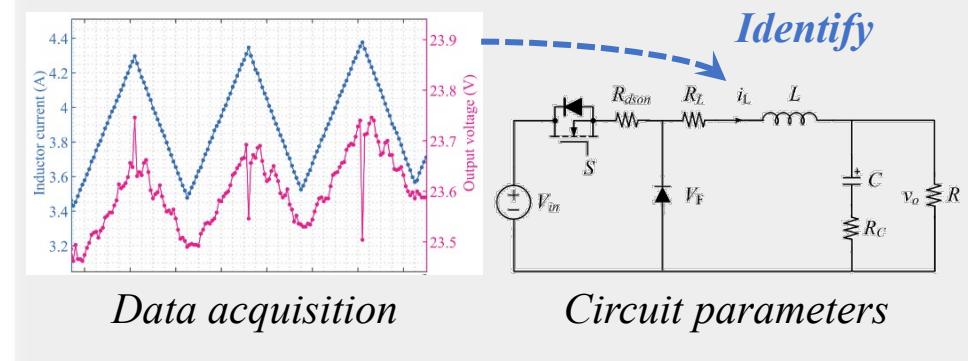
Physics-in-Loss PIML and Some Applications:



Solve PDEs with partially hidden dynamics



Parameter identification for power converters

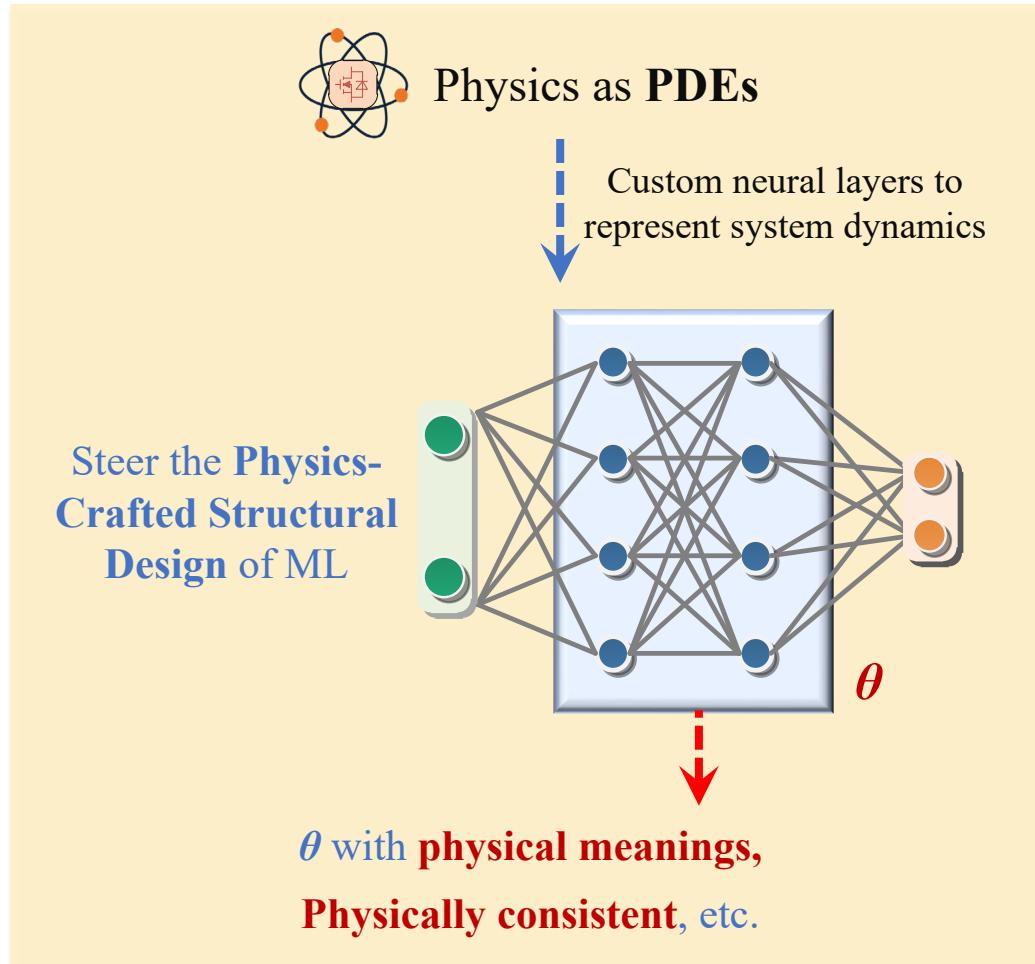


Source:

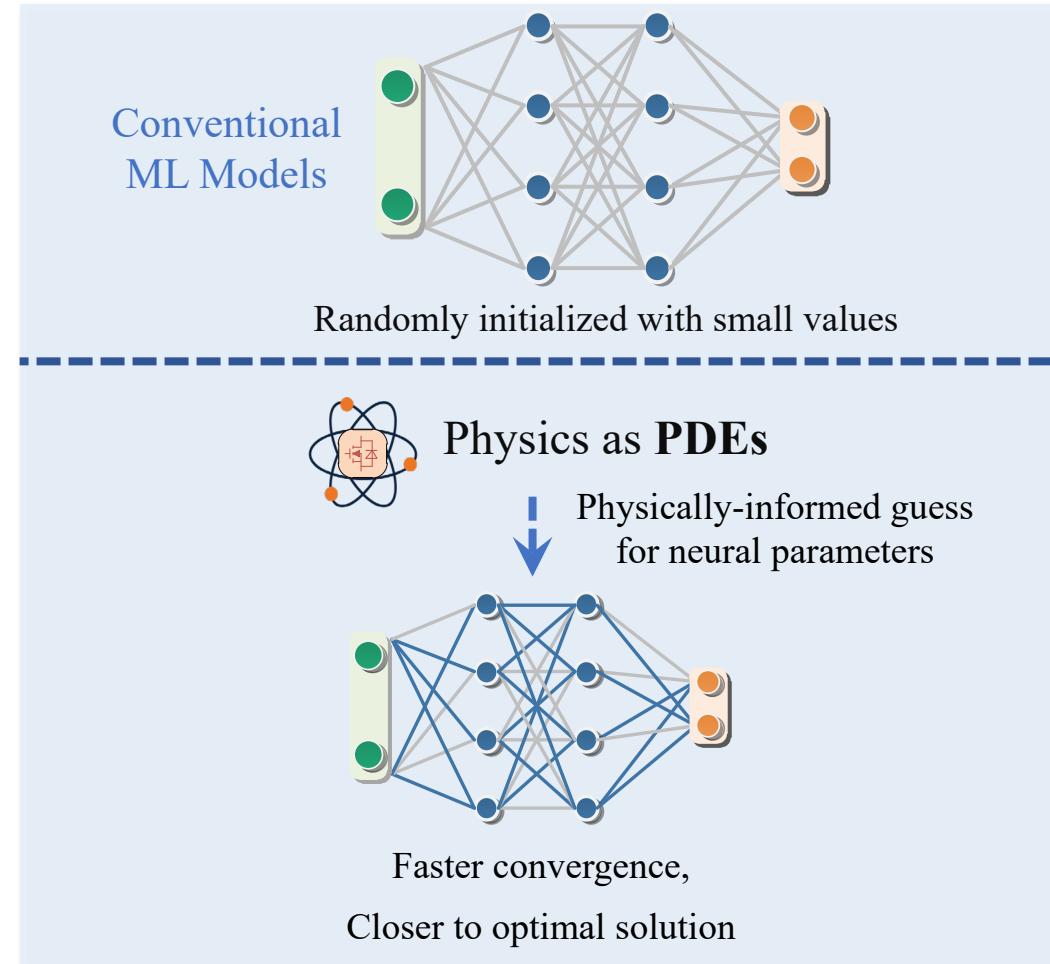
G. E. Karniadakis, I. G. Kevrekidis, L. Lu, P. Perdikaris, S. Wang, and L. Yang, "Physics-informed machine learning," *Nat Rev Phys*, vol. 3, no. 6, pp. 422–440, May 2021.S. Zhao, Y. Peng, Y. Zhang, and H. Wang, "Parameter Estimation of Power Electronic Converters With Physics-Informed Machine Learning," *IEEE Transactions on Power Electronics*, vol. 37, no. 10, pp. 11567–11578, Oct. 2022.



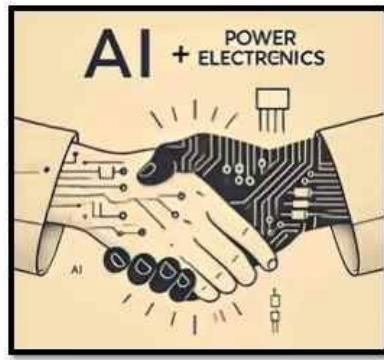
Physics-in-Architecture and Physics-in-Initialization PIMLs



Physics-in-architecture



Physics-in-initialization



Physics-in-Architecture



Physics-in-Loss

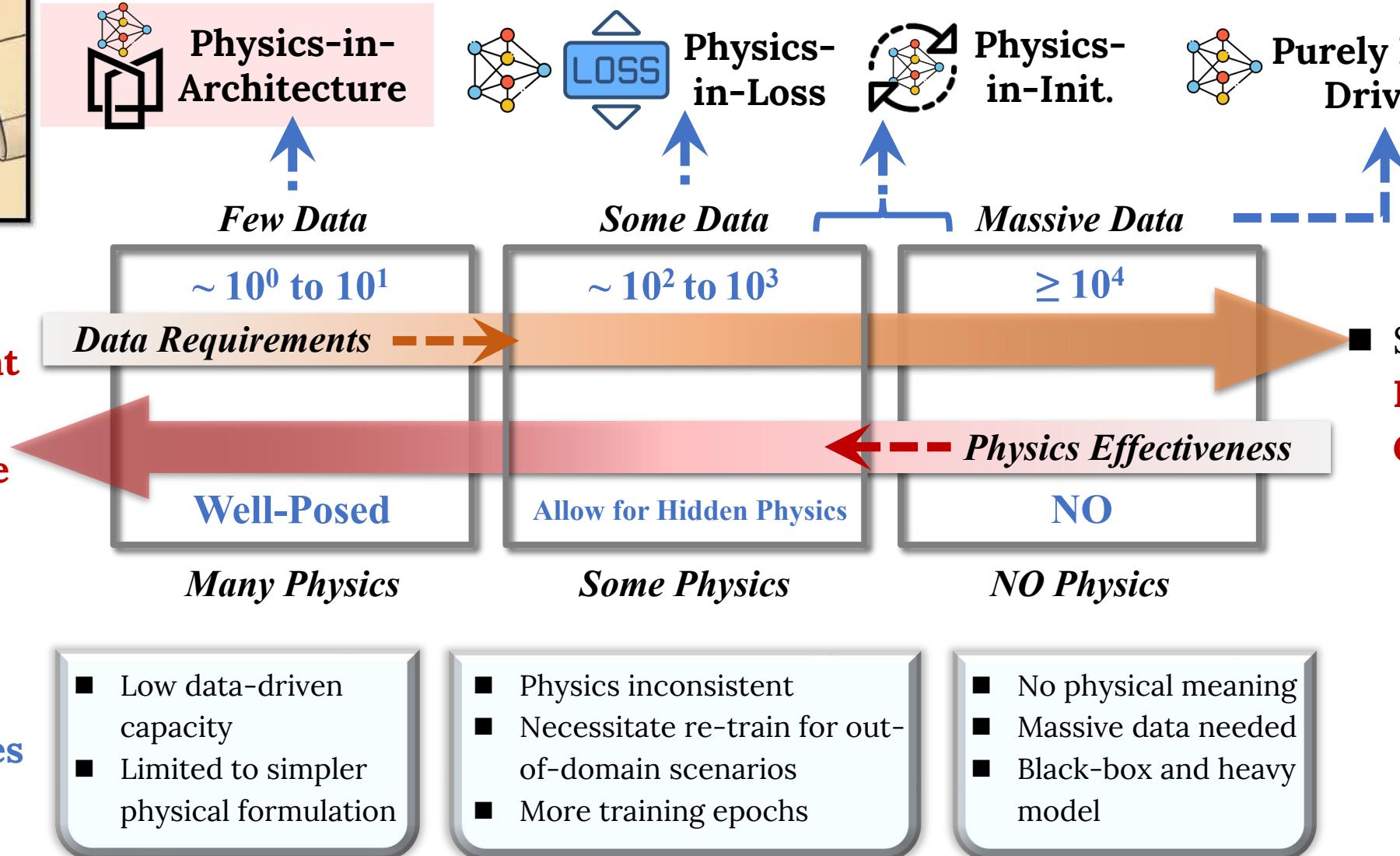


Physics-in-Init.



Purely Data-Driven

- Less **Data**
- More **Lightweight**
- More **Flexible**
- More **Explainable**



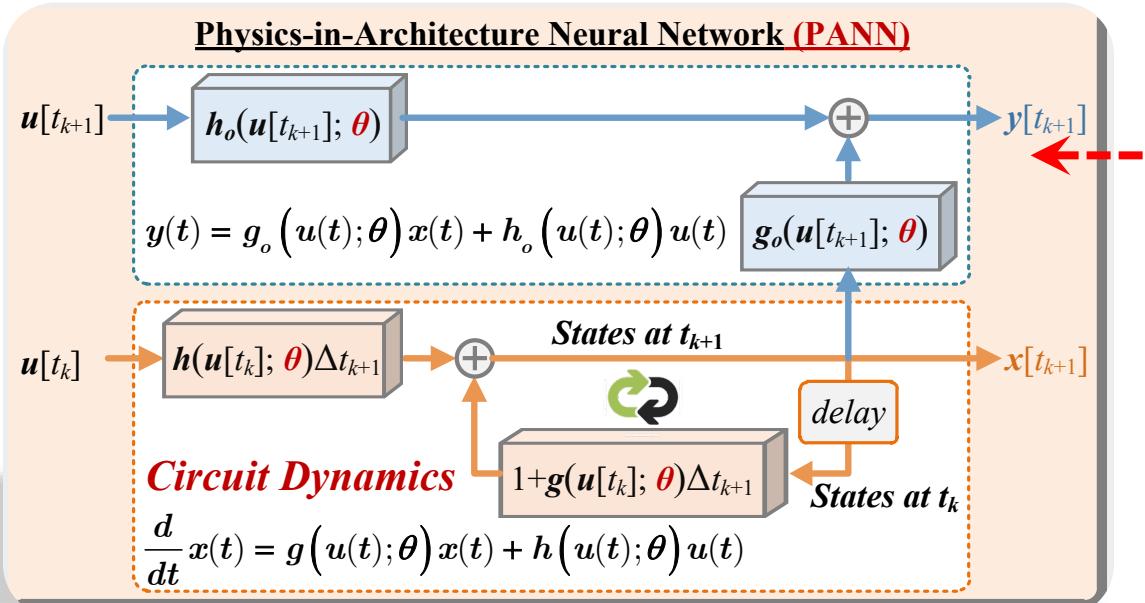
Disadvantages

NEXT GENERATION OF AI FOR POWER ELECTRONICS

- I. Applications of AI in Power Electronics and its Challenges
- II. Physics-Informed Machine Learning (PIML) for Power Electronics
- III. Fundamentals of Physics-in-Architecture Neural Network (PANN)
and its Explainability in Power Electronics**
- IV. PANN is Light in Two Aspects: Data-Light and Lightweight
- V. PANN is Flexible: “All You Need” is One PANN Model
- VI. Future Directions of PANN in Power Electronics

Concept of PANN: Physics-in-Architecture Neural Network (PANN)

<https://github.com/XinzeLee/PANN>

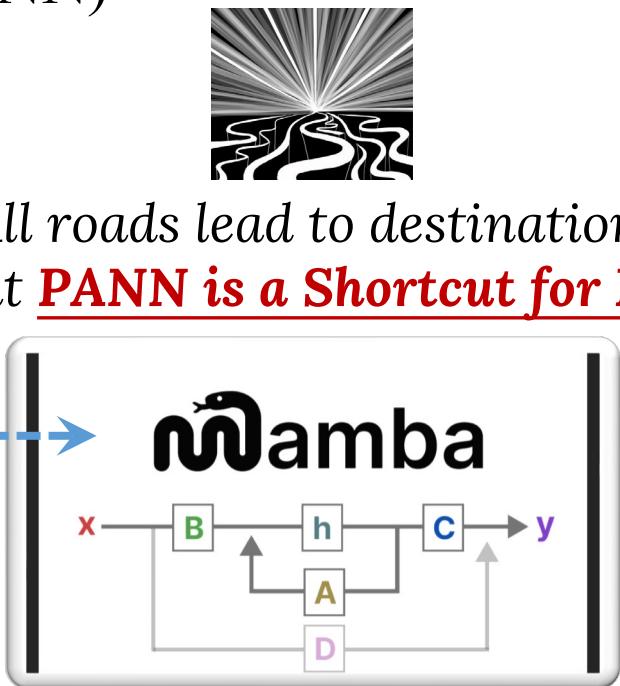


Physics-in-Architecture Neural Network (PANN)

(Submitted on 21 Jun 2023)

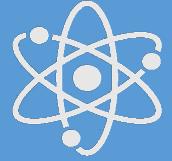
Source:

- [1] X. Li et al., "Temporal Modeling for Power Converters With Physics-in-Architecture Recurrent Neural Network," in *IEEE Transactions on Industrial Electronics*, vol. 71, no. 11, pp. 14111-14123, Nov. 2024.
- [2] X. Li, F. Lin, X. Zhang, H. Ma and F. Blaabjerg, "Data-Light Physics-Informed Modeling for the Modulation Optimization of a Dual-Active-Bridge Converter," in *IEEE Transactions on Power Electronics*, vol. 39, no. 7, pp. 8770-8785, July 2024.
- [3] F. Lin, X. Li, X. Zhang and H. Ma, "STAR: One-Stop Optimization for Dual-Active-Bridge Converter With Robustness to Operational Diversity," in *IEEE Journal of Emerging and Selected Topics in Power Electronics*, vol. 12, no. 3, pp. 2758-2773, June 2024.
- [4] X. Li et al., "A Generic Modeling Approach for Dual-Active-Bridge Converter Family via Topology Transferrable Networks," in *IEEE Transactions on Industrial Electronics*, doi: 10.1109/TIE.2024.3406858.
- [5] F. Lin et al., "PE-GPT: A New Paradigm for Power Electronics Design," in *IEEE Transactions on Industrial Electronics*, doi: 10.1109/TIE.2024.3454408.



Mamba State Space Models

(Submitted on 1 Dec 2023)

Advantages of PANN: **Explainable, Light, and Flexible****Explainable**

- Physics-crafted architecture
 - Intrinsic dynamics
- Power electronics explainable
 - Switching behaviors
 - Commutation loops

**Light**

- Data-light
 - Reduce data by 3 orders of magnitudes
 - Countable in one hand
- Lightweight
 - Deployable on edge
 - Down to kB level

**Flexible**

- Training-free to out-of-domain scenarios
 - Operating conditions
 - Modulation strategies
 - Performance metrics
 - Circuit parameters
 - Topological variants

PANN is Suitable for **Power Electronics Applications**

PANN is a form of **Neural** Partial Differential Equations (***NeuralPDE***)

Continuous

Generic state-space equations
of power converters

$$\begin{cases} \dot{x}(t) = g(u(t); \theta)x(t) + h(u(t); \theta)u(t) \\ y(t) = g_o(u(t); \theta)x(t) + h_o(u(t); \theta)u(t) \end{cases}$$

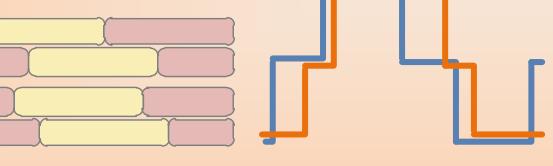
**Numerical
methods**

**Discrete,
recurrent**

$$x[t_{k+1}] = x[t_k] + \phi(x[t_k]; u[t_k], \dots; \Delta t_{k+1}; \theta) \Delta t_{k+1}$$

$$\begin{cases} x[t_{k+1}] = (1 + g(u[t_k]; \theta)\Delta t_{k+1})x[t_k] + h(u[t_k]; \theta)u[t_k]\Delta t_{k+1} \\ y[t_{k+1}] = g_o(u[t_{k+1}]; \theta)x[t_{k+1}] + h_o(u[t_{k+1}]; \theta)u[t_{k+1}] \end{cases}$$

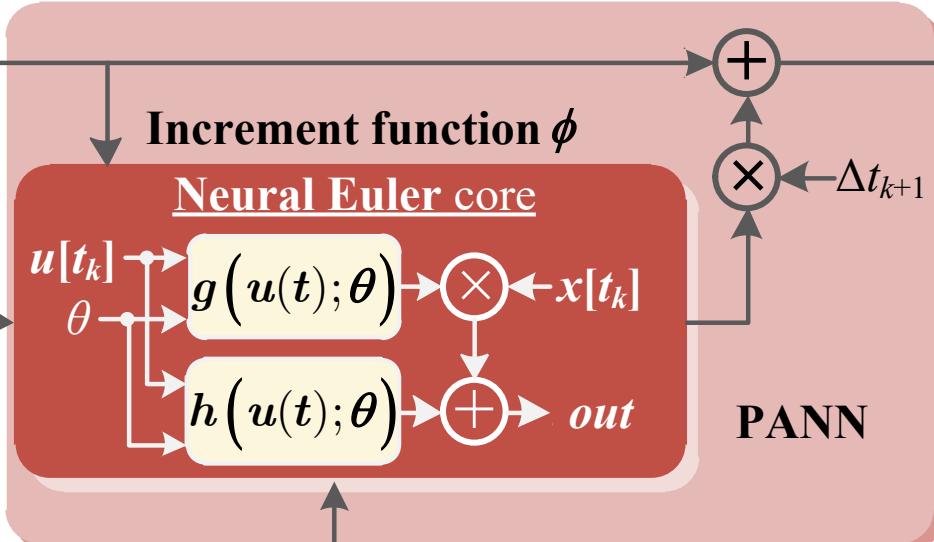
Expert Systems



Switching behaviors

Current
states $x[t_k]$

Inputs
 $u[t_k], \dots$



Next states
 $x[t_{k+1}]$

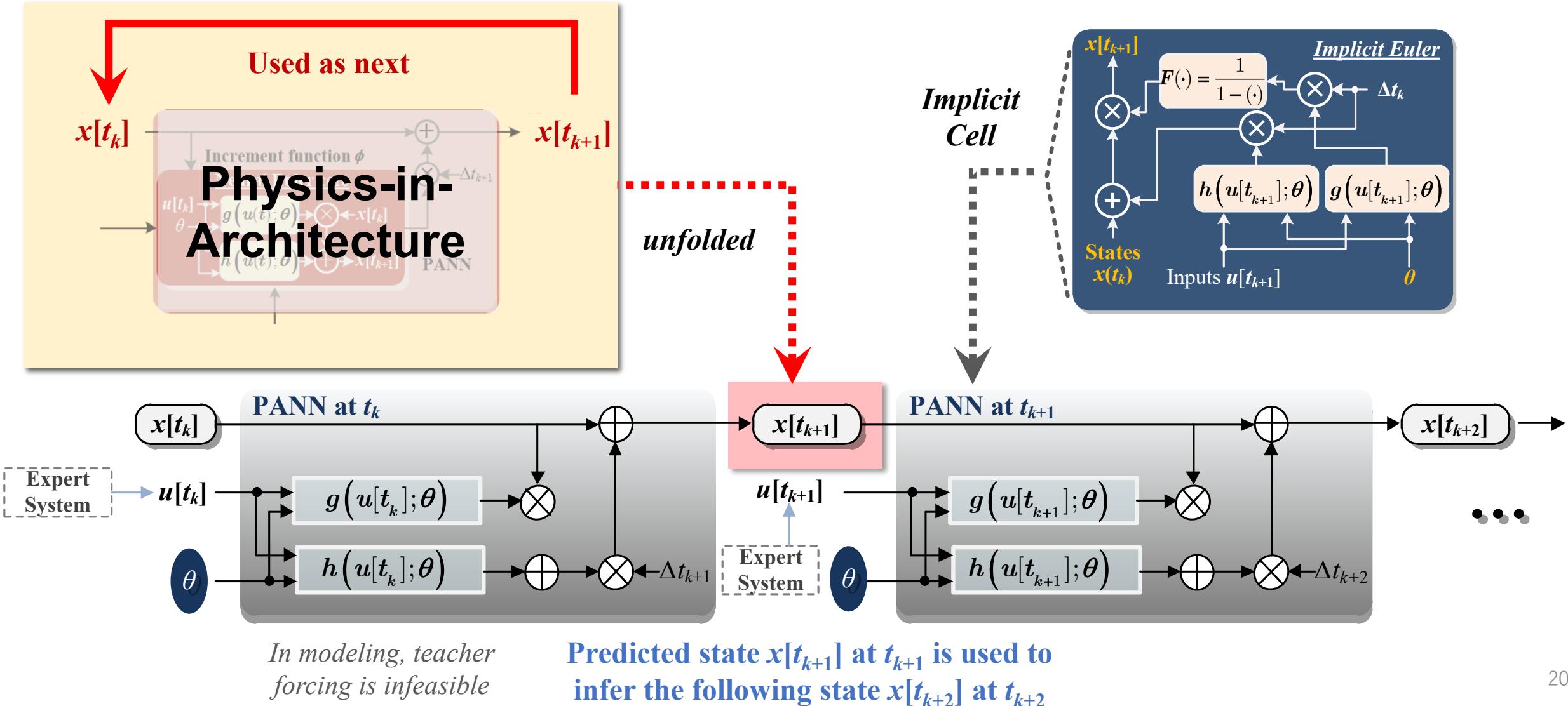
**Custom recurrent
neural architecture
of PANN**

Trainable converter
parameters, data-driven

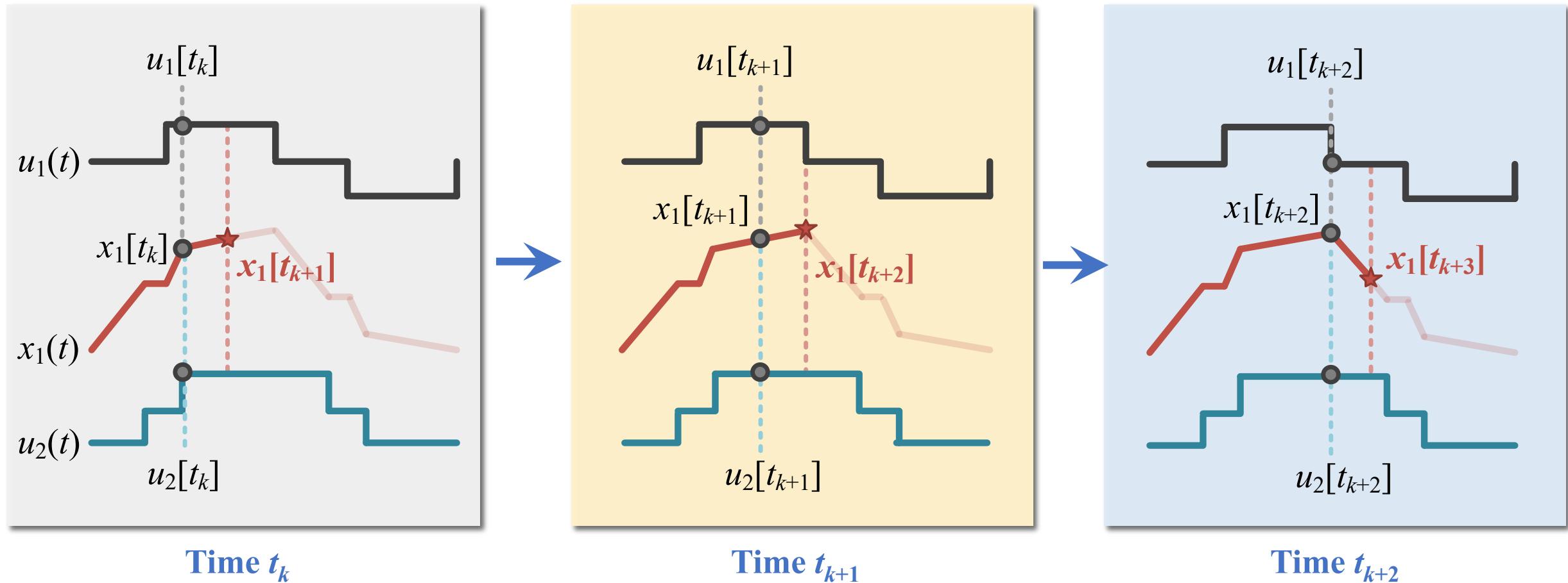
Parameters θ

Physically explainable,
direct assignment

PANN inference: *time-domain modeling of power converters*



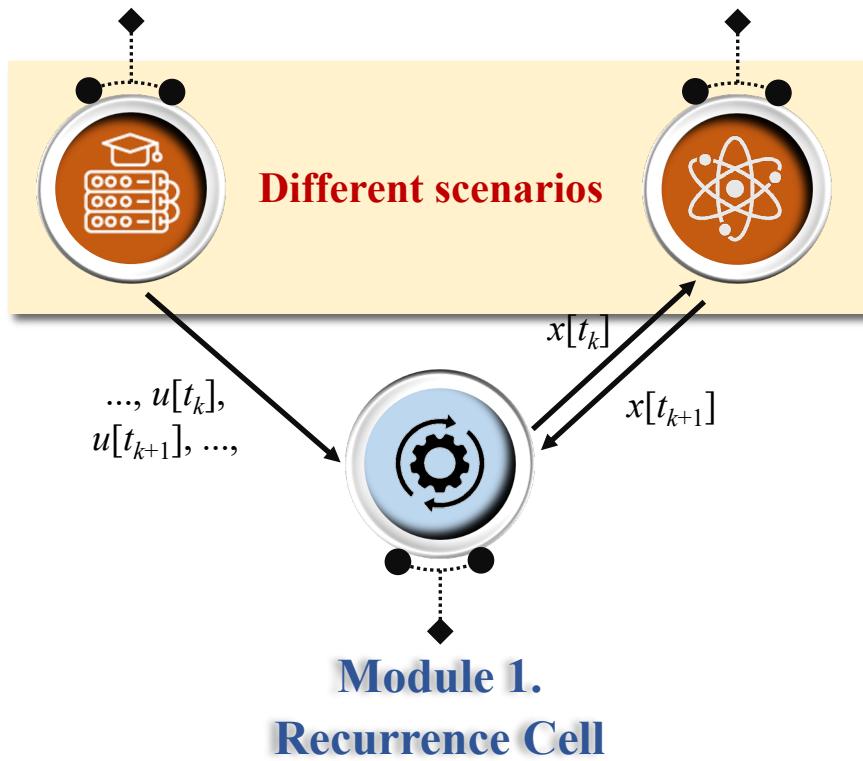
PANN inference **unfolded** over time: *predict the next state*



Code Structure for PANN Inference

Module 2. Input Blocks

Generate switching or input variables based on modulation knowledge



Module 3. Physics Cell

Infer state variables for one timestamp based on physics

Module 1. Recurrence Cell

[pann_net.py :: class PANN](#)

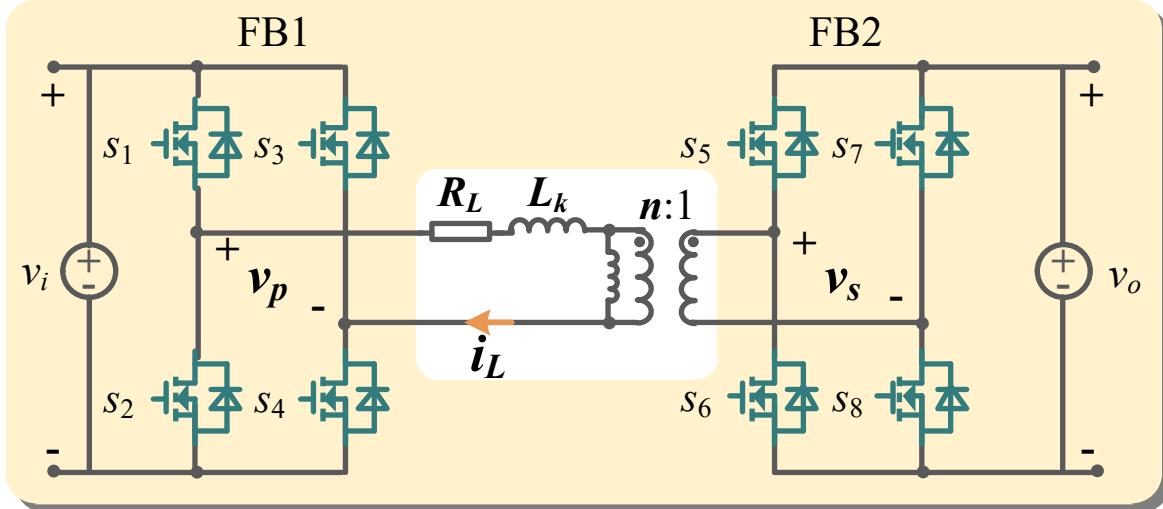
Recurrent infer

```
class PANN(nn.Module):
    """
    Define the generic physics-in-architecture neural network (PANN)
    """

    def __init__(self, cell, **kwargs):
        super(PANN, self).__init__(**kwargs)
        self.cell = cell  ■ Physics Cell

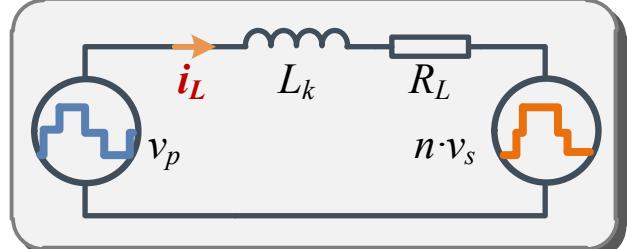
    def forward(self, inputs, x):
        outputs = []
        x = x[:, 0] # initialize the state
        for t in range(inputs.shape[1]):
            state_next = self.cell.forward(inputs[:, t, :], _x) # infer one step
            _x = state_next # the predicted state (state_next) as the current state
            outputs.append(_x) # append the predicted states to outputs
        return torch.stack(outputs, dim=1)
```

■ Recurrent Inference

Case Study 1: PANN for *DAB Converters*

Equivalent Circuit
of Inductor

	Specs
States $x(t)$	i_L
Inputs $u(t)$	v_p, v_s
Circuit θ	R_L, L_k, n



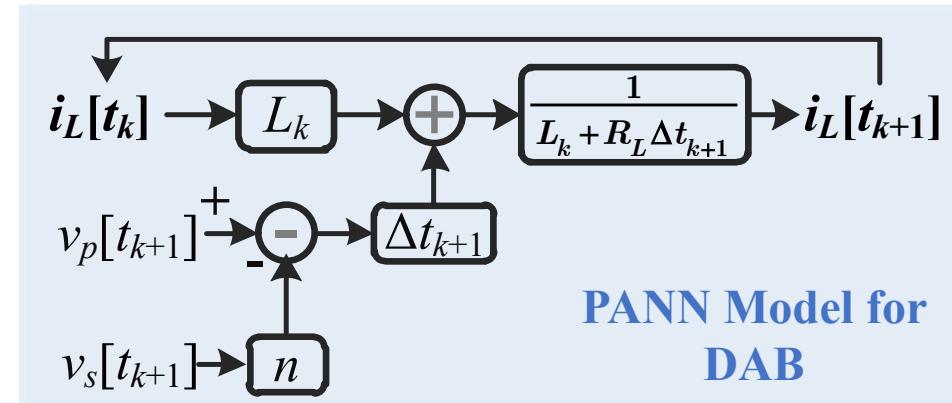
Continuous $L_k \frac{di_L(t)}{dt} + R_L i_L(t) = v_p(t) - n v_s(t)$

↓ Discretize via Implicit Euler Algorithm

$$i_L[t_{k+1}] - i_L[t_k] = (v_p[t_{k+1}] - n v_s[t_{k+1}] - R_L i_L[t_{k+1}]) \frac{\Delta t_{k+1}}{L_k}$$

↓ Simplify

$$i_L[t_{k+1}] = \frac{(\Delta t_{k+1} (v_p[t_{k+1}] - n v_s[t_{k+1}]) + L_k i_L[t_k])}{L_k + R_L \Delta t_{k+1}}$$



Case Study 1: PANN for *DAB Converters*

```

D0 = D0+D1-D2
# create a time array with the sampling period dt
t = np.linspace(0, Ts, round(Ts/dt), endpoint=False)

# create switching functions
s_pri = signal.square(2*np.pi/Ts*t, D1_cycle)
s_pri2 = deque([-s_pri])  ■ Gate drive signal for s4
s_pri2.rotate(int(np.ceil(np.round(D1*Ts/2/dt, 5))))
■ Gate drive signal for s5

s_sec = deque(signal.square(2*np.pi/Ts*t, D2_cycle))
s_sec.rotate(int(np.ceil(np.round(D0*Ts/2/dt, 5))))
s_sec2 = deque([-np.array(s_sec)]) ■ Gate drive signal for s8
s_sec2.rotate(int(np.ceil(np.round(D2*Ts/2/dt, 5)))) 

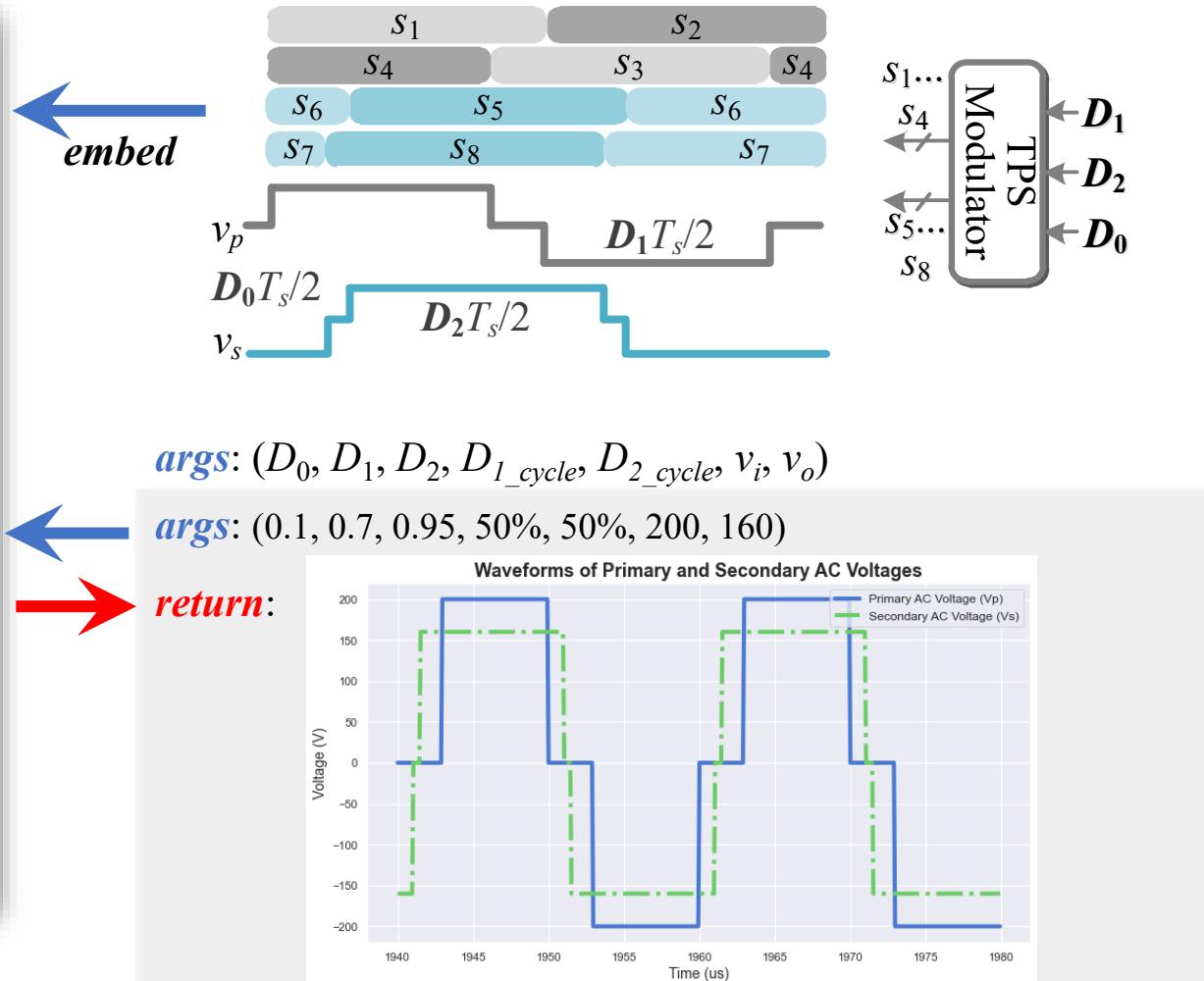
# create high-frequency ac waveforms ■ Generate vp and vs
vp = (np.array(s_pri)+np.array(s_pri2)).clip(-1, 1)*Vin
vs = (np.array(s_sec)+np.array(s_sec2)).clip(-1, 1)*Vref

```

Module 2. Input Blocks

pann_utils.py :: def create_vpv

To generate

 v_p and v_s Exemplary v_p and v_s waveforms

Case Study 1: PANN for *DAB Converters*

```

class EulerCell_DAB(nn.Module):

    def __init__(self, dt, Lr, RL, n, **kwargs):
        super(EulerCell_DAB, self).__init__(**kwargs)
        self.dt = dt # define a constant parameter time step dt
        self.Lr = nn.Parameter(torch.Tensor([Lr])) # define trainable
        self.RL = nn.Parameter(torch.Tensor([RL])) # define trainable
        self.n = nn.Parameter(torch.Tensor([n])) # define trainable ne

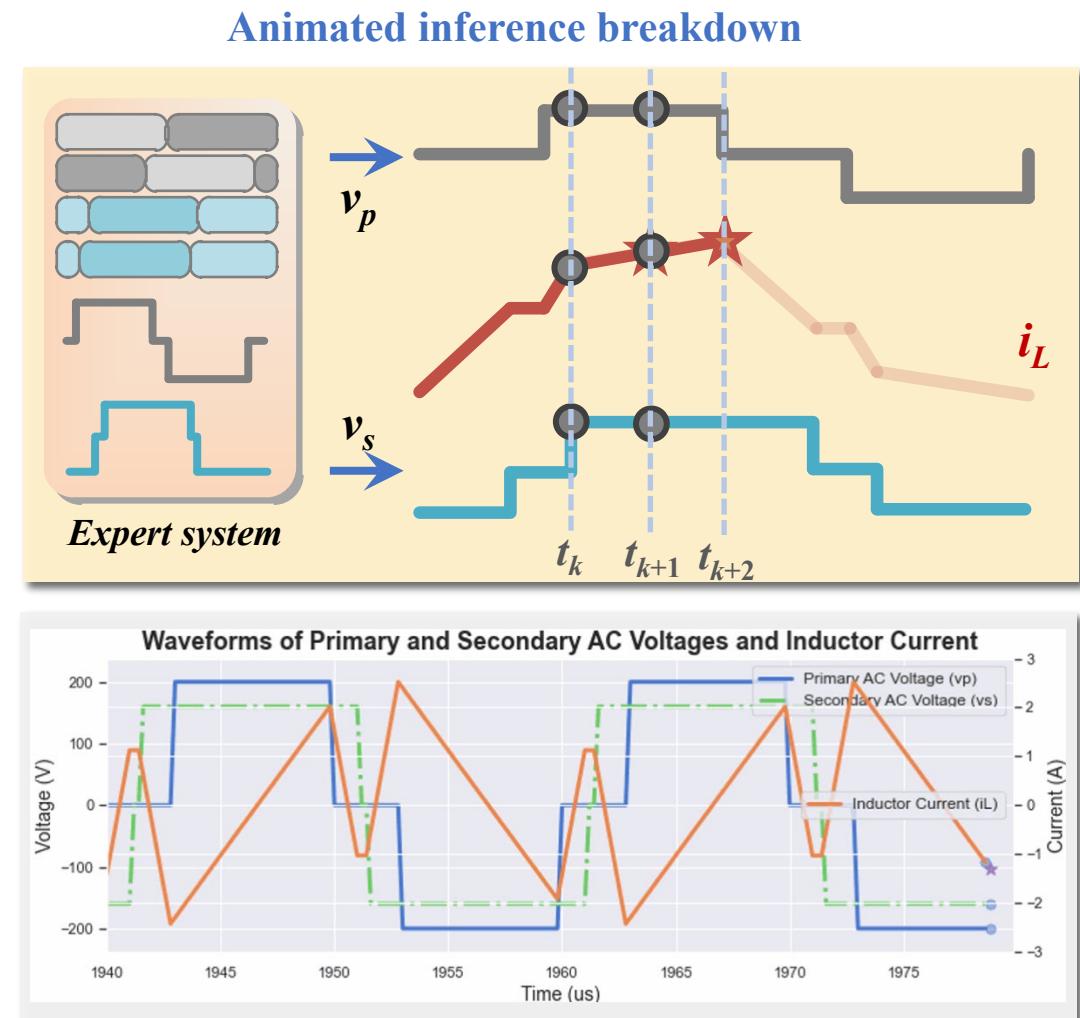
    ■ Define neural circuit parameters  $\theta$ 

    def forward(self, inputs, states):
        # inputs : represent  $v_p$ ,  $v_s$  respectively
        # states : represent  $i_L$ , respectively ■ Physics of inductor
        iL_next = (self.Lr/(self.Lr+self.RL*self.dt))*states[:, 0] + \
                  (self.dt/(self.Lr+self.RL*self.dt))*(inputs[:, 0] -
                  self.n*inputs[:, 1])

        return iL_next[:, None]
  
```

Embed
physics

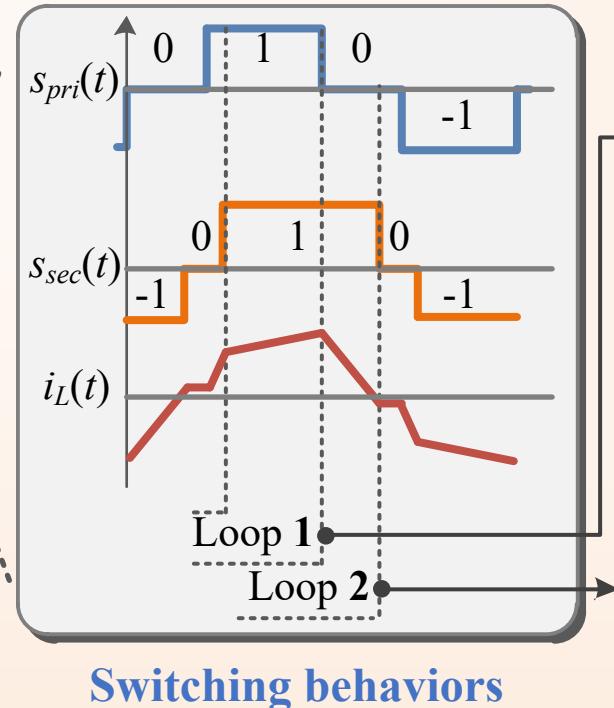
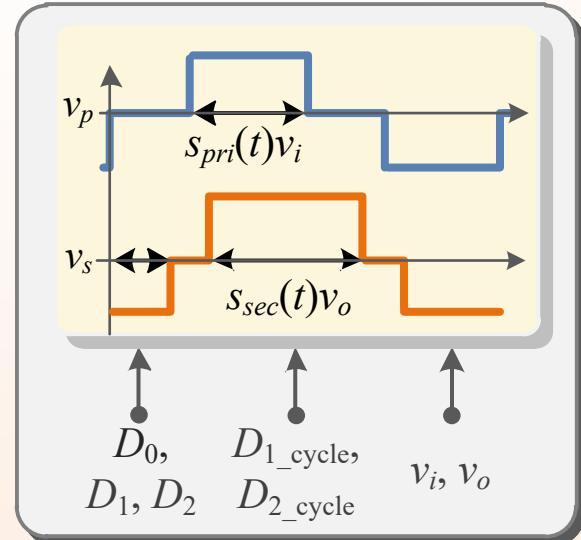
Module 3. Physics Cell
pann_net.py :: class EulerCell_DAB



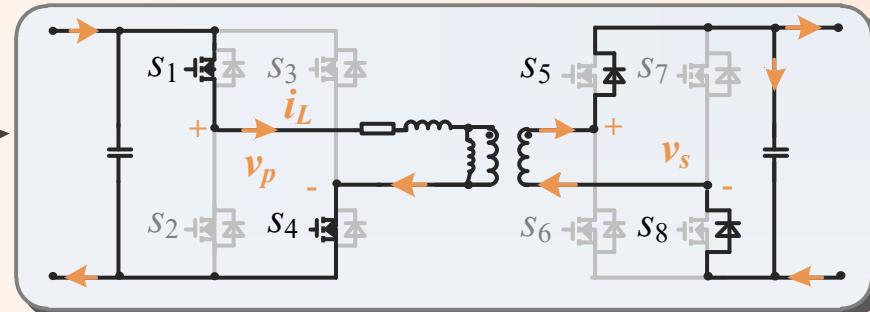
Inference Breakdown (Code Demo*)
"DAB-inference and training.ipynb"
<https://github.com/XinzeLee/PANN>



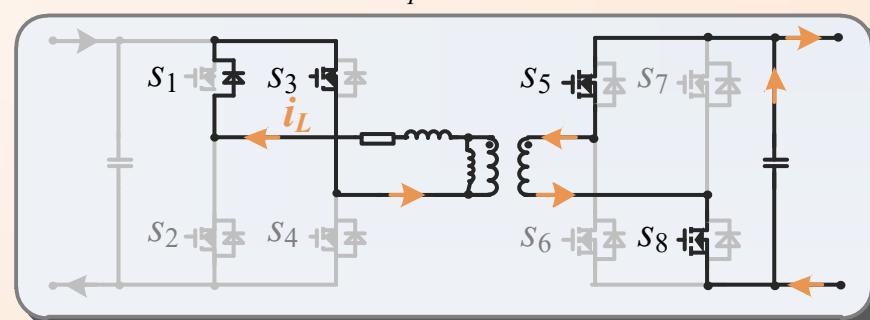
Circuit Insights Revealed in PANN



Commutation Mode: $v_p=v_i, v_s=v_o, i_L>0$



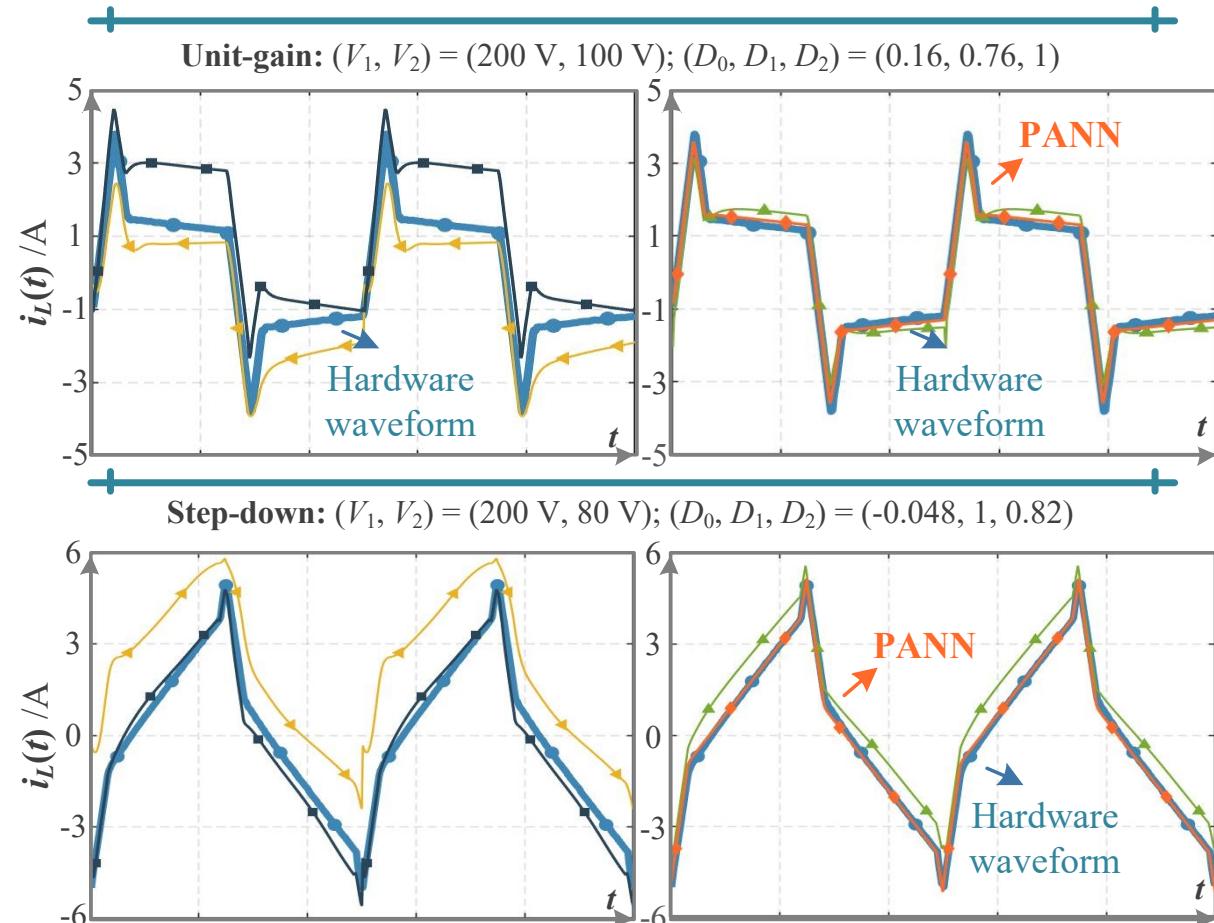
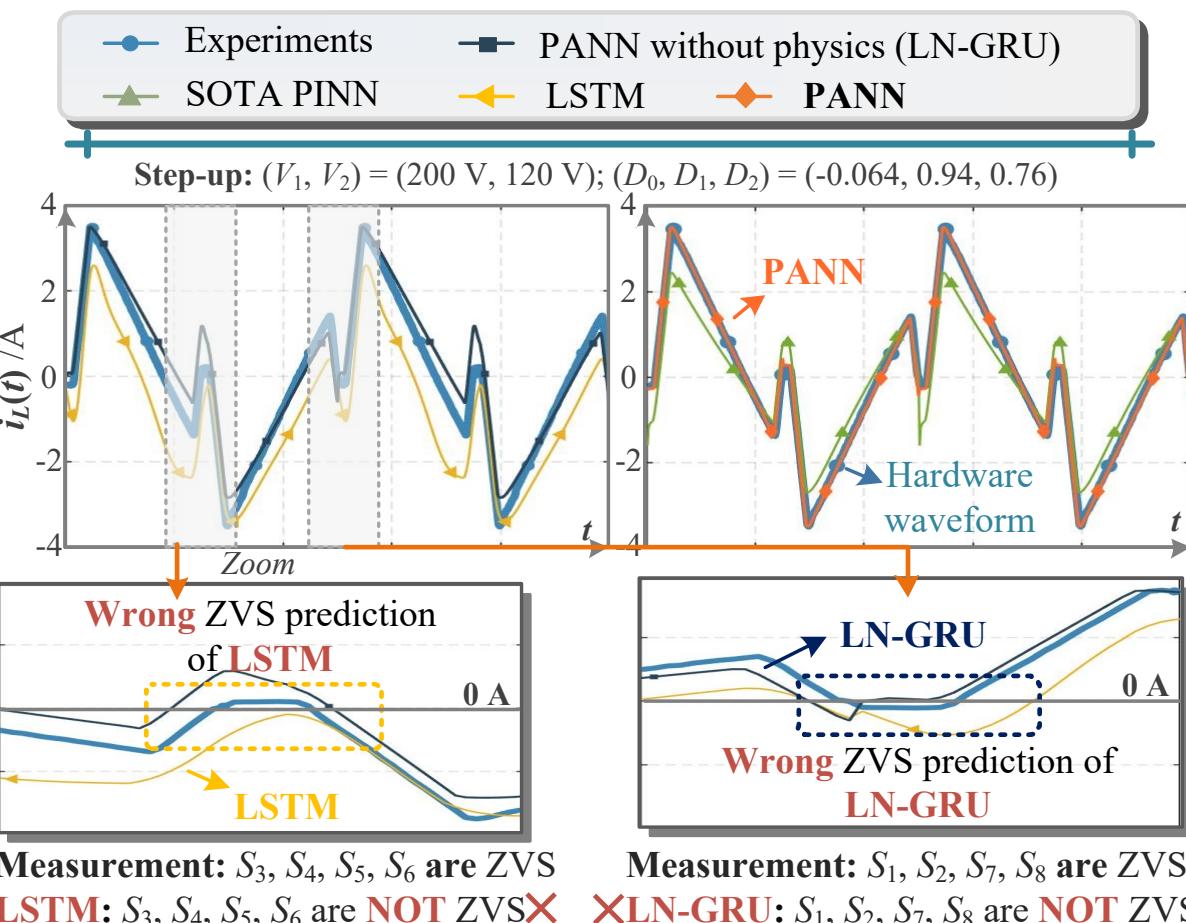
Commutation Mode: $v_p=0, v_s=v_o, i_L<0$



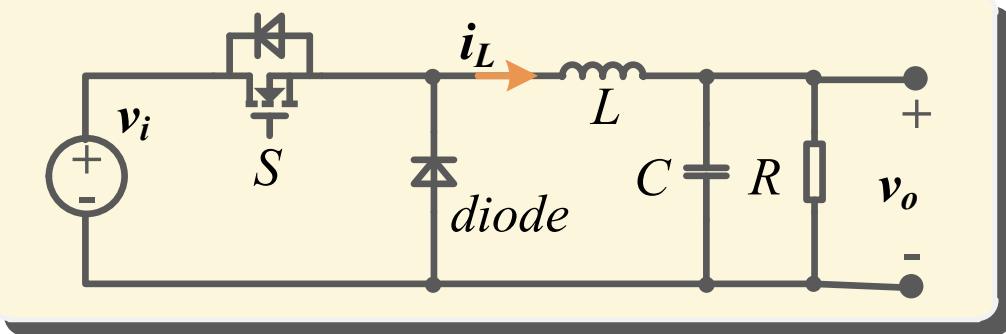
Commutation loop analysis

PANN pushes the definition of **AI explainability** to a **NEW Height:**
Power Electronics Explainable

Exemplary waveforms of PANN for DAB

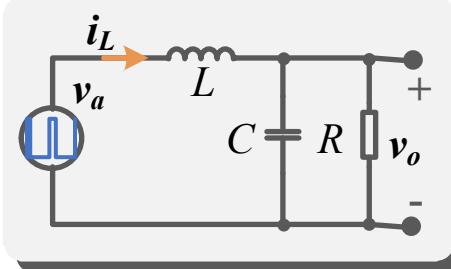
*Models for converter time-domain modeling*

A DAB Variant (NPC-DAB) under TPS Modulation

Case Study 2: PANN for *Buck Converters*

	Specs
States $x(t)$	i_L, v_o
Inputs $u(t)$	v_a
Circuit θ	R, L, C

Equivalent Circuit



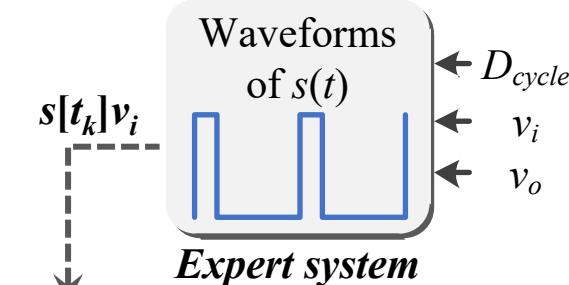
Continuous

$$\begin{cases} L \frac{d}{dt} i_L(t) = v_a(t) - v_o(t) \\ C \frac{d}{dt} v_o(t) = i_L(t) - \frac{v_o(t)}{R} \end{cases}$$

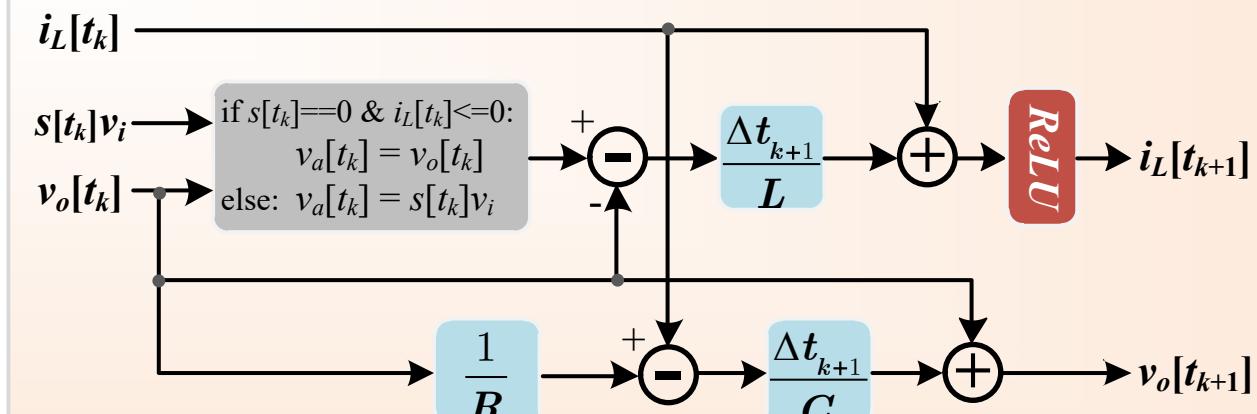
Discretize via Explicit Euler Algorithm

$$L(i_L[t_{k+1}] - i_L[t_k]) = (v_a[t_k] - v_o[t_k])\Delta t_{k+1}$$

$$C(v_o[t_{k+1}] - v_o[t_k]) = \left(i_L[t_k] - \frac{v_o[t_k]}{R} \right) \Delta t_{k+1}$$



Simplify



PANN Model for Buck

Case Study 2: PANN for *Buck Converters*

```

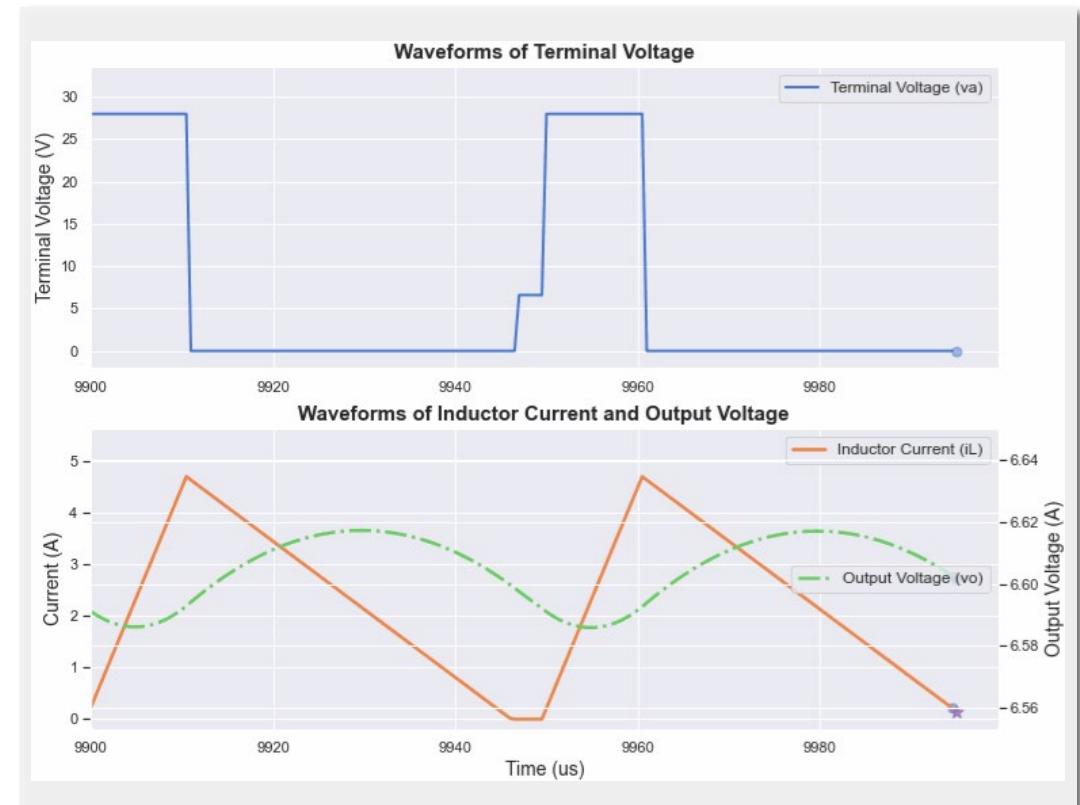
def forward(self, inputs, states):
    # explicit Euler method
    # inputs : represent s_pri*Vin, respectively
    # states : represent iL, vo, respectively
    vo = states[:, 1]
    va = inputs[:, 0] # terminal voltage

    ■ Determine DCM
    idx = (inputs[:, 0] == 0) & (states[:, 0] <= 0) # evaluate dis
    va[idx] = vo[idx]                                ■ Physics of inductor

    iL_next = states[:, 0] + self.dt / self.L * (va - vo) # physi
    iL_next = torch.relu(iL_next) # torch.relu to consider DCM
    vC_next = states[:, 1] + self.dt / self.Co * (states[:, 0]
                                                - vo / self.Ro) # Physics of capacitor

    return torch.stack((iL_next, vC_next), dim=1)

```



Embed
physics

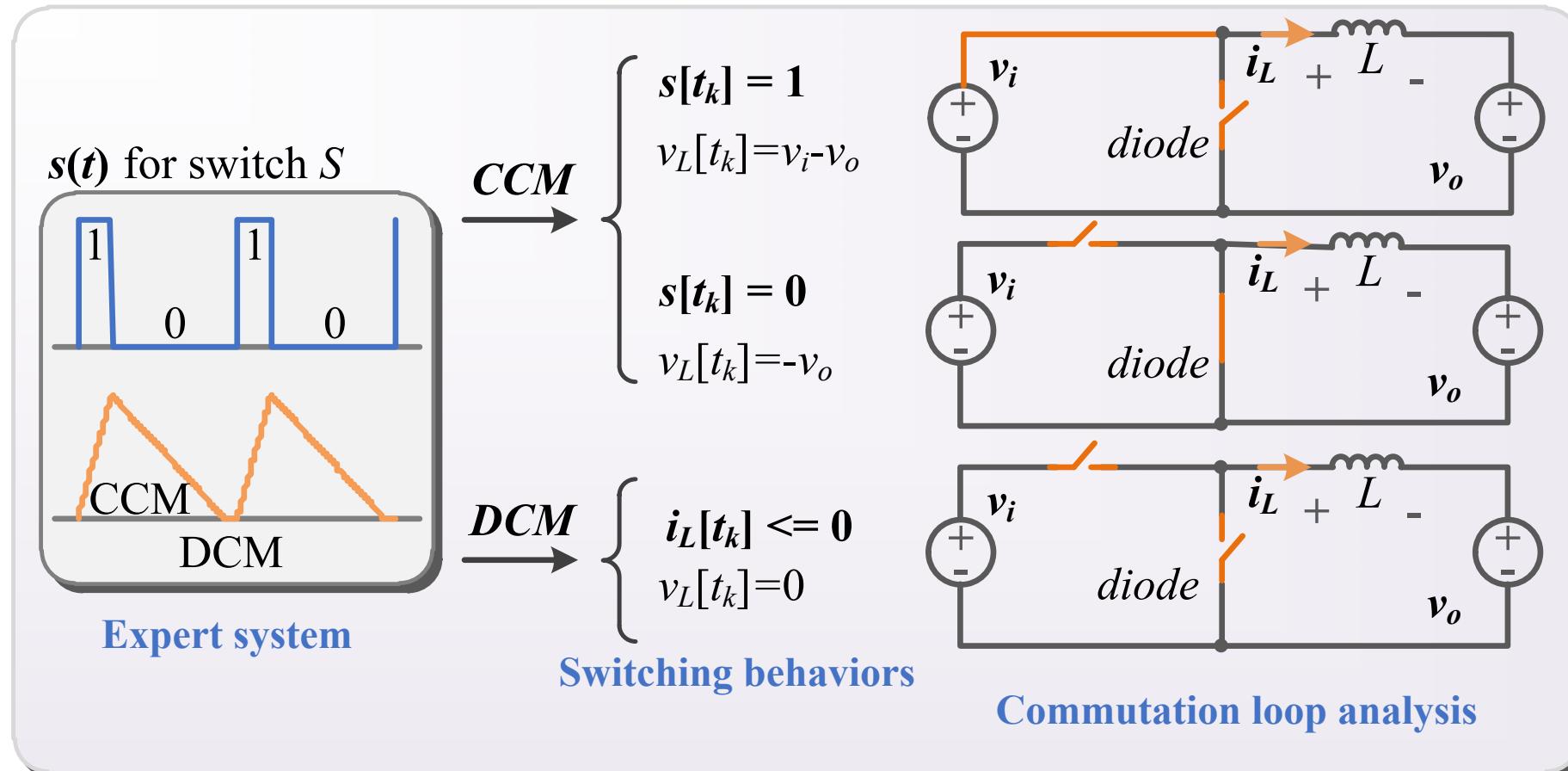
Module 3. Physics Cell

[pann_net.py :: class EulerCell_Buck](#)

Inference Breakdown (Code Demo*)

[“Buck-inference.ipynb”](#)

Circuit Insights Revealed in PANN



PANN pushes the definition of **AI explainability** to a **NEW Height**:
Power Electronics Explainable

Define states, inputs, outputs variables
 ↓
 Equivalent circuit
 ↓
Continuous state-space circuit equations
 ↓
Numerical
Discretized state-space
 ↓
 Construct PANN

1. To Customize PANN for ANY Power Converter

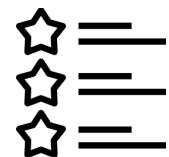


Takeaways



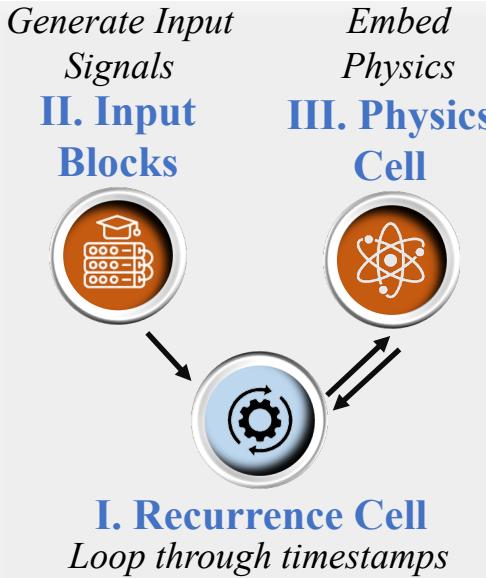
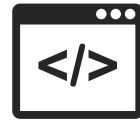
PANN
Inference

- Neural ODE/PDE
- Time-domain modeling
- Present predictions are used to **predict next states**
- **Infer state variables** $x(t)$ based on input variables $u(t)$ from expert systems



3. Main Features of PANN Inference

2. Code Structure of PANN



4. Explainability of PANN

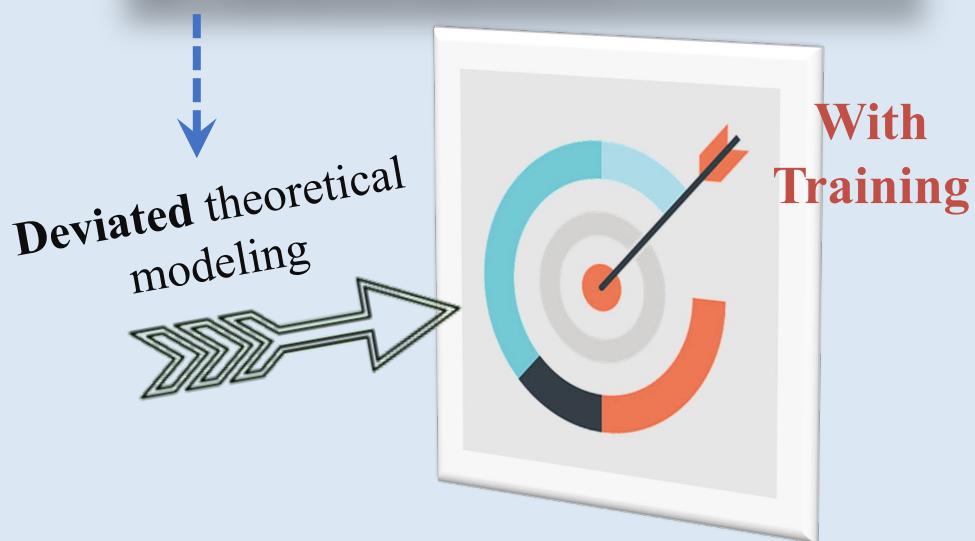
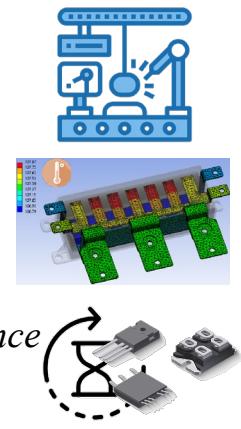
- **Physics-crafted** structure: state-space circuit physics
- **Physical compliance**
- Explainability in power electronics: switching behaviors, commutation loops, operating modes

NEXT GENERATION OF AI FOR POWER ELECTRONICS

- I. Applications of AI in Power Electronics and its Challenges
- II. Physics-Informed Machine Learning (PIML) for Power Electronics
- III. Fundamentals of Physics-in-Architecture Neural Network (PANN)
and its Explainability in Power Electronics
- IV. PANN is Light in Two Aspects: Data-Light and Lightweight**
- V. PANN is Flexible: “All You Need” is One PANN Model
- VI. Future Directions of PANN in Power Electronics

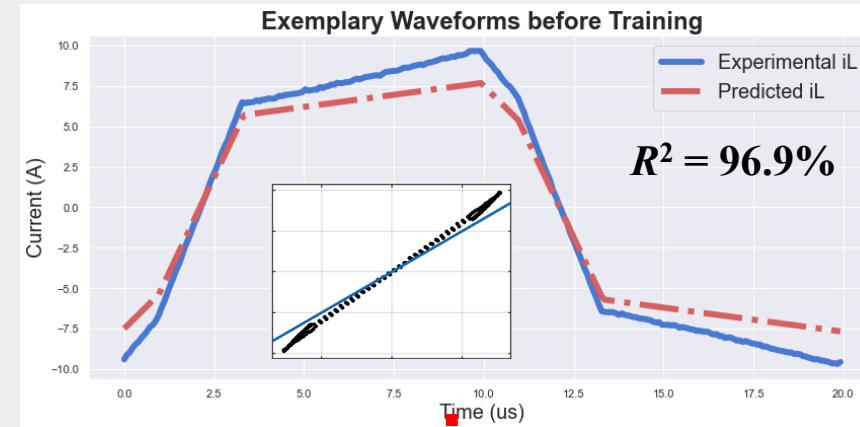
Training is *Imperative* to minimize losses

- Manufacturing tolerance
- Ambient variations
 - *Thermal drift*
 - *Operation conditions*
 - *Electromagnetic interference*
- Aging and degradation

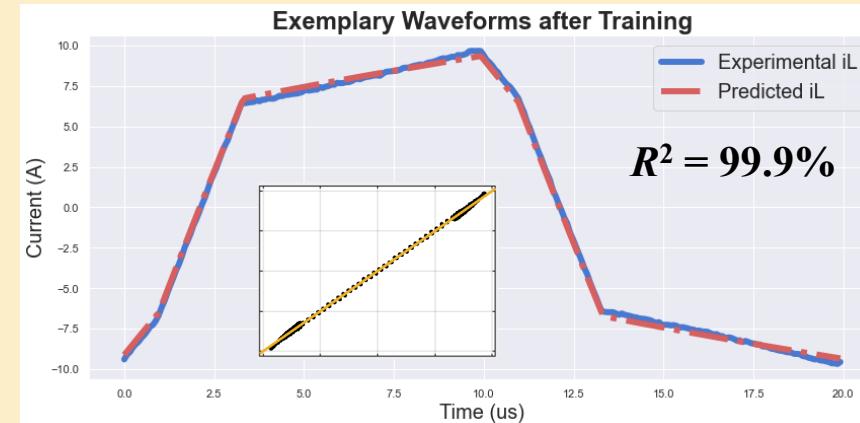


Before Training

Neural θ before Training: $R_L = 10 \text{ m}\Omega$, $L_k = 80 \mu\text{H}$, $n = 1.1$

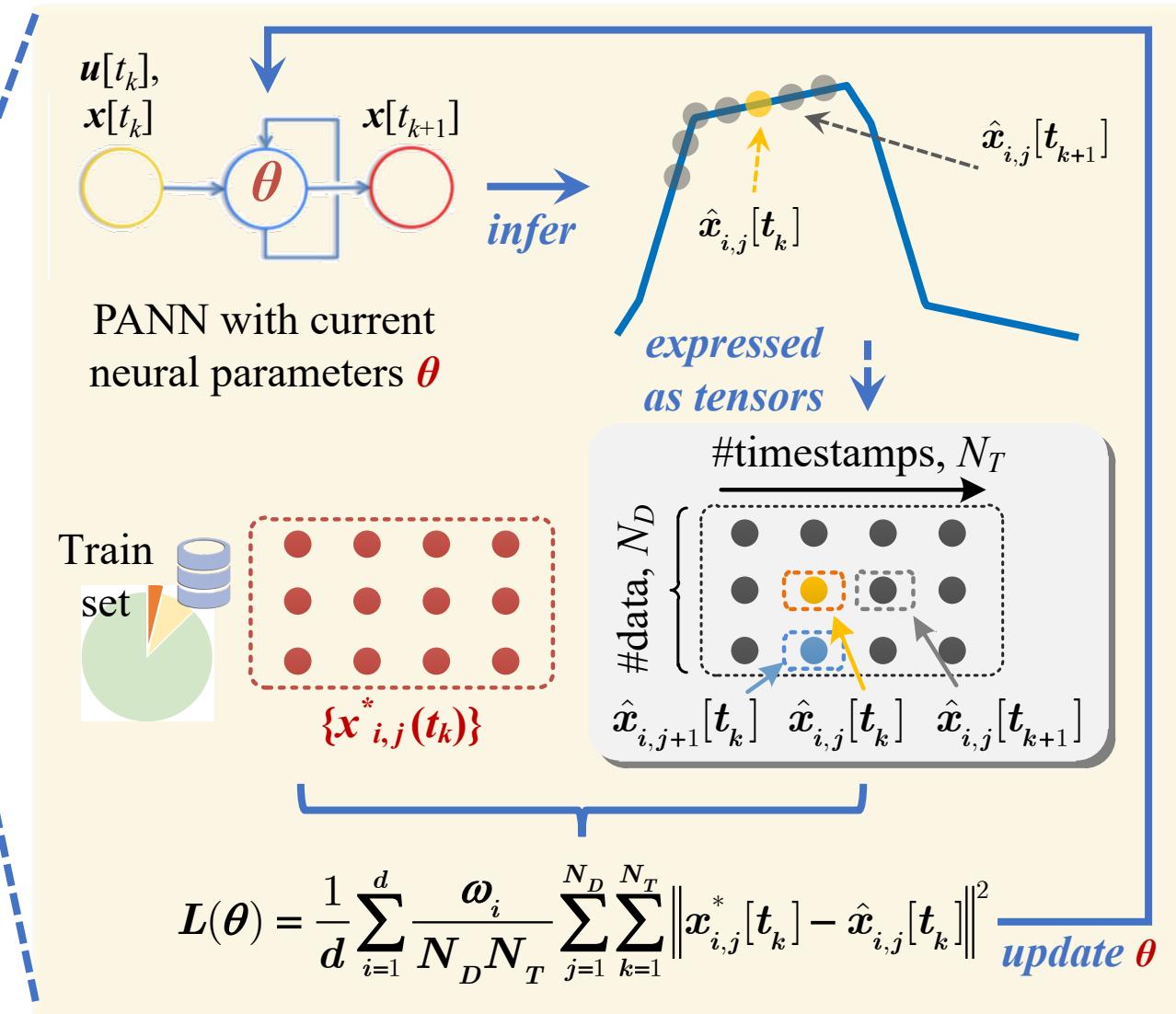
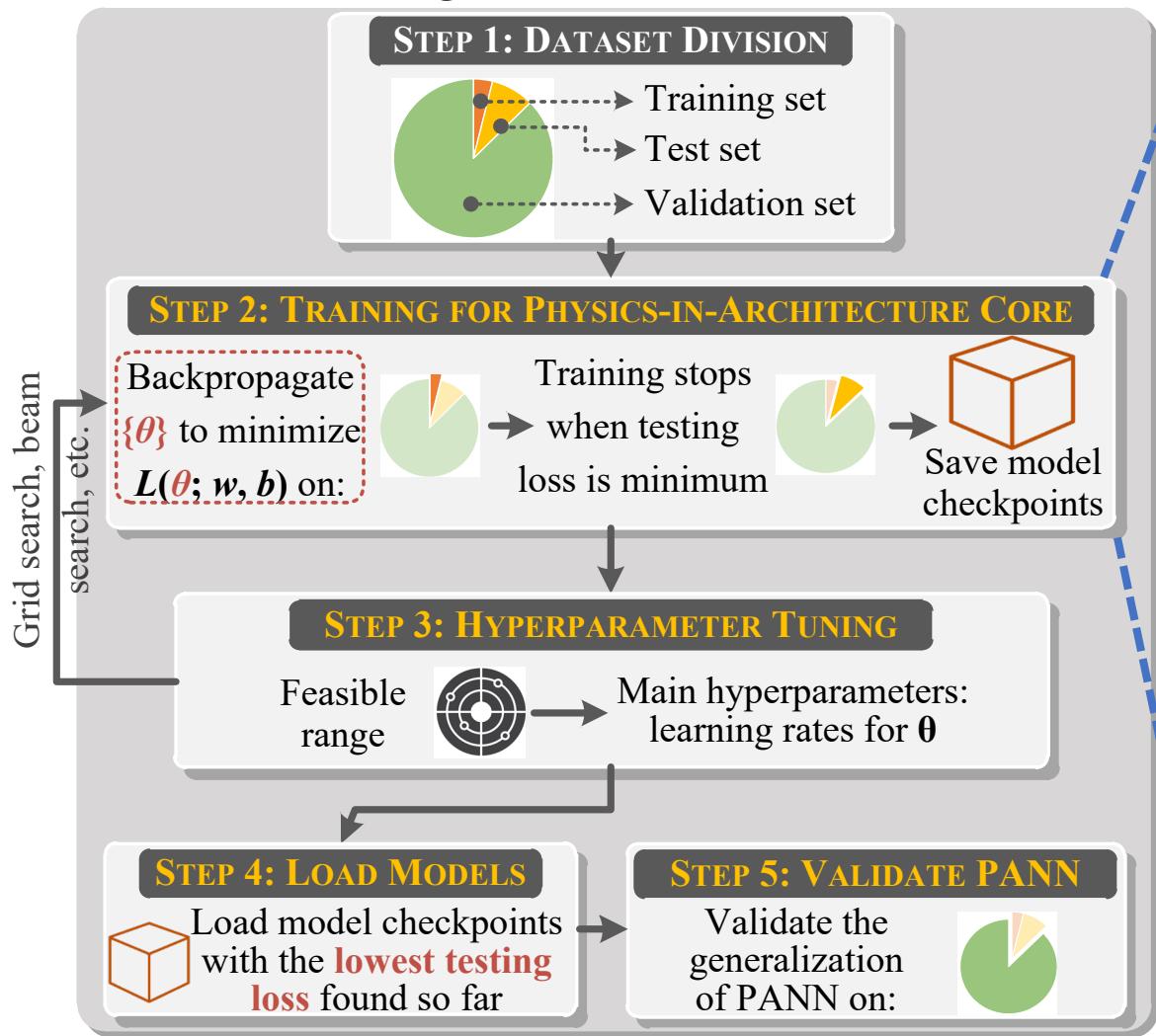


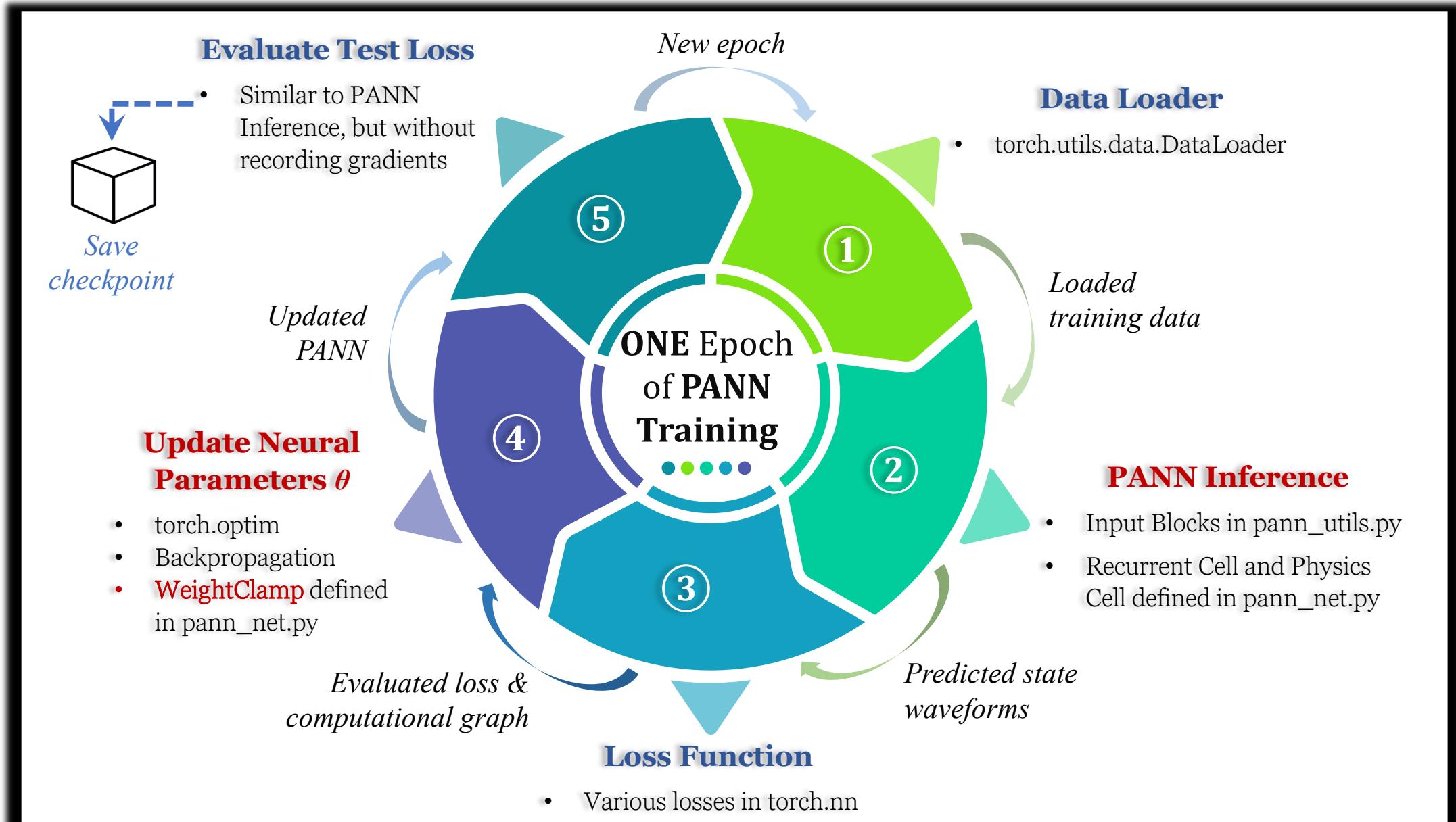
Neural θ after Training: $R_L = 1.8 \Omega$, $L_k = 64 \mu\text{H}$, $n = 1.0$



After Proper Training

PANN training flowchart





```
optimizer_pann = torch.optim.Adam([{"params": [param[1] for param in params], "lr":2e-6},
                                  {"params": [param[1] for param in params2], "lr":1e-1},
                                  {"params": [param[1] for param in params3], "lr":5e-1},])
```

Define an optimizer with different learning rates for neural θ

```
state0 = torch.zeros(state.shape) # should be zero to avoid learning
pred = model_pann.forward(input_, state0) ■ PANN Inference
pred, _ = transform(input_[:, -2*Tslen:], pred[:, -2*Tslen:], Vin,
                    Tslen, convert_to_mean=convert_to_mean)
```

```
loss_train = loss_pann(pred, target) ■ Evaluate Loss
optimizer_pann.zero_grad()
loss_train.backward()
optimizer_pann.step() ■ Backpropagate Loss
clamper(model_pann) ■ Clamp Neural Parameters  $\theta$ 
```

```
clamper = WeightClamp(['cell.Lr', 'cell.RL', 'cell.n'],
                      [(10e-6, 200e-6), (1e-3, 5e0), (0.8, 1.2)])
```

Define a clamper of class WeightClamp

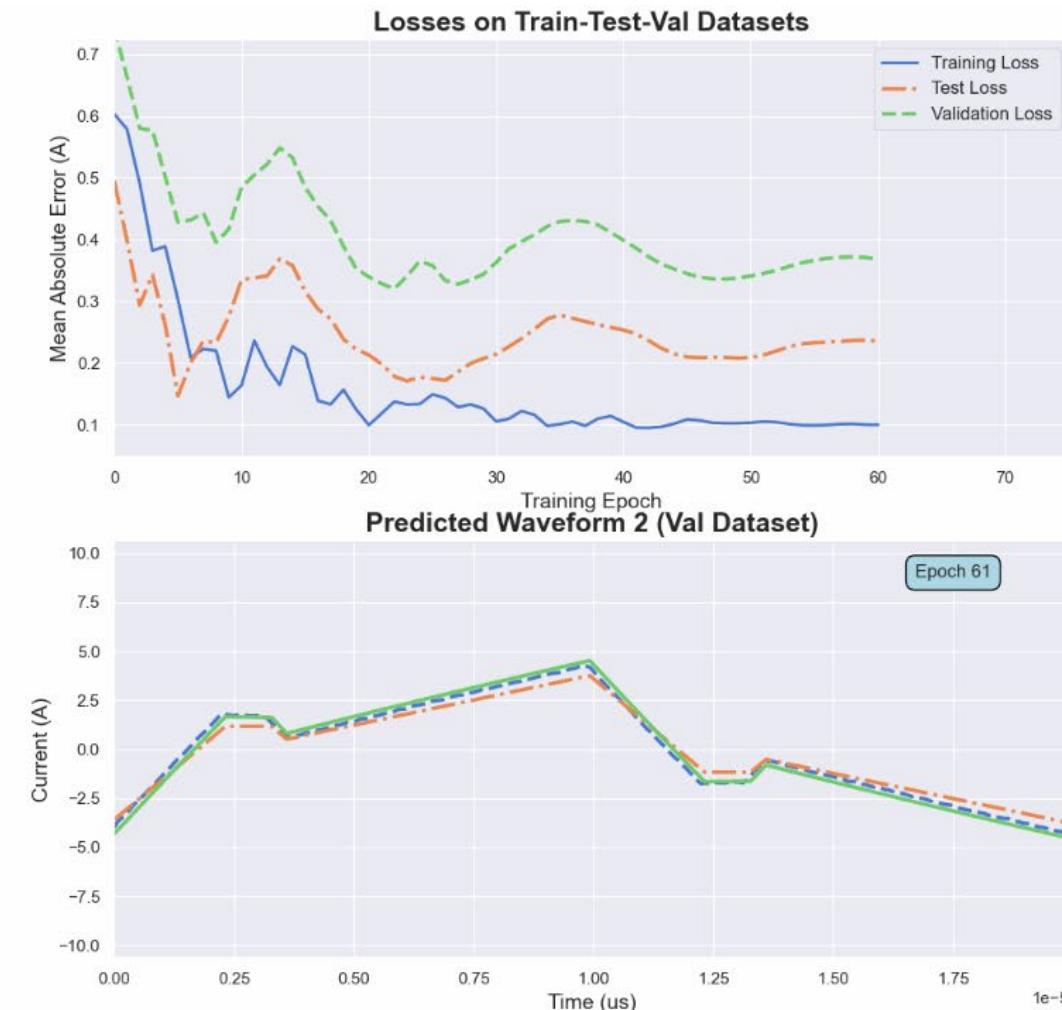
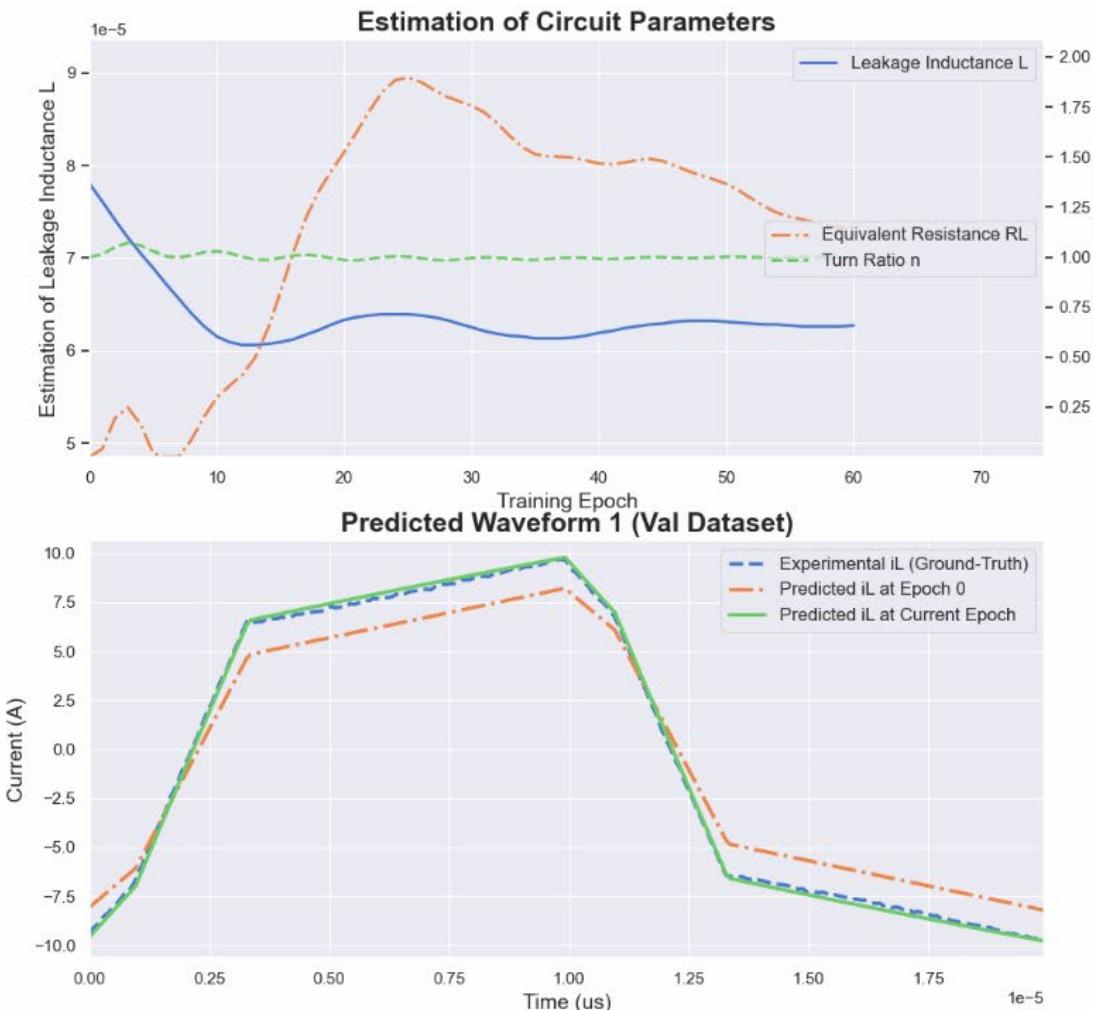
Bound the weight during training to avoid unrealistic neural θ

Main Loop of PANN
Training

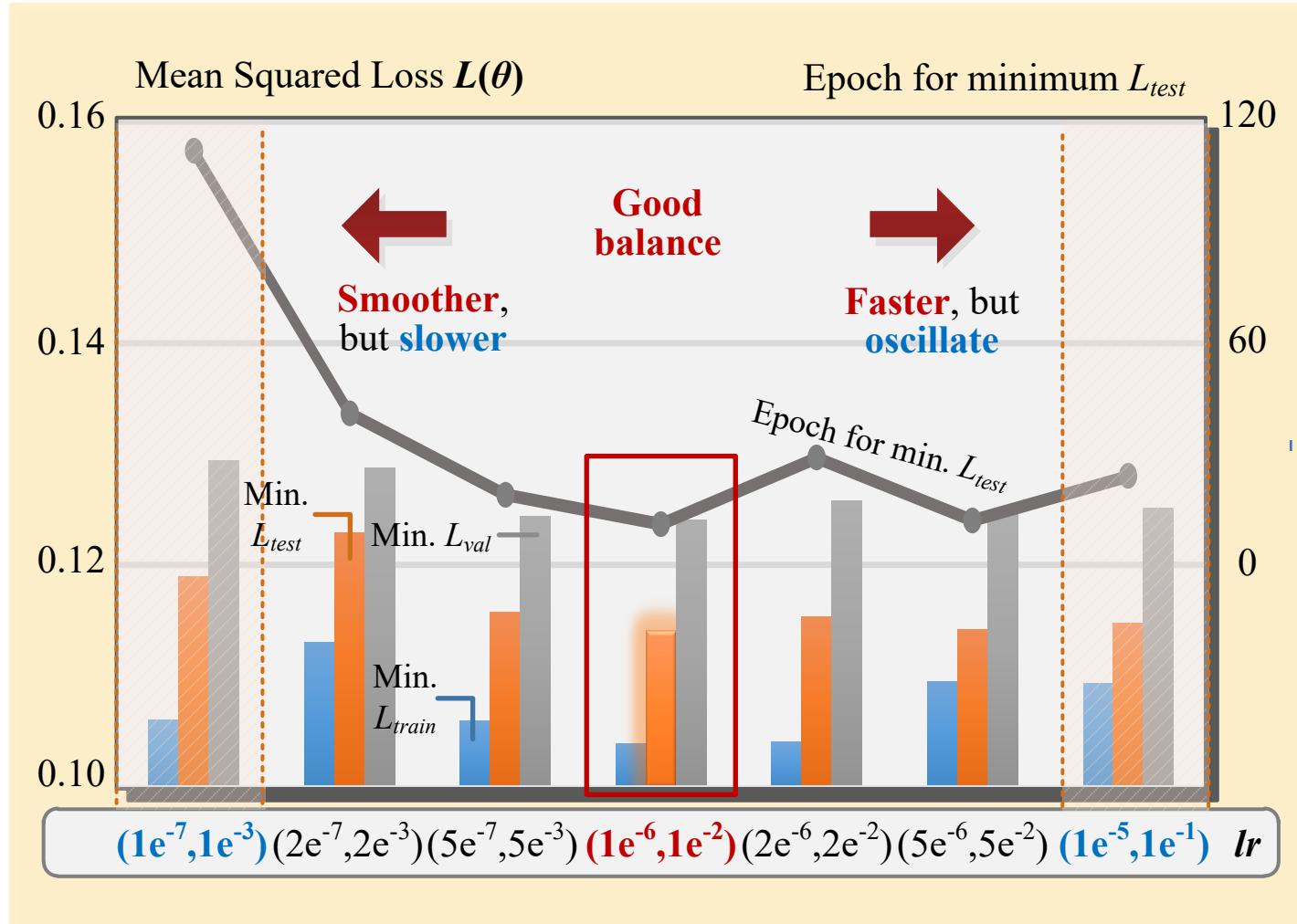
[pann_train.py :: def train](#)

Define training process

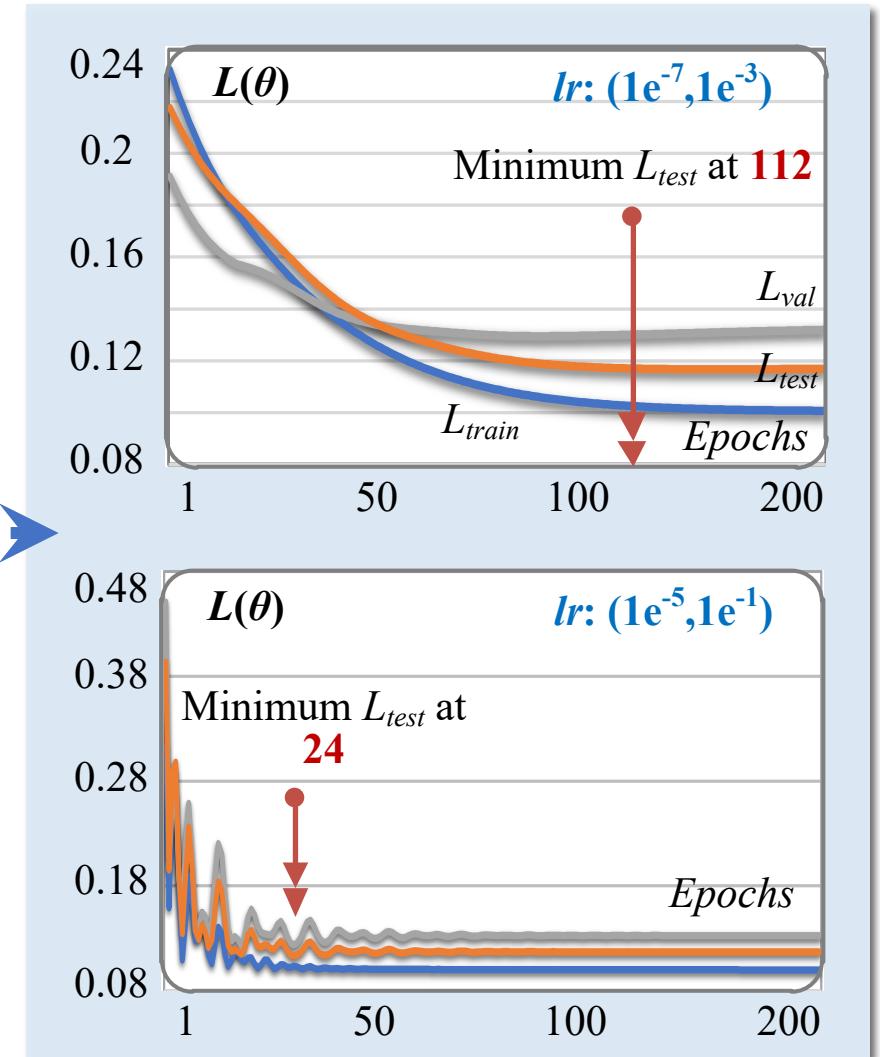
PANN Training (Code Demo*, "DAB-inference and training.ipynb")



Tuning of learning rates



Training Performances w.r.t. Learning Rates



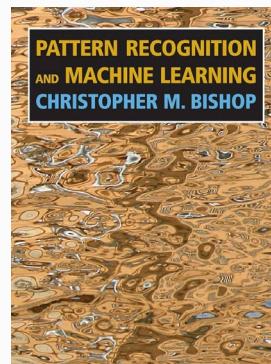
PANN is Data-Light.

MAEs / A (mean±std.)	Data Sizes for (Train, Test, Validation) Sets		
	(10, 90, 900)	(50, 90, 860)	(100, 90, 810)
Piecewise		0.311±0	
SVR	2.286±0	2.052±0	1.991±0
LSTM	1.560±9.5E-2	1.340±5.6E-2	1.193±9.9E-2
TCN	1.371±3.6E-1	0.726±6.1E-2	0.511±7.2E-2
PL-PINN	0.310±1.3E-2	0.259±1.1E-2	0.234±7.3E-3
LN-GRU	1.257±1.2E-1	0.991±1.2E-1	0.527±8.6E-2
PANN	0.160±4.1E-3	0.145±3.6E-3	0.132±3.3E-3

Popular ML: Thousands,
Millions, and MORE

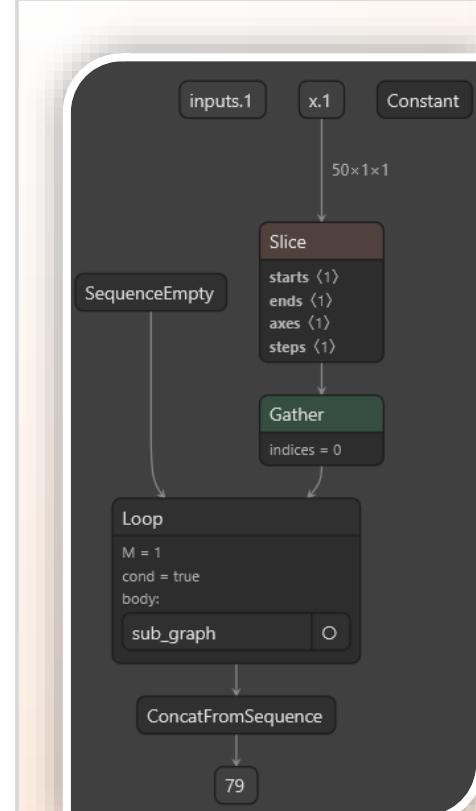
PE
Physics ↓ *Embed Data*
Invariant

PANN: a **handful** of
data, reducing by **3**
orders of magnitudes



Data Size
≥
#Params.

PANN is Lightweight.



1.72 kB of a
PANN for DAB



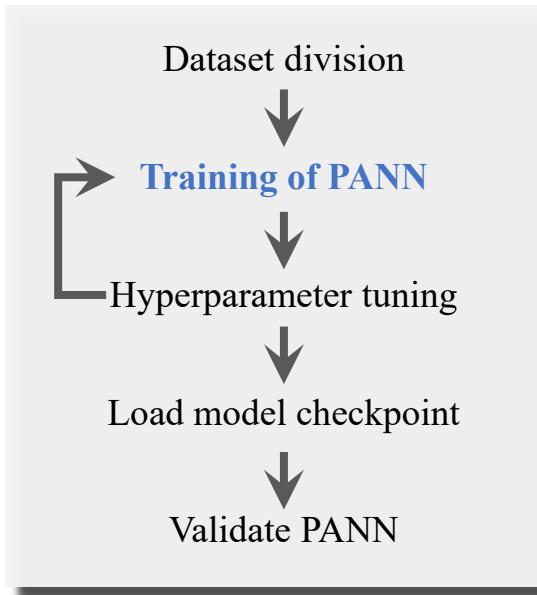
1762 strings,
Approx. 160
words

*Deployable
on Edge*

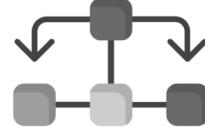


PANN pioneers the **next-generation of LIGHT AI** for power electronics in:

1. Data: **countable on ONE HAND**, 2. Model size: **deployable on EDGE DEVICE**.



1. Training and Hyper-parameter Tuning of PANN



Takeaways



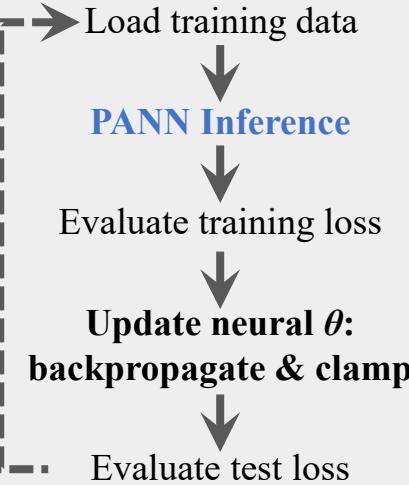
PANN Training

- Learning rates lr of PANN should be carefully tuned
- Low lr : **slow** convergence
- High lr : large **oscillation**
- **Rule of thumb:** lr to be **1%** of parameter values



3. Hyper-parameter Tuning of PANN

2. Training Loop of PANN



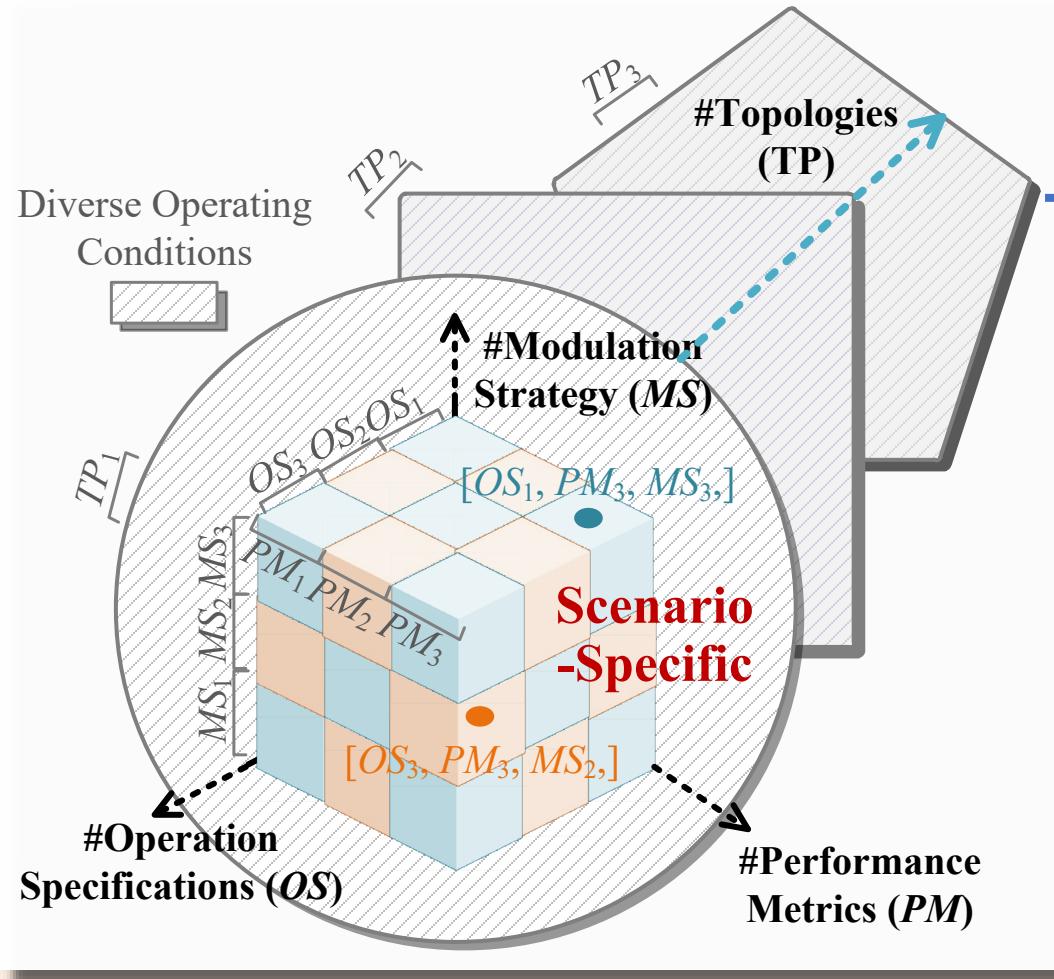
4. PANN is a “Light” AI Model



- **Data-light:** training data required for PANN is **countable on ONE HAND**
- **Lightweight:** few kBytes, deployable on resource-constrained **EDGE** devices

NEXT GENERATION OF AI FOR POWER ELECTRONICS

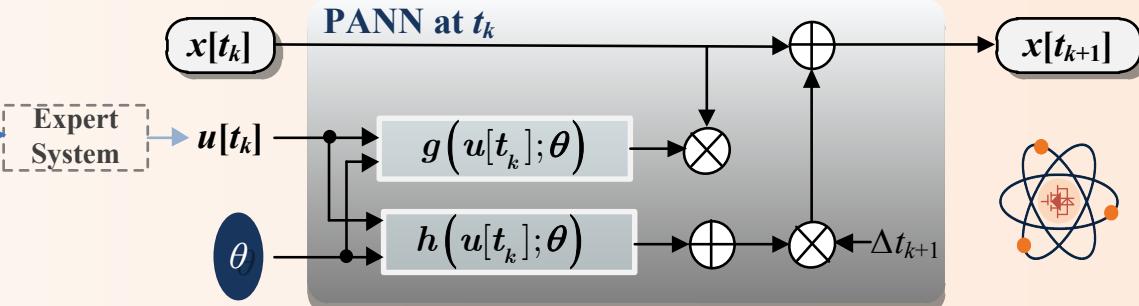
- I. Applications of AI in Power Electronics and its Challenges
- II. Physics-Informed Machine Learning (PIML) for Power Electronics
- III. Fundamentals of Physics-in-Architecture Neural Network (PANN)
and its Explainability in Power Electronics
- IV. PANN is Light in Two Aspects: Data-Light and Lightweight
- V. **PANN is Flexible: “All You Need” is One PANN Model**
- VI. Future Directions of PANN in Power Electronics



Total Number of Conventional ML Models required:

$$\underline{MS \times PM \times OS \times TP}$$

ONE PANN for Diverse Conditions and Topologies



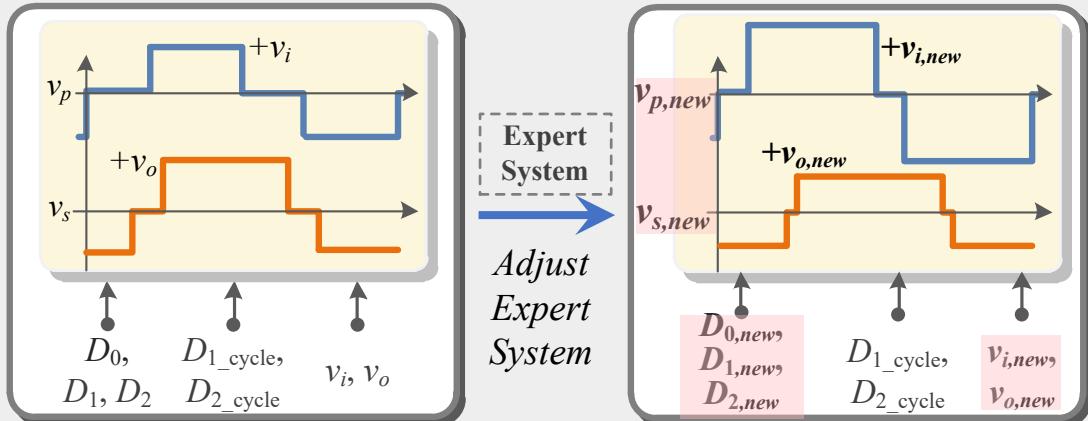
Training-FREE



Four Angles of PANN's Flexibility

Flexibility across Conditions - DAB

Flexibility 1: Out-of-Domain Operating Conditions



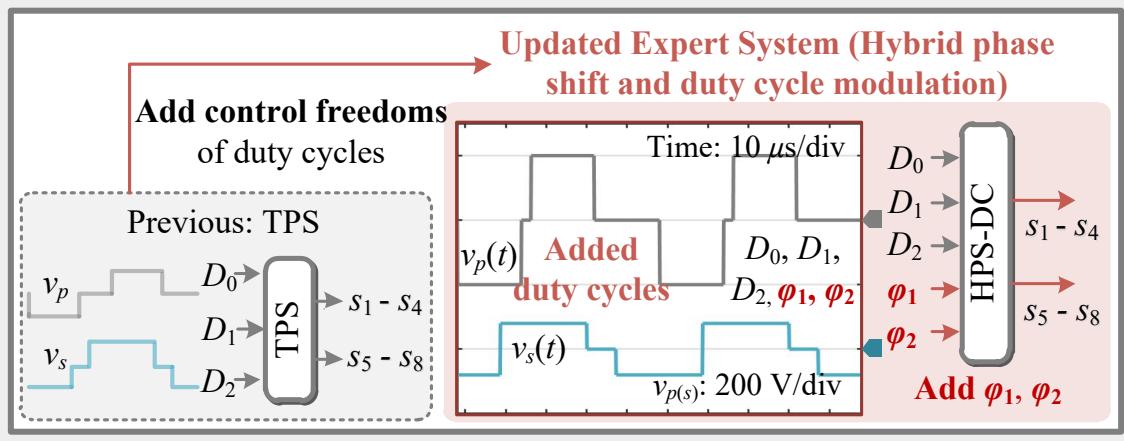
Code Implementation

Adjust the Knowledge in Input Blocks

```
# Example 3: Vin = 300 V, Vo = 220 V
D0, D1, D2 = 0.16, 0.78, 0.88
Vin, Vref = 300, 220
Tsim = 100*Ts # simulation time, considering 100 times of sw
# generate voltage waveforms (vp and vs) given one set of mo
vp3, vs3 = create_vpvs(D0, D1, D2, Vin, Vref, Tslen, dt,
                        Tsim, Ts, D1_cycle=0.5, D2_cycle=0.5)
inputs3 = np.hstack([vp3[:, None], vs3[:, None]])[None]
# convert to torch FloatTensor
inputs3 = torch.FloatTensor(inputs3)
```

- Adjust v_i and v_o
- Adjust expert system

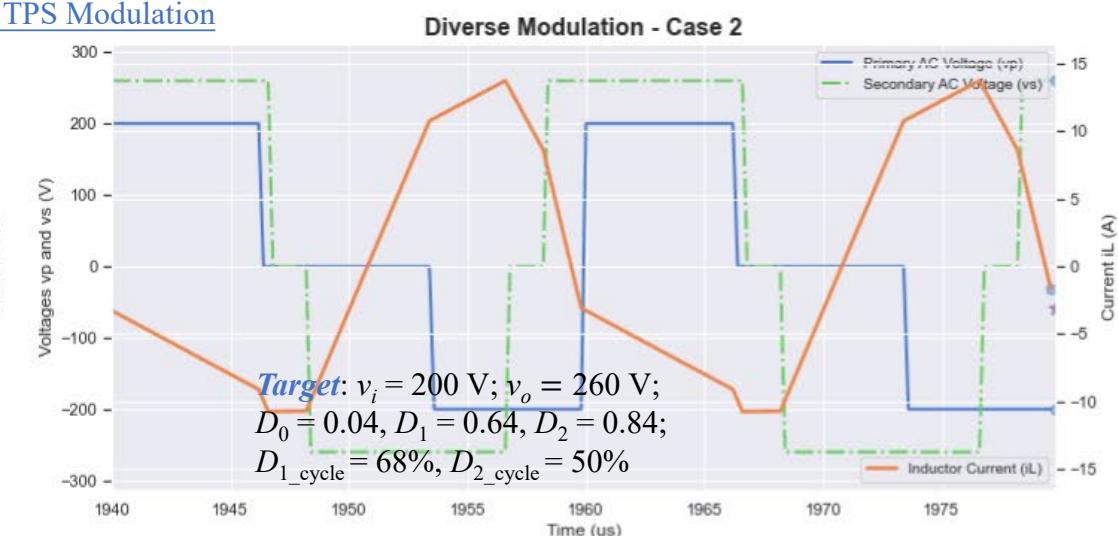
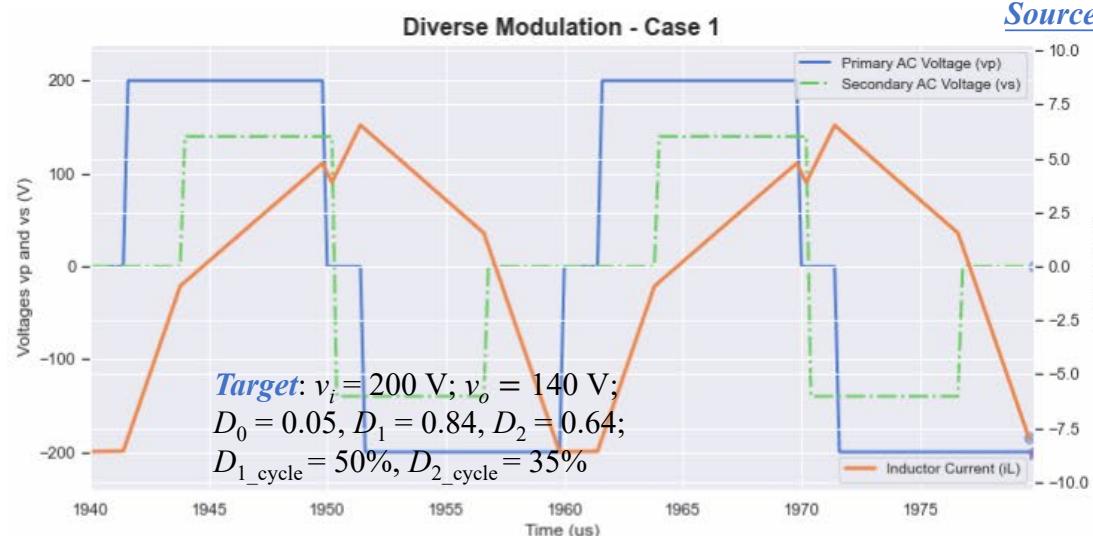
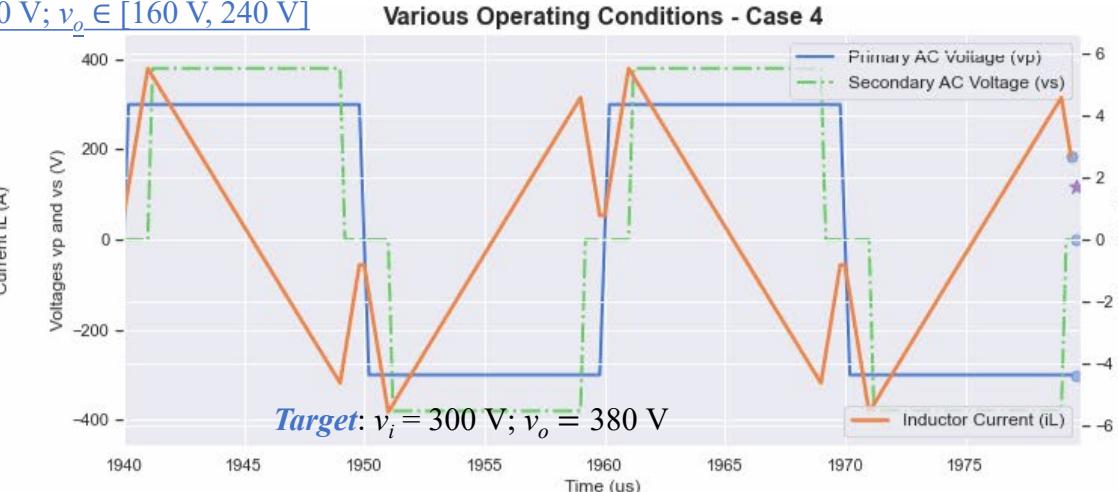
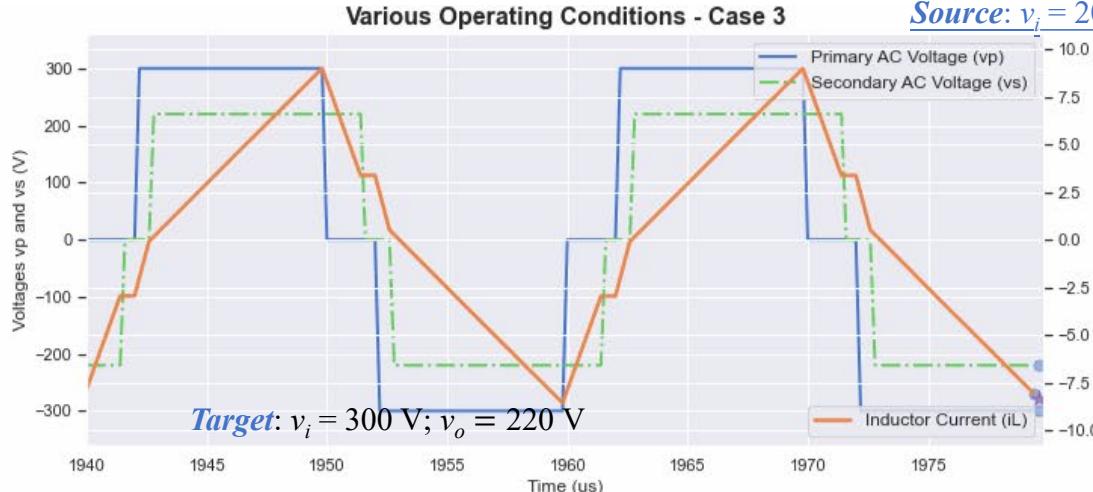
Flexibility 2: Diverse Modulation Strategies



```
# Example 1: Vin = 200 V, Vo = 140 V, (D0, D1, D2) = (0.1, 0.9).
D0, D1, D2 = 0.04, 0.84, 0.64
D1_cycle, D2_cycle = 0.5, 0.32
Vin, Vref = 200, 140
Tsim = 100*Ts # simulation time, considering 100 times of sw
# generate voltage waveforms (vp and vs) given one set of modu
vp, vs = create_vpvs(D0, D1, D2, Vin, Vref, Tslen, dt,
                      Tsim, Ts, D1_cycle=D1_cycle, D2_cycle=D2_cycle)
inputs = np.hstack([vp[:, None], vs[:, None]])[None]
# convert to torch FloatTensor
inputs = torch.FloatTensor(inputs)
```

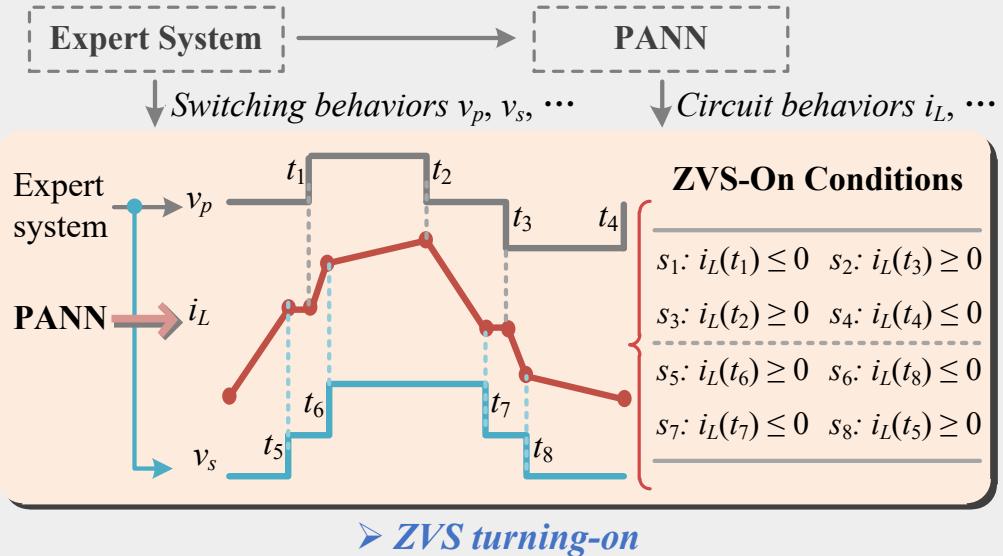
- Adjust duty cycles
- Adjust expert system

PANN's Flexibility across Conditions (Code Demo*, "DAB-Operational Transfer.ipynb")



Flexibility across Performance Metrics - DAB

Flexibility 3: Various Performance Metrics



$$v_{L,rms} = \sqrt{\frac{1}{N_T} \sum_{k=1}^{N_T} (v_L[t_k])^2} \quad i_{L,rms} = \sqrt{\frac{1}{N_T} \sum_{k=1}^{N_T} (i_L[t_k])^2}$$

$$Q = v_{L,rms} \cdot i_{L,rms} \quad i_{pp} = i_{L,max} - i_{L,min}$$

➤ Reactive power ➤ Current stress

One-Stop Metric Adapter for Diverse Performances

Code Implementation

[star_metric_adapter.py](#)

```
def soft_switching(iL, vp, vs, Vin, Vo, threshold=0.):
    # Evaluate soft switching performances of conventional DAB converter
    indices = locate(vp, Vin)
    indices2 = locate(vs, Vo)
    ZVS = np.zeros(8, )
    ZCS = np.zeros(8, )
    ##### ZVS of Primary Side Switches
    ZVS[0] = (iL[indices[1]] <= threshold).astype(int) # ZVS: S1
    ZVS[1] = (iL[indices[3]] >= -threshold).astype(int) # ZVS: S2
    ZVS[2] = (iL[indices[2]] >= -threshold).astype(int) # ZVS: S3
    ZVS[3] = (iL[indices[0]] <= threshold).astype(int) # ZVS: S4
    ##### ZVS of Secondary Side Switches
    ZVS[4] = (iL[indices2[1]] >= -threshold).astype(int) # ZVS: S5
    ZVS[5] = (iL[indices2[3]] <= threshold).astype(int) # ZVS: S6
    ZVS[6] = (iL[indices2[2]] <= threshold).astype(int) # ZVS: S7
    ZVS[7] = (iL[indices2[0]] >= -threshold).astype(int) # ZVS: S8
    return ZVS, indices, indices2
```

```
def current_stress(iL, metric="ipp"):
    assert metric in ["ipp", "irms"]
    if metric == "ipp":
        return max(iL) - min(iL)
    elif metric == "irms":
        return np.sqrt((iL**2).mean())
```

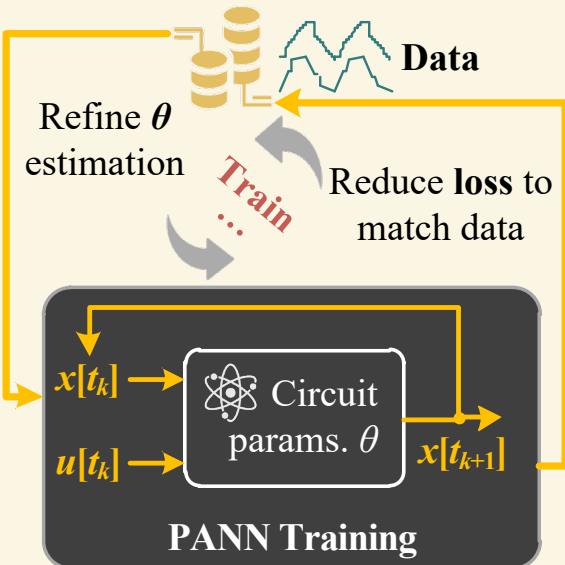
■ Peak-peak current
■ RMS current

Flexibility across Circuit Parameters - DAB

Flexibility 4-1: Different Circuit Parameters

➤ WITH DATA

Update θ via Training



Training

Direct Assignment of θ

➤ WITH KNOWLEDGE

Assign θ to its neural structure

Priori Knowledge

$$\theta := \{L_k, R_L, n\}$$

$x[t_k] \rightarrow$ Circuit params. θ $x[t_{k+1}]$

Configurable θ

Direct Assignment
Training-Free

Flexibility across Different Circuit Parameters by
Training or Direct θ Assignment

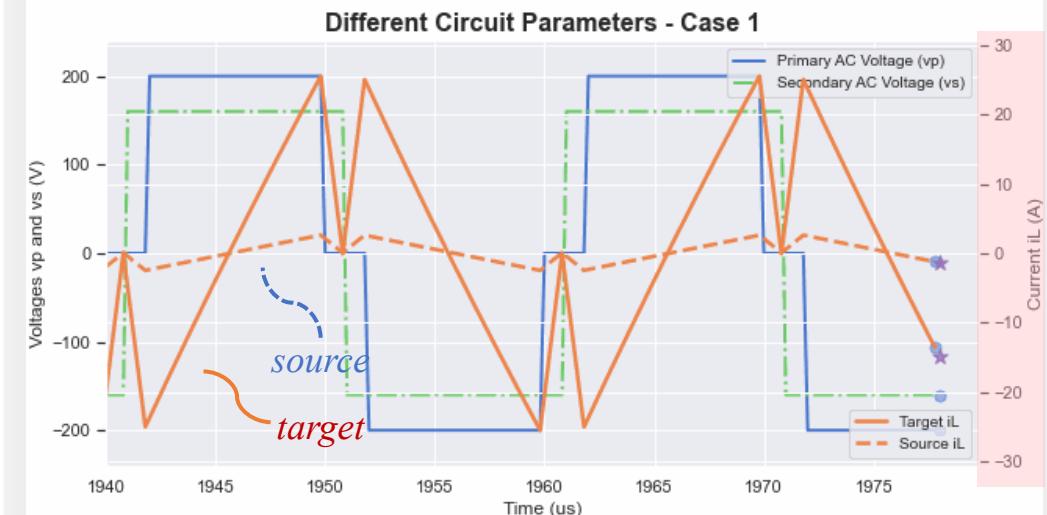
Code Implementation

Adjust Neural θ in Physics Cell Module

```
# Example Directly assign new circuit parameters to neural θ =
pann_dab.cell.n.data = torch.FloatTensor([1.0])
pann_dab.cell.RL.data = torch.FloatTensor([120e-3])
pann_dab.cell.Lr.data = torch.FloatTensor([6.3e-6])
```

source: $R_L = 120 \text{ mΩ}$; $L_k = 63 \mu\text{H}$; $n = 1$

target: $R_L = 120 \text{ mΩ}$; $L_k = 6.3 \mu\text{H}$; $n = 1$

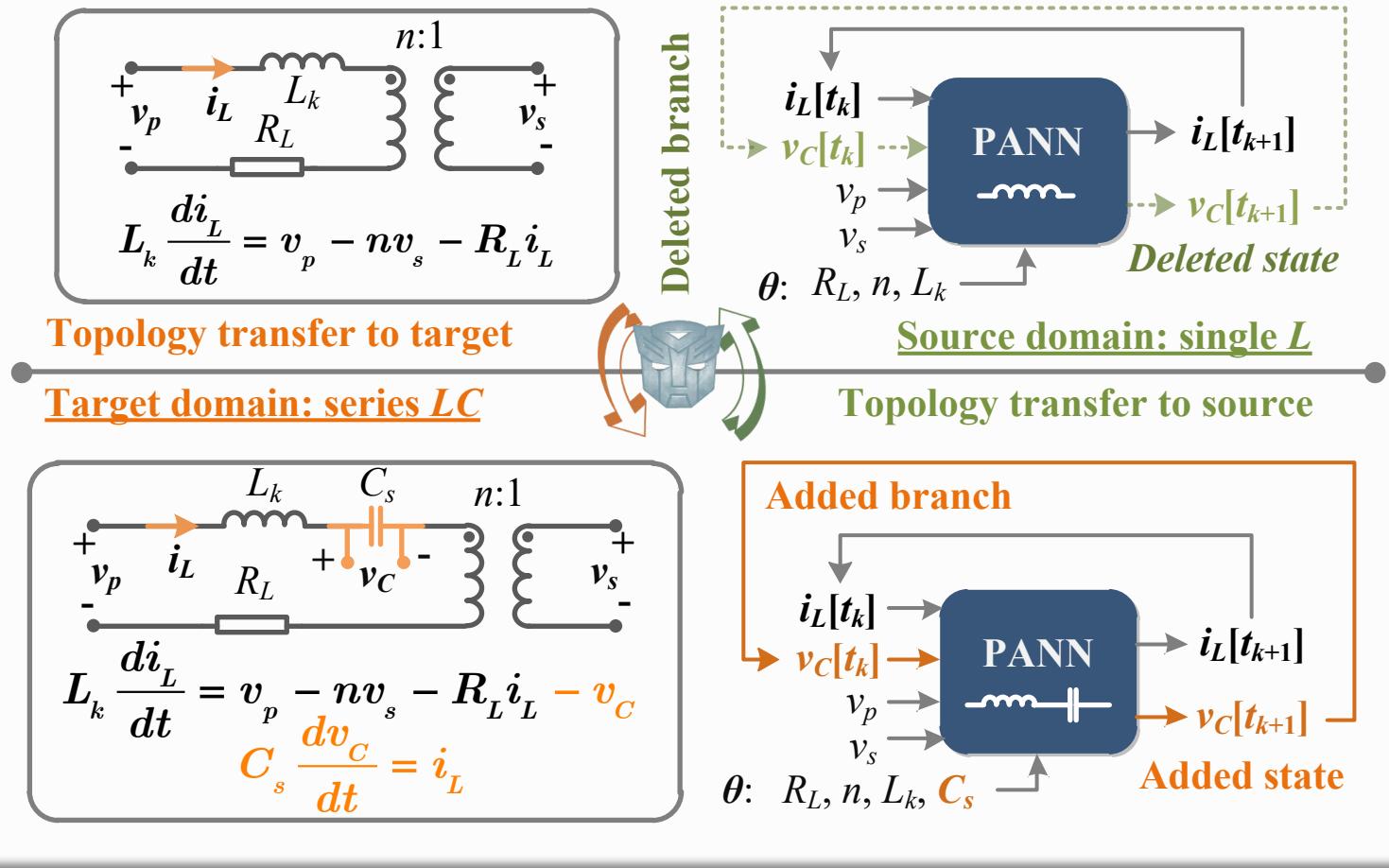


Circuit Parameter Transfer (Code Demo*)

"DAB-Operational Transfer.ipynb"

Flexibility across Topologies - DAB Family

Flexibility 4-2: Different Topological Variants



Discretized by Implicit Euler:

$$i_L[t_{k+1}] = \frac{L_k i_L[t_k] + (v_p[t_k] - nv_s[t_k]) \Delta t_{k+1}}{L_k + R_L \Delta t_{k+1}}$$

Delete Branch  *Add Branch* 

Discretized by Implicit Euler:

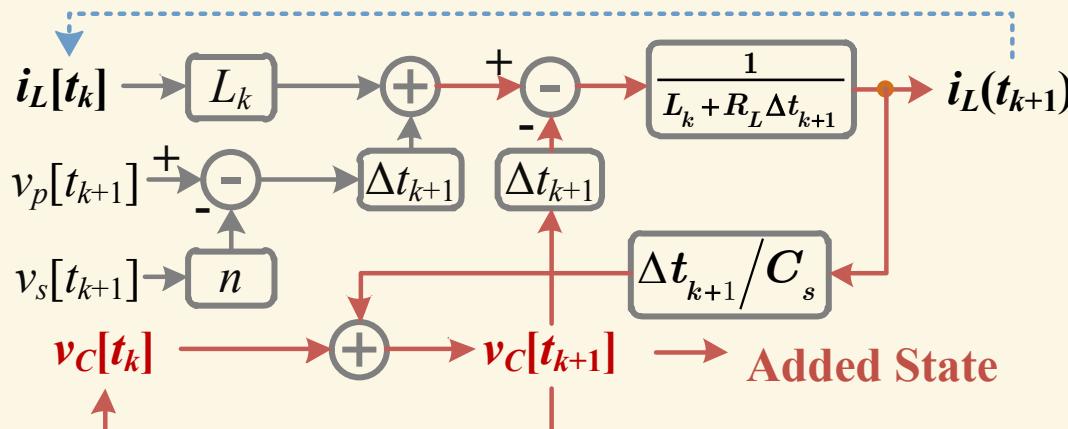
v_C is added into the voltage loop

$$\left\{ \begin{array}{l} i_L[t_{k+1}] = \frac{L_k i_L[t_k] + (v_p[t_{k+1}] - nv_s[t_{k+1}] - v_C[t_{k+1}]) \Delta t_{k+1}}{L_k + R_L \Delta t_{k+1}} \\ C_s v_C[t_{k+1}] = C_s v_C[t_k] + i_L[t_{k+1}] \Delta t_{k+1} \end{array} \right.$$

Added physics of v_C of resonant capacitor

Topology Transfer between Non-Resonant and Resonant DAB

Schematic Diagram of PANN for Topology Transfer



- Previous Structure for
 - Non-resonant DAB (L)
- Added Branched for
 - Resonant DAB (Series LC)

PANN's Neural Link Transformation

Code Implementation

[DAB-Topology Transfer.ipynb](#)

■ Non-resonant DAB with Single L

```
iL_next = (states[:, 0]+(inputs[:, 0]-  
    inputs[:, 1]*self.n)*self.dt/self.Lr)\\"  
    (1+self.RL*self.dt/self.Lr) # physics of iL
```



- Add v_C into the voltage loop
- Add physics of v_C

physics of resonant inductor iL

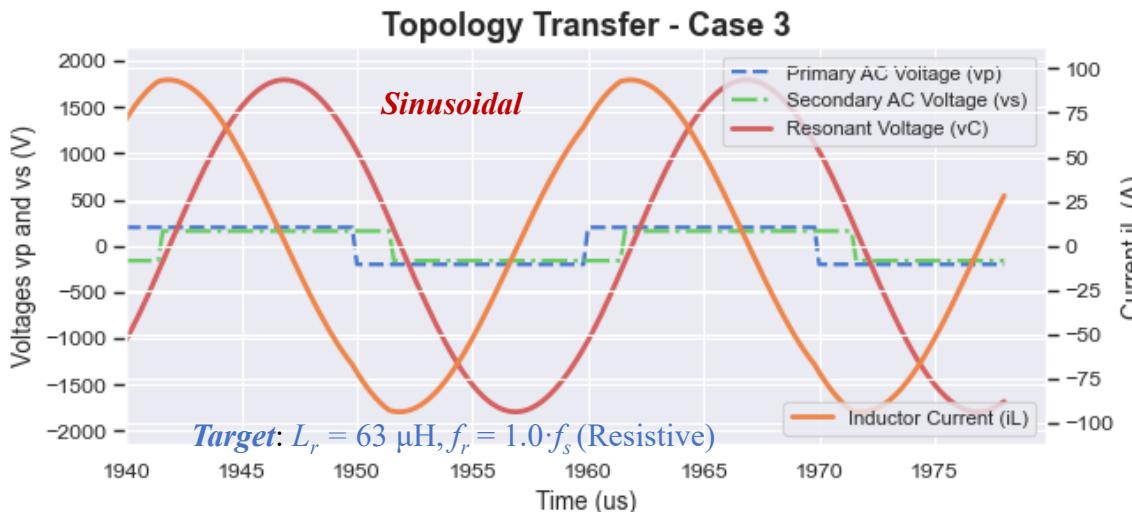
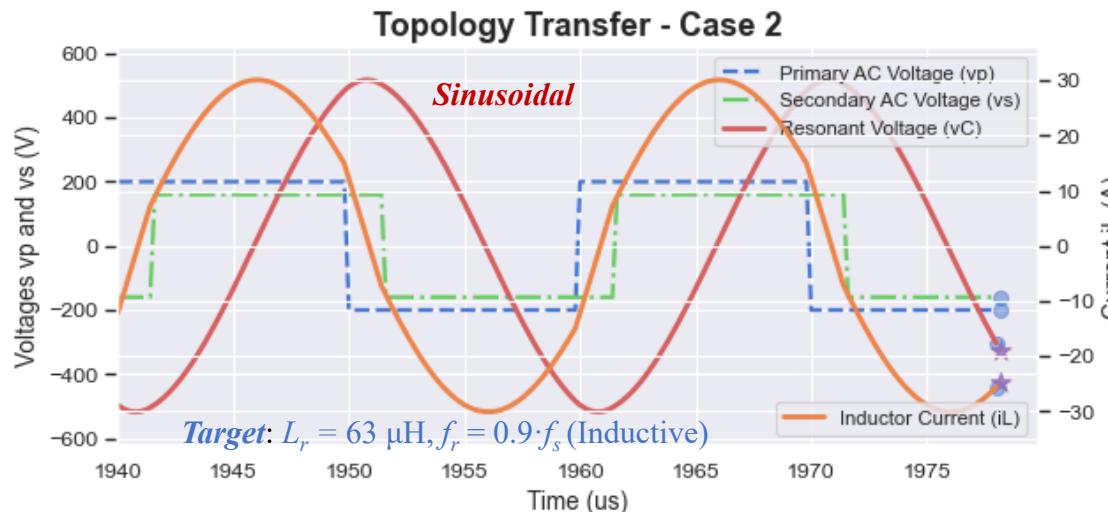
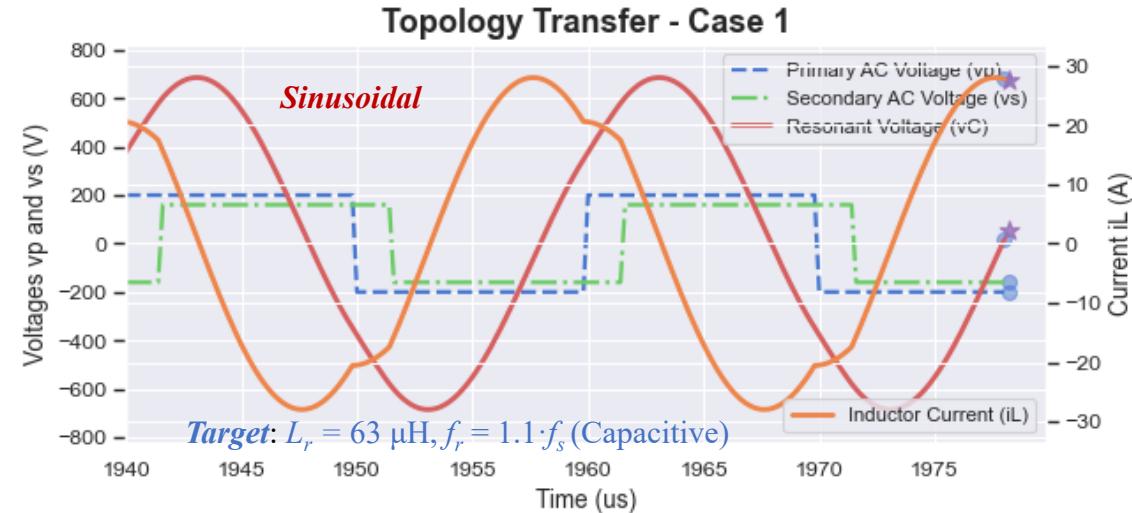
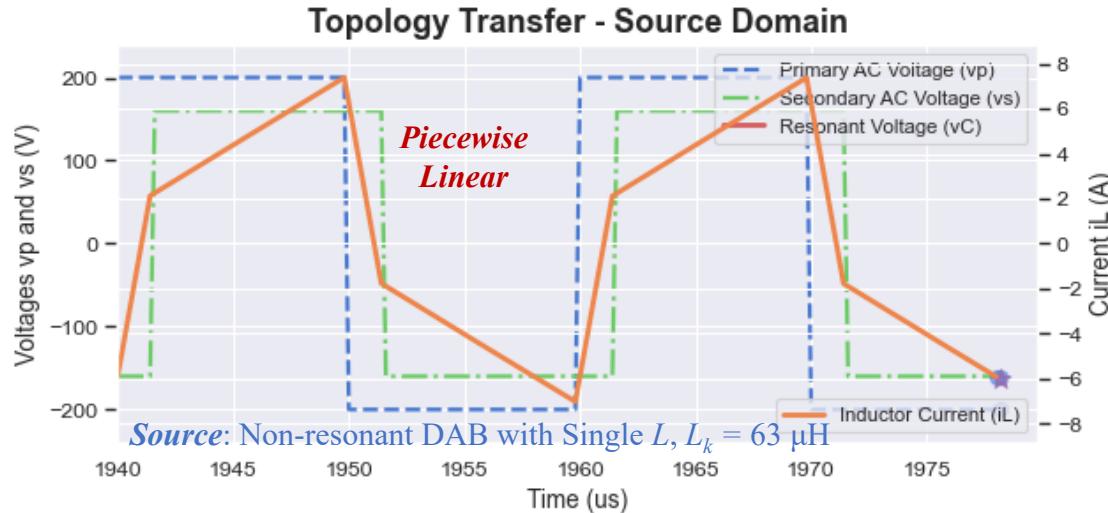
```
iL_next = (states[:, 0]+(inputs[:, 0]-  
    inputs[:, 1]*self.n)-states[:, 1])*self.dt/self.Lr)\\"  
    (1+self.RL*self.dt/self.Lr+self.dt**2/self.Lr/self.Cs)  
# physics of resonant capacitor  $v_C$ 
```

■ Add physics of v_C

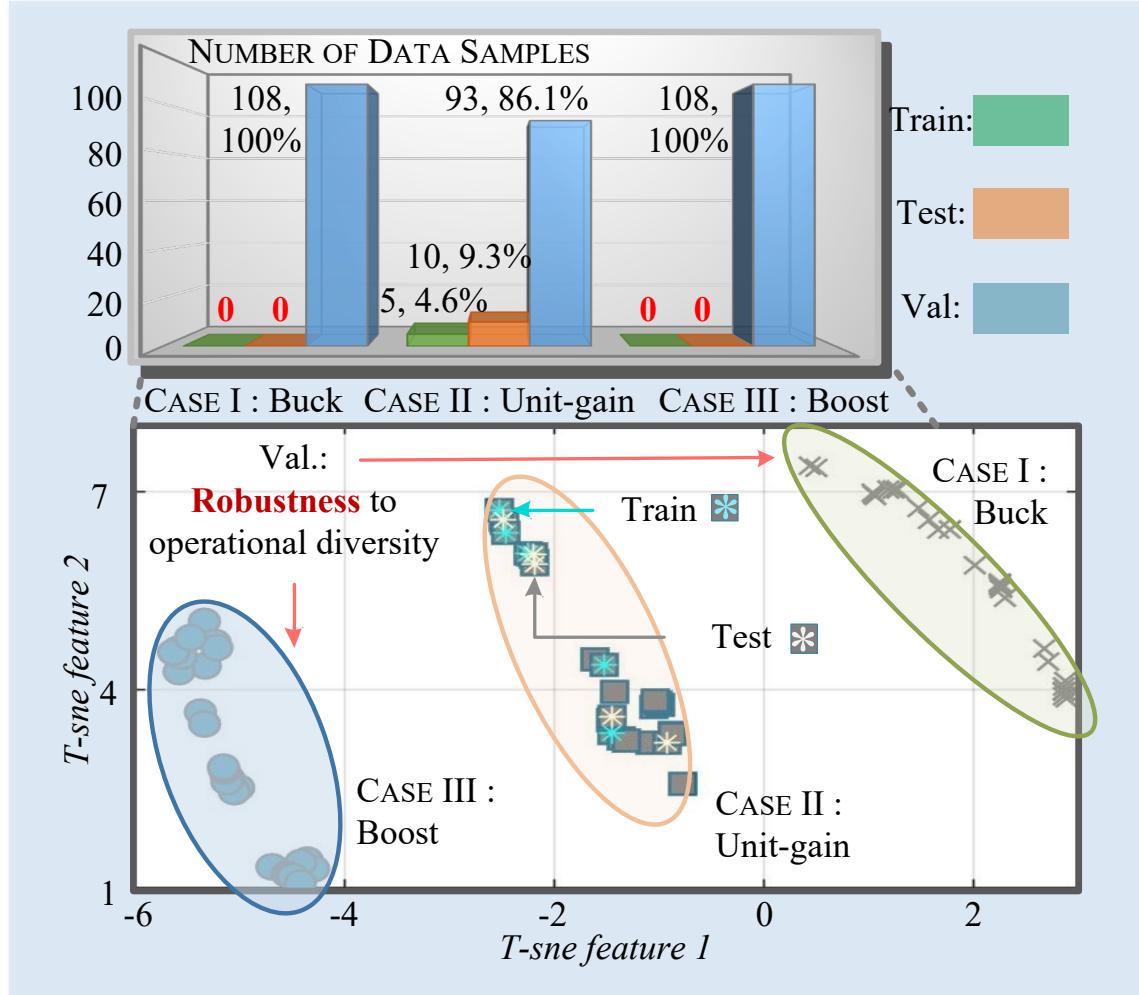
```
vC_next = iL_next/self.Cs*self.dt+states[:, 1]
```

■ Resonant DAB with Series LC

PANN's Flexibility across Topologies (Code Demo*, "DAB-Topology Transfer.ipynb")

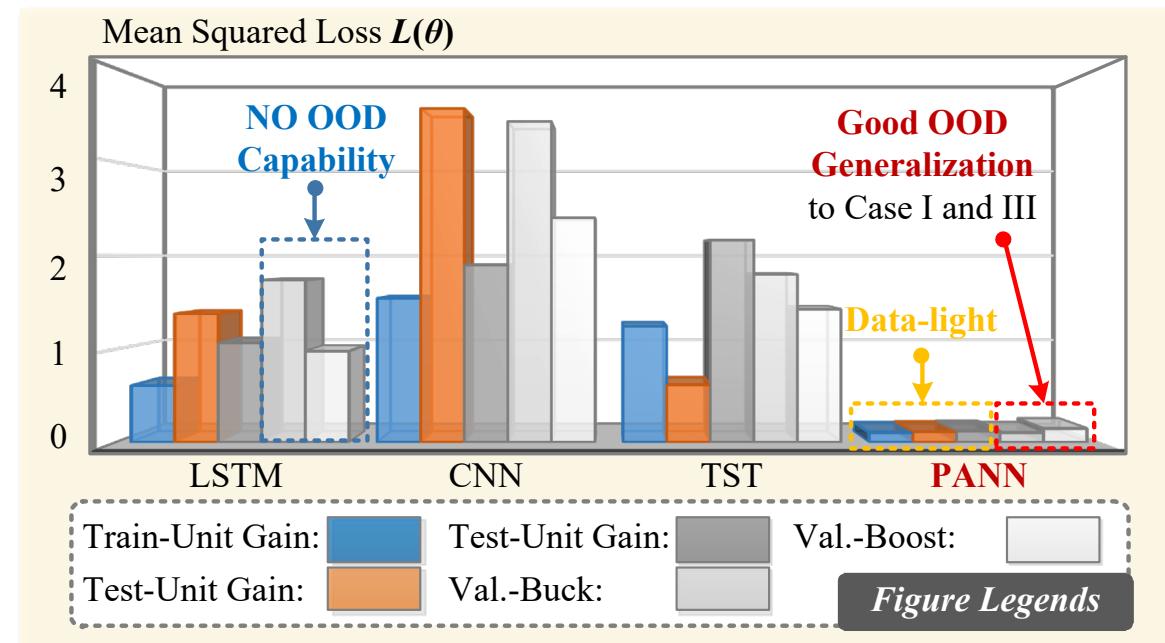


Flexibility across Conditions and Modulations

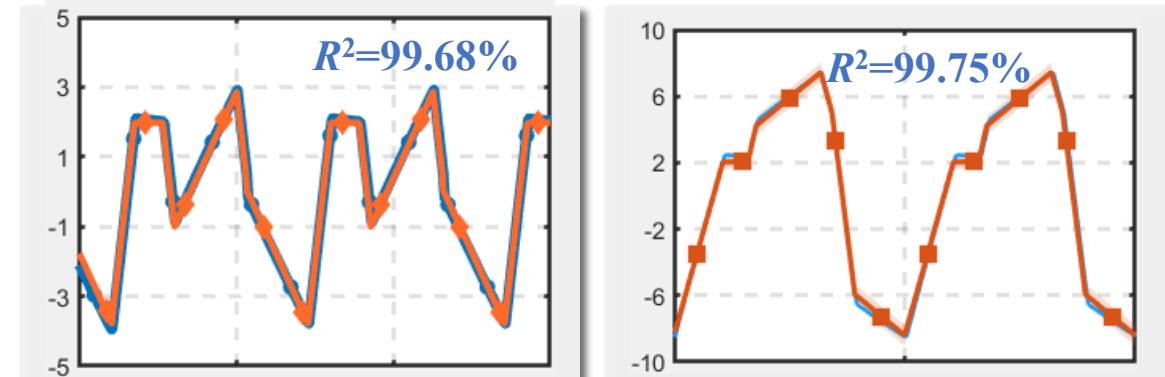


Data Partition Settings to Validate Flexibility across Conditions

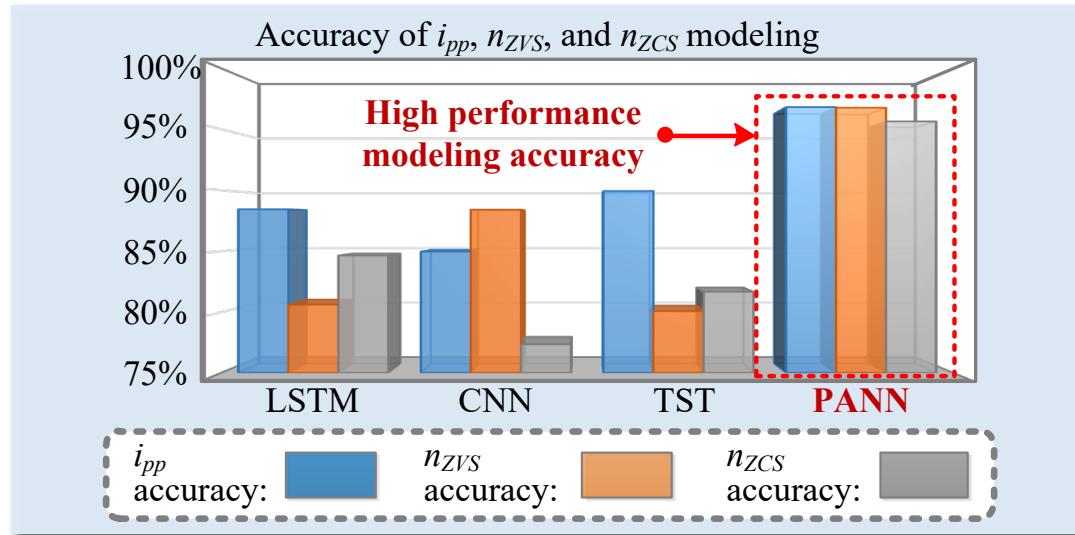
Statistics of Condition Transferability



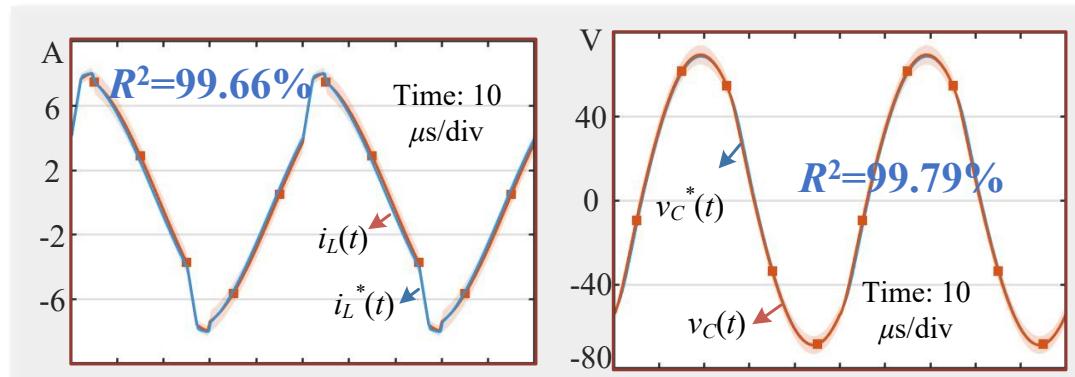
Statistics of Modulation Transferability



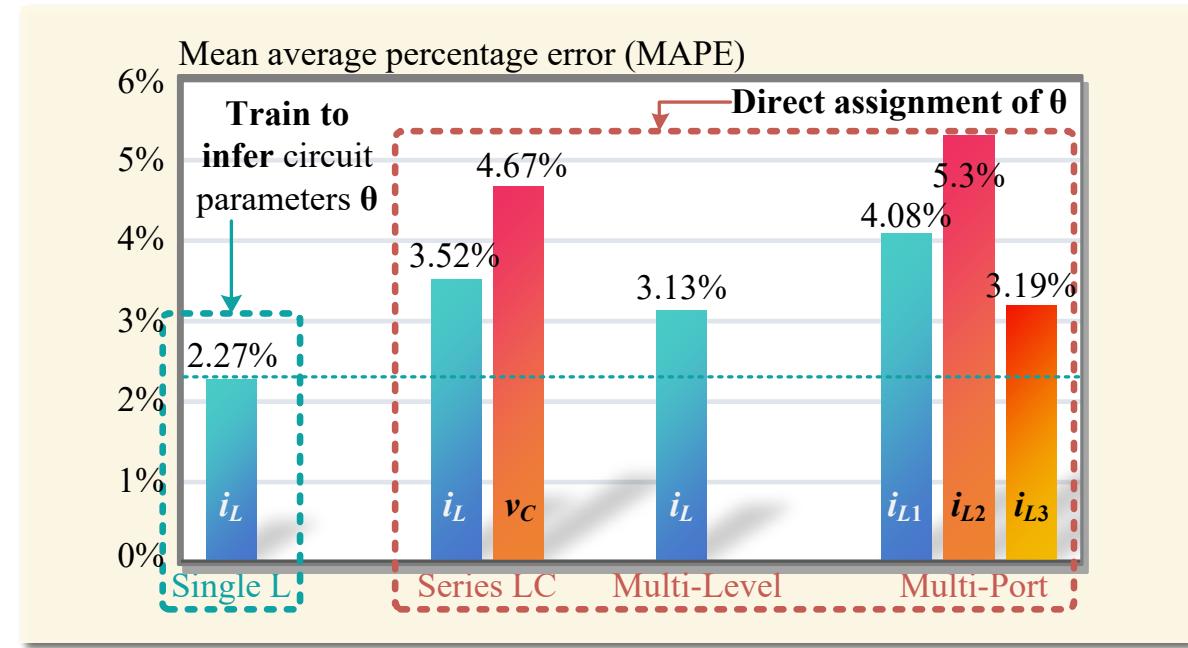
Flexibility across Performance Metrics and Topologies



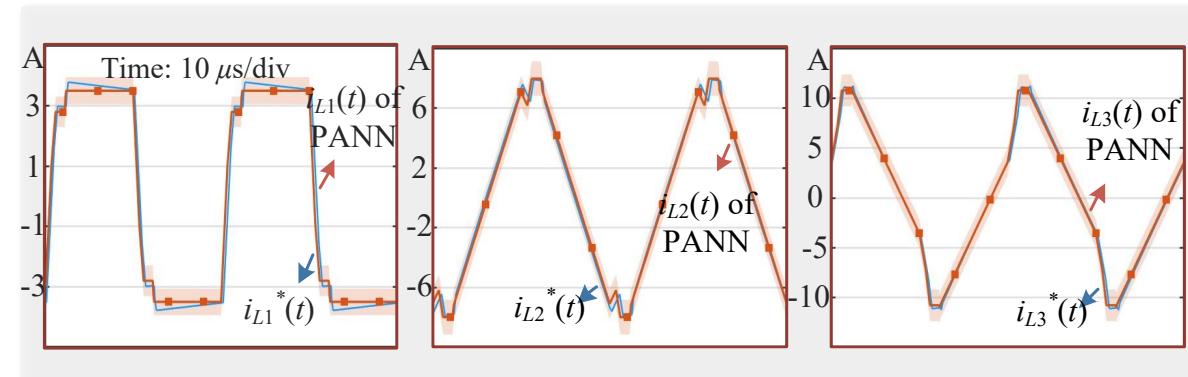
Statistics of Performance Transferability



Waveforms of Topology Transferability - LC



Statistics of Topology Transferability



Waveforms of Topology Transferability - TAB

- Simply update operating params. in **expert system** to change input signals
- **Training-free**

Flexibility I. Operating Conditions



Takeaways

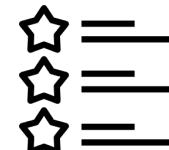
Flexibility II. Modulation Strategy



- Switching behaviors in **expert system** should be updated accordingly
- **Training-free**

- **Numerically** gauge metrics based on **input** signals from expert system and **state** waveforms from PANN
- Metric adapter is **case-specific**, rely on topology, modulation, etc.
- **Training-free**

Flexibility III. Performance Metrics



PANN's Flexibility

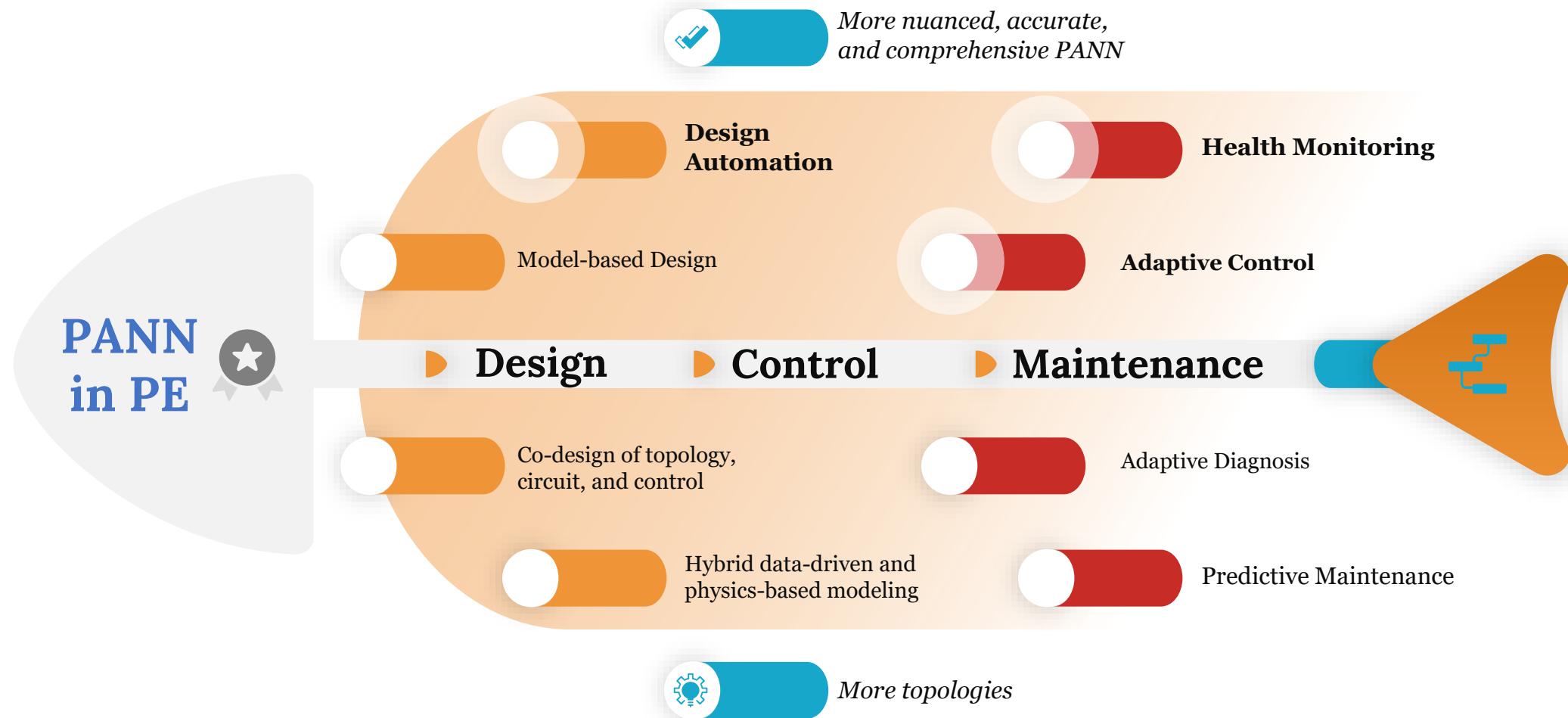


Flexibility IV. Circuit Parameters & Topology

- Circuit θ is adjustable if there is: **1) training data;**
2) priori knowledge of θ (direct value assignment)
- Topology transfer: add/del. **neural links** of PANN

NEXT GENERATION OF AI FOR POWER ELECTRONICS

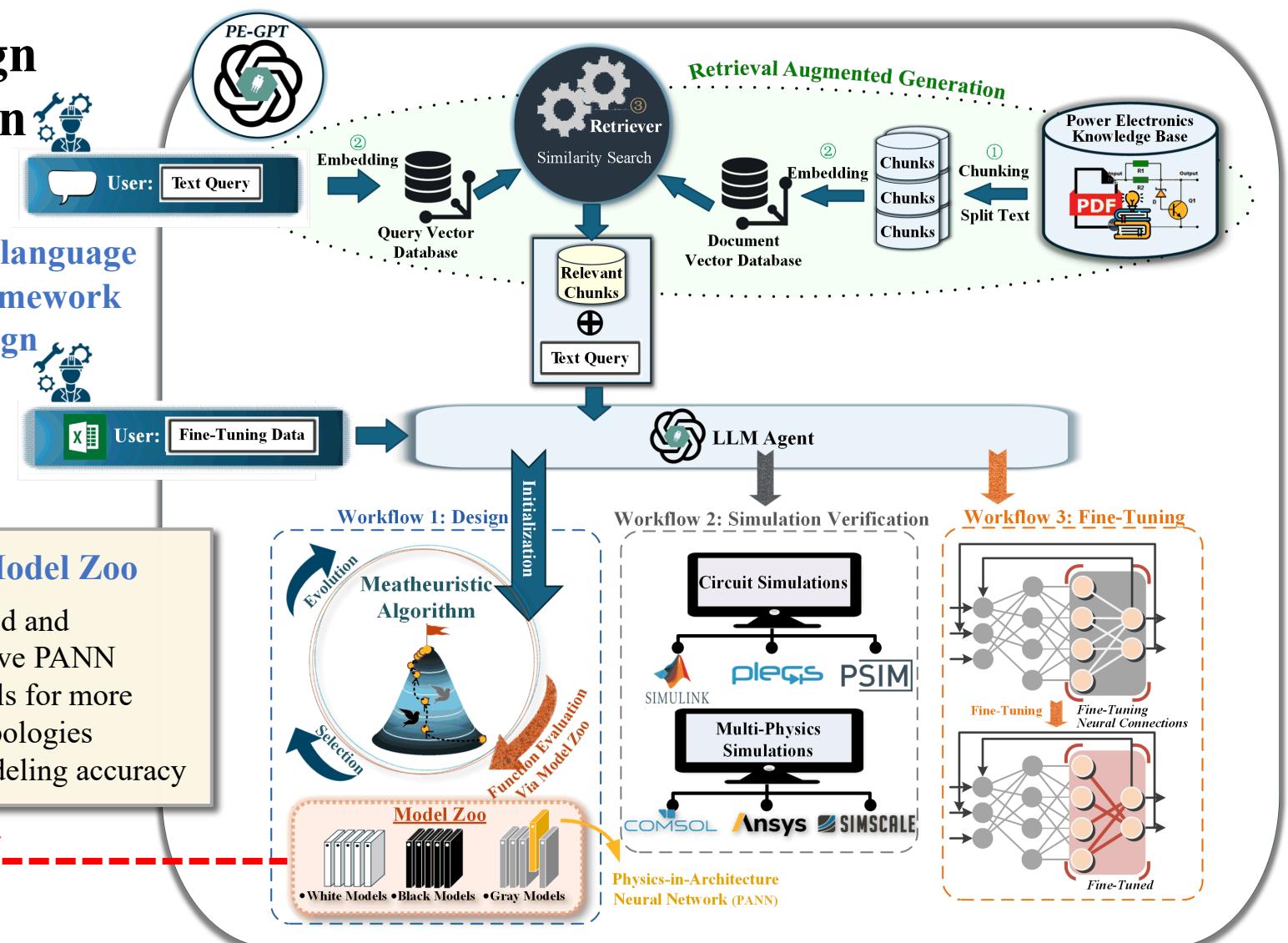
- I. Applications of AI in Power Electronics and its Challenges
- II. Physics-Informed Machine Learning (PIML) for Power Electronics
- III. Fundamentals of Physics-in-Architecture Neural Network (PANN)
and its Explainability in Power Electronics
- IV. PANN is Light in Two Aspects: Data-Light and Lightweight
- V. PANN is Flexible: “All You Need” is One PANN Model
- VI. Future Directions of PANN in Power Electronics**



PANN in the **Life Cycle** of Power Converters

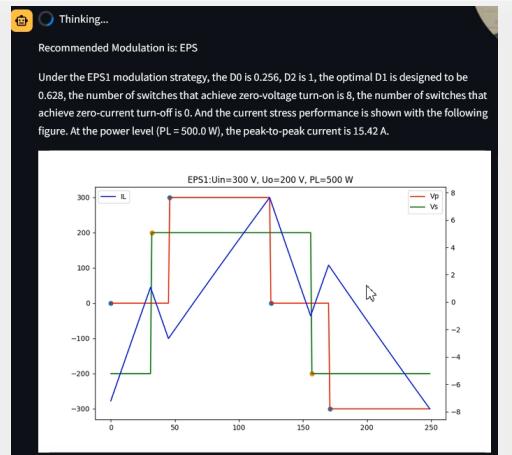
I. PE Design Automation

PE-GPT: Large language model-based framework for PE design



Source:
Code:

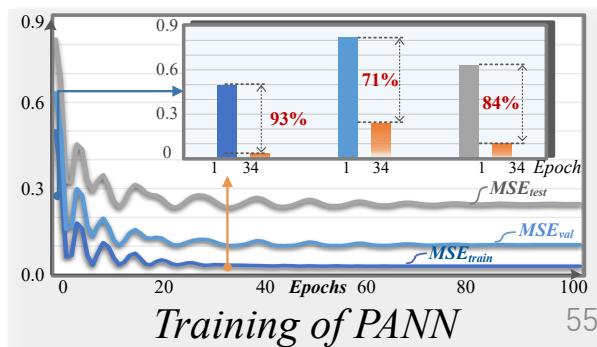
F. Lin et al., "PE-GPT: A New Paradigm for Power Electronics Design," in *IEEE Trans. on Ind. Electron.*.
<https://github.com/XinzeLee/PE-GPT>



Modeling and optimization

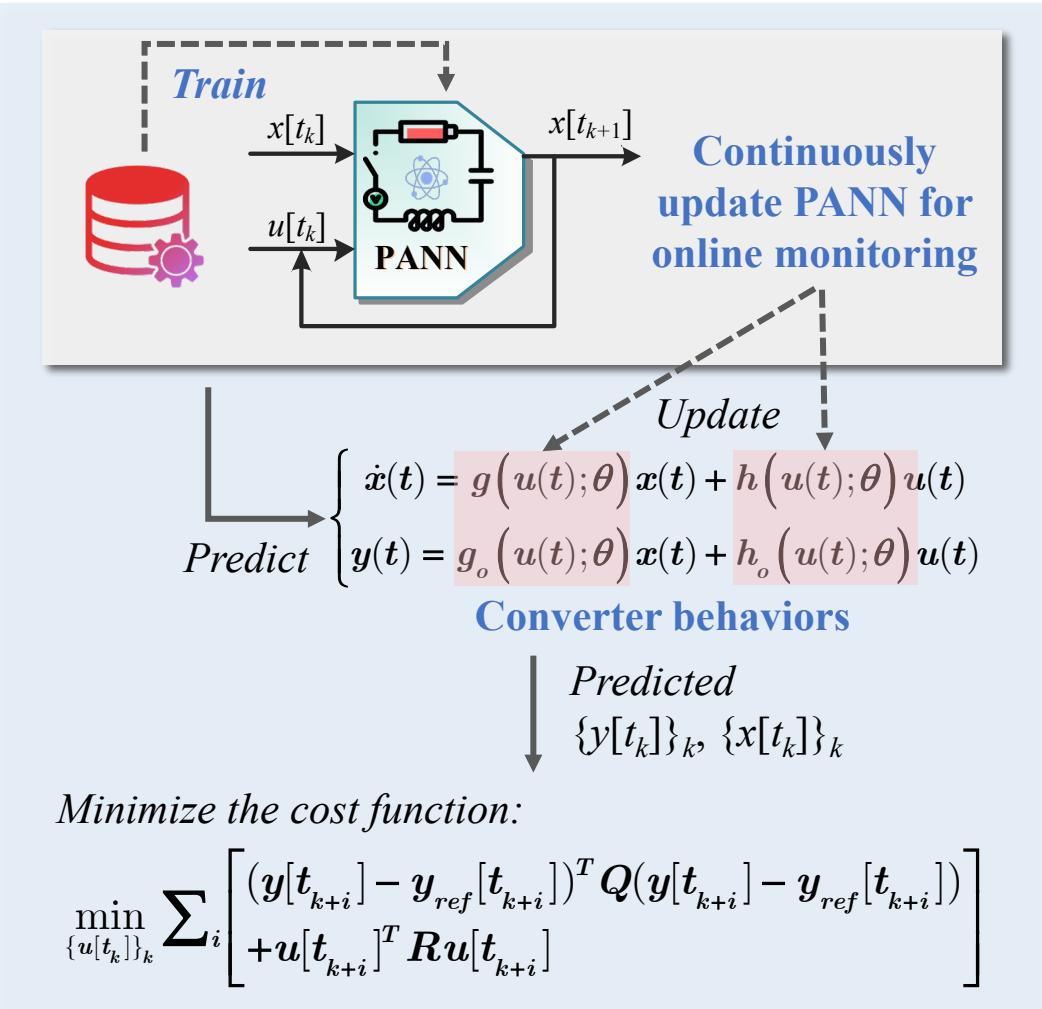
Extended Phase Shift (EPS) Modulation Insights: The EPS strategy is characterized by its ability to enhance zero-voltage switching (ZVS) across a broader range of operating conditions. In this strategy, the duty cycles are as follows: D_0 is set to 0.256, indicating the initial phase shift, and D_2 is maximized at 1, ensuring full utilization of the phase shift capability. The optimal duty cycle for D_1 is determined to be 0.628, which is a critical parameter for balancing power flow and minimizing switching losses. This configuration allows for eight switches to achieve ZVS, significantly reducing switching losses and enhancing efficiency. However, it does not facilitate zero-current switching (ZCS), which could further optimize performance by reducing current stress. At a power level of 500 W, the EPS strategy achieves a peak-to-peak current of 15.42 A, indicating a moderate level of current stress.

Optimal design insights

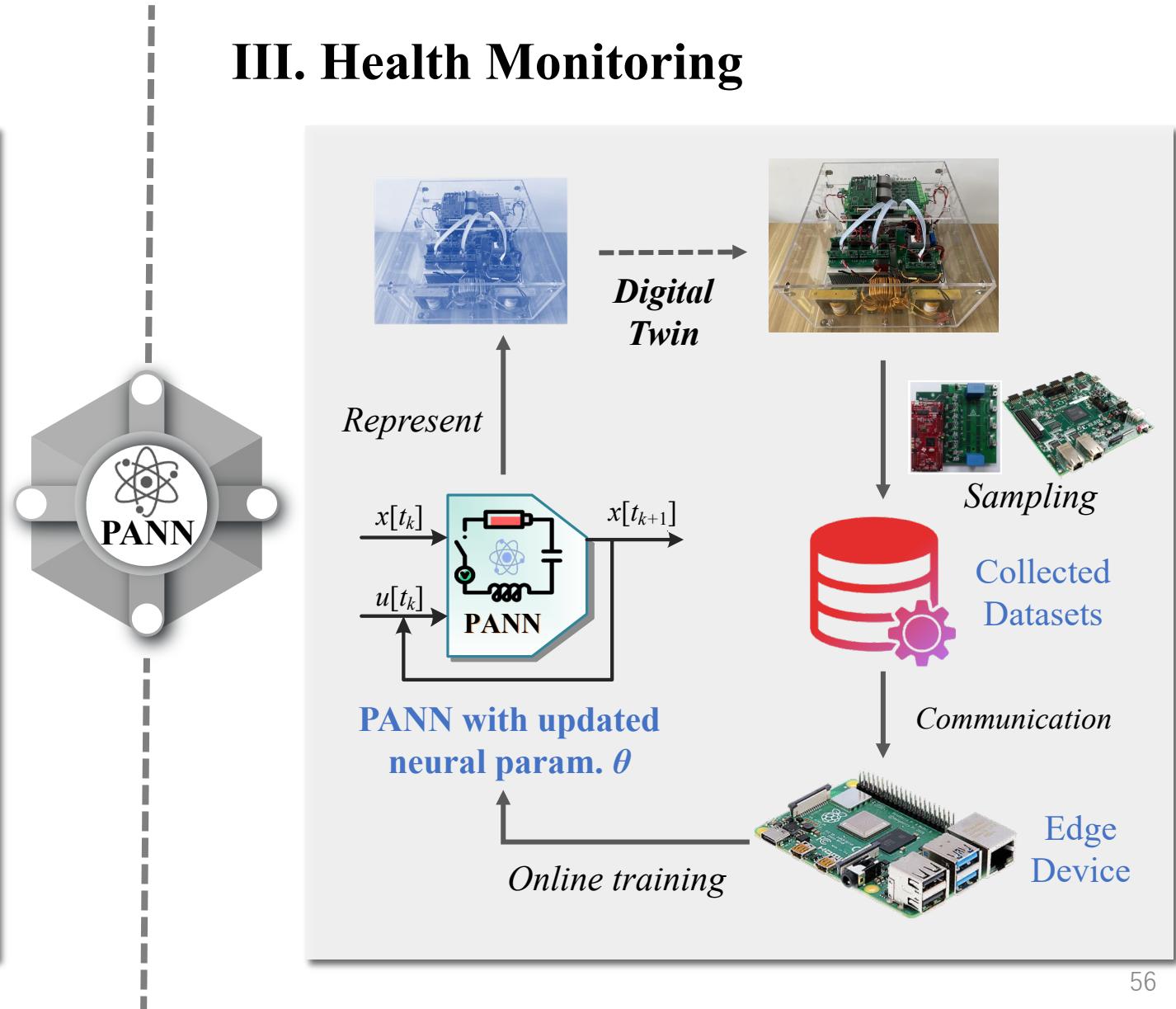


Training of PANN

II. Adaptive Control



III. Health Monitoring



Conclusions

PIML

PANN

Explainable

Light

Flexible

Future

- ◆ AI algorithms applied in PE require explainable, light, and flexible solutions
 - ◆ **PIML introduces physics expressed as PDEs into ML models to steer learning towards identifying physically consistent solutions**
 - ◆ PIMLs can solve PDEs in forward ways and identify parameters in inverse settings
 - ◆ **PIMLs are effective for ill-posed and mesh-free forward problems to solve PDEs and inverse problems to identify physical parameters.**
 - ◆ Three types of PIMLs: Physics-in-loss, physics-in-architecture, physics-in-initialization
-
- ◆ **Physically crafted recurrent networks to integrate PDEs discretized by numerical methods**
 - ◆ PANN features time-domain modeling for power electronics, where state variables are recurrently predicted leveraging the previous predictions of states
 - ◆ PANN is explainable in PE: Neural parameters of PANN are converter parameters, and switching behaviors, commutation loops, operating modes and so on are embodied
-
- ◆ Training only requires few data samples, reducing data requirements by 3 orders of magnitude
 - ◆ PANN is lightweight (several kB), deployable on edge devices
 - ◆ PANN training equals to parameter identification, and learning rates are impactful hyperparameters
-
- ◆ Flexible across operating conditions, modulation strategies, performance metrics, circuit parameters, and topological variants
 - ◆ Training-free transfer (use knowledge to replace data)
-
- ◆ Apply PANN in design automation, adaptive control, and health monitoring for power converters
 - ◆ More nuanced, accurate, and comprehensive PANN to incorporate more topologies

NEXT GENERATION OF AI FOR POWER ELECTRONICS:

The ultimate frontier of AI for power electronics is:

physics-informed AI.

Xinze Li

xinzel@uark.edu



2024 Nobel Prize - Physics
Awarded to AI Pioneers



Open Source



<https://github.com/XinzeLee>