

## ವಿಶ್ವೇಶ್ವರಯ್ಯ ತಾಂತ್ರಿಕ ವಿಶ್ವವಿದ್ಯಾಲಯ, ಬೆಳಗಾವಿ

## VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELGAUM

KARNATAKA, INDIA

## CONSOLIDATED STATEMENT OF MARKS

034251

B.E. (Electronics & Communication Engineering)

Name of the Student : NARAHARI GUDAGUDI

University Seat No: 1VK10EC030

Father's/ Mother's Name: GIRISH G

Year of Entrance :2010

Name of the College : VIVEKANANDA INSTITUTE OF TECHNOLOGY, BANGALORE

Year of Leaving : 2021

SUBJECTS	Exam Marks		IA Marks		Tot. Mark	
	Max.	Obtd.	Max.	Obtd.	Max.	Obtd
Semester	1			Lan		
1 Engineering Mathematics - I **	100	35	25	18	125	53
2 Engineering Chemistry	100	35	25	15	125	50
3 Computer Concepts & C Programming	100	43	25	20	125	63
4 Computer Aided Engineering Drawing	100	54	2.5	21	125	75
5 Basic Electronics	100	55	25	17	125	72
6 Computer Programming Lab	50	42	25	23	75	65
7 Engineering Chemistry Lab	50	26	25	19	75	45
8 Environmental Studies	50	24	25	22	75	46
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First Attempt Total: 414 / 775 70	Class o	f the S	eme	ster :		SC
1 Engineering Mathematics - II *****	100	35	25	15	125	50
2 Engineering Physics	100	35	25	16	125	51
3 Elements of Civil Engg. & Engg. Mechanics	100	51	25	16	125	67
4 Elements of Mechanical Engineering						
	100	50	25	15	125	65
5 Basic Electrical Engineering ***	100	39	25	15	125	54
6 Workshop Practice	50	33	25	20	75	53
7 Engineering Physics Lab	50	41	25	21	75	62
8 Constitution of India & Professional Ethics	50	35	25	18	75	53
First Attempt Total: 387 / 775 76	Class o	f the S	lama	ctor		SC
II Semester	1435 0	i the S	cinc	sici .		30
1 Engineering Mathematics-III	100	45	25	15	125	60
2 Analog Electronic Circuits **	100	35	25	16	125	51
3 Logic Design ***	100	35	25	15	125	50
4 Network Analysis	100	35	25	17	125	52
5 Electronic Instrumentation	100	45	25	17	125	62
6 Field Theory	100	35	25	18	125	53
7 Analog Electronics Lab	50	37	25	17	75	54
8 Logic Design Lab **	50	28	25	22	75	50
First Attempt Total: 380 / 900 76	Class o	f the S	Seme	ster		SC
V Semester						
1 Engineering Mathematics - IV ******	100	35	25	20	125	55
2 Microcontrollers	100	35	25	16	125	51
3 Control Systems ***	100	41	25	15	125	50
4 Signals & Systems	100	35	25	17	125	52
5 Fundamentals of HDL	100	45	25	17	125	62
6 Linear ICs & Applications *********						
	100	35	25	20	125	55
7 Microcontrollers Lab	50	20	25	16	75	30
8 HDL Lab		-	+	-	-	-

	SUBJECTS	Exam Marks		IA Marks		Tot. Marks	
		Max.	Obtd.	Max.	Obtd.	Max.	Obtd.
V Se	emester			aiii			
1	Management & Entrepreneurship	100	35	25	16	125	51
2	Digital Signal Processing	100	35	25	15	125	50
3	Analog Communication *****	100	35	25	15	125	50
4	Microwaves & Radar	100	38	25	18	125	56
5	Information Theory & Coding ****	100	35	25	15	125	50
6	Fundamentals of CMOS VLSI	100	44	25	16	125	60
7	DSP Lab.	50	22	25	15	75	37
8	Analog Communication Lab. + LIC Lab.	50	38	25	19	75	57
Fir	st Attempt Total 336 / 900 ;	Class	of the	sem	ester	:	SC
Л	Semester						
1	Digital Communication *******	100	38	25	15	125	53
2	Microprocessors ******	100	35	25	17	125	52
3	Microelectronics Circuits ********	100	35	25	15	125	50
4	Antennas & Propagation ********	100	43	25	20	125	63
5	Operating Systems	100	35	25	15	125	50
6	Satellite Communications	100	36	25	15	125	51
7	Advanced Communication Lab.	50	42	25	21	75	63
8	Microprocessor Lab.	50	30	25	15	75	45
Fir	st Attempt Total 337 / 900 ;	Class	of the	sem	ester	:	SC
VII	Semester						
1	Computer Communication Networks ***	100	35	25	20	125	55
2	Optical Fiber Communication **	100	35	25	17	125	52
3	Power Electronics	100	35	25	20	125	55
4	Embedded System Design	100	35	25	18	125	53
5	DSP Algorithms & Architecture **	100	35	25	19	125	54
6	Image Processing **	100	35	25	19	125	54
7	VLSI Lab.	50	26	25	20	75	46
8	Power Electronics Lab.	50	28	25	20	75	48
Fir	st Attempt Total 304 / 900 :	Class	of the	sem	ester	:	SC
VII	I Semester						
1	Wireless Communication	100	49	25	24	125	73
2	Digital Switching Systems	100	53	25	22	125	75
3	Network Security	100	35	25	22	125	57
4	GSM	100	47	25	22	125	69
5	Project Work	100	95	100	91	200	186
6	Seminar	0	0	50	47	50	47

Class of the Degree: Second Class

