SENIOR SOPHISTER ENGINEERING, 2018/19- ELECTRONIC AND COMPUTER ENGINEERING

Date of issue: 26th June, 2018

DAY	SEMESTER	0900 - 1000	1000 - 1100	1100 - 1200	1200 - 1300	1300 - 1400	1400 - 1500	1500 - 1600	1600 - 1700	1700 - 1800
MONDAY	First semester	4C16 [M20]	CS3421 [LB08]	4C7 [AP4.03] / CS4400 [LB04]	4C4 [LB04]		4C4 [LB08]	CS4052 [LB04]	4C7 [M21]	4E1 [HAM4] to 7pm
MONDAY	Second semester	4B9 tutorial [M17]	4C1 [CLT]	CS7434 [LB01]	4C15 [M21]	4C8 [LB08]		4C2 [PBSR1]	CS4407 [LB04]	
TUESDAY	First semester	4C5 [M17]	CS4053 [LB08]	CS4052 [LB04/ICTLAB1/ICTLAB2]		CS4053 [LB04 and ICTLAB1]		CS4404 [HAM3]	4E1 [HAM4]	
TUESDAY	Second semester	4C8 [CADLAB]		CS4D2B [M21]	4C15 [M21]	4B9 [LB04]	4B9 laboratories [MECH LAB] / 4C2 [PBSR1]		4B9 [2037]	
WEDNESDAY	First semester	4C16 [CADLAB]			4C5 [M20] / CS4404 [HAM4]	CS4053 [LB01 and ICTLAB1]	CS4D2A [LB01]	CS3421 [M17]	4C16 [M21]	CS4404 (weeks 3 - 6, 9, 10) [LB04]
WEDNESDAY	Second semester	CS4D2B [M20]	4C15 [AP2.04]	4B9 [2039]	4C2 [AP2.04] / CS7434 [LB01]	CS7434 [LB01 and ICTLAB2]	CS4D2B [LB01]	4C1 [M17] / CS4405 [LB08]	4C2 [PBSR2]	
THURSDAY	First semester	4C7 [M20]	CS3421 [LB08]	4C16 [LTEE2]	4C4 [LB04]		4C7 [M17] / CS4400 [LB08]		CS4D2A [LB08]	
THURSDAY	Second semester	CS4407 [LB01]	4C15 [M17]	4C8 [M21]	4C1 laboratories [CADLAB]		CS4405 [LB01]		CS4D2B [LB08]	
FRIDAY	First semester	Electronic Engineering laboratories/projects (C and CD stream) [EE LABS]					4C5 [M20]			
FRIDAY	Second semester	Electronic Engineering laboratories/projects (C and CD stream) [EE LABS]					4C8 [LTEE2]	CS4406 [LB01]		

Engineering School Office

SS(Eng)Timetable1819_CCDD.xlsx

Module Codes

4E1 = CEU44E01 Management for Engineers [5 credits]
4E2 = EEU44E02/CS4E2 Electronic/Computer
    Engineering Project [15 credits]*
4E4 = EEU44E04/CS4E04 Engineering Project
    Internship [30 credits]

4B9 = MEU44B09 Control Engineering I [5 credits]

4C1 = EEU44C01 Integrated Systems Design [5 credits]
4C2 = EEU44C02 Microelectronics [5 credits]
4C4 = EEU44C04 Next Generation Networks [5 credits]
4C5 = EEU44C05 Digital Signal Processing [5 credits]
4C7 = EEU44C07 Information and Communication
    Theory [5 credits]
4C8 = EEU44C08 Digital Image and Video
    Processing p[5 credits]
4C15 = EEU44C15 Analogue Signal Processing [5 credits]
4C16 = EEU44C16 Deep Learning and
    its Applications [10 credits]

CS3421 = CS3421 Computer Architecture II [5 credits]
CS4D2A = CS4D2A Information Management II [5 credits]
CS4D2B = CS4D2B Knowledge Engineering [5 credits]
CS4052 = CS4052 Computer Graphics [5 credits]
CS4053 = CS4053 Computer Vision [5 credits]
CS4400 = CS4400 Internet Applications [5 credits]
CS4404 = CS4404 Machine Learning with Applications
    in Data Analytics [5 credits]
CS4405 = CS4405 Optimisation Algorithms for
    Data Analysis [5 credits]
CS4406 = CS4406 Data Visualisation [5 credits]
CS4407 = CS4407 Security and Privacy [5 credits]
CS7434 = CS7434 Augmented Reality [5 credits]

Venues

2037 = Robert Emmet Lecture Theatre, Arts Building
2039 = JM Synge Theatre, Arts Building
AP2.04 = Room 2.04, Second Floor, Aras an Phiarsaigh
AP4.03 = Room 4.03, Fourth Floor, Aras an Phiarsaigh
CADLAB = Room 2.28, Second Floor, Aras an Phiarsaigh
CLT = Crossland Lecture Theatre, Parsons Building
EE LABS = Electronic Engineering Labs, Aras an Phiarsaigh
HAM3 = MacNeill Lecture Theatre, Hamilton Building
HAM4 = Joly Lecture Theatre, Hamilton Building
ICTLAB1 = ICT Laboratory 1, Ground Floor, ICT Hut
ICTLAB2 = ICT Laboratory 2, First Floor, ICT Hut
LB01 = Lecture Theatre 01, Lower Basement, Lloyd Institute
LB04 = Lecture Theatre 04, Lower Basement, Lloyd Institute
LB08 = Lecture Theatre 08, Lower Basement, Lloyd Institute
LTEE2 = Lecture Theatre 2, Lower Basement, Panoz Institute
M17 = Museum 17, First Floor, Museum Building
M20 = Museum 20, First Floor, Museum Building
M21 = Museum 21, First Floor, Museum Building
MECH LAB = Mechatronics Laboratory, Parsons Building
PBSR1/2 = Seminar Room 1/2, Parsons Building

Semester dates:

First semester: Monday, 10th September, 2018 to Friday, 30th November, 2018
Second semester: Monday, 21st January, 2019 to Friday, 12th April, 2019

Project/Assignment Weeks:

First semester: Monday, 22nd November, 2018 to Friday, 26th November, 2018
Second semester: Monday, 4th March, 2019 to Friday, 8th March, 2019

Draft examination dates (to be confirmed):

Semester 1 examinations: Saturday, 8th December, 2018 to Friday, 14th December, 2018
Semester 2 examinations: Tuesday, 23rd April, 2019 to Friday, 3rd May, 2019
Supplemental examinations: Monday, 26th August, 2019 to Saturday, 31st August, 2019

Electronic Engineering - Module Choices

In semester 1, all students must take 4E1, 4C5 and 4C16 and two further modules from CS3421, 4C4 and 4C7. In semester 2, those students taking the project must also take three modules from 4B9, 4C1, 4C2, 4C8 and 4C15 unless taking the internship.

Electronic/Computer Engineering - Module Choices

In semester 1, all students must take 4E1, 4C5 and 4C16 and a further two modules from CS3421, CS4D2A, CS4052, CS4053, CS4400, 4C4 and 4C7. In semester 2, those students taking the project must also take three modules from CS4D2B, CS4407, CS7434, 4C1, 4C8 and 4C15 unless taking the internship. Students must choose a total of at least two CS-coded modules from the optional modules (including those students intending to take the internship).

Computer Engineering - Module Choices

In semester 1, all students must take 4E1 and five further modules from CS3421, CS4D2A, CS4052, CS4053, CS4400, CS4404, 4C4 and 4C16. In semester 2, those students taking the project must also take three modules from CS4D2B, CS4405, CS4406, CS4407 and CS7434 unless taking the internship.

* For students who are completing their studies in the Senior Sophister BAI year, they MUST undertake a Computer/Electronic Engineering Project - the option of taking an Internship is only available to students proceeding to the MAI year 5.