Project Title

CS 16L2 Mini Project

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B. Tech Computer Science & Engineering

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MARCH 2015

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C E R T I F I C A T E

This is to certify that, this report titled ***Project Title*** is a bonafide record of the work done by

rn1 name1
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rn3 name3

Sixth Semester B. Tech Computer Science & Engineering

students, for the course work in **CS 16L2 Mini Project**, under our guidance and supervision, in partial fulfilment of the requirements for the award of the degree, B. Tech Computer Science and Engineering of **Cochin University of Science & Technology**.

Guide

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Acknowledgments

blah blahhhh

blah blah

team member name1
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Abstract

This project is an attempt to implement a distributed computing environment so as to demonstrate its contribution to the ever alive need for more computing power. The implementation is based on the Parallel Virtual Machine(PVM) model. The essentials of PVM as required by the programs in this project are covered from a practical approach. Algorithms to solve the following problems are used to demonstrate the concepts covered. .....

Contents

Chapter 1

Introduction

Add sufficient sections
include overview/motivation

1.1

Chapter 2

Literature Survey

2.1 Existing Systems

.....

2.1.1 Limitations of Existing Systems

- ....
- .....

2.2 Proposed System

.....

2.2.1 Advantages of Proposed System

- ....
- .....

Chapter 3

Proposed System

3.1 Problem Statement

objective/the problem

3.2 Proposed Solution

the solution

Input: input here

Output: output here

Chapter 4

Software Requirement Specification

4.1 Introduction

4.1.1 Purpose

purpose of doc

4.1.2 Intended audience

...

4.1.3 Project Scope

.....

4.1.4 Design and Implementation Constraints

.....

4.1.5 Assumptions and Dependencies

.....

4.2 Functional and Non-Functional Requirements

4.2.1 Functional Requirements

..... detailed description about each requirement

4.2.2 Non Functional Requirements

..... detailed description about each requirement

4.3 Hardware & Software Requirements

4.3.1 Hardware Requirements

- ...
- ...

4.3.2 Software Requirements

- ...
- ...

Chapter 5

System Design

5.1 Block Diagrams

include figure if any. Else remove this subsection

5.1.1 Block Diagram of Overall System

include figure if any. Else remove this subsection

5.1.2 Modular Division

include figures if any.Else remove this subsection

5.2 Dataflow Diagrams

include DFD LEVEL 0,LEVEL 1 etc

5.3 Usecase Diagrams

include figure if any. Else remove this subsection

5.4 Class Diagrams

include figure if any. Else remove this subsection

5.5 Algorithms

details about algorithm if you have any. else remove this section

5.5.1 Name of Algorithm1

Purpose and algorithm description in correct format.

5.6 Database Design

Provide the database design here like DB table, its description etc.

5.6.1 Database Tables

DB table details

5.6.2 ER Diagram

include diagrams with proper titles

Chapter 6

Implementation

6.1 Overview of Technologies Used

6.1.1 Technology1

.....

6.1.2 Technology2

..... etc.....

6.2 Testing

6.2.1 Types of Testing

.....

6.2.2 Test Plan

.....

6.3 Results

Chapter 7

Conclusion

May contain subheadings such as

7.1 Conclusion

7.2 Future Scope

References

[1] Qian Xu, Srenivas Varadarajan, Chaitali Chakrabarti, and Lina J. Karam, "A Distributed Canny Edge Detector: Algorithm and FPGA Implementation", *IEEE Transcation on Image Processing*, VOL. 23, NO. 7, July 2014.

[2] "Extensible Markup Language (XML)"[Online].
Available: $http://www.w3.org/TR/xml/$