



This is the base multiple of 8-Bit Shift register circuit to run addressable RGB LEDs in an array. The reason that this uses a multiple of 3 registers is that the LEDs have 3 legs so and the only multiple of 8 that 3 divides into evenly is 24 and therefore to make the shifting nicer each module added to the device should be a multiple of 3 registers and a multiple of 8 LEDs

Sheet:1/ File: Device Schematic.sch	
<b>Title:</b> Addressable RGB Night Light using CRGB LEDs and Shift Registers (1 Set of 8 LEDs)	
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