



This is the diagram for the 8-Segment display that is driven by the project in this folder, this is showing how to connect 2 registers together and which lines are shared, in theory this can be extend to n displays by chaining the overflow from each previous chip. The HC595 is a 100MHz chip so for low speed applications such as switching displays, this chip is a great choice

Sheet: /

File: Device Schematic.sch

Title: 5161AS 8 segment displays driven by HC595 8 bit registers

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