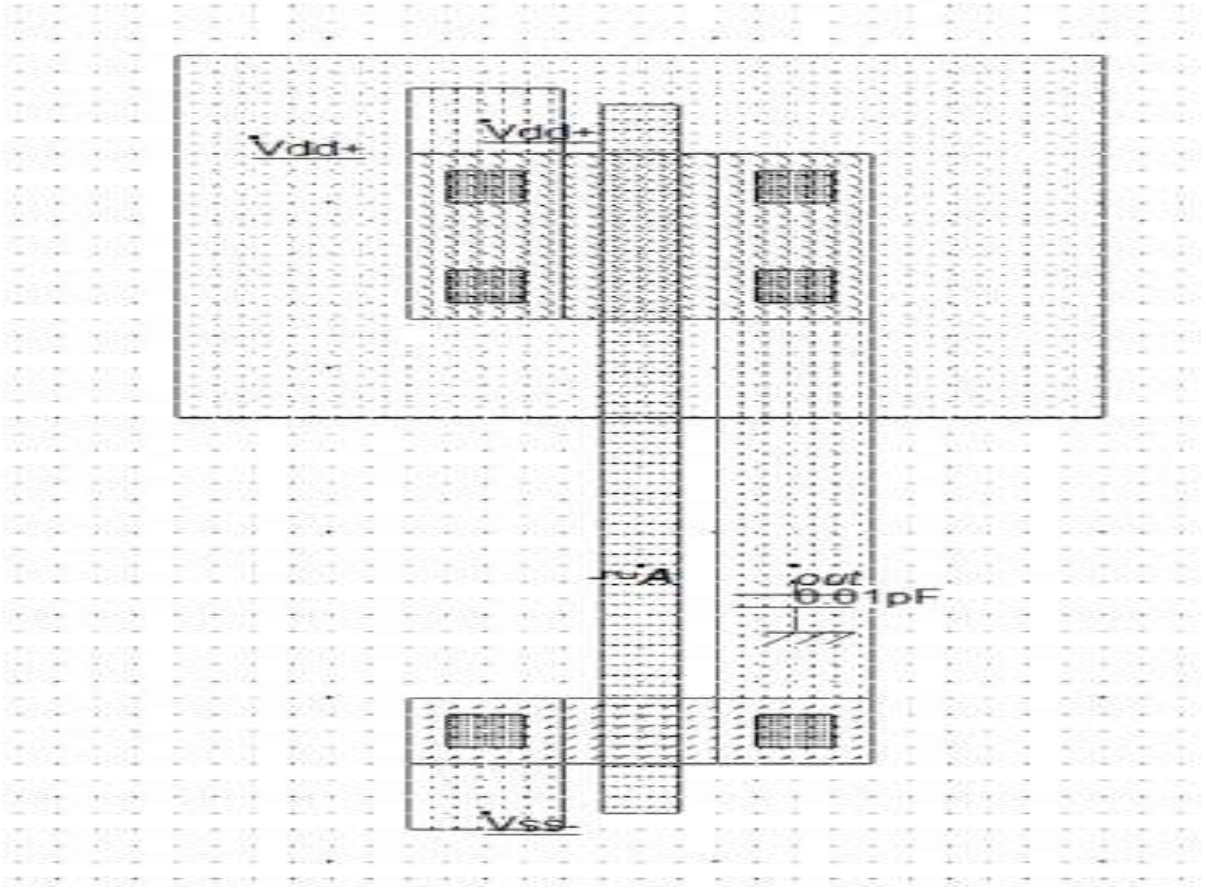
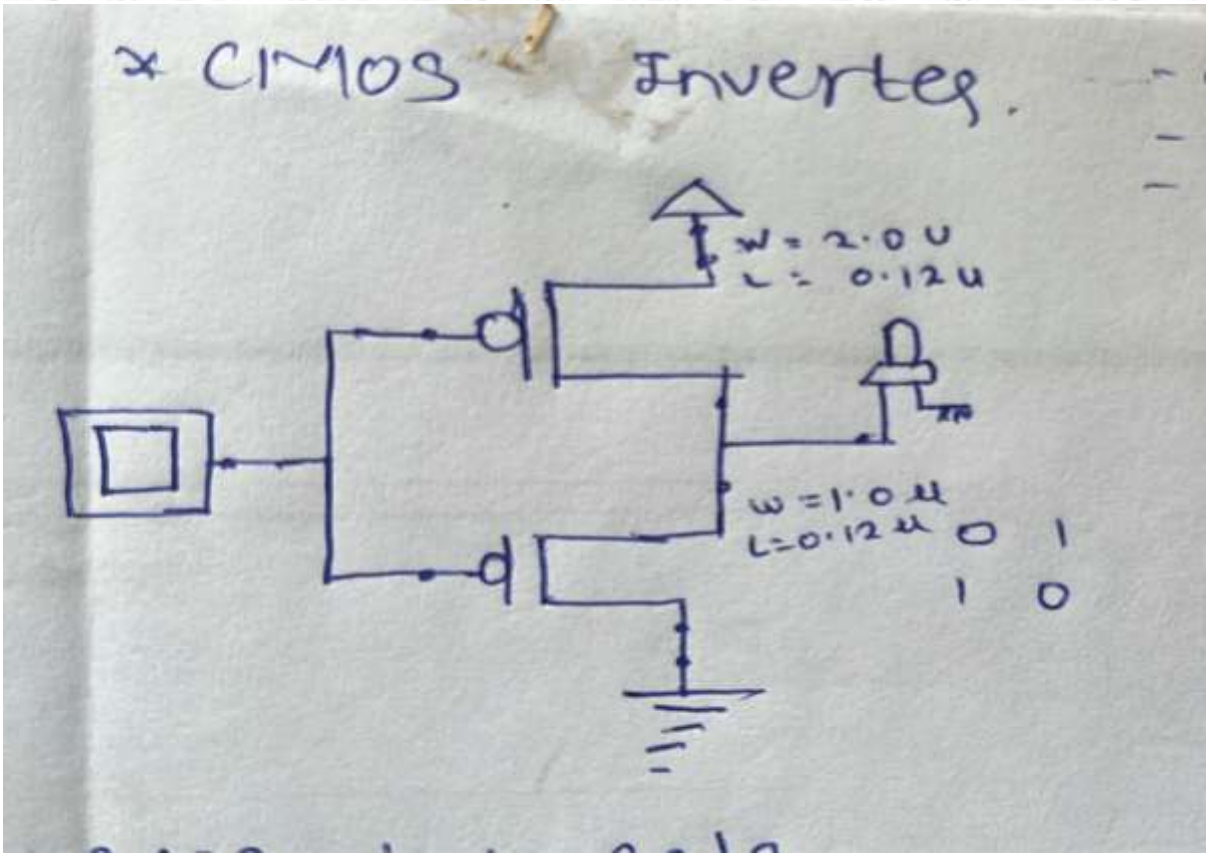
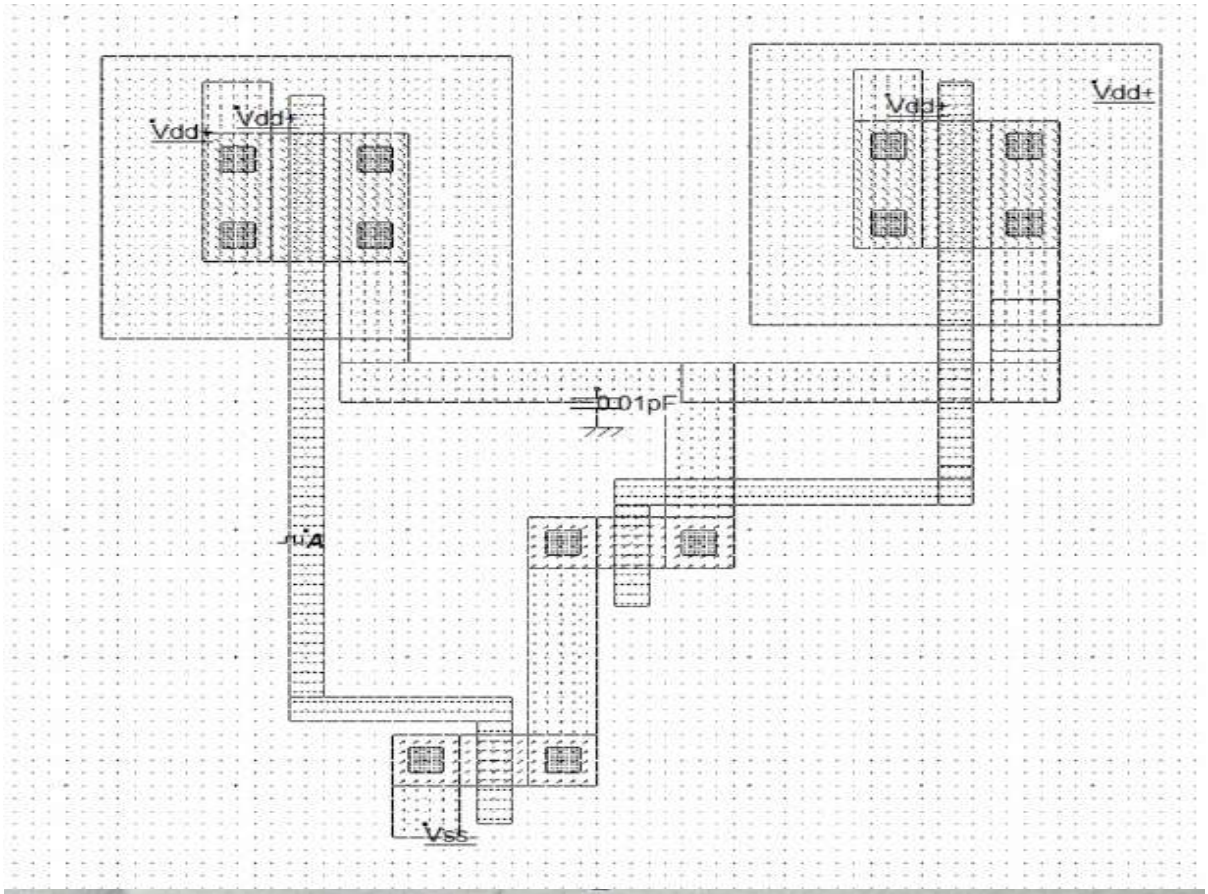
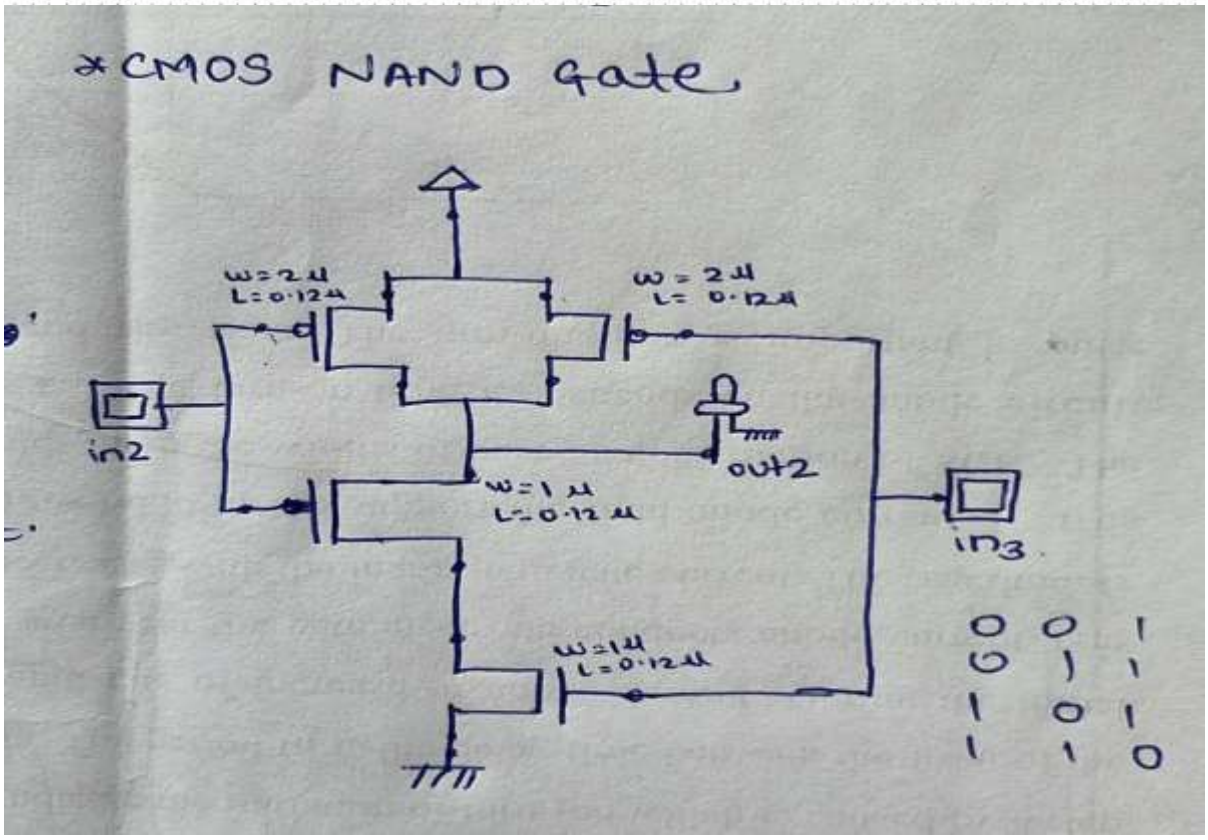
Inverter


Vdd+
Vdd+
-vA
out
0.01pF
Vss

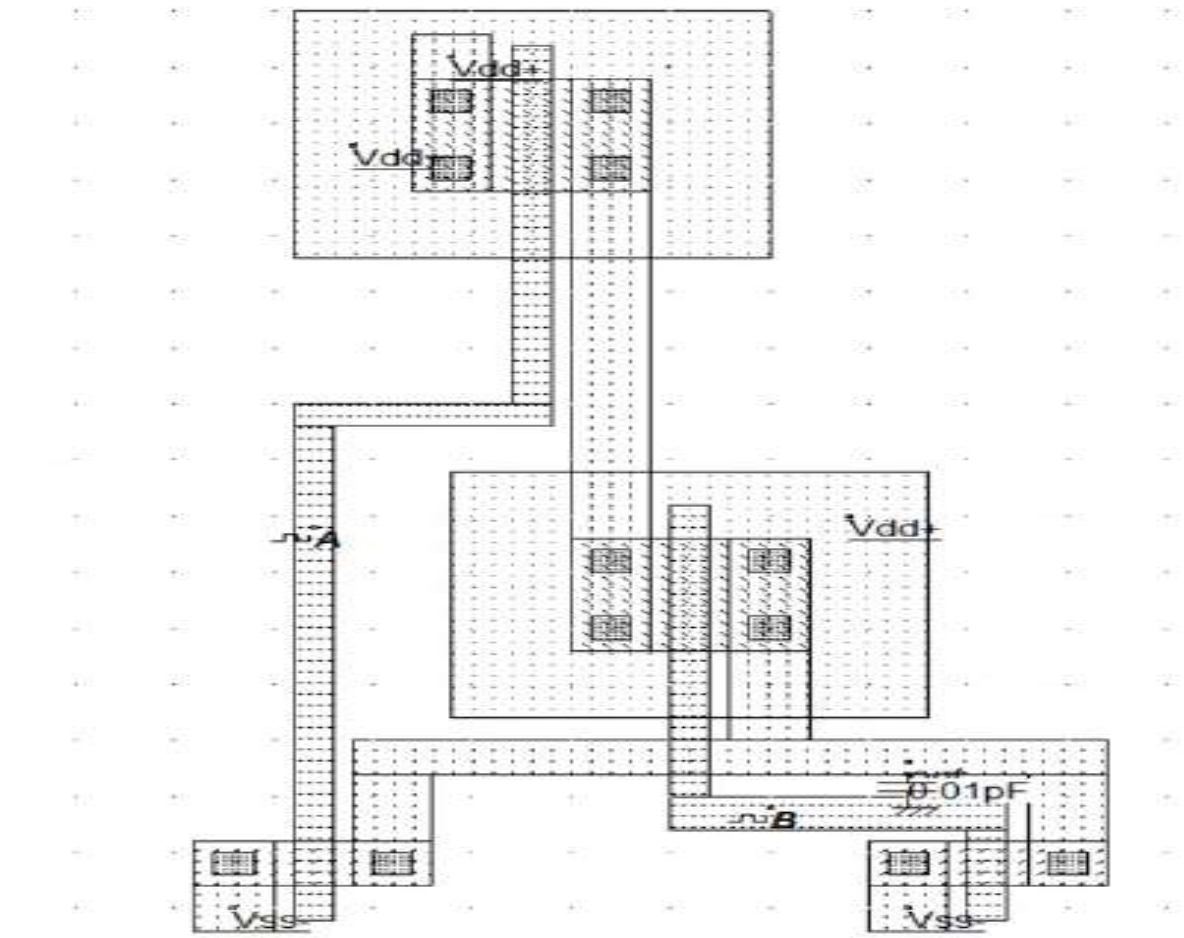

* CMOS Inverter
W = 2.0u
L = 0.12u
W = 1.0u
L = 0.12u
0 1
1 0

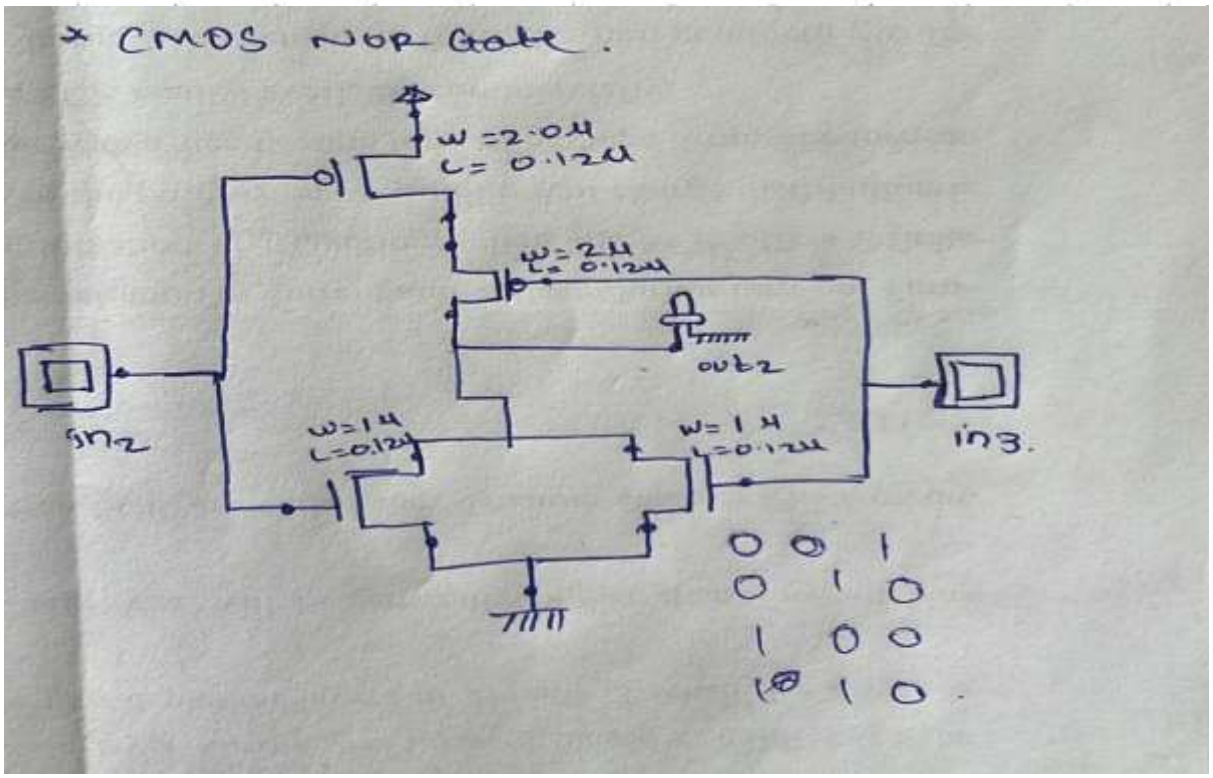
Nand


Vdd+
Vdd+
Vdd+
Vdd+
0.01pF
Vss


*CMOS NAND Gate
W=2μ
L=0.12μ
W=2μ
L=0.12μ
in2
out2
W=1μ
L=0.12μ
in3
W=1μ
L=0.12μ
0 0 1
0 1 1
1 0 1
1 1 0

NOR


Vdd+
Vdd+
Vdd+
A
B
0.01pF
Vss
Vss


* CMOS NOR Gate.
W=2.0u
L=0.12u
W=2u
L=0.12u
out2
in2
W=1u
L=0.12u
W=1u
L=0.12u
in3.
0 0 1
0 1 0
1 0 0
1 0 .