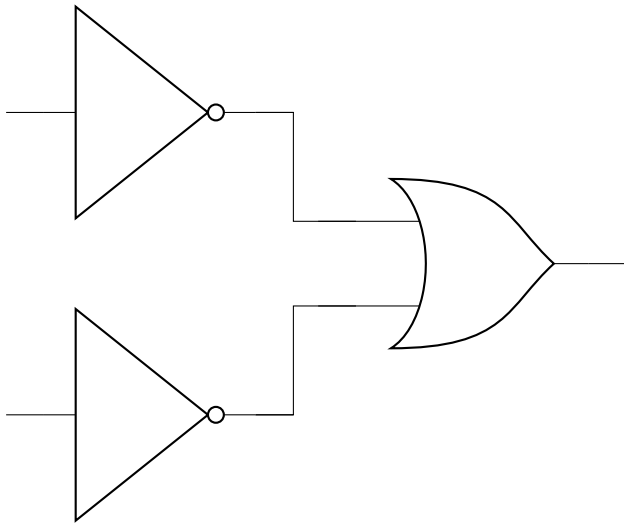


# Assignment

January 8, 2024

## Gate question



1. The above combination of logic gate represent the operation
  - (a) OR
  - (b) NAND
  - (c) AND
  - (d) NOR