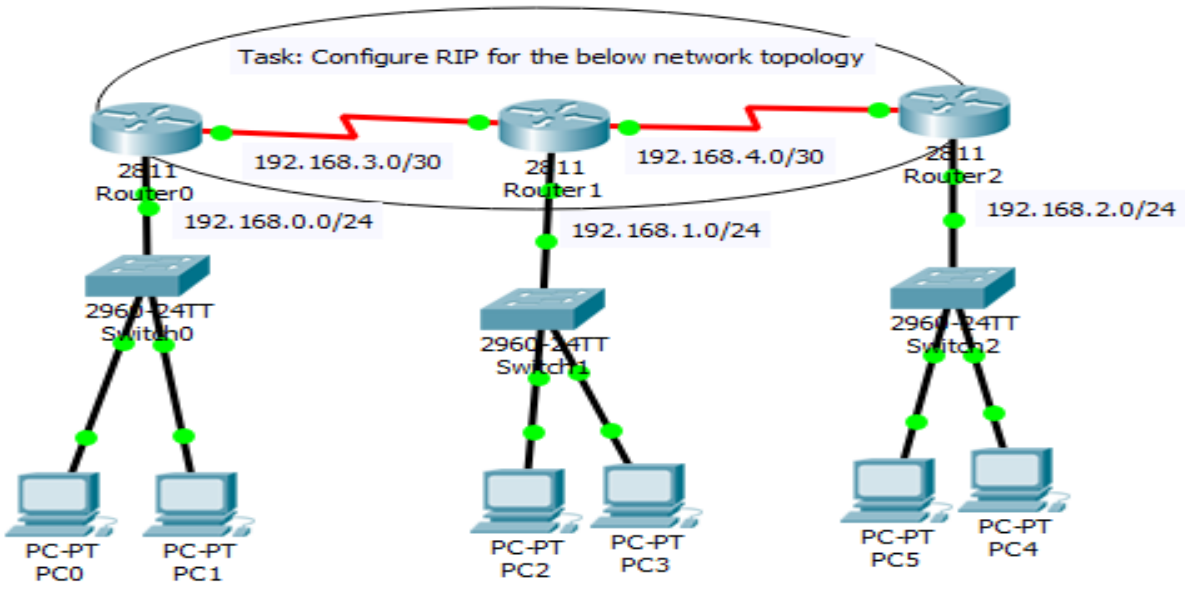
## Experiment-1

1 a) Write a C Program that simulates FIFO CPU scheduling Algorithm.

b) Implement routing information protocol (RIP) using CISCO packet tracer tool.




**Note: Use the above Topology and Addresses for Implementation**

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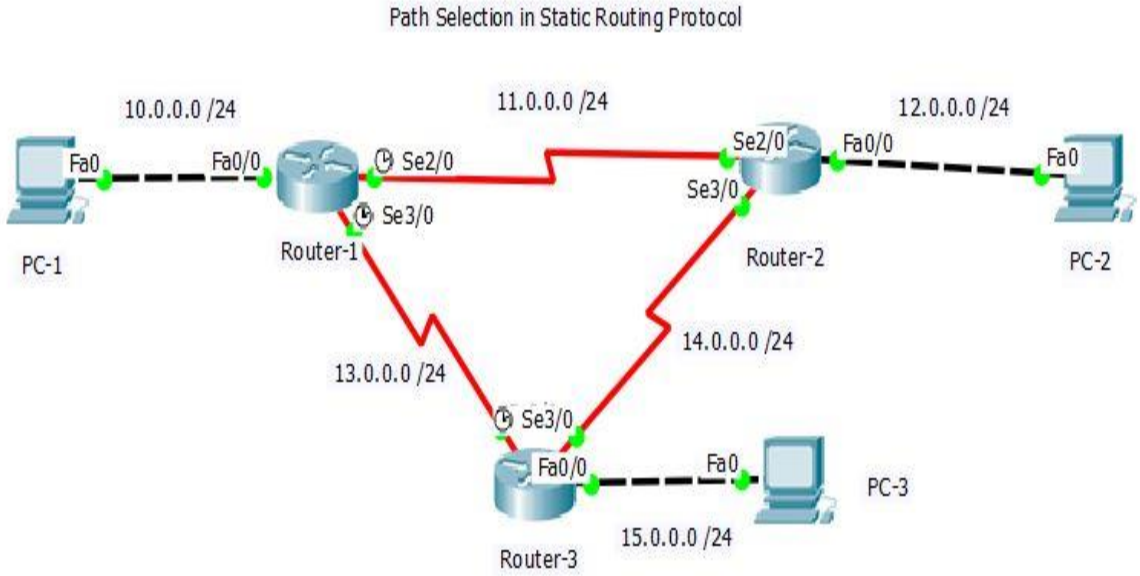
## Experiment-2

2 a) Write a C program to stimulate Producer-Consumer Problem.

b) Implement Static routing Protocol Using Cisco Packet tracer tool.

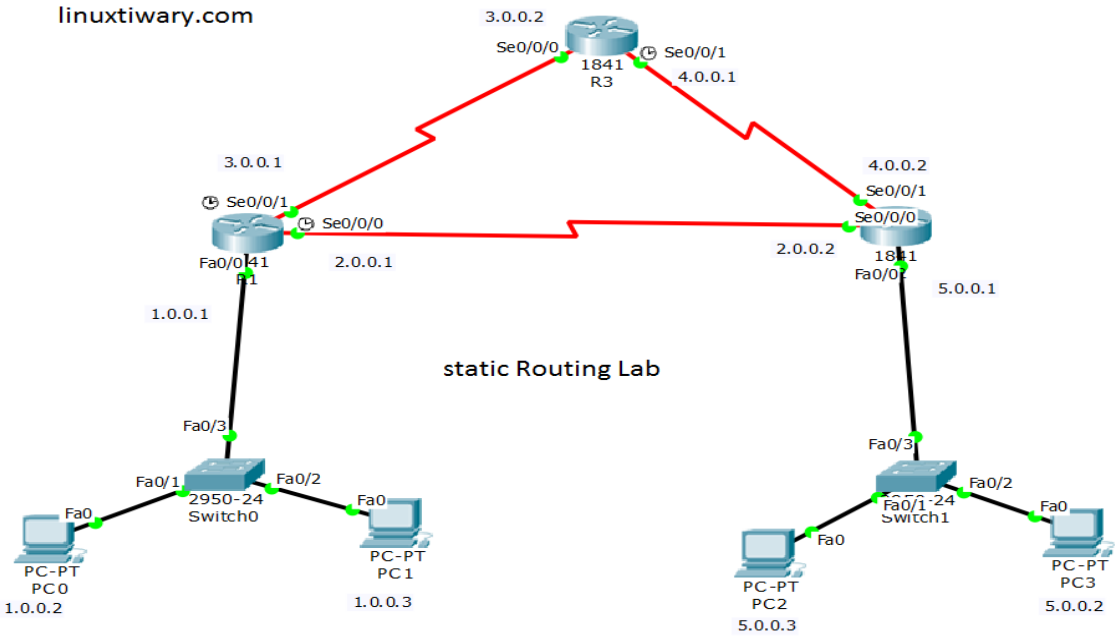



**Note: Use the above Topology and Addresses for Implementation**

## Experiment-3

3 a) Write a C Program that simulates LRU page replacement scheduling Algorithm.

b) Implement Static routing Protocol Using Cisco Packet tracer tool.




**Note: Use the above Topology and Addresses for Implementation**

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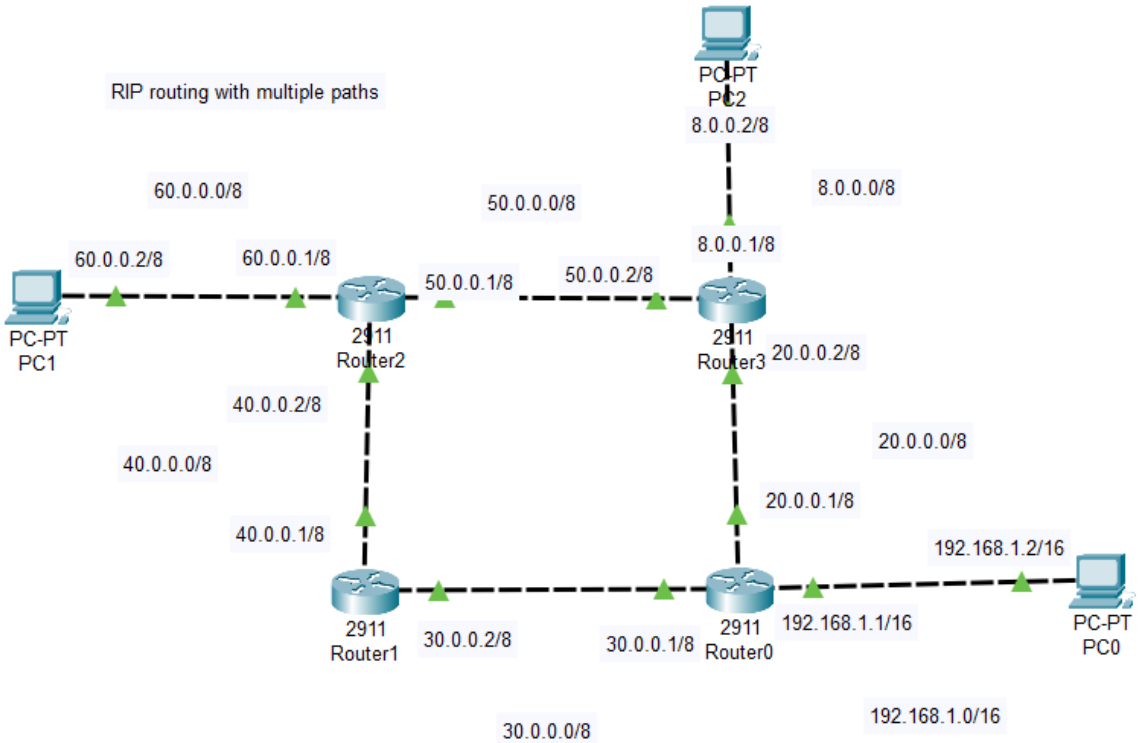
## Experiment-4

4 a) Implement routing information protocol (RIP) using CISCO packet tracer tool.



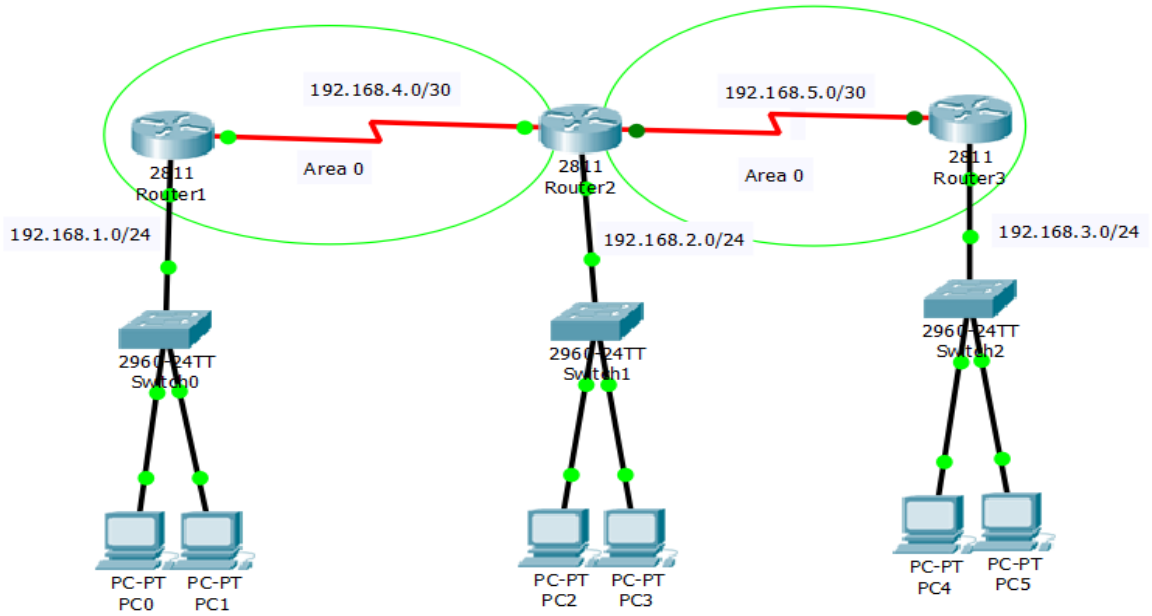

b) Write a Program to stimulate the SSTF Disk scheduling Algorithms.

**Note: Use the above Topology and Addresses for Implementation**

## Experiment-5

5 a) Write a C Program to implement Dead Lock Avoidance Banker's Algorithm.

b) Implement OSPF using CISCO packet tracer tool.




**Note: Use the above Topology and Addresses for Implementation**

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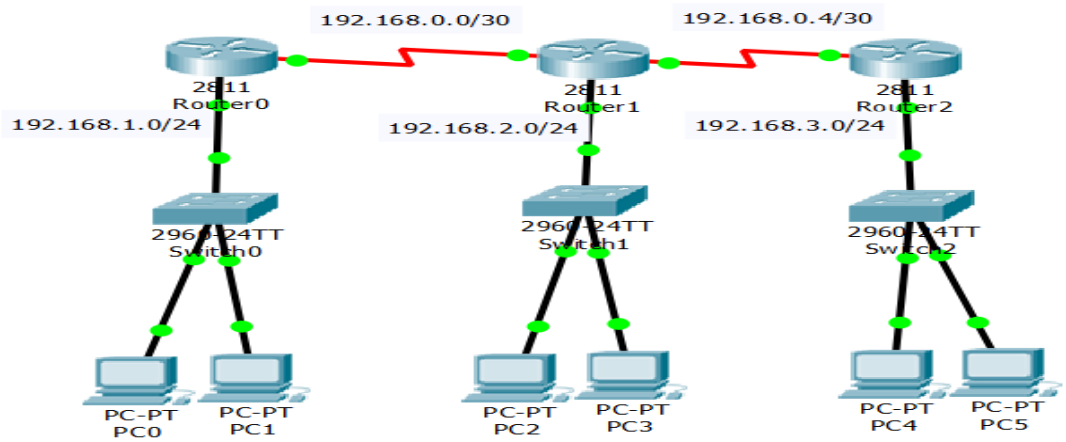
## Experiment-6

6 a) Write a C program to stimulate Priority CPU Scheduling Algorithm.

b) Implement Static routing Protocol Using Cisco Packet tracer tool.

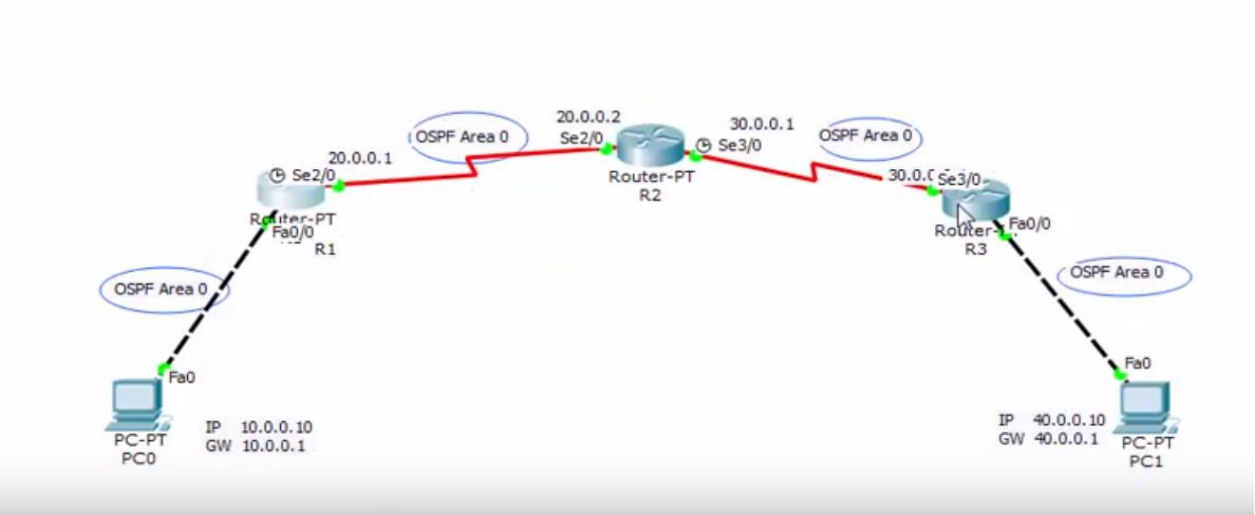



**Note: Use the above Topology and Addresses for Implementation**

## Experiment-7

7 a) Write a C Program that simulates Optimal page replacement scheduling Algorithm.

b) Implement OSPF protocol Using Cisco Packet tracer tool.




**Note: Use the above Topology and Addresses for Implementation**

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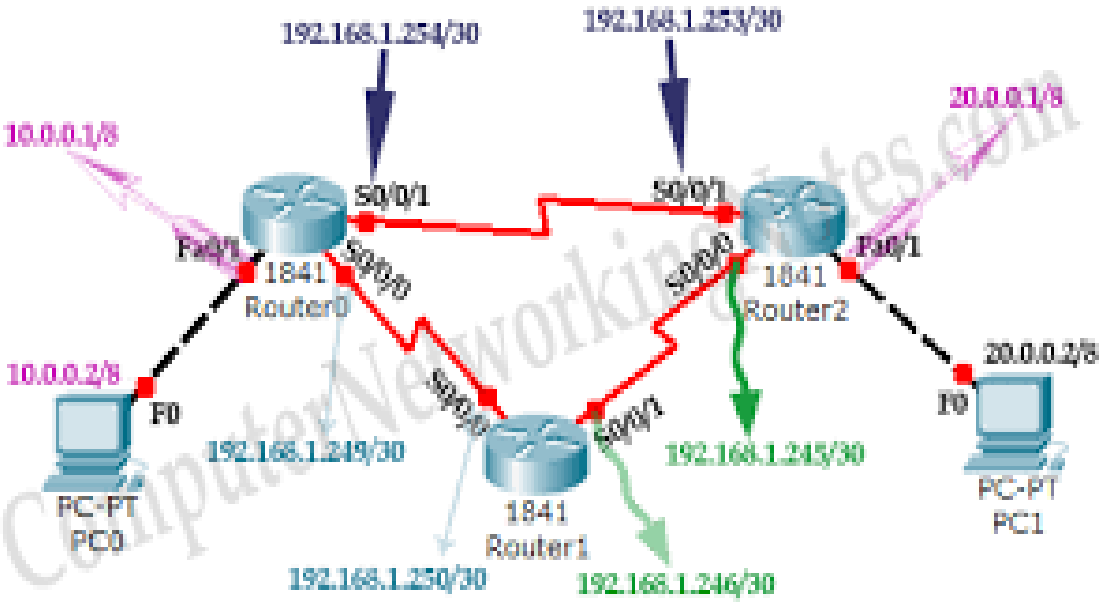
## Experiment-8

8 a) Write a C Program to stimulate MVT and MFT.

b) Implement routing information protocol (RIP) using CISCO packet tracer tool.




**Note: Use the above Topology and Addresses for Implementation**