## MODEL QUESTION PAPER

		,		 	
Hall Ticket No.:					

SRIT R20

## SRINIVASA RAMANUJAN INSTITUTE OF TECHNOLOGY

(AUTONOMOUS)

III B. Tech I Sem – Semester End Examinations – Regular – Dec 2022

## OPERATING SYSTEMS [R204GA05503]

(Computer Science and Engineering)

Time: 3 hours Max. Marks: 60

## **PART-A**

(Compulsory Question)

1		Answer the following: $(10 \times 02 = 20 \text{ Marks})$					
1	0)	Draw State Diagram of a Process.					
	a)	Make a comparison between the process and the	woods				
	b)	List methods to implement LRU Page Replace					
	c)	1					
	d)	Compute the average latency of a disk spindle					
	e)	What are the main differences between capabil	ity lists and access lists				
		(Answer all five units, 5					
		UNIT-1					
2	a)	Explain how operating systems are used in a vari	ety of computing environments	5M]			
	b)	Explain about the dual mode operation in OS with a neat block diagram.					
		(OR)					
3	Exp	lain in detail the role of Operating system as a res	ource Manager. [1	0M]			
		UNIT-2					
4	Wha	at is Semaphore? How can we achieve the synchr	onization using semaphore for [1	0M]			
		lucer consumer problem?					
		(OR)	'				
5	Con	sider the following four processes, with the lengt	n of the CPU burst time given in [1	0M]			
	mill	iseconds.					
	Proc	cess Arrival Time(ms) Burst T	ime (ms)				
	P1	1	6				
	P2		5				
	P3	3 2	5				
	P4	4 2	3				
		Average Waiting Time and Turnaround time for orithms?	given Process using FCFS and SJF				
		UNIT-3					
6	Wha	at is Thrashing? Explain the Causes of Thrashing.		0M]			
		(OR)					
7	Wha	at is the need of Page Replacement? Consider the	following reference string [1	0M]			
	-	, 1, 2, 0, 3, 0, 4, 2, 3, 0, 3, 2, 1, 2, 0, 1, 7, 0, 1.		-			
	Find	the number of Page Faults with FIFO, Optimal I	Page replacement and LRU with				

	four frames which are empty initially. Which algorithm gives the minimum number of page faults?	
	UNIT-4	
8	Explain the following disk scheduling algorithm with proper diagram	[10M]
	a) FCFS	
	b) SSTF	
	c) SCAN	
	d) LOOK	
	e) C-SCAN.	
	(OR)	
9	Explain How I/O requests are transformed to Hardware Operations	[10M]
	UNIT-5	
10		[10]
10	Describe the principles of protection. Explain the access matrix in detail.	[10M]
	(OR)	
11	Illustrate encryption methods with suitable scenarios.	[10M]

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