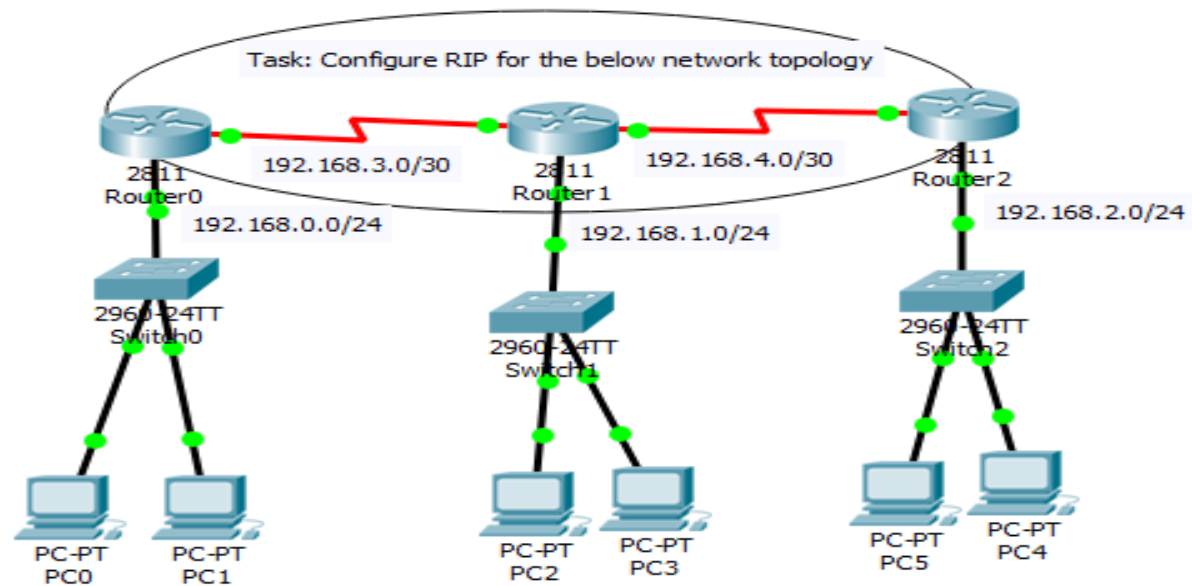


Experiment-1

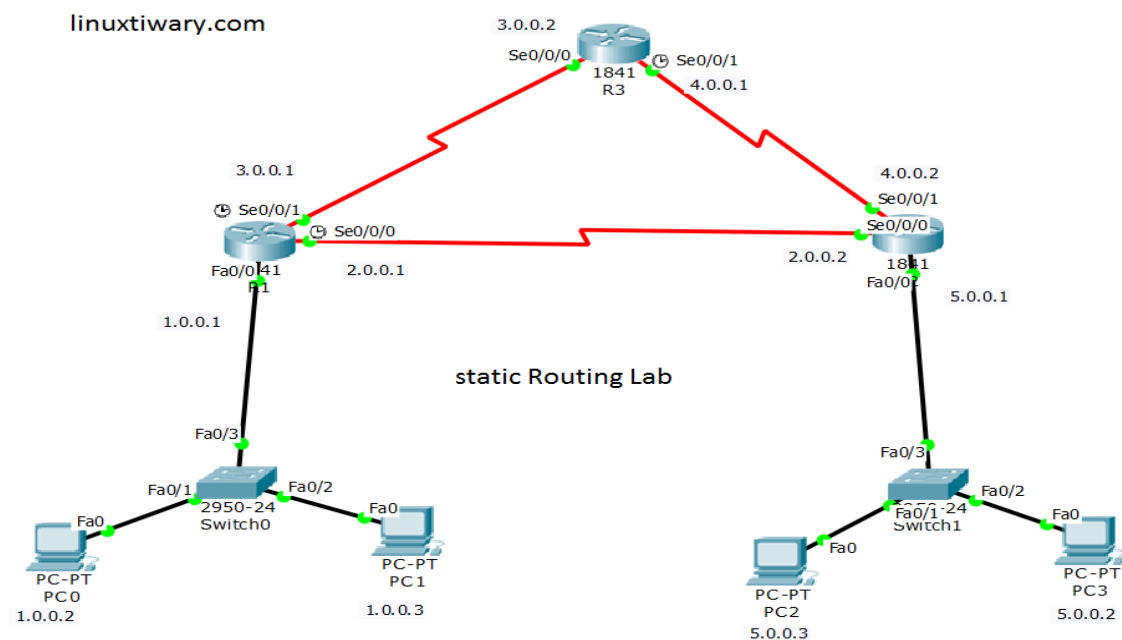
- 1 a) Write a C Program that simulates Shortest Job first CPU scheduling Algorithm.
(OR)
- b) Write a C program to stimulate Producer-Consumer Problem.
- 2 a) Implement routing information protocol (RIP) using CISCO packet tracer tool.
(OR)
- b) Implement Static routing Protocol Using Cisco Packet tracer tool.



Note: Use the above Topology and Addresses for Implementation

Experiment-2

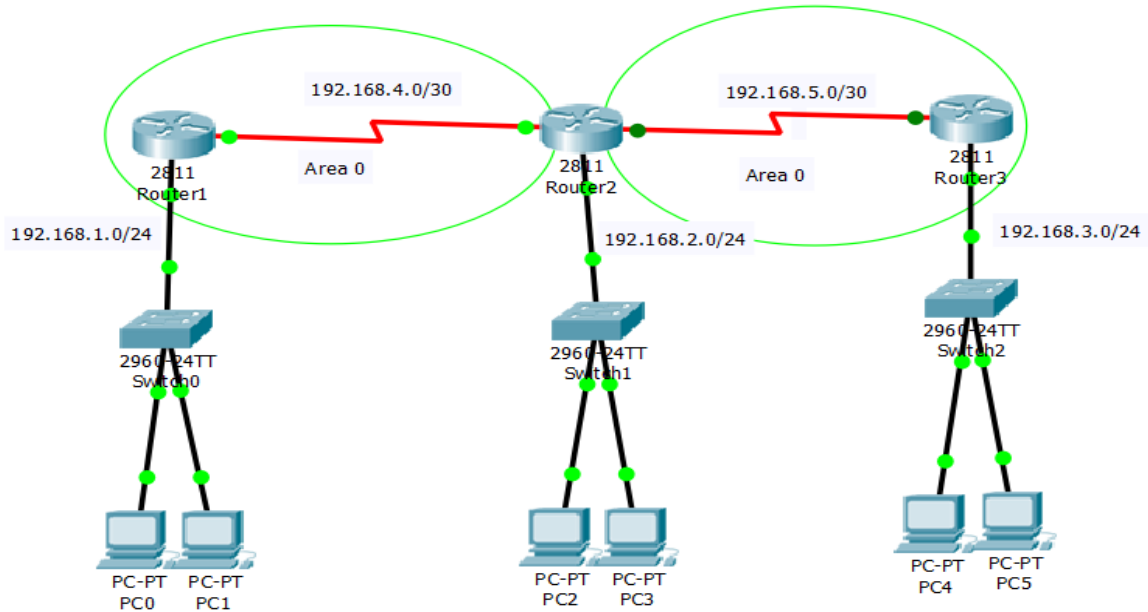
- 1 a) Write a C Program that simulates Optimal page replacement scheduling Algorithm.
(OR)
- b) Write a Program to stimulate the FCFS Disk scheduling Algorithms.
- 2 a) Implement routing information protocol (RIP) using CISCO packet tracer tool.
(OR)
- b) Implement Static routing Protocol Using Cisco Packet tracer tool.



Note: Use the above Topology and Addresses for Implementation

Experiment-3

- 1 a) Write a C Program to implement Dead Lock Avoidance Banker's Algorithm.
(OR)
b) Write a C program to stimulate Producer-Consumer Problem.
- 2 a) Implement OSPF using CISCO packet tracer tool.
(OR)
b) Implement Static routing Protocol Using Cisco Packet tracer tool.



Note: Use the above Topology and Addresses for Implementation

Experiment-4

- 1 a) Write a C Program that simulates Optimal page replacement scheduling Algorithm.
(OR)
b) Write a C Program to stimulate MVT and MFT.
- 2 a) Implement routing information protocol (RIP) using CISCO packet tracer tool.
(OR)
b) Implement OSPF protocol Using Cisco Packet tracer tool.

