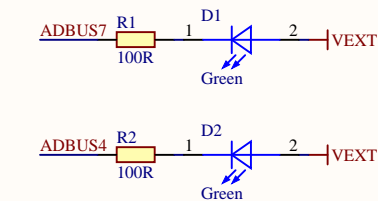
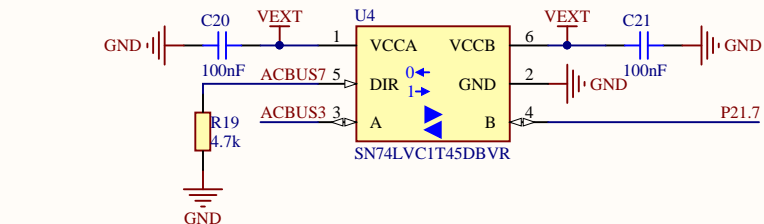
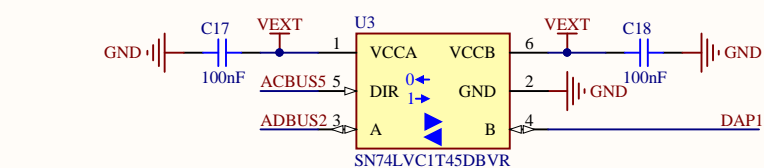
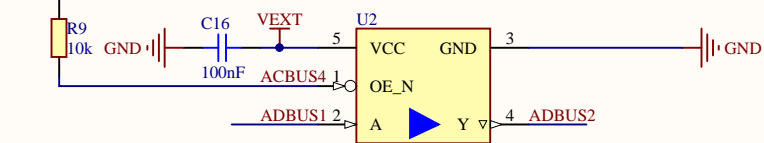
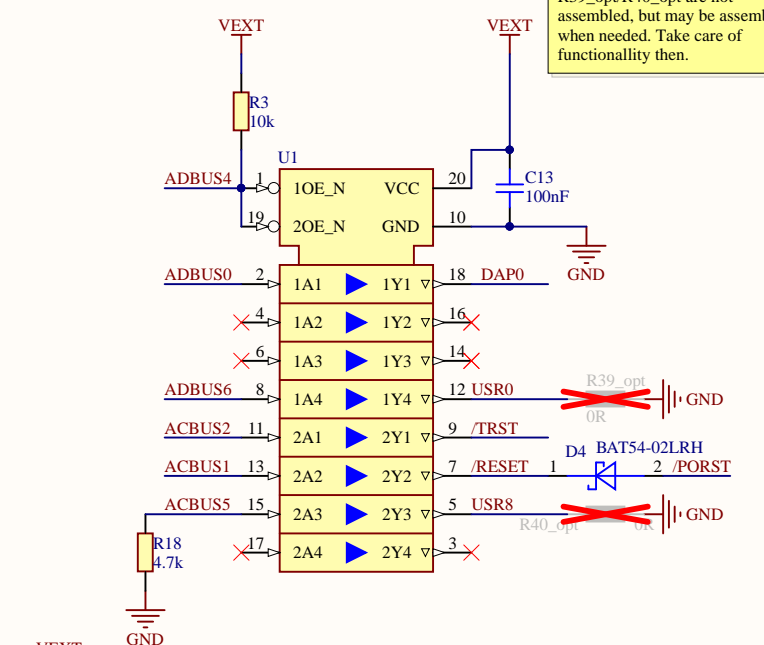


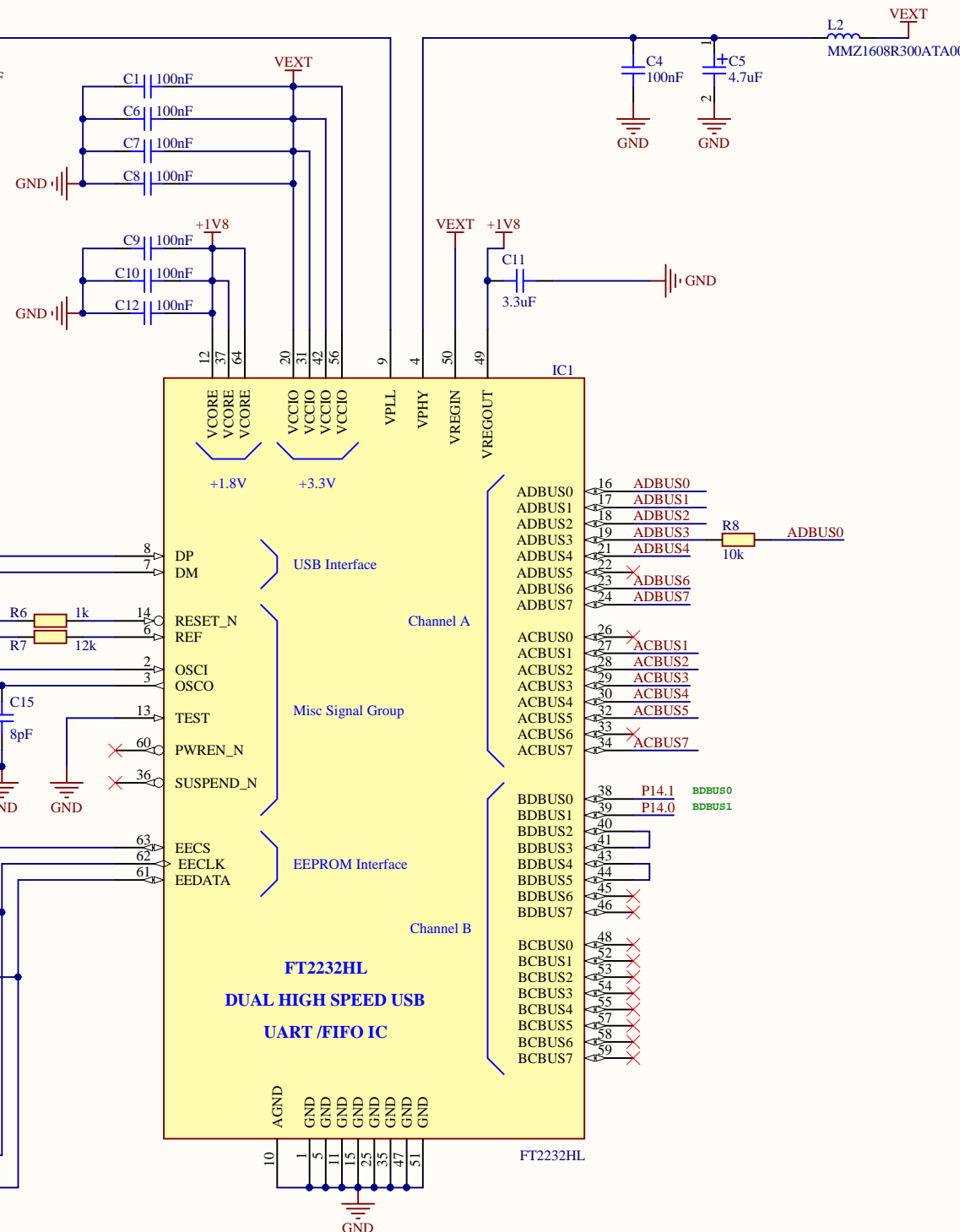
1		2		3		4		5		6		7		8		
A	Index		01_Revision_History.SchDoc								Revision History					
	Page	Schematics Page Name									Rev.	Rel.	Date	Author	Description	Page(s)
	01	Cover Sheet / Revision History									V1.0		12/2019	R.B.	Eval Board	
	02	OCDS														
	03	Power_a_Connector														
	04	CPU														
	05															
	06															
	07															
	08															
B	09															
	10															
C	TC27X SmartCar															
	Default_Assembly															
	V1.1															
D	-															
	-															
LEGAL DISCLAIMER																
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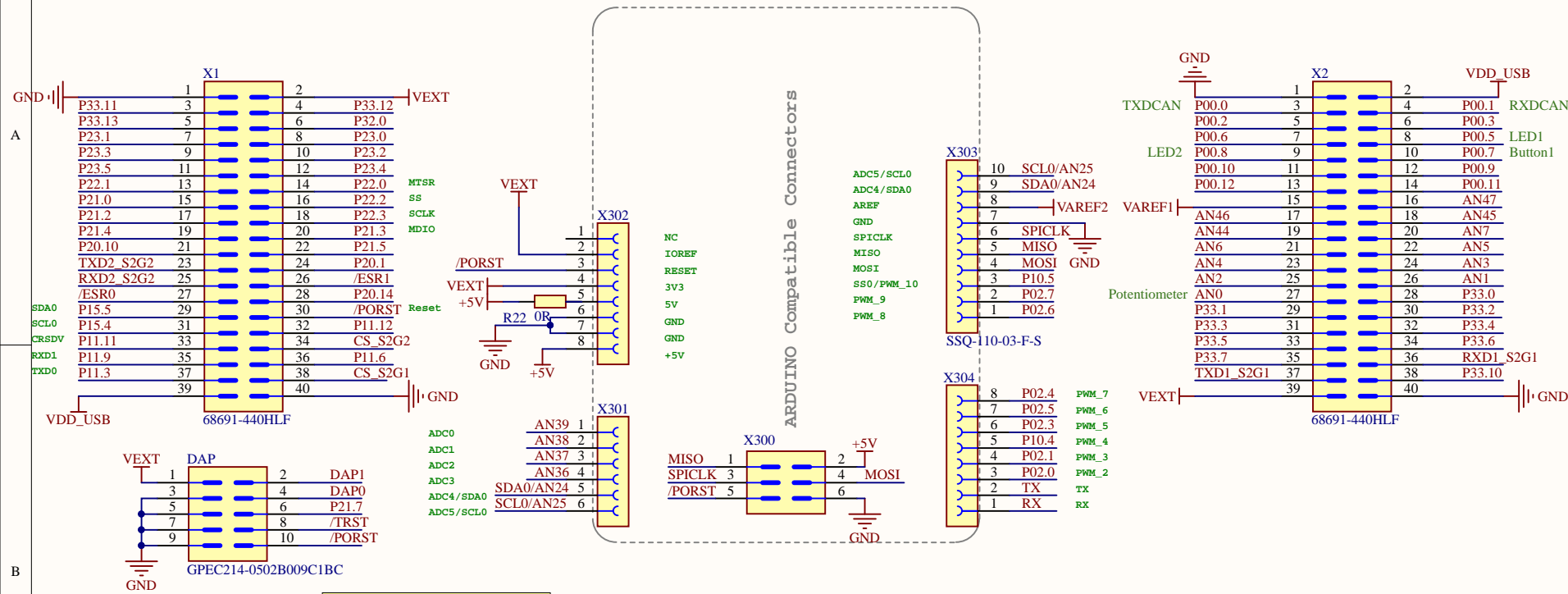
▲ R39\_opt/R40\_opt are not assembled, but may be assembled when needed. Take care of functionallity then.



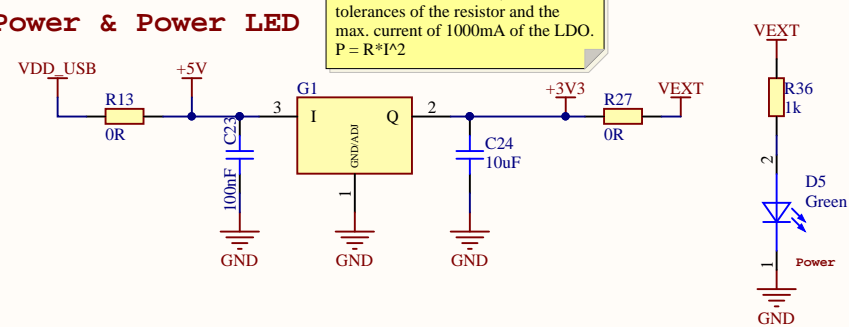
Title				TC27X SmartCar			
Variant			Default_Assembly		Approved <span style="color: blue;">&lt;Appr.&gt;</span>		
Size:	Document Name		Rel.		Rev.		
A3	02_OCDS.SchDoc		-		V1.1		
Author: -							
Date: 26.06.2020				Time: 14:55:18		SVN Revision: da75247807cb30a8ab86d75f81b	
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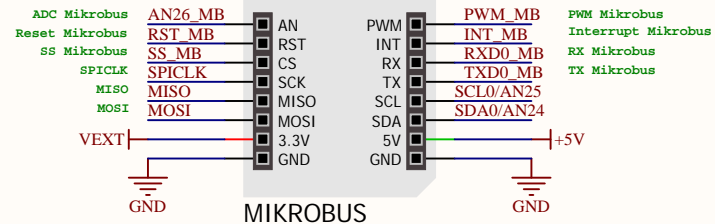
## Arduino Connector & X1/X2



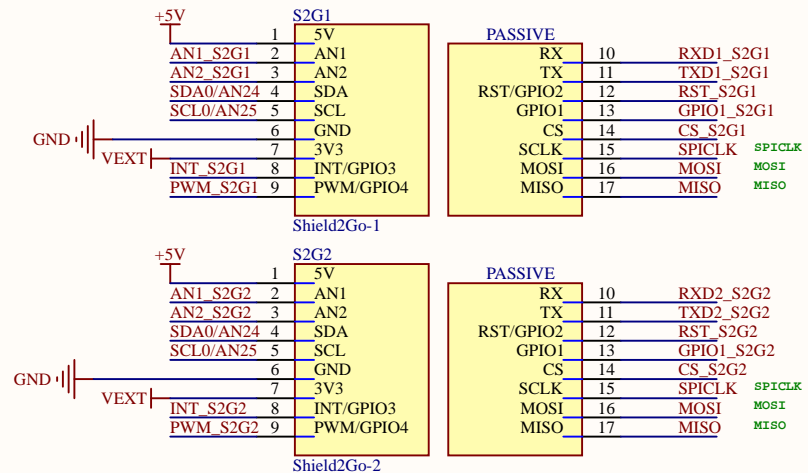
## Power & Power LED



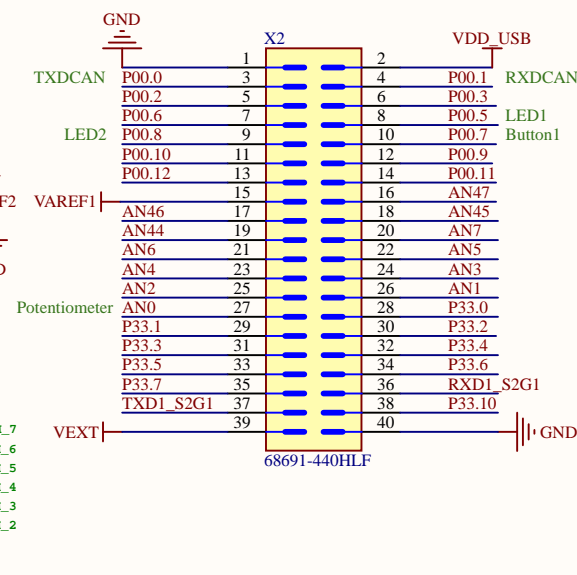
## Mikrobus



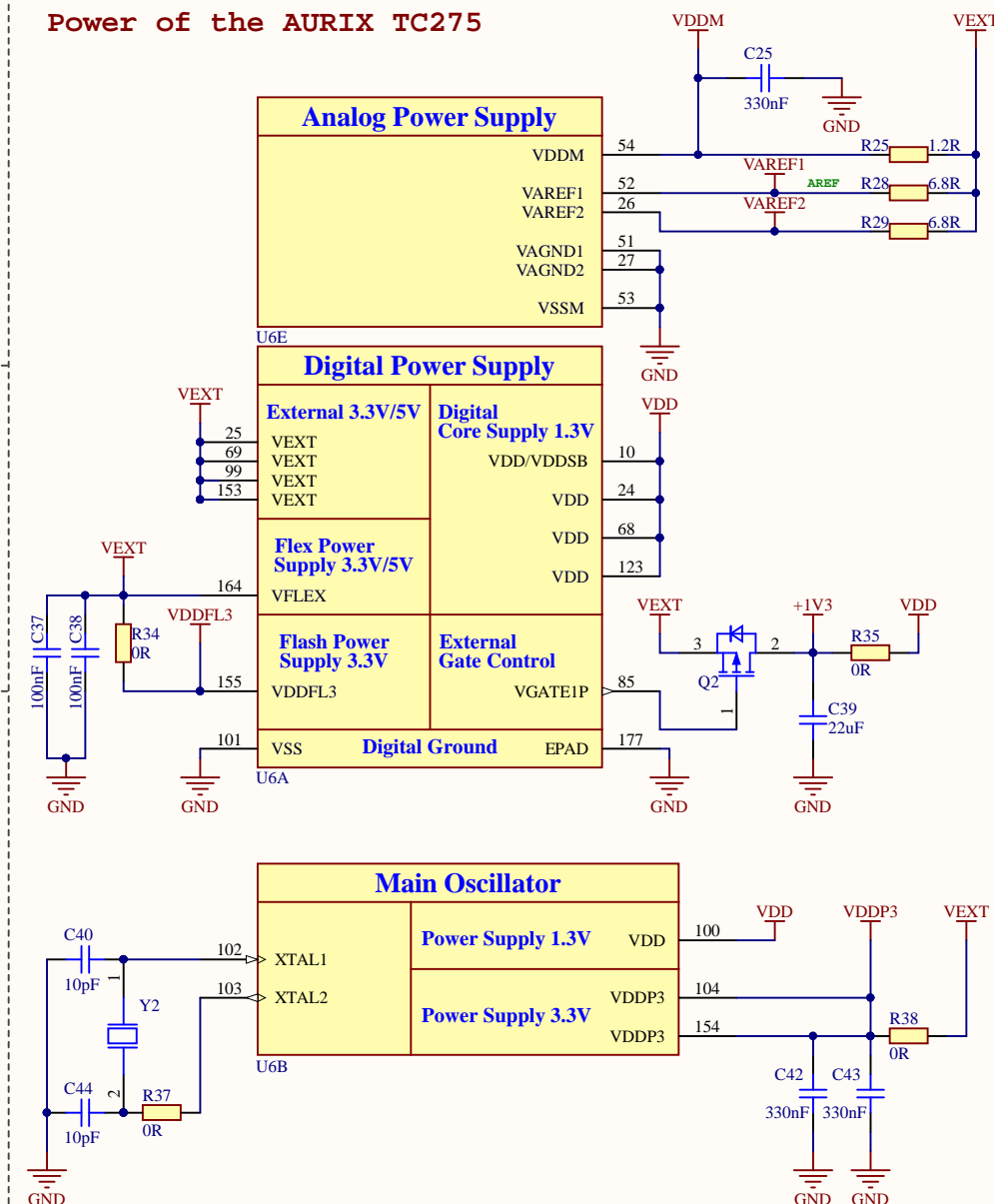
## Shield2GO Connectors



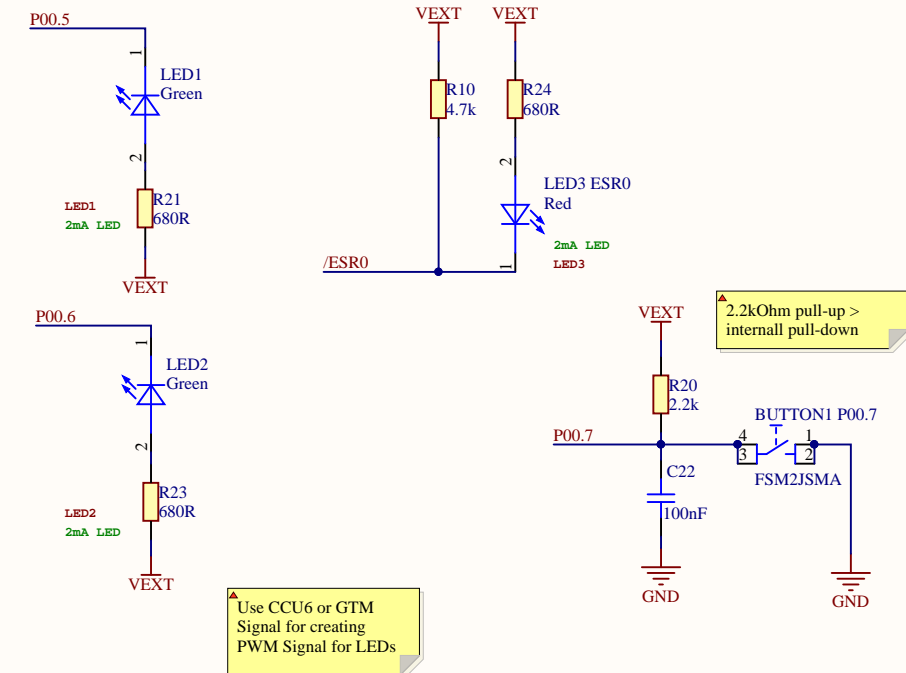
## 03\_Power\_a\_Connector.SchDoc



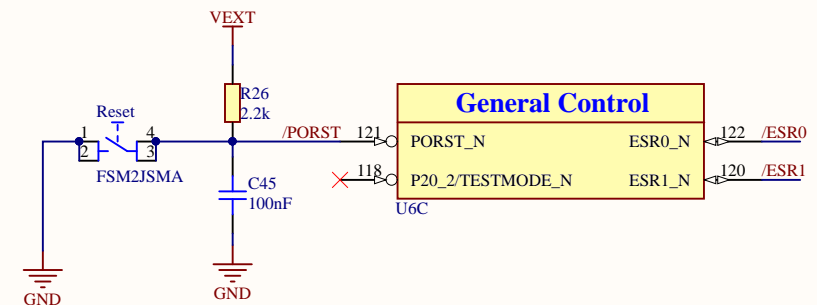
## Power of the AURIX TC275



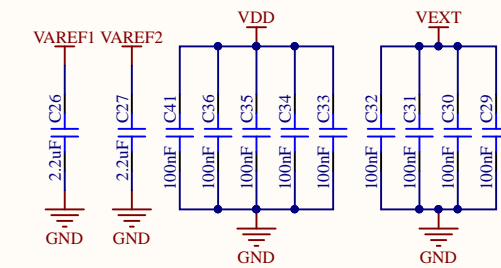
## Buttons & LEDs



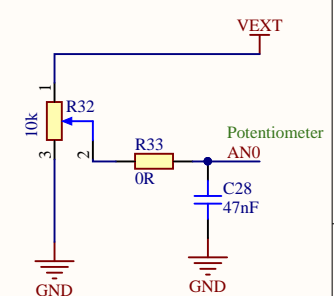
## Reset Circuit of AURIX TC275



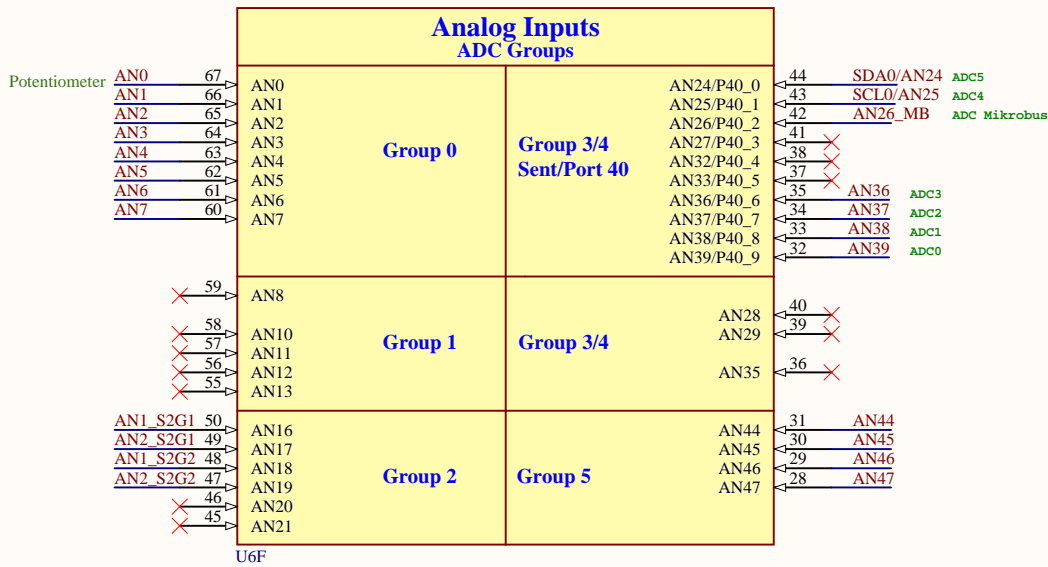
## AURIX TC275 Capacitors



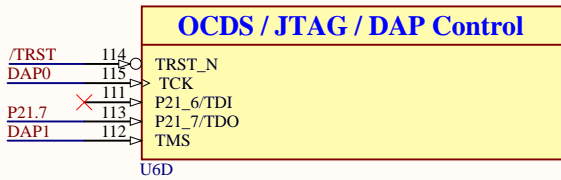
## Potentiometer



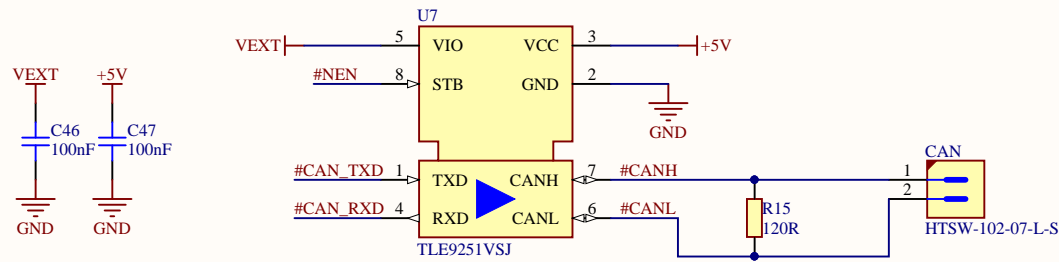
Analog Circuit of AURIX TC275



DAP Control of AURIX TC275



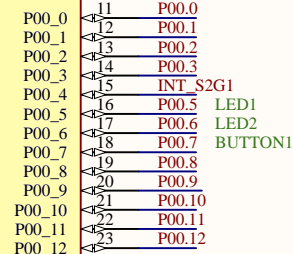
CAN Transceiver



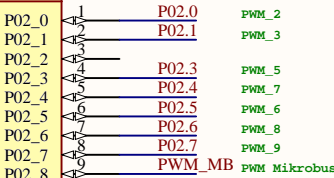
04\_CPU.SchDoc

Ports of AURIX TC275

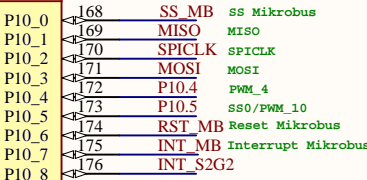
**Port 0**  
GTM/CCU6/ASC/CAN/VADC



**Port 2**  
GPIO/GTM/QSPI/CCU6



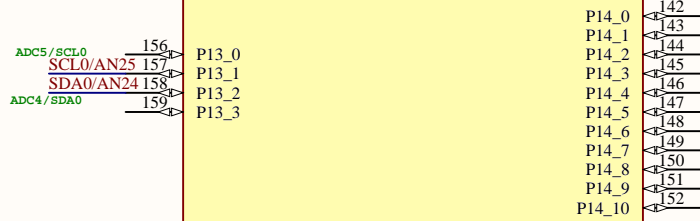
**Port 10**  
GPT/GTM/QSPI



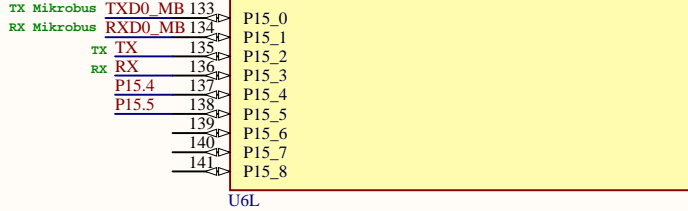
**Port 11**  
GTM/QSPI/CCU6



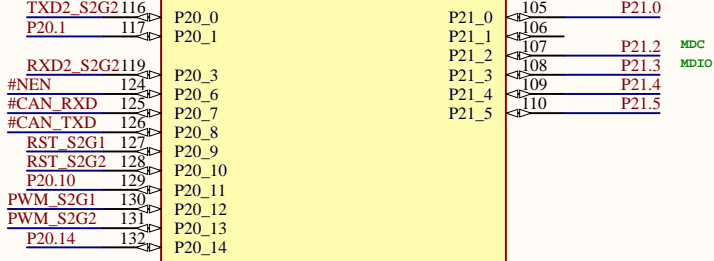
**Port 13 & 14**  
GTM/I2C0/QSPI2/CAN/CCU6/GPT120



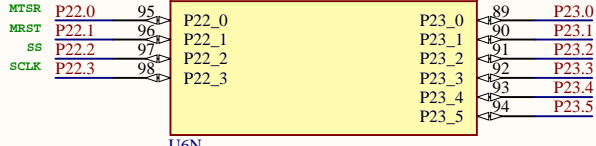
**Port 15**  
GTM/ASC1/QSPI0,2/CAN2/CCU6/I2C0



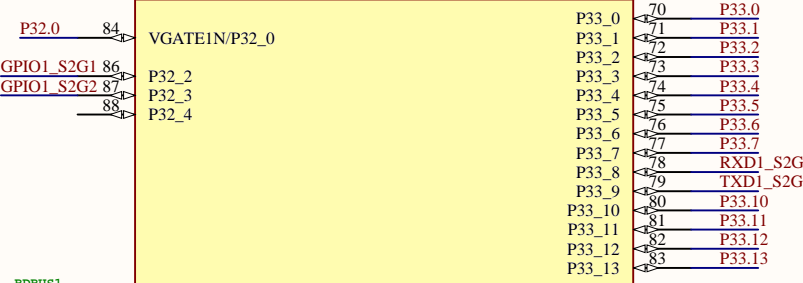
**Port 20 & 21**  
GTM/ASC3/HSCT/QSPI



**Port 22 & 23**  
GTM/ASC3/QSPI3



**Port 32 & 33**  
ASC/GTM/SENT/QSPI/DSADC/CCU6



-Resistor (R31) at P14.3 can be assembled manually. But then overboots P10.5/P10.6

-Resistor (R30) at P14.5 is mandatory