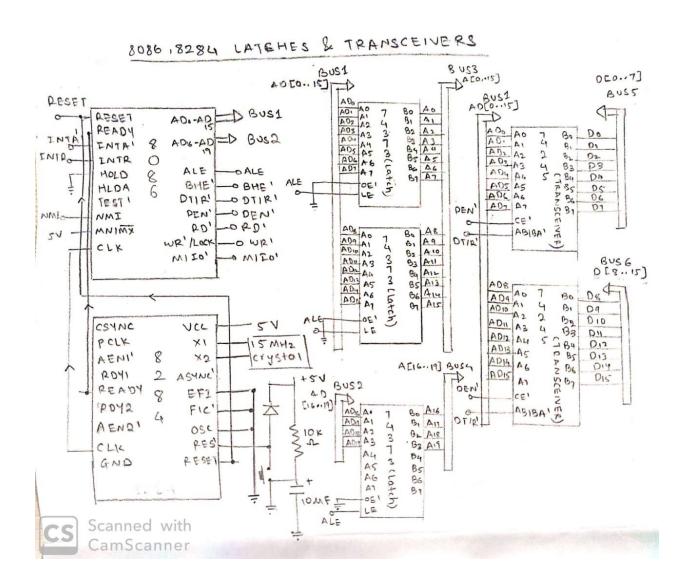
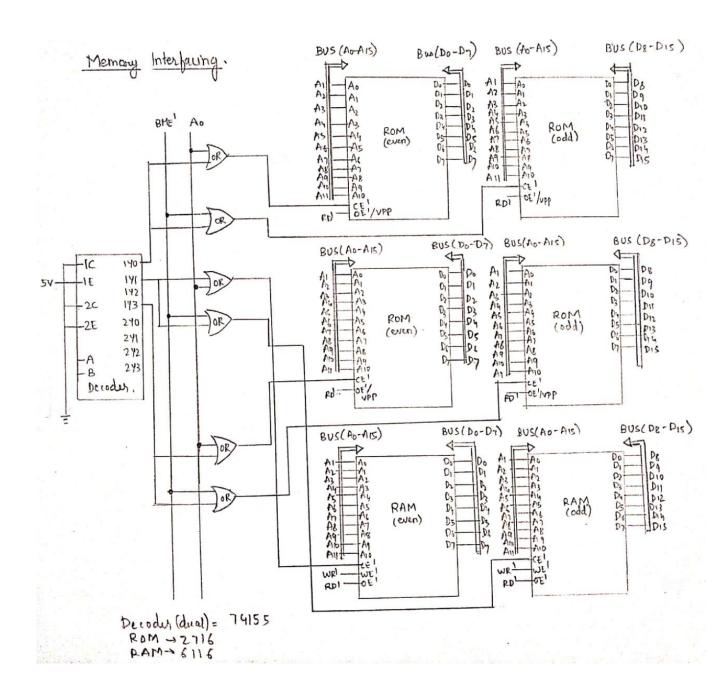
DESIGN IN MULTIPLE PARTS

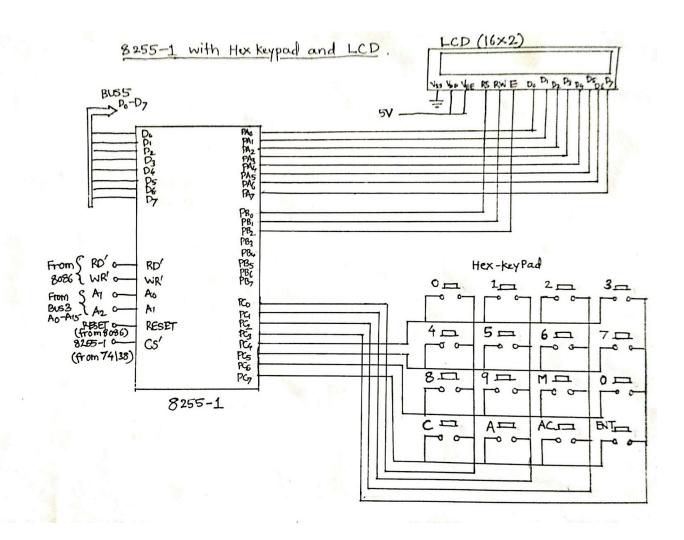
1. 8086,8284,latches and transceivers:



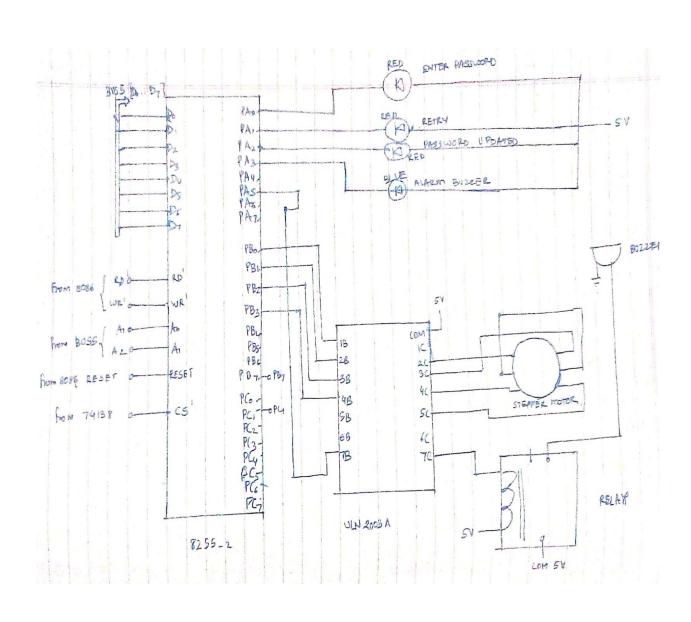
2. Memory Interfacing



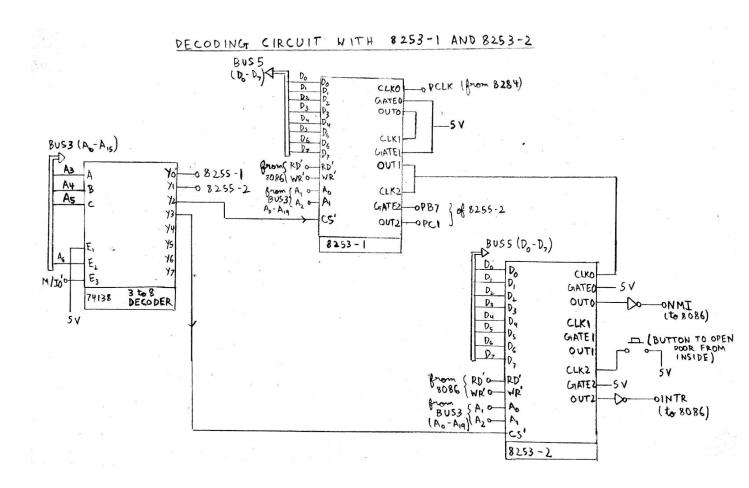
3. 8255-1 with Hexpad and LCD Display



4. 8255-2 with Darlington pair, Motor, LEDs, Alarm and Relay



5. Decoding circuit with 8253-1 and 8253-2



6.Tristate Buffer for Interrupt

