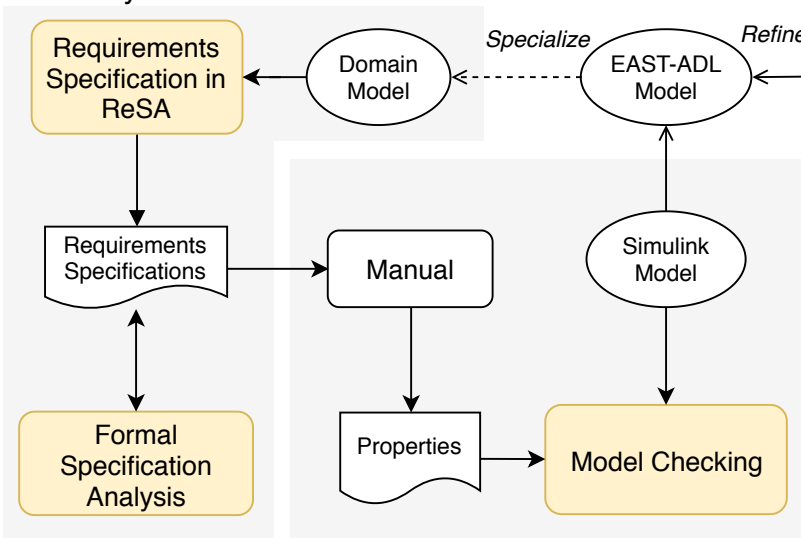
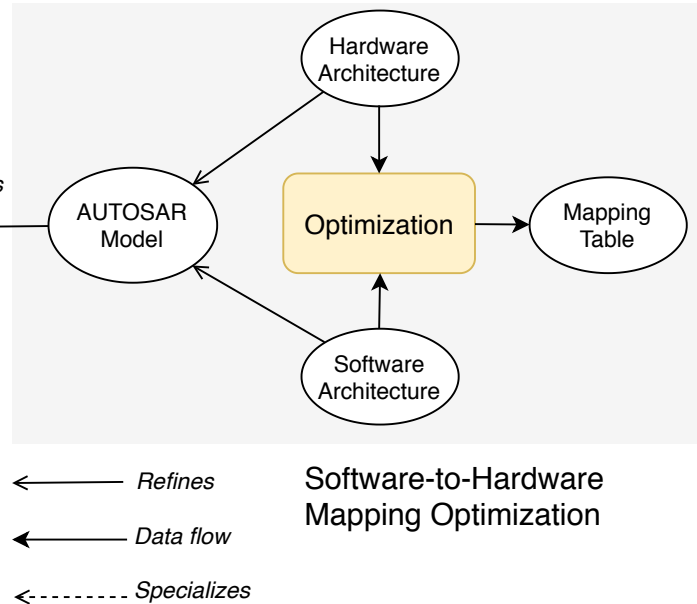


## Formal Requirements specification and analysis



## Formal Simulink analysis



## Software-to-Hardware Mapping Optimization