\$40	(read_n / write)	ID \$08		Z80B 5.37MHz OCM-PLD v2.4 or later		SMART COMMANDS TABLE		
\$41	(read_n / write)	BIT 0 BIT 1 BIT 2	Smart Command ID	CPL or \$FF (Null)		\$00	(000)	Null Command (reserved)
						\$01 \$02	(001) (002)	Set Turbo Pana Redirection OFF (default) Set Turbo Pana Redirection ON
		BIT 3				\$03 \$04	(003) (004)	Set Standard Speed 3.58MHz Set Custom Speed 4.10MHz
		BIT 5				\$05	(005)	Set Custom Speed 4.48MHz
		BIT 6 BIT 7				\$06 \$07	(006) (007)	Set Custom Speed 4.90MHz Set Custom Speed 5.39MHz
	(read / write_n)	BIT 0 BIT 1	CPU Clock Video Output (MSB)	Virtual DIP-SW1 Virtual DIP-SW2		\$08 \$09	(008) (009)	Set Custom Speed 6.10MHz Set Custom Speed 6.96MHz
		BIT 2	Video Output (LSB)	Virtual DIP-SW3		\$0A	(010)	Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default)
\$42		BIT 3 BIT 4	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Virtual DIP-SW4 Virtual DIP-SW5		\$0B \$0C	(011) (012)	Set Turbo MegaSD OFF Set Turbo MegaSD ON (default)
		BIT 5 BIT 6	Cartridge Slot-2 (LSB) Current Mapper Size	Virtual DIP-SW6 Virtual DIP-SW7		\$0D \$0E	(013) (014)	Set External Slot-1 + External Slot-2 Set Internal SCC-I Slot-1 + External Slot-2
		BIT 7	Current MegaSD Mode	Virtual DIP-SW8		\$0F	(015)	Set External Slot-1 + Internal SCC-I Slot-2
	(read / write_n)	BIT 0 BIT 1		CPU Clock Video Output		\$10 \$11	(016) (017)	Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2 Set External Slot-1 + Internal ASCII-8K Slot-2
		BIT 2 BIT 3	Lock Mak for Toggles	Audio Mixer & CMT Cartridge Slot-1		\$12 \$13	(018)	Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2 Set External Slot-1 + Internal ASCII-16K Slot-2
\$43		BIT 4		Cartridge Slot-2		\$14	(020)	Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2
		BIT 5 BIT 6		Hard Reset Key Internal Mapper		\$15 \$16	(021) (022)	Set Japanese Keyboard Layout Set Non-Japanese Keyboard Layout
		BIT 7		Internal MegaSD Led 1 Status		\$17 \$18	(023)	Set Display Mode 15KHz Composite / S-Video Set Display Mode 15KHz RGB w/ Audio Out
\$44	(read / write_n)	BIT 1		Led 2 Status		\$19	(025)	Set Display Mode 31Khz VGA Mode (60Hz only)
		BIT 2 BIT 3	3 Lights I/O 5 6	Led 3 Status Led 4 Status		\$1A \$1B	(026) (027)	Set Display Mode 31Khz VGA+ Mode (50Hz+60Hz) Set VDP Speed Normal Mode (default)
		BIT 4 BIT 5		Led 5 Status Led 6 Status		\$1C \$1D	(028) (029)	Set VDP Speed Fast Mode (V9958 only) Reserve MegaSD OFF (warm reset to set)
		BIT 6		Led 7 Status		\$1E	(030)	Reserve MegaSD ON (warm reset to set)
		BIT 7		Led 8 Status BIT 0 (LSB)		\$1F \$20	(031)	Set MegaSD Blink OFF Set MegaSD Blink ON (default)
\$45	(read / write_n)	BIT 1 BIT 2	PSG Volume Level	BIT 1 BIT 2 (MSB)		\$21 \$22	(033)	Set Lights Mode OFF w/ Auto LEDs Control (default) Set Lights Mode ON + Red Led OFF
	(read only)	BIT 3	PSG Mute	Status		\$23	(035)	Set Lights Mode ON + Red Led ON
	(read / write_n)	BIT 4 BIT 5	Master Volume Level	BIT 0 (LSB) BIT 1		\$24 \$25	(036) (037)	Set Internal Audio Mute Set Internal Audio Original Volume (like the official firmware)
	(read only)	BIT 6 BIT 7	Master Mute	BIT 2 (MSB) Status		\$26 \$27	(038)	Set Internal Audio Max Volume (default) Set CMT OFF (default) (disabled w/ MSX turbo-R BIOS)
		BIT 0		BIT 0 (LSB)		\$28	(040)	Set CMT ON (requires a cassette recorder) (disabled w/ MSX turbo-R BIOS)
\$46	(read / write_n)	BIT 1 BIT 2	OPLL Volume Level	BIT 1 BIT 2 (MSB)		\$29 \$2A	(041) (042)	Lock Turbo Toggles Unlock Turbo Toggles
	(read only)	BIT 3 BIT 4	OPLL Mute	Status BIT 0 (LSB)		\$2B \$2C	(043)	Lock Display Toggles Unlock Display Toggles
	(read / write_n)	BIT 5	SCC-I Volume Level	BIT 1		\$2D	(045)	Lock Audio Mixer & CMT Toggles
	(read only)	BIT 6	SCC-I Mute	BIT 2 (MSB) Status		\$2E \$2F	(046) (047)	Unlock Audio Mixer & CMT Toggles Lock Slot-1 Toggles
\$47	(read only)	BIT 0		BIT 0 (LSB)		\$30	(048)	Unlock Slot-1 Toggles
		BIT 1 BIT 2	CPU Custom Speed Level	BIT 1 BIT 2 (MSB)		\$31 \$32	(049) (050)	Lock Slot-2 Toggles Unlock Slot-2 Toggles
		BIT 3	Turbo MegaSD (tMSD) Turbo Pana Redirection (tPR)	Status Status		\$33 \$34	(051) (052)	Lock Slot-1 & Slot-2 Toggles Unlock Slot-1 & Slot-2 Toggles
		BIT 5	VDP Speed Mode	0=Normal, 1=Fast		\$35	(053)	Lock Hard Reset Key Unlock Hard Reset Key
		BIT 6 BIT 7	Next Mapper Size Next MegaSD Mode	0=2048kB, 1=4096kB Status		\$36 \$37	(054) (055)	Lock Mapper Toggle
	(read only)	BIT 0 BIT 1	Turbo Pana Current Keyboard Layout	Status 0=JP, 1=Non-JP		\$38 \$39	(056) (057)	Unlock Mapper Toggle Lock MegaSD Toggle
\$48		BIT 2 BIT 3	CMT (Cassette Magnetic Tape)	Status		\$3A \$3B	(058)	Unlock MegaSD Toggle Lock All Toggles
		BIT 4	Lights Mode Red Mode (Led 0)	0=Auto, 1=ON Status		\$3C	(059) (060)	Unlock All Toggles (default)
		BIT 5 BIT 6	Last Reset Flag Reset Required Flag	0=Cold, 1=Warm Status		\$3D \$3E	(061) (062)	Set Pseudo-Stereo OFF (default) Set Pseudo-Stereo ON (requires an external sound cartridge)
		BIT 7	MegaSD Blink Pseudo Stereo	Status Status		\$3F \$40	(063) (064)	Sync External Bus Clock to CPU Speed (default) Set External Bus Clock 3.58MHz
	(read only)	BIT 1	External Clock Mode	0=Sync to CPU, 1=3.58Mhz		\$41	(065)	Set Turbo Pana 5.37MHz
\$49		BIT 2 BIT 3	Machine Type ID	BIT 0 (LSB) BIT 1		\$42 \$43	(066) (067)	Set Right Inverse Audio OFF (default) Set Right Inverse Audio ON
Ş45		BIT 4	Machine Type ID	BIT 2 BIT 3 (MSB)		 \$80	 (128)	Null Command (useful for programming)
		BIT 6	NTSC/PAL Type	0=Forced, 1=Auto		\$B0 \$B7 \$B8 \$BF	(176 183)	Set Master Volume 0 7 (max is default) Set PSG Volume 0 7 (max is default)
		BIT 7	Forced Video Mode Right Inverse Audio	0=60Hz (NTSC), 1=50Hz (PAL) Status		\$60 \$6F \$C0 \$C7	(184 191) (192 199)	Set SCC-I Volume 0 7 (max is default)
\$4A	(read only)	BIT 1 BIT 2	Free	\$FF (Null)		\$C8 \$CF \$D0	(200 207) (208)	Set OPLL Volume 0 7 (max is default) Force NTSC Mode
		BIT 3 - 7				\$D1 \$D2	(209) (210)	Standard NTSC/PAL Mode (bound by R#9) (default) Force PAL Mode
\$4B	(read only)	BIT 1	Free	\$FF (Null)		\$D3	(211)	Restore Default Keyboard Layout only
,	(333 3 7 7	BIT 2 BIT 3 - 7				\$D4 \$D5	(212) (213)	Null Command (reserved) Restore Default Turbo Mode only
\$4C		BIT 0 BIT 1	CPU Clock Video Output (MSB)	Hard DIP-SW1 Hard DIP-SW2		 \$F9	 (249)	Reserve Slot-0 Primary Mode (warm reset to set)
		BIT 2	Video Output (LSB)	Hard DIP-SW3		\$FA	(250)	Reserve System Logo ON (warm reset only)
	(read only)	BIT 3 BIT 4	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Hard DIP-SW4 Hard DIP-SW5		\$FB \$FC	(251) (252)	Cold Reset Warm Reset w/ Mapper 2048kB
		BIT 5 BIT 6	Cartridge Slot-2 (LSB) Internal Mapper	Hard DIP-SW6 Hard DIP-SW7		\$FD \$FE	(253) (254)	Warm Reset Warm Reset w/ Mapper 4096kB
		BIT 7	Internal MegaSD	Hard DIP-SW8		\$FF	(255)	Restore All Default + Reserve Default Mapper & MegaSD
\$4D	(read / write_n)	BIT 0 BIT 1		BIT 0 (LSB) BIT 1				I/O Reserved to IPL-ROM
		BIT 2 BIT 3	04KB VITAIN SIOL ID (Fage O)	BIT 2 BIT 3 (MSB)				BIT 0 - 6 Free \$FF (Null)
		BIT 4		BIT 0 (LSB)		\$4E	(write_n only)	BIT 7 JIS2 Enabler 0=JIS1+JIS2, 1=JIS1 only
		BIT 5 BIT 6	64kB VRAM Slot ID (Page 1)	BIT 1 BIT 2		\$4F	(write_n only)	BIT 0 - 6 Free \$FF (Null) BIT 7 F4 Device Mode 0=Normal, 1=Inverted
\$4E	(read only)	BIT 7 BIT 0	T 0 T 1 T 2 T 3 OCM-PLD version X.Y(.Z) T 4 (main range 0.0 - 25.5) T 5	BIT 3 (MSB) BIT 0 (LSB)			fo. o.b. b.4004	
		BIT 1 BIT 2		BIT 1		More in	to at MSX Assembly Pa	age! < http://map.grauw.nl/resources/msx_io_ports.php#switch_io >
		BIT 3		BIT 2 BIT 3			Positive	0 = OFF (read / write)
		BIT 4 BIT 5		BIT 4 BIT 5		R/W Logic		1 = ON
		BIT 6		BIT 6 BIT 7 (MSB)			Negative	1 = OFF (read_n / write_n)
\$4F	(read only)	BIT 0	I/O Revision ID (0 - 31) OCM-PLD version (X.Y).Z	BIT 0 (LSB)		Toggles	CPU Clock	[F12] or [DIP-SW1]
		BIT 1 BIT 2		BIT 1 BIT 2			Video Output Audio Mixer & CM	[(SHIFT+)PRNSCR] or [DIP-SW2/3] IT [(SHIFT+)PGUP/PGDOWN/F9/F10/F11] & [SCRLCK]
		BIT 3		BIT 3 BIT 4 (MSB)			Cartridge Slot-1 Cartridge Slot-2	[SHIFT+F12] or [DIP-SW4]
		BIT 5		BIT O (LSB)			System Reset	[HARD RESET KEY] Fast or Long-Click (normal or full reboot)
		BIT 6 BIT 7	(sub range .03) Default Keyboard Layout	BIT 1 (MSB) Status			Internal Mapper Internal MegaSD	·