|              |                                  |                   |                |  | SWITCHED I/O PORTS                                    |
|--------------|----------------------------------|-------------------|----------------|--|---|
| \$40         | ( read_n / write )               |                   | \$08<br>\$D4   | MSX2+ FS-A1 Series MSX++ Computers and Compatibles   | Z80B 5.37MHz<br>OCM-PLD v2.4 or later                 |
|              |                                  | BIT<br>BIT        | \$D4<br>0<br>1 | MSX++ Computers and Compatibles  | OCIVIPLED VZ.4-01 later                               |
| \$41         | ( read_n / write )               | BIT               | 2              | Smart Command ID   | CPL or \$FF (null)                                    |
|              | (read_ii/ write)                 | BIT               | 5              |  | Creor Str (nam)                                       |
|              |                                  | BIT               | 6<br>7         | (see the list on the right)  |   |
| \$42         |                                  | BIT               | 0              | CPU Clock<br>Video Output (MSB)  | Virtual DIP-SW1<br>Virtual DIP-SW2                    |
|              | ( read / write_n )               | BIT               | 3              | Video Output (LSB) Cartridge Slot-1  | Virtual DIP-SW3<br>Virtual DIP-SW4                    |
|              |                                  | BIT<br>BIT<br>BIT | 4<br>5<br>6    | Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB) Current Mapper Size  | Virtual DIP-SW5<br>Virtual DIP-SW6<br>Virtual DIP-SW7 |
|              |                                  | BIT               | 7              | Current MegaSD Mode  | Virtual DIP-SW8 CPU Clock                             |
| \$43         | ( read / write_n )               | BIT               | 1 2            |  | Video Output<br>Audio Mixer & SCRLK                   |
|              |                                  | BIT               | 3              | Lock Mask of the Toggles   | Cartridge Slot-1<br>Cartridge Slot-2                  |
|              |                                  | BIT               | 5              |  | System Reset<br>Internal Mapper                       |
| \$44         | ( read / write_n )               | BIT               | 7              |  | Internal MegaSD<br>Led 1 Status                       |
|              |                                  | BIT               | 2              | Lights I/O + Dynamic ID (d-ID)   | Led 2 Status<br>Led 3 Status                          |
|              |                                  | BIT               | 3<br>4         | (SM-X, SX-2 and SX-E only use Led 8  | Led 4 Status<br>Led 5 Status                          |
|              |                                  | BIT               | 6              | but this register works the same way)  | Led 6 Status<br>Led 7 Status                          |
|              |                                  | BIT               | 0              | 20011 1 1/2 20   | Led 8 Status<br>BIT 0 (LSB)                           |
|              | ( read / write_n )               | BIT               | 2              | PSG Volume Level (0 - 7)   | BIT 1<br>BIT 2 (MSB)                                  |
| \$45         | ( read only ) ( read / write_n ) | BIT<br>BIT<br>BIT | 4              | PSG Mute  Master Volume Level (0 - 7)  | Status<br>BIT 0 (LSB)<br>RIT 1                        |
|              | ( read only )                    | BIT<br>BIT<br>BIT | 5<br>6<br>7    | Master Wute  | BIT 1<br>BIT 2 (MSB)<br>Status                        |
|              | ( read only )                    | BIT               | 0              | OPLL Volume Level (0 - 7)  | BIT 0 (LSB) BIT 1                                     |
|              | ( read only )                    | BIT               | 2              | OPLL Volume Level (0 - 7)  | BIT 1<br>BIT 2 (MSB)<br>Status                        |
| \$46         | ( read only ) ( read / write_n ) | BIT               | 4              | SCC-I Volume Level (0 - 7)   | BIT 0 (LSB)<br>BIT 1                                  |
|              | ( read only )                    | BIT               | 6              | SCC-I Volume Level (0 - 7)   | BIT 2 (MSB)<br>Status                                 |
|              |                                  | BIT               | 0              | CPU Custom Speed Level (1 - 7)   | BIT 0 (LSB)<br>BIT 1                                  |
| 647          | ( db. )                          | BIT               | 2              | Turbo MegaSD (tMSD)  | BIT 2 (MSB)<br>Status                                 |
| \$47         | ( read only )                    | BIT               | 4<br>5         | Turbo Pana Redirection (tPR)<br>VDP Speed Mode   | Status<br>0=Normal, 1=Fast                            |
|              |                                  | BIT<br>BIT        | 6<br>7         | Mapper Size Req<br>MegaSD Mode Req   | 0=2048 KB, 1=4096 KB<br>Status                        |
| \$48         |                                  | BIT               | 0              | Turbo Pana<br>Current Keyboard Layout  | Status<br>0=JP, 1=Non-JP                              |
|              | ( read only )                    | BIT               | 3              | SCRLK Toggle<br>Lights Mode  | Status<br>0=Auto, 1=ON                                |
|              |                                  | BIT               | 5              | Red Mode (Led 0) Last Reset Ack  | Status 0=Cold Reset ack, 1=Warm Reset ack             |
|              |                                  | BIT               | 7              | Reset Required Flag MegaSD Blink   | Status<br>Status                                      |
|              |                                  | BIT               | 0              | Pseudo Stereo External Clock Mode  | Status<br>0=Sync to CPU, 1=3.58MHz                    |
| \$49         | ( read only )                    | BIT               | 3              | Machine Type ID  (0=1chipMSX, 1=Zemmix Neo/SX-1 and related,   | BIT 0 (LSB) BIT 1                                     |
|              |                                  | BIT<br>BIT        | 5              | 2=SM-X/MC2P, 3=SX-2, 4=SM-X Mini/SM-X HB/u2-SX,<br>5=DE0CV+DEOCM, 6=SX-E/SX-Lite, 7-15=Unknown)<br>NTSC/PAL Type | BIT 2 BIT 3 (MSB) 0=Forced, 1=Auto                    |
|              |                                  | BIT               | 7              | Forced Video Mode Right Inverse Audio  | 0=60Hz (NTSC), 1=50Hz (PAL)<br>Status                 |
|              |                                  | BIT               | 1 2            | Pixel Ratio 1:1 for LED Display  | BIT 0 (LSB)<br>BIT 1                                  |
| \$4A         | ( read only )                    | BIT               | 3              | Centering YJK Modes/R25 Mask   | BIT 2 (MSB)<br>Status                                 |
|              |                                  | BIT               | 5              | Assignment of Legacy Output<br>Internal Slot-1 Linear  | 0=To VGA, 1=To VGA+<br>Status                         |
|              |                                  | BIT               | 7              | Internal Slot-2 Linear  VGA Scanlines Level (Status*25%)   | Status<br>BIT 0 (LSB)                                 |
| \$4B         | IN A,(\$44) : if A=\$00          | BIT               | 1 2            | Internal PSG2  | BIT 1 (MSB) Status only for SM-X / SX-2 / SX-E        |
| d-ID \$00    | ( read only ) [Dynamic Port 4B]  | BIT               | 3<br>4         | SDRAM Size<br>(0=8 MB, 1=16 MB, 2=32 MB, 3=Over 32 MB)   | BIT 0 (LSB)<br>BIT 1 (MSB)                            |
|              |                                  | BIT               | 6              | OCM-BIOS Reloading Req<br>Extra-Mapper 4096 KB Req   | Status<br>Status                                      |
|              |                                  | BIT               | 7              | Slot-0 Mode Req<br>Extended MegaROM Reading  | 0=Primary, 1=Expanded<br>Status                       |
| \$4B         | IN A,(\$44) : if A=\$01          | BIT               | 2              | Auxiliary SDRAM Size<br>(0=64 MB, 1=128 MB, 2=192 MB, 3=256 MB,  | BIT 0 (LSB)<br>BIT 1                                  |
| d-ID \$01    | ( read only )                    | BIT               | 4              | 4=384 MB, 5=512 MB, 6=768 MB, 7=1024 MB)   | BIT 2 (MSB)<br>BIT 0 (LSB)                            |
|              | [Dynamic Port 4B]                | BIT<br>BIT<br>BIT | 5<br>6<br>7    | Vertical Offset (Status+12)  | BIT 1<br>BIT 2<br>BIT 3 (MSB)                         |
|              | IN A,(\$44) : if A=\$02          | BIT               | 0              | Sprite Limit<br>Safe Mode  | 0=4/8 (standard), 1=8/8 (enhanced)<br>Status          |
| \$4B         |                                  | BIT               | 2              | C-BIOS Mode<br>Extra-Mapper 4096 KB Ack  | Status<br>Status                                      |
| d-ID \$02    | ( read only )                    | BIT               | 4              | Current Slot-0 Mode  VGA Interlace Field   | 0=Primary, 1=Expanded<br>0=Single, 1=Duplicate        |
|              | [Dynamic Port 4B]                | BIT               | 6              | Free   | BIT 0 (LSB) BIT 1 (MSB)                               |
|              | ( read only )                    | BIT               | 0              | CPU Clock<br>Video Output (MSB)  | Hard DIP-SW1<br>Hard DIP-SW2                          |
| \$4C         |                                  | BIT               | 2              | Video Output (LSB)<br>Cartridge Slot-1   | Hard DIP-SW3<br>Hard DIP-SW4                          |
| J4C          |                                  | BIT               | 5              | Cartridge Slot-2 (MSB)<br>Cartridge Slot-2 (LSB)   | Hard DIP-SW5<br>Hard DIP-SW6                          |
|              |                                  | BIT               | 6<br>7         | Internal Mapper<br>Internal MegaSD   | Hard DIP-SW7<br>Hard DIP-SW8                          |
|              |                                  | BIT               | 0              | 64 KB VRAM Slot ID (Page 0)  | BIT 0 (LSB) BIT 1                                     |
| \$4D         | ( read / write_n )               | BIT               | 3              |  | BIT 2<br>BIT 3 (MSB)                                  |
|              |                                  | BIT               | 5              | 64 KB VRAM Slot ID (Page 1)  | BIT 0 (LSB) BIT 1                                     |
|              | ( read only )                    | BIT               | 7              |  | BIT 2<br>BIT 3 (MSB)                                  |
|              |                                  | BIT<br>BIT<br>BIT | 0<br>1<br>2    |  | BIT 0 (LSB) BIT 1 BIT 2                               |
| \$4E         |                                  | BIT               | 3              | OCM-PLD main version X.Y(.z)<br>(range 0.0.z - 25.5.z)   | BIT 3<br>BIT 4  |
|              |                                  | BIT               | 5              | (range 0.0.2 * 25.5.2)   | BIT 5<br>BIT 6  |
|              |                                  | BIT               | 7              |  | BIT 7 (MSB)<br>BIT 0 (LSB)                            |
| \$4F         | ( read only )                    | BIT               | 1 2            | I/O Revision ID (0 - 31)   | BIT 1<br>BIT 2  |
|              |                                  | BIT               | 3              | ,  | BIT 3<br>BIT 4 (MSB)                                  |
|              |                                  | BIT               | 5              | OCM-PLD sub version (x.y).Z<br>(range x.y.0 - x.y.3)   | BIT 0 (LSB)<br>BIT 1 (MSB)                            |
|              |                                  | BIT               | 7              | Default Keyboard Layout  | Status  |
| \$4C         | (write only)                     | ріте              | 7 ^            | Reserved for IPL-ROM  VDP ID Selector  | 0-1=ID #0 (V9938), 2-255=ID #2 (V9958)                |
| \$4C<br>\$4E | ( write_n only)                  | BITS              |                | -  |   |
| \$4E<br>\$4F | ( write_n only)                  | BITS              | 7              | JIS2 Enabler<br>-  | 0=JIS1+JIS2, 1=JIS1 only<br>-                         |
| Ç41          | (te_ir only)                     | BIT               | 7              | F4 Device Mode   | 0=Normal, 1=Inverted                                  |
|              |                                  |                   |                |  |   |

|  |                               | SMART COMMANDS TABLE  |  |  |  |  |  |  |  |
|--|-------------------------------|---|--|--|--|--|--|--|--|
| \$00<br>\$01   | (000)                         | Null Command \$00 (reserved) (default/break code for IGX smart profile) Set Turbo Pana Redirection OFF (default)  |  |  |  |  |  |  |  |
| \$02<br>\$03   | (002)                         | Set Turbo Pana Redirection ON<br>Set Standard Speed 3.58MHz   |  |  |  |  |  |  |  |
| \$04<br>\$05   | (004)                         | Set Custom Speed 4.10MHz<br>Set Custom Speed 4.48MHz  |  |  |  |  |  |  |  |
| \$06<br>\$07   | (006)<br>(007)                | Set Custom Speed 4.90MHz<br>Set Custom Speed 5.39MHz  |  |  |  |  |  |  |  |
| \$08<br>\$09   | (008)                         | Set Custom Speed 6.10MHz<br>Set Custom Speed 6.96MHz  |  |  |  |  |  |  |  |
| \$0A<br>\$0B   | (010)<br>(011)                | Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default)<br>Set Turbo MegaSD OFF  |  |  |  |  |  |  |  |
| \$0C<br>\$0D   | (012)                         | Set Turbo MegaSD ON (default) Set External Slot-1 + External Slot-2   |  |  |  |  |  |  |  |
| \$0E<br>\$0F   | (014)                         | Set Internal SCC-I Slot-1 + External Slot-2<br>Set External Slot-1 + Internal SCC-I Slot-2  |  |  |  |  |  |  |  |
| \$10<br>\$11   | (016)                         | Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2 Set External Slot-1 + Internal ASCII-8K Slot-2  |  |  |  |  |  |  |  |
| \$12<br>\$13   | (018)                         | Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2<br>Set External Slot-1 + Internal ASCII-16K Slot-2   |  |  |  |  |  |  |  |
| \$14<br>\$15   | (020)                         | Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2<br>Set Japanese Keyboard Layout   |  |  |  |  |  |  |  |
| \$16<br>\$17   | ( 022 )<br>( 023 )            | Set Non-Japanese Keyboard Layout<br>Set Display Mode 15KHz Composite/S-Video  |  |  |  |  |  |  |  |
| \$18<br>\$19   | (024)                         | Set Display Mode 15KHz RGB w/ Audio Out Set Display Mode 31KHz VGA for LED TV or LED Display  | also HDMI AV on SM-X                                       |  |  |  |  |  |  |
| \$1A<br>\$1B   | ( 026 )<br>( 027 )            | Set Display Mode 31KHz VGA+ for CRT Monitor (legacy output) Set VDP Speed Normal Mode (default)   | also HDMI AV on SM-X                                       |  |  |  |  |  |  |
| \$1C<br>\$1D   | (028)                         | Set VDP Speed Fast Mode (V9958 only) Reserve MegaSD OFF (warm reset to go)  |  |  |  |  |  |  |  |
| \$1E<br>\$1F   | (030)<br>(031)                | Reserve MegaSD ON (warm reset to go) Set MegaSD Blink OFF   |  |  |  |  |  |  |  |
| \$20<br>\$21   | (032)                         | Set MegaSD Blink ON (default) Set Lights Mode OFF w/ Auto LEDs Control (default)  |  |  |  |  |  |  |  |
| \$22<br>\$23   | (034)                         | Set Lights Mode ON + Red Led OFF<br>Set Lights Mode ON + Red Led ON<br>Internal Audio Preset #1 "Mute Sound"  |  |  |  |  |  |  |  |
| \$24<br>\$25<br>\$26   | (036)                         | Internal Audio Preset #2 "Middle Sound" Internal Audio Preset #3 "High Sound" (default)   |  |  |  |  |  |  |  |
| \$26<br>\$27   | (038)                         | Set CMT OF (default) (disabled w/ MSXtR BIOS)  Set CMT ON (needs a cassette recorder) (disabled w/ MSXtR BIOS)  | n/a on SM-X / SX-2 / SX-E                                  |  |  |  |  |  |  |
| \$28<br>\$29<br>\$2A   | (040)<br>(041)<br>(042)       | Lock CPU Clock Toggles Unlock CPU Clock Toggles Unlock CPU Clock Toggles  | n/a on SM-X / SX-2 / SX-E                                  |  |  |  |  |  |  |
| \$2B<br>\$2C   | (043)                         | Lock Video Output Toggles Unlock Video Output Toggles   |  |  |  |  |  |  |  |
| \$2D<br>\$2E   | (045)                         | Lock Audio Mixer & SCRLK Toggles Unlock Audio Mixer & SCRLK Toggles   |  |  |  |  |  |  |  |
| \$2F<br>\$30   | (047)                         | Lock Slot-1 Toggles Unlock Slot-1 Toggles   |  |  |  |  |  |  |  |
| \$31<br>\$32   | (049)                         | Lock Slot-2 Toggles<br>Unlock Slot-2 Toggles  |  |  |  |  |  |  |  |
| \$33<br>\$34   | (051)                         | Lock Slot-1 & Slot-2 Toggles<br>Unlock Slot-1 & Slot-2 Toggles  |  |  |  |  |  |  |  |
| \$35<br>\$36   | (053)                         | Lock System Reset (Reset Combos & Hard Reset Key) Unlock System Reset (Reset Combos & Hard Reset Key)   |  |  |  |  |  |  |  |
| \$37<br>\$38   | (055)                         | Lock Internal Mapper Toggle Unlock Internal Mapper Toggle   |  |  |  |  |  |  |  |
| \$39<br>\$3A   | (057)                         | Lock Internal MegaSD Toggle<br>Unlock Internal MegaSD Toggle  |  |  |  |  |  |  |  |
| \$3B<br>\$3C   | (059)                         | Lock All Toggles Unlock All Toggles (default)   |  |  |  |  |  |  |  |
| \$3D<br>\$3E   | (061)                         | Set Pseudo-Stereo OFF (default) Set Pseudo-Stereo ON (needs an external sound cartridge) Set Pseudo-Stereo ON (needs an external sound cartridge)                                       |  |  |  |  |  |  |  |
| \$3F<br>\$40   | (063)                         | Sync External Bus Clock to CPU Speed (default) Set External Bus Clock 3.58MHz   |  |  |  |  |  |  |  |
| \$41<br>\$42   | ( 065 )<br>( 066 )<br>( 067 ) | Set Turbo Pana 5.37MHz Set Right Inverse Audio OFF (default) Set Right Inverse Audio ON   |  |  |  |  |  |  |  |
| \$43<br>\$44<br>\$45   | (068)                         | Internal Audio Preset #4 "Emphasis PSG Sound" Internal Audio Preset #5 "Emphasis SCC-I Sound"   |  |  |  |  |  |  |  |
| \$46<br>\$47   | (070)                         | Internal Audio Preset #6 "Emphasis OPLL Sound"  Vertical Offset 16 (useful for Ark-A-Noah)  |  |  |  |  |  |  |  |
| \$48<br>\$49   | ( 072 )<br>( 073 )            | Vertical Offset 17<br>Vertical Offset 18  |  |  |  |  |  |  |  |
| \$4A<br>\$4B   | ( 074 )<br>( 075 )            | Vertical Offset 19 (default) Vertical Offset 20   |  |  |  |  |  |  |  |
| \$4C<br>\$4D   | ( 076 )<br>( 077 )            | Vertical Offset 21<br>Vertical Offset 22  |  |  |  |  |  |  |  |
| \$4E<br>\$4F   | ( 078 )<br>( 079 )            | Vertical Offset 23<br>Vertical Offset 24 (useful for Space Manbow)  |  |  |  |  |  |  |  |
| \$50<br>\$51   | (080)                         | Set VGA Scanlines 0% (default) Set VGA Scanlines 25%  |  |  |  |  |  |  |  |
| \$52<br>\$53   | (082)                         | Set VGA Scanlines 50%<br>Set VGA Scanlines 75%  |  |  |  |  |  |  |  |
| \$54<br>\$55   | (084)                         | Set Internal PSG2 OFF (default) Set Internal PSG2 ON (this second PSG acts as an external PSG) Reserve Extra-Mapper 4096 KB OFF (default)   | only for SM-X / SX-2 / SX-E<br>only for SM-X / SX-2 / SX-E |  |  |  |  |  |  |
| \$56<br>\$57   | (086)                         | Reserve Extra-Mapper 4096 KB ON (only available if SDRAM > 8 MB)  |  |  |  |  |  |  |  |
| \$58<br>\$59<br>\$5A   | ( 088 )<br>( 089 )<br>( 090 ) | Set Extended MegaROM Reading OFF (default for compatibility) Set Extended MegaROM Reading ON (ASCII-8K/16K max size playable)   |  |  |  |  |  |  |  |
| \$5B<br>\$5C   | (091)                         | Set Sprite Limit 4/8 (standard mode) (default) Set Sprite Limit 8/8 (force MSX1 screens to use 8 sprites per line) Set VGA Interlace Single Field (not affected by scanlines) (default) |  |  |  |  |  |  |  |
| \$5D<br>\$7E   | (093)                         | Set VGA Interlace Duplicate Field  Reserve System Logo ON (warm reset only) [No effect via IPL-ROM]   | old revisions had \$FA (250)                               |  |  |  |  |  |  |
| \$7F<br>\$80   | (127)                         | Pixel Ratio 1:1 for LED Display (default is 0) (range 0 - 7) (60Hz only)  Null Command \$80 (reserved) (useful for programming)   |  |  |  |  |  |  |  |
| \$81<br>\$82   | (129)<br>(130)                | Assign Legacy Output to VGA Assign Legacy Output to VGA+ (default)  |  |  |  |  |  |  |  |
| \$83<br>\$84   | (131)<br>(132)                | Set Internal Slot-1 Linear OFF (default) Set Internal Slot-1 Linear ON (requires SCC-I preset)  |  |  |  |  |  |  |  |
| \$85<br>\$86   | (133)<br>(134)                | Set Internal Slot-2 Linear OFF (default) Set Internal Slot-2 Linear ON (requires SCC-I or ASCII-8K/16K preset)  |  |  |  |  |  |  |  |
| \$87<br>\$88   | (135)                         | Set Internal OPL3 OFF (default) Set Internal OPL3 ON  | only for SM-X / SX-2 / SX-E<br>only for SM-X / SX-2 / SX-E |  |  |  |  |  |  |
| \$89 \$8F  | ( 137 143 )<br>               | Reserved (Ducasp)   | only for SM-X / SX-2 / SX-E                                |  |  |  |  |  |  |
| \$B0 \$B7<br>\$B8 \$BF   | ( 176 183 )<br>( 184 191 )    | Set Master Volume 0 7 (default level is 7)<br>Set PSG Volume 0 7 (default level is 4)   |  |  |  |  |  |  |  |
| \$C0 \$C7<br>\$C8 \$CF   | (192 199)<br>(200 207)        | Set SCC-I Volume 0 7 (default level is 4) Set OPLL Volume 0 7 (default level is 4)  |  |  |  |  |  |  |  |
| \$D0<br>\$D1   | (208)                         | Force NTSC Mode<br>Standard NTSC/PAL Mode (bound by Control Register 9) (default)   |  |  |  |  |  |  |  |
| \$D2<br>\$D3   | (210)<br>(211)                | Force PAL Mode<br>Restore Default Keyboard Layout   |  |  |  |  |  |  |  |
| \$D4<br>\$D5<br>\$D6   | (212)<br>(213)<br>(214)       | Null Command \$D4 (reserved) + Set C-BIOS Mode ON [Reserved for IPL-ROM] Restore Default Turbo Modes Set Centering YJK Modes/R25 Mask OFF (default)                                     |  |  |  |  |  |  |  |
| \$D6<br>\$D7<br>\$F8   | (214)                         | Set Centering YIK Modes/R25 Mask OFF (derault) Set Centering YIK Modes/R25 Mask ON Reserve OCM-BIOS Reloading (cold reset or warm reset to go)  |  |  |  |  |  |  |  |
| \$F9<br>\$FA   | (249)                         | Reserve Slot-0 Primary Mode (warm reset to go) (internal OPLL disabled) Reserve Slot-0 Expanded Mode (warm reset to go) (default)   |  |  |  |  |  |  |  |
| \$FB<br>\$FC   | (251)<br>(252)                | Cold Reset [No effect via IPL-ROM] Warm Reset w/ Mapper 2048 KB (RAM size 6144 KB if Extra-Mapper is ON) [Onli  | y set Mapper via IPL-ROM]                                  |  |  |  |  |  |  |
| \$FD<br>\$FE   | (253)<br>(254)                | Warm Reset [No effect via IPL-ROM] Warm Reset w/ Mapper 4096 KB (RAM size 8192 KB if Extra-Mapper is ON) [Onli Restore All Defaults + Reserve Default Mapper & MegaSD                   | y set Mapper via IPL-ROM]                                  |  |  |  |  |  |  |
|  |                               |   |  |  |  |  |  |  |  |
| More info on Switched I/O ports at MSX Assembly Page! < http://map.grauw.nl/resources/msx_jo_ports.php#switch_jo > |                               |   |  |  |  |  |  |  |  |
| R/W Logic  | Positive                      | 0 = OFF<br>1 = ON (read / write)  |  |  |  |  |  |  |  |
| .y ** 20gic  | Negative                      | 0 = ON<br>1 = OFF ( read_n / write_n )  |  |  |  |  |  |  |  |
|  | CPU Clock<br>Video Outp       |   | SCRLK key could handle                                     |  |  |  |  |  |  |

| R/W Logic  | Positive            | 0 = OFF<br>1 = ON | ( read / write )  |                        |  |  |
|------------|---------------------|-------------------|---|------------------------|--|--|
| Ny W LOGIC | Negative            | 0 = ON<br>1 = OFF | ( read_n / write_n )  |                        |  |  |
|            | CPU Clock           | [F12] or          | [DIP-SW1]   |                        |  |  |
|            | Video Output        | [(SHIFT+)         | PRTSCR] or [DIP-SW2/3]  | SCRLK key could handle |  |  |
|            | Audio Mixer & SCRLK | [(SHIFT+)         | PGUP/PGDOWN/F9/F10/F11] & [SCRLK]   | CMT or OPL3 depending  |  |  |
| Toggles    | Cartridge Slot-1    | [SHIFT+F          | 12] or [DIP-SW4]  | on the type of machine |  |  |
| TOESIES    | Cartridge Slot-2    | [SHIFT+S          | CRLK] or [DIP-SW5/6]  |                        |  |  |
|            | System Reset        | [LCTRL+(          | [LCTRL+(SHIFT+)F12] Cold or Full Reset + [HARD RESET KEY] Short or Long-Click |                        |  |  |
|            | Internal Mapper     | [DIP-SW]          | 7] only   |                        |  |  |
|            | Internal MegaSD     | [DIP-SW           | B] only   |                        |  |  |