\$40	(read_n / write)		\$08	MSX2+ FS-A1 Series	Z80B 5.37MHz			SMART COMMANDS TABLE
	, _ ,		\$D4 0	1chipMSX / Zemmix Neo	OCM-PLD v2.4 or later	\$00	(000)	Null Command (reserved)
			1 2			\$01 \$02	(001)	Set Turbo Pana Redirection OFF (default) Set Turbo Pana Redirection ON
\$41	(read_n / write)	BIT	3	Smart Command ID	CPL or \$FF (Null)	\$03	(003)	Set Standard Speed 3.58MHz
	,		4 5			\$04 \$05	(004) (005)	Set Custom Speed 4.10MHz Set Custom Speed 4.48MHz
			6 7			\$06 \$07	(006) (007)	Set Custom Speed 4.90MHz Set Custom Speed 5.39MHz
		BIT	0	CPU Clock	Virtual DIP-SW1	\$08	(008)	Set Custom Speed 6.10MHz
	(read / write_n)	BIT BIT	1 2	Video Output (MSB) Video Output (LSB)	Virtual DIP-SW2 Virtual DIP-SW3	\$09 \$0A	(009) (010)	Set Custom Speed 6.96MHz Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default)
\$42		BIT	3 4	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Virtual DIP-SW4	\$0B \$0C	(011)	Set Turbo MegaSD OFF Set Turbo MegaSD ON (default)
		BIT	5	Cartridge Slot-2 (LSB)	Virtual DIP-SW5 Virtual DIP-SW6	\$0D	(013)	Set External Slot-1 + External Slot-2
			6 7	Current Mapper Size Current MegaSD Mode	Virtual DIP-SW7 Virtual DIP-SW8	\$0E \$0F	(014) (015)	Set Internal SCC-I Slot-1 + External Slot-2 Set External Slot-1 + Internal SCC-I Slot-2
			0		CPU Clock	\$10	(016)	Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2
		BIT BIT	2		Video Output Audio Mixer & CMT	\$11 \$12	(017) (018)	Set External Slot-1 + Internal ASCII-8K Slot-2 Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2
\$43	(read / write_n)		3 4	Lock Mak for Toggles	Cartridge Slot-1 Cartridge Slot-2	\$13 \$14	(019) (020)	Set External Slot-1 + Internal ASCII-16K Slot-2 Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2
		BIT	5		Hard Reset Key	\$15	(021)	Set Japanese Keyboard Layout
			6 7		Internal Mapper Internal MegaSD	\$16 \$17	(022) (023)	Set Non-Japanese Keyboard Layout Set Display Mode 15KHz Composite/S-Video
\$44	(read / write_n)		0		Led 1 Status Led 2 Status	\$18 \$19	(024) (025)	Set Display Mode 15KHz RGB w/ Audio Out (requires a VGA to SCART cable) Set Display Mode 31Khz VGA for LED TV or LED Display (50Hz+60Hz)
		BIT	T 2 T 3 T 4 T 5 T 6		Led 3 Status	\$1A	(026)	Set Display Mode 31Khz VGA+ for CRT Monitor (legacy output) (50Hz+60Hz)
				Lights I/O	Led 4 Status Led 5 Status	\$1B \$1C	(028)	Set VDP Speed Normal Mode (default) Set VDP Speed Fast Mode (V9958 only)
					Led 6 Status Led 7 Status	\$1D \$1E		Reserve MegaSD OFF (warm reset to set) Reserve MegaSD ON (warm reset to set)
			7		Led 8 Status	\$1F	(031)	Set MegaSD Blink OFF
	(read / write_n)	BIT		PSG Volume Level	BIT 0 (LSB) BIT 1	\$20 \$21	(032) (033)	Set MegaSD Blink ON (default) Set Lights Mode OFF w/ Auto LEDs Control (default)
A	(read only)		2	PSG Mute	BIT 2 (MSB) Status	\$22 \$23	(034) (035)	Set Lights Mode ON + Red Led OFF Set Lights Mode ON + Red Led ON
\$45		BIT	4	Master Volume Level	BIT 0 (LSB)	\$24	(036)	Internal Audio Preset #1 "Mute Sound" Internal Audio Preset #2 "Middle Sound"
	(read / write_n)	BIT	5 6		BIT 1 BIT 2 (MSB)	\$25 \$26		Internal Audio Preset #3 "High Sound" (default)
	(read only)		7	Master Mute	Status BIT 0 (LSB)	\$27 \$28	(039) (040)	Set CMT OFF (default) (disabled w/ MSX turbo-R BIOS) Set CMT ON (requires a cassette recorder) (disabled w/ MSX turbo-R BIOS)
\$46	(read / write_n)	BIT	1	OPLL Volume Level	BIT 1	\$29	(041)	Lock Turbo Toggles
	(read only)	BIT		OPLL Mute	BIT 2 (MSB) Status	\$2A \$2B	(043)	Unlock Turbo Toggles Lock Display Toggles
	(read / write_n)		4 5	SCC-I Volume Level	BIT 0 (LSB) BIT 1	\$2C \$2D		Unlock Display Toggles Lock Audio Mixer & CMT Toggles
		BIT	6	CCC I Musto	BIT 2 (MSB)	\$2E	(046)	Unlock Audio Mixer & CMT Toggles
	(read only)		7	SCC-I Mute	Status BIT 0 (LSB)	\$2F \$30	(047) (048)	Lock Slot-1 Toggles Unlock Slot-1 Toggles
	(read only)		1 2	CPU Custom Speed Level	BIT 1 BIT 2 (MSB)	\$31 \$32	(049) (050)	Lock Slot-2 Toggles Unlock Slot-2 Toggles
\$47		BIT	3	Turbo MegaSD (tMSD)	Status	\$33	(051)	Lock Slot-1 & Slot-2 Toggles
			4 5	Turbo Pana Redirection (tPR) VDP Speed Mode	Status 0=Normal, 1=Fast	\$34 \$35	(052) (053)	Unlock Slot-1 & Slot-2 Toggles Lock Hard Reset Key
			6 7	Next Mapper Size Next MegaSD Mode	0=2048kB, 1=4096kB Status	\$36 \$37	(054) (055)	Unlock Hard Reset Key Lock Mapper Toggle
	(read only)	BIT	0	Turbo Pana	Status	\$38	(056)	Unlock Mapper Toggle
			1 2	Current Keyboard Layout CMT (Cassette Magnetic Tape)	0=JP, 1=Non-JP Status	\$39 \$3A	(057) (058)	Lock MegaSD Toggle Unlock MegaSD Toggle
\$48			3 4	Lights Mode Red Mode (Led 0)	0=Auto, 1=ON Status	\$3B \$3C	. ,	Lock All Toggles Unlock All Toggles (default)
		BIT	5	Last Reset Flag	0=Cold, 1=Warm	\$3D \$3E		Set Pseudo-Stereo OFF (default) Set Pseudo-Stereo ON (requires an external sound cartridge)
			7	Reset Required Flag MegaSD Blink	Status Status	\$3F	(062)	Sync External Bus Clock to CPU Speed (default)
\$49	(read only)		0	Pseudo Stereo External Clock Mode	Status 0=Sync to CPU, 1=3.58Mhz	\$40 \$41	(064) (065)	Set External Bus Clock 3.58MHz Set Turbo Pana 5.37MHz
		BIT	2	Machine Type ID	BIT 0 (LSB) BIT 1	\$42 \$43	(066) (067)	Set Right Inverse Audio OFF (default) Set Right Inverse Audio ON
		BIT	4	(0=1chipMSX, 1=Zemmix Neo,)	BIT 2	\$44	(068)	Internal Audio Preset #4 "Emphasis PSG Sound"
			5 6	NTSC/PAL Type	BIT 3 (MSB) 0=Forced, 1=Auto	\$45 \$46	(069) (070)	Internal Audio Preset #5 "Emphasis SCC-I Sound" Internal Audio Preset #6 "Emphasis OPLL Sound"
			7	Forced Video Mode Right Inverse Audio	0=60Hz (NTSC), 1=50Hz (PAL) Status	 \$7F	(127)	Pixel Ratio 1:1 for LED Display (default is 0) (range 0-7) (60Hz only)
	(read only)	BIT	1		BIT 0 (LSB)	\$80	(128)	Null Command (useful for programming)
\$4A			2 3	Pixel Ratio 1:1 for LED Display	BIT 1 BIT 2 (MSB)	\$81 \$82	(129) (130)	Assign Legacy Output to VGA Assign Legacy Output to VGA+ (default)
YTA			4 5	Centering YJK Modes/R25 Mask Assignment of Legacy Output	Status 0=To VGA, 1=To VGA+	\$B0 \$ \$B8 \$		·
		BIT	6	Free	\$FF (Null)	\$C0 \$	C7 (192 199	Set SCC-I Volume 0 7 (default level is 4)
		BIT BIT	0			\$C8 \$ \$D0		Force NTSC Mode
\$4B	(read only)		1 2			\$D1 \$D2	(209)	Standard NTSC/PAL Mode (bound by Control Register 9) (default) Force PAL Mode
		BIT	3	Free	\$FF (Null)	\$D3	(211)	Restore Default Keyboard Layout only
		BIT	5			\$D4 \$D5	, ,	Null Command (reserved) Restore Default Turbo Mode only
			6 7			\$D6 \$D7	(214) (215)	Set Centering YJK Modes/R25 Mask OFF (default) Set Centering YJK Modes/R25 Mask ON
\$4C	(read only)		0 1	CPU Clock Video Output (MSB)	Hard DIP-SW1	 \$F9	(249)	Reserve Slot-0 Primary Mode (warm reset to set) (internal OPLL disabled)
		BIT	2	Video Output (LSB)	Hard DIP-SW2 Hard DIP-SW3	\$FA	(250)	Reserve System Logo ON (warm reset only)
			3 4	Cartridge Slot-1 Cartridge Slot-2 (MSB)	Hard DIP-SW4 Hard DIP-SW5	\$FB \$FC	(251) (252)	Cold Reset Warm Reset w/ Mapper 2048kB
		BIT BIT	5	Cartridge Slot-2 (LSB) Internal Mapper	Hard DIP-SW6 Hard DIP-SW7	\$FD \$FE	(253)	Warm Reset Warm Reset w/ Mapper 4096kB
		BIT	7	Internal MegaSD	Hard DIP-SW8	\$FF	(255)	Restore All Default + Reserve Default Mapper & MegaSD
\$4D	(read / write_n)		0 1	GALD VIDAM CLOSUD (Deep C)	BIT 0 (LSB) BIT 1			I/O Pecanyod to IDL DOM
		BIT	2	64kB VRAM Slot ID (Page 0)	BIT 2 BIT 3 (MSB)			I/O Reserved to IPL-ROM BIT 0 - 6 Free \$FF (Null)
		BIT	4		BIT O (LSB)	\$4E	(write_n on	BIT 7 JIS2 Enabler 0=JIS1+JIS2, 1=JIS1 only
			6	64kB VRAM Slot ID (Page 1)	BIT 1 BIT 2	\$4F	(write_n onl	y) BIT 0 - 6 Free \$FF (Null) BIT 7 F4 Device Mode 0=Normal, 1=Inverted
\$4E	(read only)		7 0		BIT 3 (MSB) BIT 0 (LSB)			
		BIT	1		BIT 1		More info at MS	X Assembly Page! < http://map.grauw.nl/resources/msx_io_ports.php#switch_io >
		BIT	2	OCM-PLD main version X.Y(.z)	BIT 2 BIT 3		Positive	0 = OFF (read / write)
			4 5	(range 0.0.z - 25.5.z)	BIT 4 BIT 5	R/W Lo	gic	1 = UN 0 = ON
		BIT	6		BIT 6		Negative	1 = OFF (read_n / write_n)
		BIT	7 0		BIT 7 (MSB) BIT 0 (LSB)		CPU Clo	
			1 2	I/O Revision ID (0 - 31)	BIT 1 BIT 2		Video Ou Audio Mixe	
\$4F	(read only)	BIT	3	,	BIT 3	Toggles	Cartridge	Slot-1 [SHIFT+F12] or [DIP-SW4]
	(33.3)/	BIT 4	OCM-PLD sub version (x.y).Z	BIT 4 (MSB) BIT 0 (LSB)		System F	leset [HARD RESET KEY] Fast or Long-Click (normal or full reboot)	
			6 7	(range x.y.0 - x.y.3) Default Keyboard Layout	BIT 1 (MSB) Status		Internal M Internal M	
		2.1						