					SWITCHED I/O PORTS
\$40	(read_n / write)	ID ID	\$08 \$D4	MSX2+ FS-A1 Series	Z80B 5.37MHz OCM-PLD v2.4 or later
		BIT	0	MSX++ Computers and Compatibles	OCM-PLD V2.4 or later
\$41	(read_n / write)	BIT	2	Smort Command ID	CPL or \$FF (null)
241	(read_ii/ write)	BIT	4 5	Smart Command ID	CPLOI SPP (IIIII)
		BIT	6 7	(see the table on the right)	
	(read / write_n)	BIT	1	CPU Clock Video Output (MSB)	Virtual DIP-SW1 Virtual DIP-SW2
\$42		BIT BIT BIT	3	Video Output (LSB) Cartridge Slot-1 Castridge Slot-2 (MSR)	Virtual DIP-SW3 Virtual DIP-SW4 Virtual DIP-SW5
,		BIT	5	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB) Current Mapper Size	Virtual DIP-SW6 Virtual DIP-SW7
		BIT	7	Current MegaSD Mode	Virtual DIP-SW8 CPU Clock
	(read / write_n)	BIT	1 2		Video Output Audio Mixer & SCRLK
\$43		BIT	3	Lock Mask of the Toggles	Cartridge Slot-1 Cartridge Slot-2
		BIT	6		System Reset Internal Mapper
	(read / write_n)	BIT BIT BIT	7		Internal MegaSD Led 1 Status Led 2 Status
		BIT	2	Lights I/O + Dynamic ID (d-ID)	Led 2 Status Led 3 Status Led 4 Status
\$44		BIT	4	(SM-X, SX-2 and SX-E only use Led 8 but this register works the same way)	Led 5 Status Led 6 Status
		BIT	6 7	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Led 7 Status Led 8 Status
	(read / write_n)	BIT	0	PSG Volume Level (0 - 7)	BIT 0 (LSB) BIT 1
\$45	(read only)	BIT	2	PSG Mute	BIT 2 (MSB) Status
	(read / write_n)	BIT	5	Master Volume Level (0 - 7)	BIT 0 (LSB) BIT 1
	(read only)	BIT	7	Master Mute	BIT 2 (MSB) Status
	(read / write_n)	BIT	1	OPLL Volume Level (0 - 7)	BIT 0 (LSB) BIT 1 BIT 2 (MASB)
\$46	(read only)	BIT BIT BIT	3	OPLL Mute	BIT 2 (MSB) Status BIT 0 (LSB)
	(read / write_n)	BIT	5	SCC-I Volume Level (0 - 7)	BIT 1 BIT 2 (MSB)
	(read only)	BIT	7	SCC-I Mute	Status BIT 0 (LSB)
		BIT	2	CPU Custom Speed Level (1 - 7)	BIT 1 BIT 2 (MSB)
\$47	(read only)	BIT	3 4	Turbo MegaSD (tMSD) Turbo Pana Redirection (tPR)	Status Status
		BIT	5 6	VDP Speed Mode Mapper Size Req	0=Normal, 1=Fast 0=2048 KB, 1=4096 KB
		BIT	0	MegaSD Mode Req Turbo Pana	Status Status
		BIT	2	Current Keyboard Layout SCRLK Toggle	0=JP, 1=Non-JP Status
\$48	(read only)	BIT BIT BIT	3 4 5	Lights Mode Red Mode (Led 0) Last Reset Ack	0=Auto, 1=ON Status 0=Cold Reset ack, 1=Warm Reset ack
		BIT	6	Reset Required Flag MegaSD Blink	Status Status
		BIT	0	Pseudo Stereo External Clock Mode	Status 0=Sync to CPU, 1=3.58MHz
Ć40	(db.)	BIT	2	Machine Type ID (0=1chipMSX, 1=Zemmix Neo/SX-1 and related,	BIT 0 (LSB)
\$49	(read only)	BIT	4	2=SM-X/MC2P, 3=SX-2, 4=SM-X Mini/SM-X HB/u2-SX, 5=DEOCV+DEOCM, 6=SX-E/SX-Lite, 7-15=Unknown)	BIT 2 BIT 3 (MSB)
		BIT BIT	6 7	NTSC/PAL Type Forced Video Mode	0=Forced, 1=Auto 0=60Hz (NTSC), 1=50Hz (PAL)
		BIT	0	Right Inverse Audio	Status BIT 0 (LSB)
\$4A	(read only)	BIT	3	Pixel Ratio 1:1 for LED Display	BIT 1 BIT 2 (MSB)
		BIT BIT BIT	4 5 6	Centering YJK Modes/R25 Mask Assignment of Legacy Output Internal Slot-1 Linear	Status 0=To VGA, 1=To VGA+ Status
		BIT	7	Internal Slot-2 Linear	Status
	IN A,(\$44) : if A=\$00	BIT BIT BIT	0 1 2	VGA Scanlines Level (0=None, 1=Light, 2=Medium, 3=Heavy) Internal PSG2	BIT 0 (LSB) BIT 1 (MSB) Status only for SM-X / SX-2 / SX-E
\$4B d-ID \$00	(read only)	BIT	3	SDRAM Size (0=8 MB, 1=16 MB, 2=32 MB, 3=Over 32 MB)	BIT 0 (LSB) BIT 1 (MSB)
a-ID \$00	[Dynamic Port 4B]	BIT	5 6	OCM-BIOS Reloading Req Extra-Mapper 4096 KB Req	Status Status
		BIT	7	Slot-0 Mode Req Extended MegaROM Reading	0=Primary, 1=Expanded Status
\$4B	IN A,(\$44) : if A=\$01	BIT	2	Auxiliary SDRAM Size (0=64 MB, 1=128 MB, 2=192 MB, 3=256 MB,	BIT 0 (LSB) BIT 1
d-ID \$01	(read only)	BIT	4	4=384 MB, 5=512 MB, 6=768 MB, 7=1024 MB)	BIT 2 (MSB) BIT 0 (LSB)
	[Dynamic Port 4B]	BIT BIT BIT	5 6 7	Vertical Offset (Status+12)	BIT 1 BIT 2 BIT 3 (MSB)
	IN A,(\$44) : if A=\$02	BIT	0	Sprite Limit VGA Interlace Field	0=4/8 (standard), 1=8/8 (enhanced) 0=Single, 1=Duplicate
\$4B		BIT	2	VGA Interface Field VGA Scanlines Variant C-BIOS Mode	0=Low-Scale, 1=High-Scale Status
d-ID \$02	(read only)	BIT	4	Extra-Mapper 4096 KB Ack Current Slot-0 Mode	Status 0=Primary, 1=Expanded
	[Dynamic Port 4B]	BIT BIT	6 7	Safe Mode F2 Device (ESP8266 BIOS)	Status Status
		BIT BIT	0	CPU Clock Video Output (MSB)	Hard DIP-SW1 Hard DIP-SW2
\$4C	(read only)	BIT	3	Video Output (LSB) Cartridge Slot-1	Hard DIP-SW3 Hard DIP-SW4
,		BIT	5	Cartridge Slot-2 (MSB) Cartridge Slot-2 (LSB)	Hard DIP-SW5 Hard DIP-SW6
		BIT BIT	6 7 0	Internal Mapper Internal MegaSD	Hard DIP-SW7 Hard DIP-SW8 BIT 0 (LSB)
		BIT	1 2	64 KB VRAM Slot ID (Page 0)	BIT 1 BIT 2
\$4D	(read / write_n)	BIT	3		BIT 3 (MSB) BIT 0 (LSB)
		BIT	5 6	64 KB VRAM Slot ID (Page 1)	BIT 1 BIT 2
		BIT	7		BIT 3 (MSB) BIT 0 (LSB)
		BIT	2		BIT 1 BIT 2
		BIT	3	OCM-PLD main version X.Y(.z) (range 0.0.z - 25.5.z)	BIT 3 BIT 4
\$4E	(read only)	BIT	4		DIT 5
\$4E	(read only)	BIT BIT BIT	5 6		BIT 5 BIT 6 BIT 7 (MASB)
\$4E	(read only)	BIT BIT BIT BIT BIT	5 6 7 0		BIT 6 BIT 7 (MSB) BIT 0 (LSB)
		BIT BIT BIT BIT BIT	5 6 7	I/O Revision ID (0 - 31)	BIT 6 BIT 7 (MSB)
\$4E \$4F	(read only)	BIT BIT BIT BIT BIT BIT BIT	5 6 7 0 1 2	I/O Revision ID (0 - 31) OCM-PLD sub version (x.y).Z	BIT 6 BIT 7 (MSB) BIT 0 (LSB) BIT 1 BIT 2
		BIT BIT BIT BIT BIT BIT BIT BIT BIT	5 6 7 0 1 2 3 4		BIT 6 BIT 7 (MSB) BIT 0 (LSB) BIT 1 BIT 2 BIT 3 BIT 4 (MSB)
		BIT BIT BIT BIT BIT BIT BIT BIT BIT BIT	5 6 7 0 1 2 3 4 5 6	OCM-PLD sub version (x.y).Z (range x.y.0 - x.y.3)	BIT 6 BIT 7 (MSB) BIT 0 (LSB) BIT 1 BIT 2 BIT 2 BIT 3 BIT 3 BIT 4 (MSB) BIT 1 BIT 4 (MSB) BIT 0 (LSB) BIT 1 (MSB)
		BIT BIT BIT BIT BIT BIT BIT BIT BIT BIT	5 6 7 0 1 2 3 4 5 6 7	OCM-PLD sub version (x.y).2 (range x.y.0 - x.y.3) Default Keyboard Layout	BIT 6 BIT 7 (MSB) BIT 0 (LSB) BIT 1 BIT 2 BIT 2 BIT 3 BIT 3 BIT 4 (MSB) BIT 1 BIT 4 (MSB) BIT 0 (LSB) BIT 1 (MSB)
\$4F	(read only)	BIT BIT BIT BIT BIT BIT BIT BIT BIT BIT	5 6 7 0 1 2 3 4 5 6 7 7 0	OCM-PLD sub version (x,y), 2 (range x,y,0 - x,y, 3) Default Keyboard Layout Reserved for IPL-ROM	BIT 6 BIT 7 (MSB) BIT 0 (LSB) BIT 1 BIT 2 BIT 3 BIT 2 BIT 3 BIT 3 BIT 3 BIT 4 (MSB) BIT 0 (LSB) BIT 1 (MSB) Status

		SMART COMMANDS TABLE							
\$00	(000)	Null Command \$00 (reserved) (default/break code for IGX smart profile)							
\$01 \$02	(001)	Set Turbo Pana Redirection OFF (default) Set Turbo Pana Redirection ON							
\$03 \$04	(003) (004)	Set Standard Speed 3.58MHz Set Custom Speed 4.10MHz							
\$05 \$06	(005)	Set Custom Speed 4.48MHz Set Custom Speed 4.90MHz							
\$07 \$08	(007)	Set Custom Speed 5.39MHz Set Custom Speed 6.10MHz							
\$09 \$0A	(009)	Set Custom Speed 6.96MHz Set Custom Speed 8.06MHz (aka "Turbo 10MHz") (default)							
\$0B	(011)	Set Turbo MegaSD OFF							
\$0C \$0D	(012) (013)	Set Turbo MegaSD ON (default) Set External Slot-1 + External Slot-2							
\$0E \$0F	(014) (015)	Set Internal SCC-I Slot-1 + External Slot-2 Set External Slot-1 + Internal SCC-I Slot-2							
\$10 \$11	(016) (017)	Set Internal SCC-I Slot-1 + Internal SCC-I Slot-2 Set External Slot-1 + Internal ASCII-8K Slot-2							
\$12 \$13	(018)	Set Internal SCC-I Slot-1 + Internal ASCII-8K Slot-2 Set External Slot-1 + Internal ASCII-16K Slot-2							
\$14 \$15	(020)	Set Internal SCC-I Slot-1 + Internal ASCII-16K Slot-2 Set Japanese Keyboard Layout							
\$16 \$17	(022)	Set Non-Japanese Keyboard Layout Set Display Mode 15KHz Composite/S-Video							
\$18	(024)	Set Display Mode 15KHz RGB w/ Audio Out							
\$19 \$1A	(025) (026)	Set Display Mode 31KHz VGA for LED TV or LED Display Set Display Mode 31KHz VGA+ for CRT Monitor (legacy output)	also HDMI AV on SM-X also HDMI AV on SM-X						
\$1B \$1C	(027) (028)	Set VDP Speed Normal Mode (default) Set VDP Speed Fast Mode (V9958 only)							
\$1D \$1E	(029) (030)	Reserve MegaSD OFF (warm reset to go) Reserve MegaSD ON (warm reset to go)							
\$1F \$20	(031)	Set MegaSD Blink OFF Set MegaSD Blink ON (default)							
\$21	(033)	Set Lights Mode OFF w/ Auto LEDs Control (default)							
\$22 \$23	(034)	Set Lights Mode ON + Red Led OFF Set Lights Mode ON + Red Led ON							
\$24 \$25	(036) (037)	Internal Audio Preset #1 "Mute Sound" Internal Audio Preset #2 "Middle Sound"							
\$26 \$27	(038)	Internal Audio Preset #3 "High Sound" (default) Set CMT OFF (default) (disabled w/ MSXtR BIOS)	n/a on SM-X / SX-2 / SX-E						
\$28 \$29	(040)	Set CMT ON (needs a cassette recorder) (disabled w/ MSXtR BIOS) Lock CPU Clock Toggles	n/a on SM-X / SX-2 / SX-E						
\$2A	(042)	Unlock CPU Clock Toggles							
\$2B \$2C	(043)	Lock Video Output Toggles Unlock Video Output Toggles							
\$2D \$2E	(045)	Lock Audio Mixer & SCRLK Toggles Unlock Audio Mixer & SCRLK Toggles							
\$2F \$30	(047)	Lock Slot-1 Toggles Unlock Slot-1 Toggles							
\$31 \$32	(049)	Lock Slot-2 Toggles Unlock Slot-2 Toggles							
\$33 \$34	(051)	Lock Slot-1 & Slot-2 Toggles Unlock Slot-1 & Slot-2 Toggles							
\$35	(053)	Lock System Reset (Reset Combos & Hard Reset Key)							
\$36 \$37	(054) (055)	Unlock System Reset (Reset Combos & Hard Reset Key) Lock Internal Mapper Toggle							
\$38 \$39	(056) (057)	Unlock Internal Mapper Toggle Lock Internal MegaSD Toggle							
\$3A \$3B	(058) (059)	Unlock Internal MegaSD Toggle Lock All Toggles							
\$3C \$3D	(060)	Unlock All Toggles (default) Set Pseudo-Stereo OFF (default)							
\$3E	(062)	Set Pseudo-Stereo ON (needs an external sound cartridge) Sync External Bus Clock to CPU Speed (default)							
\$3F \$40	(063)	Set External Bus Clock 3.58MHz							
\$41 \$42	(<mark>065)</mark> (066)	Set Turbo Pana 5.37MHz Set Right Inverse Audio OFF (default)							
\$43 \$44	(067)	Set Right Inverse Audio ON Internal Audio Preset #4 "Emphasis PSG Sound"							
\$45 \$46	(069)	Internal Audio Preset #5 "Emphasis SCC-I Sound" Internal Audio Preset #6 "Emphasis OPLL Sound"							
\$47	(071)	Vertical Offset 16 (useful for Ark-A-Noah) Vertical Offset 17							
\$48 \$49	(073)	Vertical Offset 18							
\$4A \$4B	(074) (075)	Vertical Offset 19 (default) Vertical Offset 20							
\$4C \$4D	(076) (077)	Vertical Offset 21 Vertical Offset 22							
\$4E \$4F	(078) (079)	Vertical Offset 23 Vertical Offset 24 (useful for Space Manbow)							
\$50 \$51	(080)	Set VGA Scanlines None (default) Set VGA Scanlines Light	0% low-scale, 0% high-scale						
\$52	(082)	Set VGA Scanlines Medium	12% low-scale, 25% high-scale 25% low-scale, 50% high-scale						
\$53 \$54	(083)	Set VGA Scanlines Heavy Set Internal PSG2 OFF (default)	50% low-scale, 75% high-scale only for SM-X / SX-2 / SX-E						
\$55 \$56	(085) (086)	Set Internal PSG2 ON (this second PSG acts as an external PSG) Reserve Extra-Mapper 4096 KB OFF (default)	only for SM-X / SX-2 / SX-E						
\$57 \$58	(087)	Reserve Extra-Mapper 4096 KB ON (only available if SDRAM > 8 MB) Set Extended MegaROM Reading OFF (default for compatibility)							
\$59 \$5A	(089)	Set Extended MegaROM Reading ON (ASCII-8K/16K max size playable) Set Sprite Limit 4/8 (standard mode) (default)							
\$5B	(091)	Set Sprite Limit 8/8 (force MSX1 screens to use 8 sprites per line)							
\$5C \$5D	(092) (093)	Set VGA Interlace Single Field (not affected by scanlines) (default) Set VGA Interlace Duplicate Field Persona System Loro ON (warm recet only) [No effect via IRL-POM]							
\$7E \$7F	(126) (127)	Reserve System Logo ON (warm reset only) [No effect via IPL-ROM] Pixel Ratio 1:1 for LED Display (default is 0) (range 0 - 7) (60Hz only)	old I/O specs had \$FA (250)						
\$80 \$81	(128) (129)	Null Command \$80 (reserved) (useful for programming) Assign Legacy Output to VGA							
\$82 \$83	(130)	Assign Legacy Output to VGA+ (default) Set Internal Slot-1 Linear OFF (default)							
\$84 \$85	(132)	Set Internal Slot-1 Linear ON (requires SCC-I preset) Set Internal Slot-2 Linear OFF (default)							
\$86 \$86 \$87	(134) (135)	Set Internal Slot-2 Linear ON (requires SCC-I or ASCII-8K/16K preset) Set Internal OPL3 OFF (default)	only for SMA V / SW 3 / SW 5						
\$88	(136)	Set Internal OPL3 ON	only for SM-X / SX-2 / SX-E only for SM-X / SX-2 / SX-E						
\$89 \$8A	(137) (138)	Set F2 Device OFF (allows I/O access to PlaySoniq and other cartridges) Set F2 Device ON (essential to execute ESP8266 BIOS) (default)							
\$8B \$8F \$B0 \$B7	(139 143)	Reserved (Ducasp) Set Master Volume 0 7 (default level is 7)	only for SM-X / SX-2 / SX-E						
\$B8 \$BF \$C0 \$C7		Set PSG Volume 0 7 (default level is 4) Set SCC-I Volume 0 7 (default level is 4)							
\$C8 \$CF \$D0	(200207)	Set OPLL Volume 0 7 (default level is 4) Force NTSC Mode							
\$D1 \$D2	(209)	Standard NTSC/PAL Mode (bound by Control Register 9) (default) Force PAL Mode							
\$D3	(211)	Restore Default Keyboard Layout							
\$D4 \$D5	(212)	Null Command \$D4 (reserved) + Set C-BIOS Mode ON [Reserved for IPL-ROM] Restore Default Turbo Modes							
\$D6 \$D7	(214) (215)	Set Centering YJK Modes/R25 Mask OFF (default) Set Centering YJK Modes/R25 Mask ON							
\$F8 \$F9	(248) (249)	Reserve OCM-BIOS Reloading (cold reset or warm reset to go) Reserve Slot-0 Primary Mode (warm reset to go) (internal OPLL disabled)							
\$FA \$FB	(250)	Reserve Slot-0 Expanded Mode (warm reset to go) (default) Cold Reset [No effect via IPL-ROM]							
\$FC \$FD	(252)	Warm Reset w/ Mapper 2048 KB (RAM size 6144 KB if Extra-Mapper is ON) [C Warm Reset [No effect via IPL-ROM]	Only set Mapper via IPL-ROM]						
\$FE \$FF	(254) (255)	Warm Reset w/ Mapper 4096 KB (RAM size 8192 KB if Extra-Mapper is ON) [C Restore All Defaults + Reserve Default Mapper & MegaSD	Only set Mapper via IPL-ROM]						
ŞFF									
	More info on Switched I/O ports at MSX Assembly Page! http://map.grauw.nl/resources/msx_io_ports.php#switch_io								
	Positive	0 = OFF (read/write)							
R/W Logic		1 = ON 0 = ON (read n/write n)							
	CPU Clo	1 = OFF ('read_ii') write_ii')							
	Video Out		SCRLK key could handle						

Positive	0 = OFF	(read / write)	
	1 = ON	(*****)	
Negative	0 = ON	(read n/write n)	
Negative	1 = OFF	(Tead_IT/ WIRE_IT/	
CPU Clock	[F12] or	[DIP-SW1]	
Video Output	[(SHIFT+	-)PRTSCR] or [DIP-SW2/3]	SCRLK key could handle
Audio Mixer & SCRLK	[(SHIFT+	-)PGUP/PGDOWN/F9/F10/F11] & [SCRLK]	CMT or OPL3 depending
Cartridge Slot-1	[SHIFT+	F12] or [DIP-SW4]	on the type of machine
Cartridge Slot-2	[SHIFT+	SCRLK] or [DIP-SW5/6]	
System Reset	[LCTRL+	(SHIFT+)F12] Cold or Full Reset + [HARD RESET KEY] S	hort or Long-Click
Internal Mapper	[DIP-SW	/7] only	
Internal MegaSD	[DIP-SW	/8] only	
	Video Output Audio Mixer & SCRLK Cartridge Slot-1 Cartridge Slot-2 System Reset Internal Mapper	Positive	Negative