

\$40	( read_n / write )	ID 508 ID 5D4	MSX2+ FS-A1 Series MSX++ Computers and Compatibles	Z80B 5.37MHz OCM-PLD v2.4 or later
\$41	( read_n / write )	BIT 0	Smart Command ID  (see the list on the right)	CPL or \$FF (null)
		BIT 1		
		BIT 2		
		BIT 3		
		BIT 4		
		BIT 5		
		BIT 6		
\$42	( read / write_n )	BIT 0	CPU Clock	Virtual DIP-SW1
		BIT 1		Virtual DIP-SW2
		BIT 2		Video Output (MSB)
		BIT 3		Video Output (LSB)
		BIT 4		Cartridge Slot-1
		BIT 5		Cartridge Slot-2 (MSB)
		BIT 6		Cartridge Slot-2 (LSB)
\$43	( read / write_n )	BIT 0	Lock Mask of the Toggles	Virtual DIP-SW7
		BIT 1		Current Mapper Size
		BIT 2		Current MegaSD Mode
		BIT 3		Virtual DIP-SW8
		BIT 4		CPU Clock
		BIT 5		Video Output
		BIT 6		Audio Mixer & SCRLK
\$44	( read / write_n )	BIT 0	Lights I/O + Dynamic ID (d-ID)  (SM-X, SX-2 and SX-E only use Led 8 but this register works the same way)	Cartridge Slot-1
		BIT 1		Cartridge Slot-2
		BIT 2		Hard Reset Key
		BIT 3		Internal Mapper
		BIT 4		Internal MegaSD
		BIT 5		Led 1 Status
		BIT 6		Led 2 Status
\$45	( read / write_n )	BIT 0	PSG Volume Level (0 - 7)	Led 3 Status
		BIT 1		Led 4 Status
		BIT 2		Led 5 Status
		BIT 3		Led 6 Status
		BIT 4		Led 7 Status
		BIT 5		Led 8 Status
		BIT 6		Led 9 Status
\$46	( read / write_n )	BIT 0	PSG Mute	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$47	( read only )	BIT 0	Master Volume Level (0 - 7)	Status
		BIT 1		BIT 0 (LSB)
		BIT 2		BIT 1
		BIT 3		BIT 2 (MSB)
		BIT 4		Status
		BIT 5		BIT 0 (LSB)
		BIT 6		BIT 1
\$48	( read only )	BIT 0	OPLL Volume Level (0 - 7)	BIT 2 (MSB)
		BIT 1		Status
		BIT 2		BIT 0 (LSB)
		BIT 3		BIT 1
		BIT 4		BIT 2 (MSB)
		BIT 5		Status
		BIT 6		BIT 0 (LSB)
\$49	( read only )	BIT 0	SCC-I Volume Level (0 - 7)	BIT 1
		BIT 1		BIT 2 (MSB)
		BIT 2		Status
		BIT 3		BIT 0 (LSB)
		BIT 4		BIT 1
		BIT 5		BIT 2 (MSB)
		BIT 6		Status
\$4A	( read only )	BIT 0	SCC-I Mute	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4B	( read only )	BIT 0	CPU Custom Speed Level (1 - 7)	Status
		BIT 1		BIT 0 (LSB)
		BIT 2		BIT 1
		BIT 3		BIT 2 (MSB)
		BIT 4		Status
		BIT 5		BIT 0 (LSB)
		BIT 6		BIT 1
\$4C	( read only )	BIT 0	Turbo MegaSD (tMSD)	BIT 2 (MSB)
		BIT 1		Status
		BIT 2		BIT 0 (LSB)
		BIT 3		BIT 1
		BIT 4		BIT 2 (MSB)
		BIT 5		Status
		BIT 6		BIT 0 (LSB)
\$4D	( read / write_n )	BIT 0	Turbo Pana Redirection (tPR)	BIT 1
		BIT 1		BIT 2 (MSB)
		BIT 2		Status
		BIT 3		BIT 0 (LSB)
		BIT 4		BIT 1
		BIT 5		BIT 2 (MSB)
		BIT 6		Status
\$4E	( read only )	BIT 0	VDP Speed Mode	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4F	( write_n only )	BIT 0	Mapper Size Req	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4F	( write_n only )	BIT 0	MegaSD Mode Req	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4B	( read only )	BIT 0	Turbo Pana	Status
		BIT 1		0=JP, 1=Non-JP
		BIT 2		SCRLK Toggle
		BIT 3		Lights Mode
		BIT 4		0=Auto, 1=ON
		BIT 5		Red Mode (Led 0)
		BIT 6		Status
\$4C	( read only )	BIT 0	Last Reset Ack	0=Cold Reset ack, 1=Warm Reset ack
		BIT 1		Status
		BIT 2		Reset Required Flag
		BIT 3		Status
		BIT 4		MegaSD Blink
		BIT 5		Status
		BIT 6		Status
\$4D	( read / write_n )	BIT 0	Pseudo Stereo	Status
		BIT 1		External Clock Mode
		BIT 2		0=Sync to CPU, 1=3.58MHz
		BIT 3		Machine Type ID
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4E	( read only )	BIT 0	NTSC/PAL Type	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4F	( read only )	BIT 0	Forced Video Mode	0=Forced, 1=Auto
		BIT 1		0=60Hz (NTSC), 1=50Hz (PAL)
		BIT 2		Status
		BIT 3		Right Inverse Audio
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4B	( read only )	BIT 0	Pixel Ratio 1:1 for LED Display	Status
		BIT 1		BIT 0 (LSB)
		BIT 2		BIT 1
		BIT 3		BIT 2 (MSB)
		BIT 4		Status
		BIT 5		0=To VGA, 1=To VGA+
		BIT 6		Assignment of Legacy Output
\$4C	( read only )	BIT 0	Internal Slot-1 Linear	Status
		BIT 1		Internal Slot-2 Linear
		BIT 2		Status
		BIT 3		BIT 0 (LSB)
		BIT 4		BIT 1
		BIT 5		BIT 2 (MSB)
		BIT 6		Status
\$4D	( read / write_n )	BIT 0	Extended MegaROM Reading	Status
		BIT 1		Auxiliary SDRAM Size
		BIT 2		0=64 MB, 1=128 MB, 2=192 MB, 3=256 MB, 4=384 MB, 5=512 MB, 6=768 MB, 7=1024 MB
		BIT 3		BIT 0 (LSB)
		BIT 4		BIT 1
		BIT 5		BIT 2 (MSB)
		BIT 6		Status
\$4E	( read only )	BIT 0	Vertical Offset (Status+12)	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4F	( read only )	BIT 0	Sprite Limit	0=4/8 (standard), 1=8/8 (enhanced)
		BIT 1		Status
		BIT 2		Safe Mode
		BIT 3		C-BIOS Mode
		BIT 4		Extra-Mapper 4096 KB Ack
		BIT 5		Current Slot-0 Mode
		BIT 6		0=Primary, 1=Expanded
\$4B	( read only )	BIT 0	Free	BIT 0 (LSB)
		BIT 1		BIT 1
		BIT 2		BIT 2 (MSB)
		BIT 3		Status
		BIT 4		BIT 0 (LSB)
		BIT 5		BIT 1
		BIT 6		BIT 2 (MSB)
\$4C	( read only )	BIT 0	CPU Clock	Hard DIP-SW1
		BIT 1		Hard DIP-SW2
		BIT 2		Video Output (MSB)
		BIT 3		Video Output (LSB)
		BIT 4		Cartridge Slot-1
		BIT 5		Cartridge Slot-2 (MSB)
		BIT 6		Cartridge Slot-2 (LSB)
\$4D	( read / write_n )	BIT 0	Internal Mapper	Hard DIP-SW6
		BIT 1		Hard DIP-SW7
		BIT 2		Hard DIP-SW8
		BIT 3		Internal MegaSD
		BIT 4		Hard DIP-SW1
		BIT 5		Hard DIP-SW2
		BIT 6		Hard DIP-SW3
\$4E	( read only )	BIT 0	64 KB VRAM Slot ID (Page 0)	Hard DIP-SW4
		BIT 1		Hard DIP-SW5
		BIT 2		Hard DIP-SW6
		BIT 3		Hard DIP-SW7
		BIT 4		Hard DIP-SW8
		BIT 5		Internal Mapper
		BIT 6		Internal MegaSD
\$4F	( read only )	BIT 0	64 KB VRAM Slot ID (Page 1)	Hard DIP-SW1
		BIT 1		Hard DIP-SW2
		BIT 2		Hard DIP-SW3
		BIT 3		Hard DIP-SW4
		BIT 4		Hard DIP-SW5
		BIT 5		Hard DIP-SW6
		BIT 6		Hard DIP-SW7
\$4B	( read only )	BIT 0	I/O Revision ID (0 - 31)	Hard DIP-SW8
		BIT 1		Internal Mapper
		BIT 2		Internal MegaSD
		BIT 3		Hard DIP-SW1
		BIT 4		Hard DIP-SW2
		BIT 5		Hard DIP-SW3
		BIT 6		Hard DIP-SW4
\$4C	( write only )	BITS 7 .. 0	VDP ID Selector	0=1-ID #0 (V9938), 2=255-ID #2 (V9958)
		BITS 6 .. 0		-
		BITS 5 .. 0		-
		BITS 4 .. 0		-
		BITS 3 .. 0		-
		BITS 2 .. 0		-
		BITS 1 .. 0		-
\$4D	( write_n only )	BITS 7 .. 0	JIS2 Enabler	0=JIS1+JIS2, 1=JIS1 only
		BITS 6 .. 0		-
		BITS 5 .. 0		-
		BITS 4 .. 0		-
		BITS 3 .. 0		-
		BITS 2 .. 0		-
		BITS 1 .. 0		-
\$4E	( write_n only )	BITS 7 .. 0	F4 Device Mode	0=Normal, 1=Inverted
		BITS 6 .. 0		-