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REQUIREMENTS NOT MET

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N/A

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PROBLEMS ENCOUNTERED

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N/A

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**HOMEWORK EXERCISES**

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Figure 1: Homework 10 - VDHL of Code for Combinational Logic Block for Hex to 7-Segment Decoder

Homework 10

Name: Natalie Poche

Class #: 11198

Description: combinational logic block with VHDL code for Hex to 7-Segment Decoder

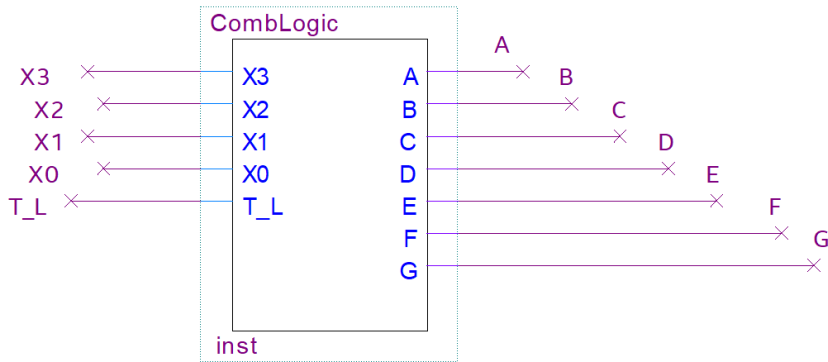
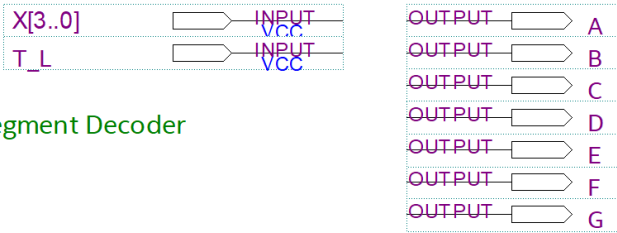


Figure 2: Homework 10 - BDF of Combinational Logic Block for Hex to 7-Segment Decoder

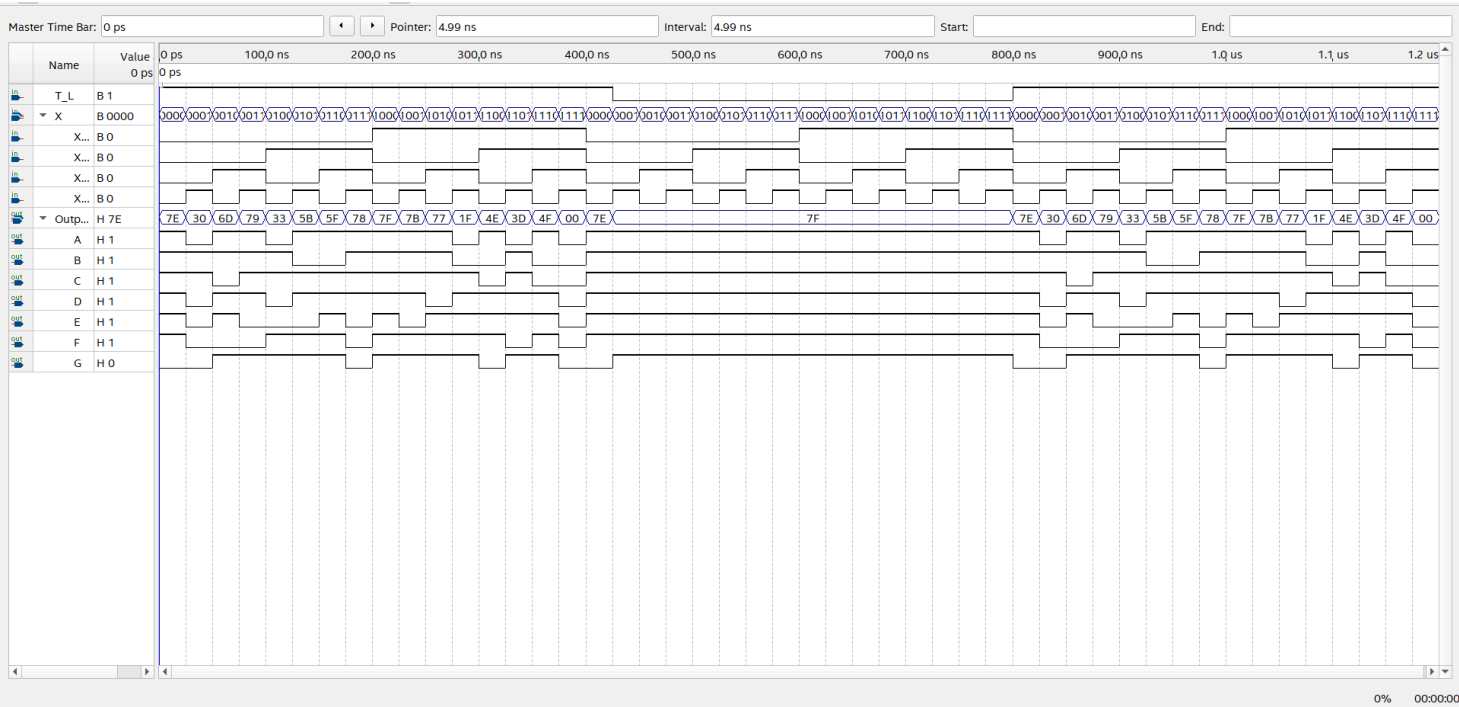


Figure 3: Homework 10 - Functional Simulation of Hex to 7-Segment Decoder