

Hypothesis Testing for Error Mitigation

Quantum Wednesday

Nate Stemen

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Today's Paper

HYPOTHESIS TESTING FOR ERROR MITIGATION: HOW TO EVALUATE ERROR MITIGATION

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https://arxiv.org/abs/2301.02690

Overview

Two main questions:

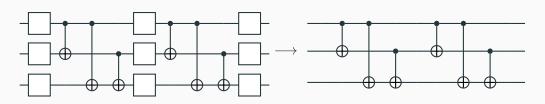
- 1. How can we understand "stacking" error mitigation techniques?
- 2. How can we assess the tradeoffs associated with a variety of techniques in a way that captures overhead?

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Noise Estimation Circuits



Mitigating depolarizing noise on quantum computers with noise-estimation circuits

Miroslav Urbanek, 1, * Benjamin Nachman, Vincent R. Pascuzzi, Andre He, 2, † Christian W. Bauer, and Wibe A. de Jong 1, ‡

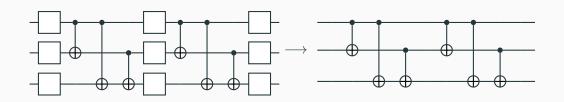
¹ Computational Research Division, Lawrence Berkeley National Laboratory, Berkeley, CA 94720, USA
² Physics Division, Lawrence Berkeley National Laboratory, Berkeley, CA 94720, USA

A significant problem for current quantum computers is noise. While there are many distinct noise channels, the depolarizing noise model often appropriately describes average noise for large circuits involving many qubits and gates. We present a method to mitigate the depolarizing noise by first estimating its rate with a noise-estimation circuit and then correcting the output of the target circuit using the estimated rate. The method is experimentally validated on the simulation of the Heisenberg model. We find that our approach in combination with readout-error correction, randomized compiling, and zero-noise extrapolation produces results close to exact results even for circuits containing hundreds of CNOT gates.

https://arxiv.org/abs/2103.08591

$\langle O \rangle \equiv \frac{\#b_{00\cdots 0}}{N}$

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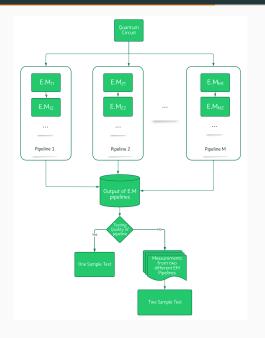
Mitigating depolarizing noise on quantum computers with noise-estimation circuits

A significant problem for current quantum computers is noise. While there are many distinct noise channels, the depolarizing noise model often appropriately describes average noise for large circuits involving many qubits and gates. We present a method to mitigate the depolarizing noise by first estimating its rate with a noise-estimation circuit and then correcting the output of the target circuit using the estimated rate. The method is experimentally validated on the simulation of the Heisenberg model. We find that our approach in combination with readout-error correction, randomized compiling, and zero-noise extrapolation produces results close to exact results even for circuits containing hundreds of CNOT gates.

$$\langle O \rangle = \frac{\langle O \rangle_{\text{noisy}}}{\frac{\#b_{00\cdots 0}}{N}}$$

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Error Mitigation Pipelines



Pipeline	Composition
\mathcal{P}_1	ZNE
\mathcal{P}_2	${\tt ZNE} + {\tt MEM}$
\mathcal{P}_3	${\tt ZNE} + {\tt DD}$
\mathcal{P}_4	${\tt ZNE} + {\tt DD} + {\tt MEM}$
\mathcal{P}_5	${\tt ZNE} + {\tt RC}$
\mathcal{P}_6	${\tt ZNE} + {\tt RC} + {\tt MEM}$
\mathcal{P}_7	${\tt ZNE} + {\tt RC} + {\tt DD}$
\mathcal{P}_8	${\tt ZNE} + {\tt RC} + {\tt DD} + {\tt MEM}$

 $\mathcal{P}_i^{(E)}$ indicates extrapolation performed on scaled expectation values.

- 1. Zero-Noise extrapolation
 - Scale factors: [1, 3, 5]
 - Folding methods: Global folding, CNOT folding
 - Extrapolation methods: Linear, Quadratic
- Measurement Error Mitigation

•
$$C_{\text{mitigated}} = M_{\text{calib}}^{-1} \cdot C_{\text{noisy}}$$

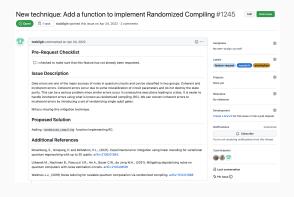
- 3. Randomized Compiling
 - Convert coherent errors into incoherent ones
- 4. Dynamic Decoupling
 - X-X sequence

```
mitiq.zne.execute_with_zne(
    circuit,
    executor,
    factory=LinearFactory([1.0, 2.0, 3.0]),
    scale_noise=fold_global,
)
```

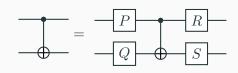
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```
mitiq.rem.execute_with_rem(
    circuit,
    executor,
    observable,
    inverse_confusion_matrix=icm,
)
```

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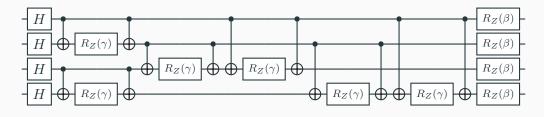
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P	Q	R	S
I	I	I	I
I	X	I	X
I	Y	Z	Y
:			

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Circuits



Ten (γ, β) pairs chosen to cover the possible ideal expectation values (-0.62, 2).

Figure of Merit

What's harder

- 1. running 1 circuit with 10,000 shots, or
- 2. running 10 circuits with 1,000 shots each?

$$R = T(1+S)$$

$$S = -\sum_{i} p_{C_i} \ln(p_{C_i})$$

$$T = \sum_{i} N_{C_i} D_{C_i}$$

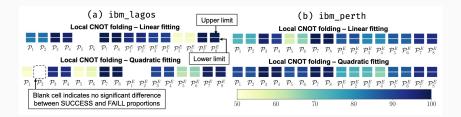
$$p_{C_i} = \frac{N_{C_i} D_{C_i}}{T}$$

$$M = \frac{PSR(\%)}{\epsilon \cdot R}$$

$$N_{C_i} =$$
 number of shots for circuit C_i
$$D_{C_i} = \text{duration of circtui } C_i$$

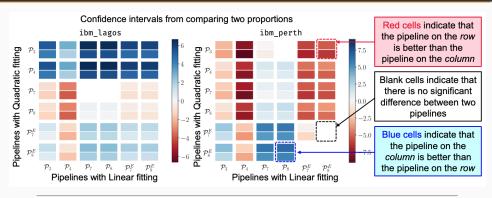
$$\epsilon = \text{median improvement factor (REM)}$$

Results



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Results



Device	Pipeline	R	REM (lin)	REM (quad)	PSR (lin)	PSR (quad)	M (lin)	M (quad)
ibm_lagos	\mathcal{P}_3	2.8494	0.5397	0.3126	0.9858	0.9352	64.1009	104.9981
	\mathcal{P}_4	3.8006	0.5097	0.2435	0.9609	0.9339	49.6020	100.9129
	\mathcal{P}_8	9.9802	0.2815	0.0992	1.0000	0.9981	35.5945	100.8167
	\mathcal{P}_7^E	20.1687	0.1170	0.1240	0.9915	0.9780	42.0164	39.1058
ibm_perth	\mathcal{P}_3	2.3061	0.6899	0.4267	0.9623	0.9948	60.4875	101.0925
	\mathcal{P}_4	3.1189	0.5950	0.3007	0.9055	0.9811	48.7956	104.6124
	\mathcal{P}_7^E	16.5019	0.1578	0.1625	0.9258	0.9363	35.5540	34.9154

Results

