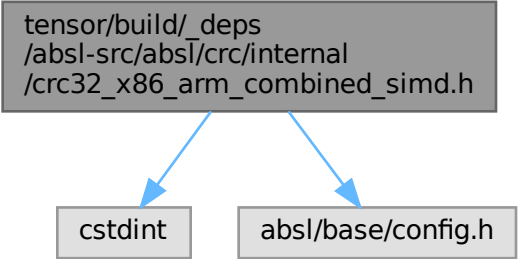


tensor/build/_deps
/absl-src/absl/crc/internal
/crc32_x86_arm_combined_simd.h



```
graph TD; A["tensor/build/_deps<br>/absl-src/absl/crc/internal<br>/crc32_x86_arm_combined_simd.h"] --> B["cstdint"]; A --> C["absl/base/config.h"]
```

cstdint

absl/base/config.h