

absl::crc\_internal  
::CrcNonTemporalMemcpyAVXEngine  
::Compute

absl::crc\_internal  
::CrcNonTemporalMemcpyEngine  
::Compute

absl::crc\_internal  
::FallbackCrcMemcpyEngine  
::Compute

absl::ComputeCrc32c

absl::ExtendCrc32c

```
graph LR; A["absl::crc_internal::CrcNonTemporalMemcpyAVXEngine::Compute"] --> D["absl::ExtendCrc32c"]; B["absl::crc_internal::CrcNonTemporalMemcpyEngine::Compute"] --> D; C["absl::crc_internal::FallbackCrcMemcpyEngine::Compute"] --> D; E["absl::ComputeCrc32c"] --> D;
```

The diagram illustrates a function dispatch or selection mechanism. On the left, there are four white rectangular boxes, each containing a fully qualified function name. On the right, there is a single grey rectangular box representing the target function. Four blue arrows originate from the right side of each white box and point towards the left side of the grey box, indicating that each of the four functions on the left can be used to implement or dispatch to the target function.