

Eigen::internal::pdiv
< Packet8us >

Eigen::internal::plset
< Packet8us >

Eigen::internal::plset
< Packet8us >

Eigen::internal::pset1
< Packet8us >

```
graph LR; A["Eigen::internal::pdiv<br>< Packet8us >"] --> D["Eigen::internal::pset1<br>< Packet8us >"]; B["Eigen::internal::plset<br>< Packet8us >"] --> D; C["Eigen::internal::plset<br>< Packet8us >"] --> D;
```

The diagram illustrates a data flow or dependency. On the left, there are three white rectangular boxes with black borders. The top box contains the text 'Eigen::internal::pdiv' followed by '< Packet8us >' on the next line. The middle box contains 'Eigen::internal::plset' followed by '< Packet8us >'. The bottom box contains 'Eigen::internal::plset' followed by '< Packet8us >'. On the right, there is a single gray rectangular box with a black border, containing the text 'Eigen::internal::pset1' followed by '< Packet8us >'. Three blue arrows point from the right side of each of the three white boxes to the left side of the gray box, indicating that the three white boxes are inputs or dependencies for the gray box.