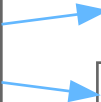


Eigen::TensorSycl::  
internal::TensorContraction  
Kernel::compute\_block\_per\_tile



```
graph LR; A["Eigen::TensorSycl::  
internal::TensorContraction  
Kernel::compute_block_per_tile"] --> B["Eigen::internal::pmadd"]; A --> C["Eigen::TensorSycl::  
internal::PacketWrapper  
::set_packet"]
```

Eigen::internal::pmadd

Eigen::TensorSycl::  
internal::PacketWrapper  
::set\_packet