

Eigen::internal::pdiv
< Packet16c >

Eigen::internal::plset
< Packet16c >

Eigen::internal::plset
< Packet16c >

Eigen::internal::pset1
< Packet16c >

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graph LR; A[Eigen::internal::pdiv<br/>< Packet16c >] --> D[Eigen::internal::pset1<br/>< Packet16c >]; B[Eigen::internal::plset<br/>< Packet16c >] --> D; C[Eigen::internal::plset<br/>< Packet16c >] --> D;
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The diagram illustrates a data flow where three separate source components on the left are combined into a single destination component on the right. The source components are 'Eigen::internal::pdiv < Packet16c >', 'Eigen::internal::plset < Packet16c >', and another 'Eigen::internal::plset < Packet16c >'. All three have blue arrows pointing to a single grey-shaded destination box labeled 'Eigen::internal::pset1 < Packet16c >'.