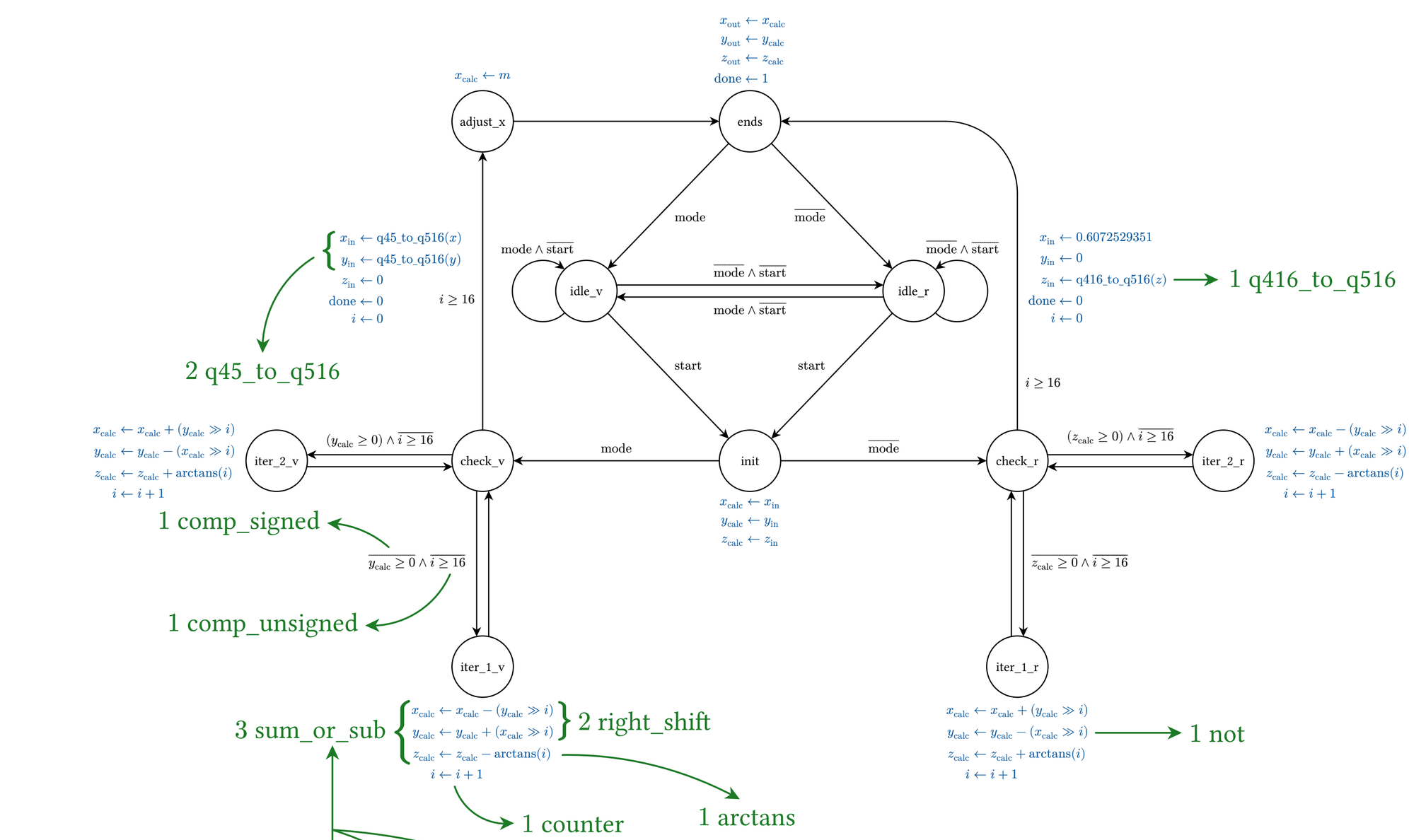

bits:

x_{in}: 9	x_{calc}: 21	x_{out}: 21	start: 1	i: 1
y_{in}: 9	y_{calc}: 21	y_{out}: 21	done: 1	
z_{in}: 20	z_{calc}: 21	x_{out}: 21	mode: 1	

muxes:

x_{in}: q45_to_q516(x) $\vee$ 0.6072529351 $\rightarrow$ 1 mux_2x1

y_{in}: q45_to_q516(y) $\vee$ 0 $\rightarrow$ 1 mux_2x1

z_{in}: 0 $\vee$ q416_to_q516(z) $\rightarrow$ 1 mux_2x1

x_{calc}: $(x_{in}) \vee (x_{calc} \pm (y_{calc} \gg i)) \vee (m)$ $\rightarrow$ 1 mux_3x1

y_{calc}: $(y_{in}) \vee (y_{calc} \pm (x_{calc} \gg i))$ $\rightarrow$ 1 mux_2x1

z_{calc}: $(z_{in}) \vee \arctan s(i)$ $\rightarrow$ 1 mux_2x1

zero_comp arg: $(y_{calc}) \vee (z_{calc})$ $\rightarrow$ 1 mux_2x1

1st sum_or_sub args: $[x_{calc},\ (y_{calc} \gg i)] \vee [(x_{calc} \gg 1),\ (x_{calc} \gg 3)]$ $\rightarrow$ 2 mux_2x1

2nd sum_or_sub args: $[y_{calc},\ (x_{calc} \gg i)] \vee \{[(x \gg 1) + (x \gg 3)],\ [(x \gg 6) + (x \gg 9) + (x \gg 12)]\}$ $\rightarrow$ 2 mux_2x1

3rd sum_or_sub args: $[z_{calc},\ \arctan s(i)] \vee [(x_{calc} \gg 6),\ (x_{calc} \gg 9)]$ $\rightarrow$ 2 mux_2x1