

CORDIC

1. Rotation Mode

1.1. Inputs:

- mode (1 bit) $\leftarrow 1$
- start (1 bit) $\leftarrow 1$
- $x_{in} \leftarrow x$
- $y_{in} \leftarrow x$
- z_{in} (20 bits) \leftarrow angle (Q4.16)

1.2. Set internal values:

- done (1 bit) $\leftarrow 0$
- x_{calc} (21 bits) $\leftarrow 0.6072529351$ (Q5.16)
- y_{calc} (21 bits) $\leftarrow 0$
- z_{calc} (21 bits) $\leftarrow q416_to_q516(z_{in})$
- i (5 bits) $\leftarrow 0$

1.3. Iteractive process:

while $i \neq 16$:

if $z_{calc} \geq 0$:

- $x_{temp} \leftarrow x_{calc} - (y_{calc} \gg i)$
- $y_{temp} \leftarrow y_{calc} + (x_{calc} \gg i)$
- $z_{calc} \leftarrow z_{calc} - \text{arctans_list}(i)$

else :

- $x_{temp} \leftarrow x_{calc} + (y_{calc} \gg i)$
- $y_{temp} \leftarrow y_{calc} - (x_{calc} \gg i)$
- $z_{calc} \leftarrow z_{calc} + \text{arctans_list}(i)$
- $x_{calc} \leftarrow x_{temp}$
- $y_{calc} \leftarrow y_{temp}$
- $i \leftarrow i + 1$

1.4. Outputs:

- done (1 bit) $\leftarrow 1$
- x_{out} (21 bits) $\leftarrow x_{calc}$
- y_{out} (21 bits) $\leftarrow y_{calc}$
- z_{out} (21 bits) $\leftarrow 0$

1.5. Conclusion:

- $x_{out} = \cos(z_{in})$
- $y_{out} = \sin(z_{in})$
- $z_{out} = 0$

2. Vectoring Mode

2.1. Inputs:

- mode (1 bit) $\leftarrow 0$
- start (1 bit) $\leftarrow 1$
- x_{in} (9 bits) $\leftarrow x$ (Q4.5)
- y_{in} (9 bits) $\leftarrow y$ (Q4.5)
- $z_{in} \leftarrow x$

2.2. Set internal values:

- done (1 bit) $\leftarrow 0$
- x_{calc} (21 bits) $\leftarrow q45_to_q516(x_{in})$
- y_{calc} (21 bits) $\leftarrow q45_to_q516(y_{in})$
- z_{calc} (21 bits) $\leftarrow 0$
- i (5 bits) $\leftarrow 0$

2.3. Iteractive process:

while $i \neq 16$:

if $y_{calc} \geq 0$:

- $x_{temp} \leftarrow x_{calc} + (y_{calc} \gg i)$
- $y_{temp} \leftarrow y_{calc} - (x_{calc} \gg i)$
- $z_{calc} \leftarrow z_{calc} + \text{arctans_list}(i)$

else :

- $x_{temp} \leftarrow x_{calc} - (y_{calc} \gg i)$
- $y_{temp} \leftarrow y_{calc} + (x_{calc} \gg i)$
- $z_{calc} \leftarrow z_{calc} - \text{arctans_list}(i)$
- $x_{calc} \leftarrow x_{temp}$
- $y_{calc} \leftarrow y_{temp}$
- $i \leftarrow i + 1$

2.4. Adjust x_{calc} :

- $x_{calc} \leftarrow x_{calc} \cdot 0.6072529351$

2.5. Outputs:

- done (1 bit) $\leftarrow 1$
- x_{out} (21 bits) $\leftarrow x_{calc}$
- y_{out} (21 bits) $\leftarrow 0$
- z_{out} (21 bits) $\leftarrow z_{calc}$

2.6. Conclusion:

- $x_{out} = \|\vec{v}\| = \sqrt{x_{in}^2 + y_{in}^2}$
- $y_{out} = 0$
- $z_{out} = \arctan\left(\frac{y_{in}}{x_{in}}\right)$

$$x \cdot 0.6072529351 = [(x \gg 1) + (x \gg 3)] - \{[(x \gg 6) + (x \gg 9)] + (x \gg 12)\}$$