Distributed Algorithms as Register Automata

Benedikt Bollig Patricia Bouyer <u>Fabian Reiter</u>

LSV, University of Paris-Saclay

5 December 2018 @ LaBRI, Bordeaux

Identifiers in Registers

Describing Network Algorithms with Logic

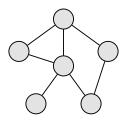
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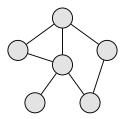
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∃ SECOND-ORDER LOGIC

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Example: Hamiltonian path

∃ SECOND-ORDER LOGIC

Example: Hamiltonian path

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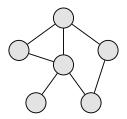
∃ SECOND-ORDER LOGIC

Example: Hamiltonian path

 $\exists R ("R \text{ is a strict total order"} \land$

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∃ SECOND-ORDER LOGIC



Example: Hamiltonian path

 $\exists R \text{ ("R is a strict total order" } \land$ "R-successors are adjacent")

∃ SECOND-ORDER LOGIC

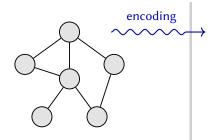
Example: Hamiltonian path

 $\exists R ("R \text{ is a strict total order"} \land "R-successors are adjacent"})$

NP TURING MACHINES

∃ SECOND-ORDER LOGIC

NP TURING MACHINES

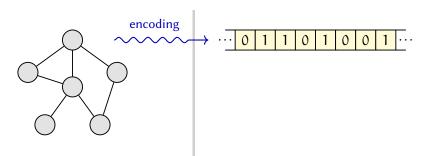


Example: Hamiltonian path

 $\exists R ("R \text{ is a strict total order"} \land "R-successors are adjacent")$

∃ SECOND-ORDER LOGIC

NP TURING MACHINES

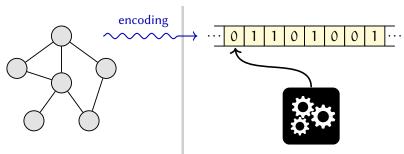


Example: Hamiltonian path

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NP TURING MACHINES



Example: Hamiltonian path

∃R ("R is a strict total order" ∧

"R-successors are adjacent")

∃ SECOND-ORDER LOGIC

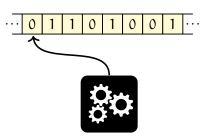
encoding

Example: Hamiltonian path

∃R ("R is a strict total order" ∧

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NP TURING MACHINES



Nondeterministic moves

∃ SECOND-ORDER LOGIC

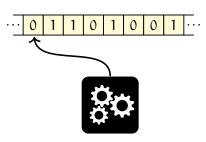
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Example: Hamiltonian path

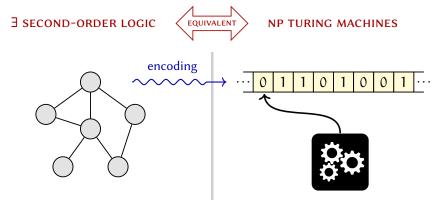
∃R ("R is a strict total order" ∧

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NP TURING MACHINES



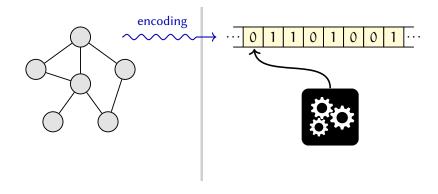
- Nondeterministic moves
- Polynomial running time



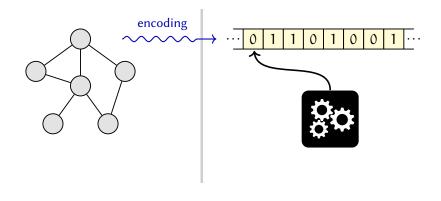
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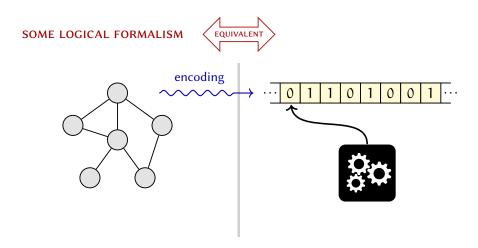
 "R-successors are adjacent")

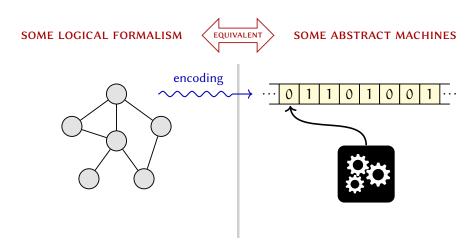
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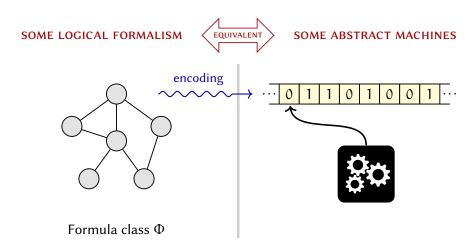


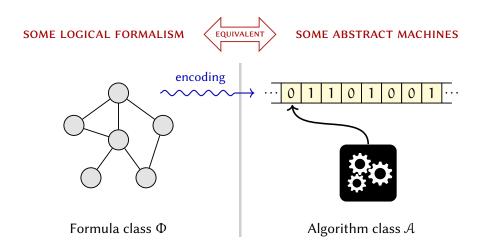
SOME LOGICAL FORMALISM



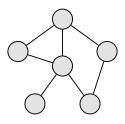








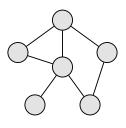
SOME LOGICAL FORMALISM



Formula class Φ





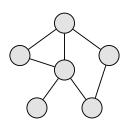


Formula class Φ

SOME LOGICAL FORMALISM



COMMUNICATING MACHINES

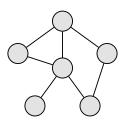


Formula class Φ

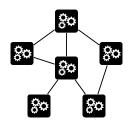
SOME LOGICAL FORMALISM



COMMUNICATING MACHINES



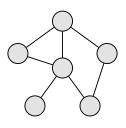
Formula class Φ



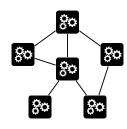
SOME LOGICAL FORMALISM



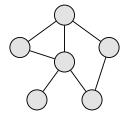
COMMUNICATING MACHINES

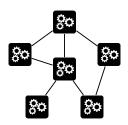


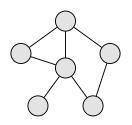
Formula class Φ

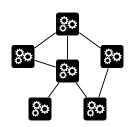


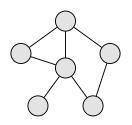
Distributed algorithm class $\ensuremath{\mathcal{A}}$

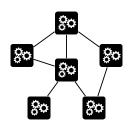






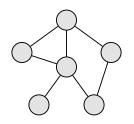


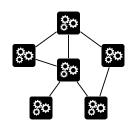




$$\overset{\text{\tiny Q}}{\circ} \circ : (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$

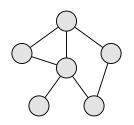
DISTR. REGISTER AUTOMATA

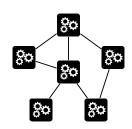




$$\bigcirc \circ : (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$

► Finite-state & registers



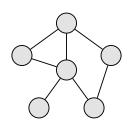


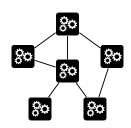
$$^{\circ}_{\circ} \circ : \left(\, Q \times \mathbb{N}^{R} \, \right)^{\scriptscriptstyle +} \, \rightarrow \, Q \times \mathbb{N}^{R}$$

- ► Finite-state & registers
- ► Synchronous execution

FUNCTIONAL FIXPOINT LOGIC restricted to ordered graphs





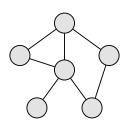


$$Q_{Q} \circ (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$

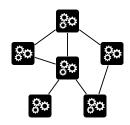
- ► Finite-state & registers
- Synchronous execution

FUNCTIONAL FIXPOINT LOGIC restricted to ordered graphs





$$\begin{aligned} & \textbf{pfp} \begin{bmatrix} f_1 \colon \phi_1(f_1, f_2, \text{in}, \text{out}) \\ f_2 \colon \phi_2(f_1, f_2, \text{in}, \text{out}) \end{bmatrix} \psi \end{aligned}$$

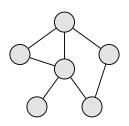


$$? \circ : (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$

- ► Finite-state & registers
- Synchronous execution

Contribution

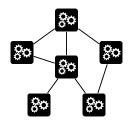
FUNCTIONAL FIXPOINT LOGIC restricted to ordered graphs



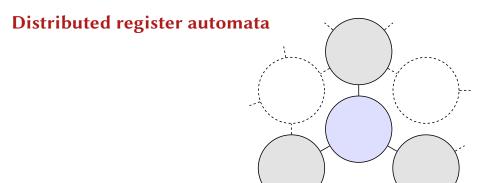
$$\mathbf{pfp}\begin{bmatrix} f_1 \colon \phi_1(f_1, f_2, IN, OUT) \\ f_2 \colon \phi_2(f_1, f_2, IN, OUT) \end{bmatrix} \psi$$



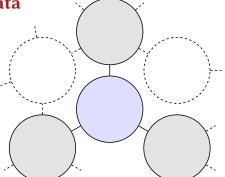
DISTR. REGISTER AUTOMATA



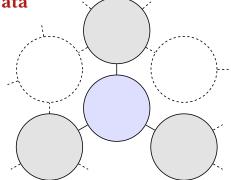
- $Q : (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$
- ► Finite-state & registers
- Synchronous execution



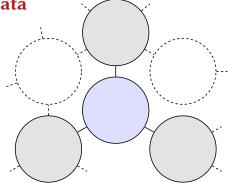
► Connected, undirected network



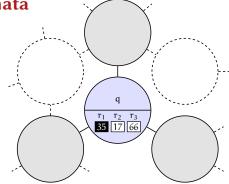
- ► Connected, undirected network
- Synchronous execution



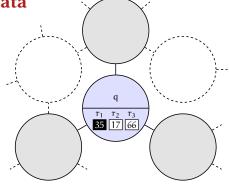
- ► Connected, undirected network
- Synchronous execution
- ightharpoonup Unique identifiers in $\mathbb N$



- Connected, undirected network
- Synchronous execution
- ▶ Unique identifiers in \mathbb{N}



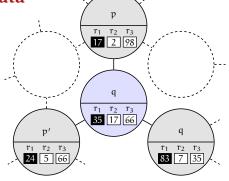
- Connected, undirected network
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$$Q = \{p, ..., q'\} \Leftrightarrow \text{states}$$

 $R = \{r_1, r_2, r_3\} \Leftrightarrow \text{registers}$

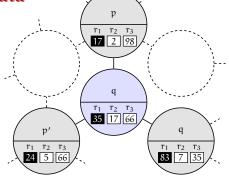
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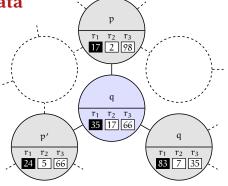
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$$^{\circ}_{\circ} \circ : \left(\, Q \times \mathbb{N}^{R} \, \right)^{\scriptscriptstyle +} \, \rightarrow \, Q \times \mathbb{N}^{R}$$





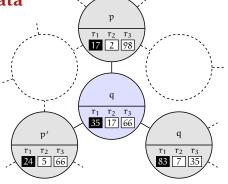
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$$\bigcirc \circ : (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$





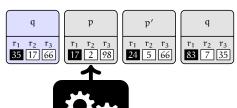
$$Q = \{p, ..., q'\} \iff \text{states}$$

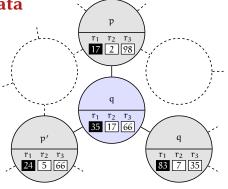
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- Connected, undirected network
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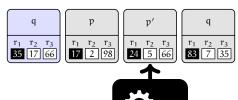


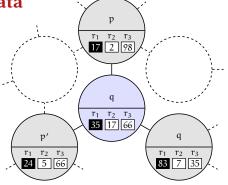
$$Q = \{p, ..., q'\} \sim \text{states}$$

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- Connected, undirected network
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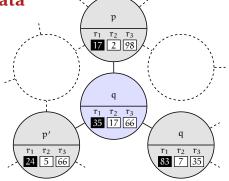
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$${\overset{\circ}{\circ}} {\overset{\circ}{\circ}} : (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$





$$Q = \{p, ..., q'\} \Leftrightarrow \text{states}$$

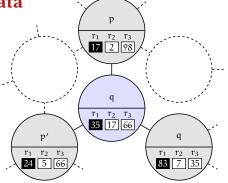
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- Connected, undirected network
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$${}^{\bullet}_{\sigma} \circ (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$





$$Q = \{p, ..., q'\} \Leftrightarrow \text{states}$$

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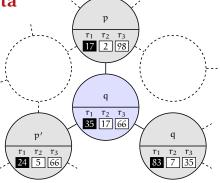
- Connected, undirected network
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Transition maker





$$Q = \{p, ..., q'\} \iff \text{states}$$

$$R = \{r_1, r_2, r_3\} \iff \text{registers}$$

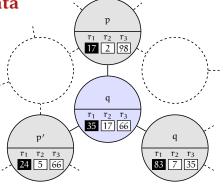


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Transition maker





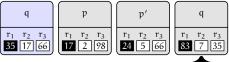
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- Connected, undirected network
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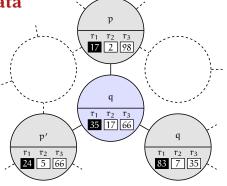
$$^{\circ}_{\sigma} \circ (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$



Transition maker can:

- compare registers (<),
- copy register values.



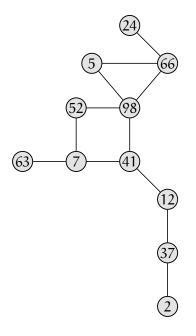


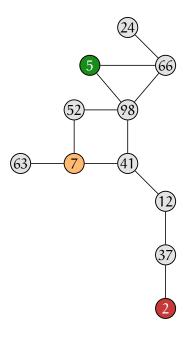
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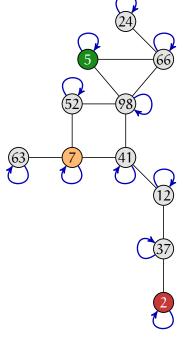
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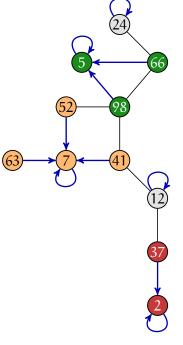


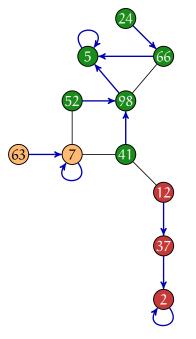


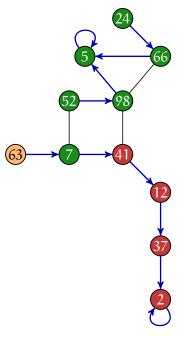


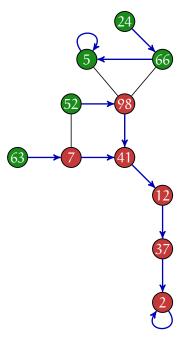


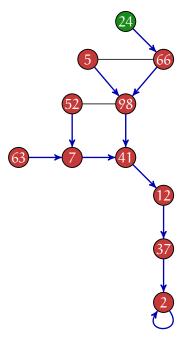


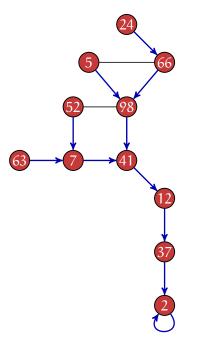




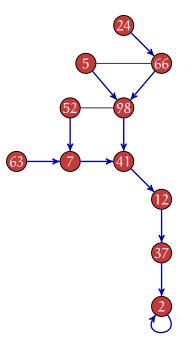








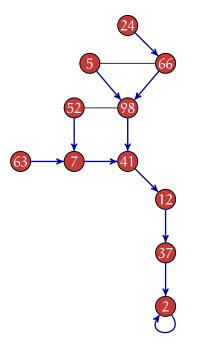
 $R = \{self, parent, root\}$



A. If ∃ neighbor NB (NB.root < MY.root):

 $\mathsf{MY.parent} \leftarrow \mathsf{NB.self}$

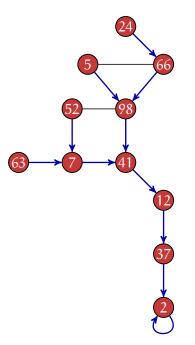
MY.root ← NB.root



$$Q = \{a, b, c\}$$
 with a initial $R = \{\text{self}, \text{parent}, \text{root}\}$

A. If ∃ neighbor NB (NB.root < MY.root):

```
MY.parent ← NB.self
MY.root ← NB.root
```

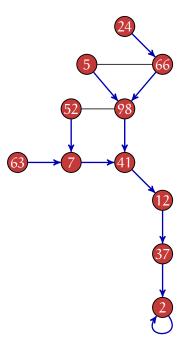


$$Q = \{a, b, c\}$$
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A. If ∃ neighbor NB (NB.root < MY.root):

 $\begin{aligned} & \text{MY.parent} \leftarrow \text{NB.self} \\ & \text{MY.root} \leftarrow \text{NB.root} \end{aligned}$

 $MY.state \leftarrow \alpha$

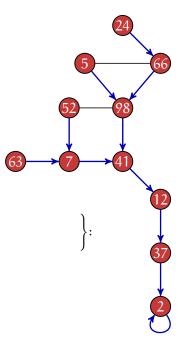


$$Q = \{a, b, c\}$$
 with a initial $R = \{\text{self}, \text{parent}, \text{root}\}$

A. If ∃ neighbor NB (NB.root < MY.root):

$$MY.parent \leftarrow NB.self$$
 $MY.root \leftarrow NB.root$
 $MY.state \leftarrow \alpha$

B. If \forall neighbor NB $\begin{cases} NB.root = MY.root \land \\ NB.parent \neq MY.self \end{cases}$ $MY.state \leftarrow b$



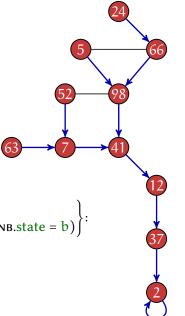
$$Q = \{a, b, c\}$$
 with a initial $R = \{\text{self}, \text{parent}, \text{root}\}$

A. If \exists neighbor NB (NB.root < MY.root):

MY.parent ← NB.self $MY.root \leftarrow NB.root$

 $MY.state \leftarrow \alpha$

B. If \forall neighbor NB $\begin{cases} NB.root = MY.root \land \\ (NB.parent \neq MY.self \lor NB.state = b) \end{cases}$: $MY.state \leftarrow b$



$$Q = \{a, b, c\}$$
 with a initial $R = \{\text{self}, \text{parent}, \text{root}\}$

A. If ∃ neighbor NB (NB.root < MY.root):

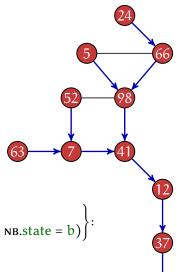
 $MY.root \leftarrow NB.root$

 $MY.state \leftarrow \alpha$

B. If \forall neighbor NB $\begin{cases}
NB.root = MY.root \land \\
(NB.parent \neq MY.self \lor NB.state = b)
\end{cases}$: $MY.state \leftarrow b$

C. If
$$(MY.root = MY.self \land MY.state = b)$$

 $MY.state \leftarrow c$



Computing a spanning tree

$$Q = \{a, b, c\}$$
 with a initial $R = \{\text{self}, \text{parent}, \text{root}\}$

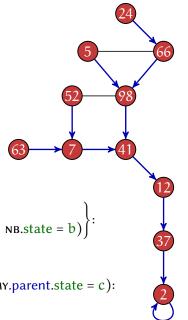
A. If ∃ neighbor NB (NB.root < MY.root):

 $\mathsf{MY}.\mathsf{root} \leftarrow \mathsf{NB}.\mathsf{root}$

$$MY.state \leftarrow a$$

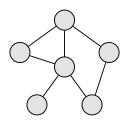
B. If \forall neighbor NB $\begin{cases} NB.root = MY.root \land \\ (NB.parent \neq MY.self \lor NB.state = b) \end{cases}$:

C. If
$$(MY.root = MY.self \land MY.state = b) \lor (MY.parent.state = c)$$
:
 $MY.state \leftarrow c$



Contribution

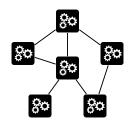
FUNCTIONAL FIXPOINT LOGIC restricted to ordered graphs



$$\mathbf{pfp} \begin{bmatrix} f_1 \colon \varphi_1(f_1, f_2, IN, OUT) \\ f_2 \colon \varphi_2(f_1, f_2, IN, OUT) \end{bmatrix} \psi$$

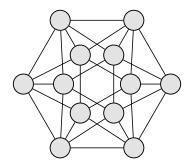


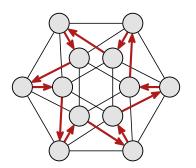
DISTR. REGISTER AUTOMATA

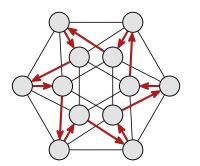


$${}^{\diamond}_{\circ} \circ : (Q \times \mathbb{N}^R)^+ \to Q \times \mathbb{N}^R$$

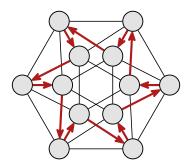
- ► Finite-state & registers
- ► Synchronous execution



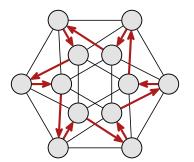


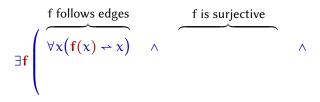


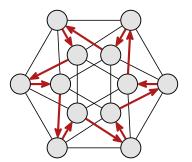




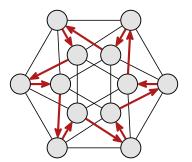
 $\exists \mathbf{f} \left(\begin{array}{c} f \text{ follows edges} \\ \forall x (\mathbf{f}(x) \rightleftharpoons x) \\ \end{pmatrix} \wedge \wedge \right)$



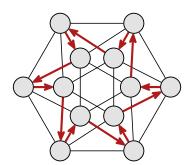


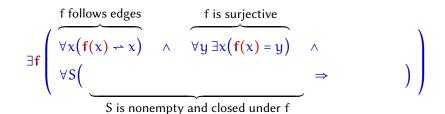


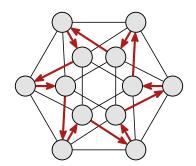
$$\exists \mathbf{f} \left(\begin{array}{c} \text{f follows edges} \\ \forall x \big(\mathbf{f}(x) \rightleftharpoons x \big) \\ \end{pmatrix} \wedge \begin{array}{c} \text{f is surjective} \\ \forall y \exists x \big(\mathbf{f}(x) = y \big) \\ \end{pmatrix} \wedge$$

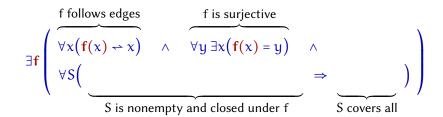


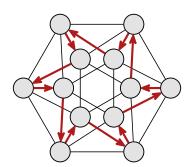
$$\exists \mathbf{f} \begin{pmatrix} f \text{ follows edges} & f \text{ is surjective} \\ \forall x (\mathbf{f}(x) \rightarrow x) & \land & \forall y \exists x (\mathbf{f}(x) = y) & \land \\ \forall S (\Rightarrow) \end{pmatrix}$$

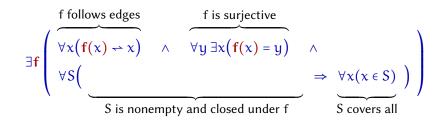


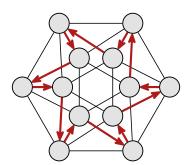






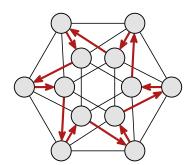




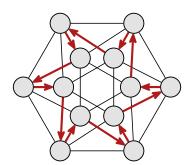


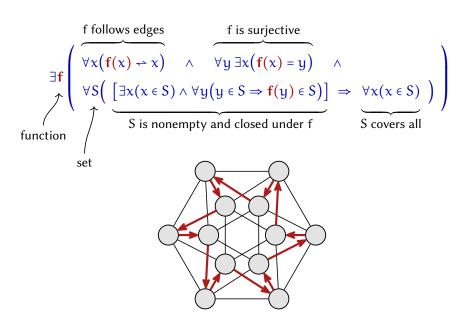
$$\exists \mathbf{f} \left(\begin{array}{c} f \text{ follows edges} & f \text{ is surjective} \\ \forall x (\mathbf{f}(x) \leftrightarrow x) & \land & \forall y \exists x (\mathbf{f}(x) = y) & \land \\ \forall S (\left[\exists x (x \in S)\right] & \Rightarrow & \forall x (x \in S) \end{array} \right) \right)$$

$$S \text{ is nonempty and closed under } f$$



$$\exists \mathbf{f} \left(\begin{array}{c} f \text{ follows edges} & f \text{ is surjective} \\ \\ \forall x \big(\mathbf{f}(x) \hookrightarrow x \big) & \land & \forall y \exists x \big(\mathbf{f}(x) = y \big) & \land \\ \\ \forall S \Big(\left[\exists x (x \in S) \land \forall y \big(y \in S \Rightarrow \mathbf{f}(y) \in S \big) \right] \Rightarrow \forall x (x \in S) \\ \\ S \text{ is nonempty and closed under } f & S \text{ covers all} \\ \end{array} \right)$$

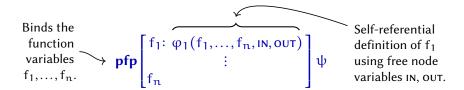


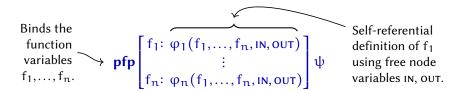


$$\text{pfp} \begin{bmatrix} f_1 \\ \\ \\ f_n \end{bmatrix} \psi$$



```
Binds the function variables f_1, \dots, f_n. pfp \begin{bmatrix} f_1 \colon \phi_1(f_1, \dots, f_n, IN, OUT) \\ \vdots \\ f_n \end{bmatrix} \psi
```





Extends first-order logic with a partial fixpoint operator:

Binds the function variables
$$f_1, \ldots, f_n$$
.

Self-referential definition of f_1 using free node variables IN, OUT)

 $f_n: \varphi_n(f_1, \ldots, f_n, \text{IN}, \text{OUT})$

$$\begin{pmatrix} f_1^0 = id \\ \vdots \\ f_n^0 = id \end{pmatrix}$$

Extends first-order logic with a partial fixpoint operator:

Binds the function variables
$$pfp \begin{bmatrix} f_1 \colon \phi_1(f_1,\ldots,f_n,\text{IN},\text{OUT}) \\ \vdots \\ f_n \colon \phi_n(f_1,\ldots,f_n,\text{IN},\text{OUT}) \end{bmatrix} \psi$$
 Self-referential definition of f_1 using free node variables IN, OUT.

$$\begin{pmatrix} f_1^0 = id \\ \vdots \\ f_n^0 = id \end{pmatrix} \mapsto \begin{pmatrix} f_1^1 \\ \vdots \\ f_n^1 \end{pmatrix}$$

Extends first-order logic with a partial fixpoint operator:

Binds the function variables
$$pfp$$
 $f_1: \phi_1(f_1,...,f_n,\text{IN},\text{OUT})$ ψ using free node variables in, out.

$$\begin{pmatrix} f_1^0 = id \\ \vdots \\ f_n^0 = id \end{pmatrix} \mapsto \begin{pmatrix} f_1^1 \\ \vdots \\ f_n^1 \end{pmatrix} \mapsto \begin{pmatrix} f_1^2 \\ \vdots \\ f_n^2 \end{pmatrix}$$

Extends first-order logic with a partial fixpoint operator:

Binds the function variables
$$\phi$$
 pfp
$$\begin{bmatrix} f_1 \colon \phi_1(f_1,\ldots,f_n,\text{IN},\text{OUT}) \\ \vdots \\ f_n \colon \phi_n(f_1,\ldots,f_n,\text{IN},\text{OUT}) \end{bmatrix} \psi \quad \text{using free node variables IN, OUT.}$$

$$\begin{pmatrix} f_1^0 = \mathrm{id} \\ \vdots \\ f_n^0 = \mathrm{id} \end{pmatrix} \mapsto \begin{pmatrix} f_1^1 \\ \vdots \\ f_n^1 \end{pmatrix} \mapsto \begin{pmatrix} f_1^2 \\ \vdots \\ f_n^2 \end{pmatrix} \mapsto \cdots$$

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 pfp
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$$\begin{pmatrix} f_1^0 = id \\ \vdots \\ f_n^0 = id \end{pmatrix} \mapsto \begin{pmatrix} f_1^1 \\ \vdots \\ f_n^1 \end{pmatrix} \mapsto \begin{pmatrix} f_1^2 \\ \vdots \\ f_n^2 \end{pmatrix} \mapsto \cdots \qquad \begin{pmatrix} f_1^\infty \\ \vdots \\ f_n^\infty \end{pmatrix}$$

Extends first-order logic with a partial fixpoint operator:

Binds the function variables
$$pfp \begin{bmatrix} f_1 \colon \phi_1(f_1, \dots, f_n, \text{IN}, \text{OUT}) \\ \vdots \\ f_n \colon \phi_n(f_1, \dots, f_n, \text{IN}, \text{OUT}) \end{bmatrix} \psi$$
 Self-referential definition of f_1 using free node variables IN, OUT.

To compute the partial fixpoint:

$$\begin{pmatrix} f_1^0 = \mathrm{id} \\ \vdots \\ f_n^0 = \mathrm{id} \end{pmatrix} \mapsto \begin{pmatrix} f_1^1 \\ \vdots \\ f_n^1 \end{pmatrix} \mapsto \begin{pmatrix} f_1^2 \\ \vdots \\ f_n^2 \end{pmatrix} \mapsto \cdots \qquad \begin{pmatrix} f_1^\infty \\ \vdots \\ f_n^\infty \end{pmatrix} = \begin{cases} \begin{pmatrix} f_1^k \\ \vdots \\ f_n^k \end{pmatrix} & \text{if } \exists k : \begin{pmatrix} f_1^k \\ \vdots \\ f_n^k \end{pmatrix} = \begin{pmatrix} f_1^{k+1} \\ \vdots \\ f_n^{k+1} \end{pmatrix}$$

8/9

Extends first-order logic with a partial fixpoint operator:

Binds the function variables
$$pfp \begin{bmatrix} f_1 \colon \phi_1(f_1, \dots, f_n, \text{IN}, \text{OUT}) \\ \vdots \\ f_n \colon \phi_n(f_1, \dots, f_n, \text{IN}, \text{OUT}) \end{bmatrix} \psi$$
 Self-referential definition of f_1 using free node variables IN, OUT.

To compute the partial fixpoint:

To compute the partial fixpoint:
$$\begin{pmatrix} f_1^0 = id \\ \vdots \\ f_n^0 = id \end{pmatrix} \mapsto \begin{pmatrix} f_1^1 \\ \vdots \\ f_n^l \end{pmatrix} \mapsto \begin{pmatrix} f_1^2 \\ \vdots \\ f_n^k \end{pmatrix} \mapsto \cdots$$

$$\begin{pmatrix} f_1^\infty \\ \vdots \\ f_n^\infty \end{pmatrix} = \begin{cases} \begin{pmatrix} f_1^k \\ \vdots \\ f_n^k \end{pmatrix} & \text{if } \exists k : \begin{pmatrix} f_1^k \\ \vdots \\ f_n^k \end{pmatrix} = \begin{pmatrix} f_1^{k+1} \\ \vdots \\ f_n^{k+1} \end{pmatrix} \\ \begin{pmatrix} id \\ \vdots \\ id \end{pmatrix} & \text{otherwise}$$

8/9

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$$\begin{pmatrix} f_1^\infty \\ \vdots \\ f_n^\infty \end{pmatrix} = \begin{pmatrix} \begin{pmatrix} f_1^k \\ \vdots \\ f_n^k \end{pmatrix} & \text{if } \exists k : \begin{pmatrix} f_1^k \\ \vdots \\ f_n^k \end{pmatrix} = \begin{pmatrix} f_1^{k+1} \\ \vdots \\ f_n^{k+1} \end{pmatrix}$$

$$\begin{pmatrix} id \\ \vdots \\ id \end{pmatrix} & \text{otherwise}$$

On ordered graphs:

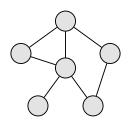
pfp can express quantification over functions and sets.

Contribution

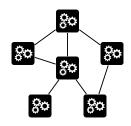
FUNCTIONAL FIXPOINT LOGIC restricted to ordered graphs



DISTR. REGISTER AUTOMATA



$$\begin{aligned} & \textbf{pfp} \begin{bmatrix} f_1 \colon \phi_1(f_1, f_2, \text{in}, \text{out}) \\ f_2 \colon \phi_2(f_1, f_2, \text{in}, \text{out}) \end{bmatrix} \psi \end{aligned}$$



$${}^{\circ}_{\circ}\circ:(Q\times\mathbb{N}^{R})^{+}\to Q\times\mathbb{N}^{R}$$

- ► Finite-state & registers
- Synchronous execution

LOGICAL DESCRIPTIONS:

▶ A tool to specify and synthesize distributed algorithms?

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Thanks!