Notes about the three types of pin-compatibility:

- (1): STM32CubeMX listed as pin-compatible (with JTAG, USB, OSC, and OSC32 peripherals se
- (2): STM32CubeMX listed as pin-compatible (with only SWD peripheral selected)
- (3): Pinouts listed in the respective datasheets appeared to be pin-compatible (to me), with som

Sum: 14 32

Part No	Manufacturer	Pin compatible (1)?	Pin compatible (2)?
Part No	Wanutacturer	Pin compatible (1)?	Pin compatible (2)?
GD32E230C8T6	CigaDavias	FALSE	FALSE
GD32E230C610	GigaDevice	FALSE	FALSE
GD32F130C6T6	GigaDevice	FALSE	FALSE
GB021 1000010	Olgabevice	TALOL	TALOL
GD32E231C8T6	GigaDevice	FALSE	FALSE
GBOZEZOTOOTO	Olgabevice	TALOL	TALOL
GD32F130C8T6	GigaDevice	FALSE	FALSE
0202. 1000010	l .ga.z aaa		. ,
GD32F330C6T6	GigaDevice	FALSE	FALSE
STM32L010C6T6	ST Microelectonics	FALSE	FALSE
STM32G030C8T6	ST Microelectonics	FALSE	FALSE
GD32F330C8T6	GigaDevice	FALSE	FALSE
	_		
STM32F031C6T6TR	ST Microelectonics	FALSE	FALSE

	1		_
GD32F330CBT6	GigaDevice	FALSE	FALSE
OB021 000 OB 10	Olgabevice	TALOL	TALOL
GD32F150C6T6	GigaDevice	FALSE	FALSE
OD00545000T0	Olara Davida a	EAL OF	EAL OF
GD32F150C8T6	GigaDevice	FALSE	FALSE
GD32F350C6T6	GigaDevice	FALSE	FALSE
STM32G070CBT6	ST Microelectonics	FALSE	FALSE
GD32F350C8T6	GigaDevice	FALSE	FALSE
GD32F330C8T6	GigaDevice	FALSE	FALSE
GD32F101CBT6	GigaDevice	FALSE	FALSE
GD32E103C8T6	GigaDevice	FALSE	FALSE
GD32F350CBT6	GigaDevice	FALSE	FALSE
STM32G031C6T6	ST Microelectonics	FALSE	FALSE
3 1 W 3 2 G 0 3 1 C 0 1 0	31 WILCI DETECTORICS	FALSE	FALOE
STM32F031C6T6	ST Microelectonics	FALSE	FALSE
STM32G031C8T6	ST Microelectonics	FALSE	FALSE
07140000740775		EAL 05	EAL 05
STM32G071CBT6	ST Microelectonics	FALSE	FALSE

STM32F051C6T6	ST Microelectonics	FALSE	FALSE
STM32F302C8T6	ST Microelectonics	TRUE	TRUE
011VI321 302C010	O I WIICIOEIECIOTIICS	INOL	INOL
STM32L052C8T6	ST Microelectonics	FALSE	FALSE
STM32F103C6T6A	ST Microelectonics	TRUE	TRUE
STM32G030C6T6	ST Microelectonics	FALSE	FALSE
STM32F301C8T6	ST Microelectonics	FALSE	TRUE
STM32F072CBT6	ST Microelectonics	FALSE	FALSE
0.101		.,,===	.,,
CD22F100C9T6	CigaDavias	FALSE	FALSE
GD32F190C8T6 STM32F101C8T6	GigaDevice ST Microelectonics	FALSE	TRUE
CTMOZI TOTOGTO	CT WIIGI GOLGGEOTHIGG	TALOL	TROE
STM32F091CCT6	ST Microelectonics	FALSE	FALSE
STM32F070CBT6	ST Microelectonics	FALSE	FALSE
31W32F070CB10	31 WIICI DETECTORICS	FALSE	FALSE
STM32F091CBT6	ST Microelectonics	FALSE	FALSE
STM32L072CBT6	ST Microelectonics	FALSE	FALSE
31W32L072CD10	31 WIICI DETECTORICS	TALGE	TALOL
STM32F071CBT6	ST Microelectonics	FALSE	FALSE
GD32F303CCT6	GigaDevice	FALSE	FALSE
STM32F100CBT6B	ST Microelectonics	FALSE	TRUE
STM32F071C8T6	ST Microelectonics	FALSE	FALSE
STM32F101CBT6	ST Microelectonics	FALSE	TRUE
	_ : :::::::::::::::::::::::::::::::::::		

STM32F030C8T6TR	ST Microelectonics	FALSE	FALSE
STM32L051C6T6	ST Microelectonics	FALSE	FALSE
STM32L412C8T6	ST Microelectonics	FALSE	FALSE
STM32L051C8T6	ST Microelectonics	FALSE	FALSE
STM32F100C4T6B	ST Microelectonics	FALSE	TRUE
STM32F303C8T6	ST Microelectonics	FALSE	TRUE
STM32L151C8T6A	ST Microelectonics	FALSE	FALSE
STM32F100C8T6B	ST Microelectonics	FALSE	TRUE
STM32L431CCT6	ST Microelectonics	FALSE	FALSE
STM32L431CBT6	ST Microelectonics	FALSE	FALSE
011110110110	- mereciceternes	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
STM32F303CCT6	ST Microelectonics	TRUE	TRUE
STM32F303CBT6	ST Microelectonics	TRUE	TRUE
STM32F030C8T6	ST Microelectonics	FALSE	FALSE

	1		
STM32F334C4T6	ST Microelectonics	FALSE	TRUE
STM32F042C4T6	ST Microelectonics	FALSE	FALSE
STM32F030C6T6	ST Microelectonics	FALSE	FALSE
STM32G071C8T6	ST Microelectonics	FALSE	FALSE
STM32F302CBT6	ST Microelectonics	TRUE	TRUE
GD32F103CBT6	GigaDevice	FALSE	FALSE
STM32F031C4T6	ST Microelectonics	FALSE	FALSE
STM32G431CBT6	ST Microelectonics	FALSE	FALSE
STM32F373C8T6	ST Microelectonics	FALSE	FALSE
STM32L433CBT6	ST Microelectonics	FALSE	FALSE
STM32L151CBT6	ST Microelectonics	FALSE	FALSE
STM32F301C6T6	ST Microelectonics	FALSE	TRUE

STM32L071CBT6	ST Microelectonics	FALSE	FALSE
OTIVIOZEO/ TODTO	OT WIICTOETECTOTTICS	TALUL	TALGE
STM32F334C8T6	ST Microelectonics	FALSE	TRUE
011/1021 00+0010	O 1 WHOI OCICOTOTHOS	TALOL	ITOL
STM32F334C6T6	ST Microelectonics	FALSE	TRUE
51111021 00 100 10		17,202	
STM32F051C8T6	ST Microelectonics	FALSE	FALSE
		.,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	
STM32L152C8T6A	ST Microelectonics	FALSE	FALSE
STM32L433CCT6	ST Microelectonics	FALSE	FALSE
STM32L152CBT6A	ST Microelectonics	FALSE	FALSE
STM32L412CBT6	ST Microelectonics	FALSE	FALSE
STM32F102CBT6	ST Microelectonics	TRUE	TRUE
STM32G431C6T6	ST Microelectonics	FALSE	FALSE
STM32L072CZT7	ST Microelectonics	FALSE	FALSE
STM32F103C8T6	ST Microelectonics	TRUE	TRUE
STM32G431C8T6	ST Microelectonics	FALSE	FALSE
STM32F103CBT6	ST Microelectonics	TRUE	TRUE
STM32F042C6T6	ST Microelectonics	FALSE	FALSE

STM32L071CZT6	ST Microelectonics	FALSE	FALSE
STM32F072C8T6	ST Microelectonics	FALSE	FALSE
STM32F030CCT6	ST Microelectonics	FALSE	FALSE
STM32G473CBT6	ST Microelectonics	FALSE	FALSE
STM32G474CBT6	ST Microelectonics	FALSE	FALSE
STM32G473CCT6	ST Microelectonics	FALSE	FALSE
STM32F373CCT6	ST Microelectonics	FALSE	FALSE
STM32G474CCT6	ST Microelectonics	FALSE	FALSE
STM32G473CET6	ST Microelectonics	FALSE	FALSE
STM32G474CET6	ST Microelectonics	FALSE	FALSE
STM32G484CET6	ST Microelectonics	FALSE	FALSE
STM32L452CET6	ST Microelectonics	FALSE	FALSE
STM32L053C8T6	ST Microelectonics	FALSE	FALSE

STM32F038C6T6	ST Microelectonics	FALSE	FALSE
STM32F051C4T6	ST Microelectonics	FALSE	FALSE
STM32F070C6T6	ST Microelectonics	FALSE	FALSE
STM32F078CBT6	ST Microelectonics	FALSE	FALSE
011VI021 0700D10	31 MICLOGIECIONICS	IALGE	IALSE
STM32F098CCT6	ST Microelectonics	FALSE	FALSE
STM32F100C6T6	ST Microelectonics	FALSE	TRUE
STM32F101C4T6	ST Microelectonics	FALSE	TRUE
STM32F101C6T6	ST Microelectonics	FALSE	TRUE
STM32F102C4T6	ST Microelectonics	TRUE	TRUE
STM32F102C6T6	ST Microelectonics	TRUE	TRUE
STM32F102C8T6	ST Microelectonics	TRUE	TRUE
STM32F103C4T6	ST Microelectonics	TRUE	TRUE
STM32F302C6T6	ST Microelectonics	TRUE	TRUE
0714005000000	OT Missas Is stories	TDUE	TDUE
STM32F302CCT6	ST Microelectonics	TRUE	TRUE
CTM22F202CcTc	CT Migra de stanica	FALSE	TDUE
STM32F303C6T6	ST Microelectonics	FALSE	TRUE
STM32F318C8T6	ST Microelectonics	FALSE	TRUE
STM32F328C8T6	ST Microelectonics	FALSE	TRUE

STM32F358CCT6	ST Microelectonics	FALSE	TRUE
STM32F373CBT6	ST Microelectonics	FALSE	FALSE
STM32F378CCT6	ST Microelectonics	FALSE	FALSE
STM32F410CBT6	ST Microelectonics	FALSE	FALSE
STM32G031C4T6	ST Microelectonics	FALSE	FALSE
STM32G041C6T6	ST Microelectonics	FALSE	FALSE
STM32G041C8T6	ST Microelectonics	FALSE	FALSE

STM32G071C6T6	ST Microelectonics	FALSE	FALSE
STM32G081CBT6	ST Microelectonics	FALSE	FALSE
STM32G441CBT6	ST Microelectonics	FALSE	FALSE
STM32G471CCT6	ST Microelectonics	FALSE	FALSE
STM32G471CET6	ST Microelectonics	FALSE	FALSE
STM32G483CET6	ST Microelectonics	FALSE	FALSE
STM32GBK1CBT6	ST Microelectonics	FALSE	FALSE
STM32L031C4T6	ST Microelectonics	FALSE	FALSE
STM32L031C6T6	ST Microelectonics	FALSE	FALSE
STM32L041C6T6	ST Microelectonics	FALSE	FALSE
STM32L052C6T6	ST Microelectonics	FALSE	FALSE
STM32L053C6T6	ST Microelectonics	FALSE	FALSE
STM32L063C8T6	ST Microelectonics	FALSE	FALSE
	2		
STM32L071C8T6	ST Microelectonics	FALSE	FALSE
STM32L073CBT6	ST Microelectonics	FALSE	FALSE

STM32L073CZT6	ST Microelectonics	FALSE	FALSE
STM32L081CBT6	ST Microelectonics	FALSE	FALSE
OTMON 004 0770	CT Missas also stemics	FALCE	FALCE
STM32L081CZT6	ST Microelectonics	FALSE	FALSE
STM32L083CBT6	ST Microelectonics	FALSE	FALSE
STM32L083CZT6	ST Microelectonics	FALSE	FALSE
31W32L003CZ10	3 i Microelectoriles	I ALGE	I ALGE
STM32L151C6T6	ST Microelectonics	FALSE	FALSE
STM32L151C6T6A	ST Microelectonics	FALSE	FALSE
STM32L151C8T6	ST Microelectonics	FALSE	FALSE
STM32L151CBT6A	ST Microelectonics	FALSE	FALSE
STM32L151CCT6	ST Microelectonics	FALSE	FALSE
STM32L152C6T6	ST Microelectonics	FALSE	FALSE
STM32L152C6T6A	ST Microelectonics	FALSE	FALSE
STM32L152C8T6	ST Microelectonics	FALSE	FALSE
STM32L152CBT6	ST Microelectonics	FALSE	FALSE
STM32L152CCT6	ST Microelectonics	FALSE	FALSE
OTMODI MACORTOR	OT Missas de stant	FALOE	EAL OF
STM32L412CBT6P	ST Microelectonics	FALSE	FALSE

STM32L422CBT6	ST Microelectonics	FALSE	FALSE
STM32L443CCT6	ST Microelectonics	FALSE	FALSE
STM32L451CET6	ST Microelectonics	FALSE	FALSE
STM32L462CET6	ST Microelectonics	FALSE	FALSE
STM32L4P5CET6	ST Microelectonics	FALSE	FALSE
OTWOZETI OOLTO	OT WHOTOCICOTOTIOS	TALOL	TALOL
STM32L4P5CGT6	ST Microelectonics	FALSE	FALSE
STM32L4P5CGT6P	ST Microelectonics	FALSE	FALSE
STM32L4Q5CGT6	ST Microelectonics	FALSE	FALSE
STM32L4Q5CGT6P	ST Microelectonics	FALSE	FALSE

STM32L552CCT6	ST Microelectonics	FALSE	FALSE
STM32L552CET6	ST Microelectonics	FALSE	FALSE
31W32L332CE10	ST MICTOELECTORICS	FALSE	FALSE
STM32L552CET6P	ST Microelectonics	FALSE	FALSE
O TWOZEGOZOE TOP	31 MICIOEIECIONICS	TALSE	TALGE
STM32L562CET6	ST Microelectonics	FALSE	FALSE
STM32L562CET6P	ST Microelectonics	FALSE	FALSE

	How to update this spreadsheet with the latest price and availability info
elected)	(1): Download the latest JLCPCB SMT Parts Library spreadsheet from h
	(2): Replace the tab in this spreadsheet called "JLCPCB SMT Parts Libr
e notes	

124 74

Pin compatible (3)?	On JLCPCB?	Part status	Price	e @ Qty: 1	Price	@ Qty: 10
TRUE	TRUE	Extended	\$	0.7985	\$	0.6136
TRUE	TRUE	Extended	\$	0.8606	\$	0.8606
IIIOL	TROE	Exteriaca	Ψ	0.0000	Ψ	0.0000
TRUE	TRUE	Extended	\$	1.1485	\$	0.8833
11102	11.02	Exterior	Ψ	1.1100	Ψ	0.0000
TD. 15	TD. 15			0.0040		0.0040
TRUE	TRUE	Extended	\$	0.8848	\$	0.8848
TRUE	TRUE	Extended	\$	0.8997	\$	0.8997
TRUE	TRUE	Extended	\$	1.2455	\$	0.9182
FALOE	TDUE		φ.	4.0000	ф	0.0040
FALSE	TRUE	Extended	\$	1.2606	\$	0.9348
TRUE	TRUE	Extended	\$	0.9498	\$	0.9498
TRUE	TRUE	Extended	\$	1.2939	\$	0.9727

TRUE	TRUE	Extended	\$	1.0182	\$ 1.0182
TRUE	TRUE	Extended	\$	1.0255	\$ 1.0255
	<b>TD::</b>			4 0 400	4 0 4 0 4
TRUE	TRUE	Extended	\$	1.0439	\$ 1.0439
TRUE	TRUE	Extended	\$	1.0448	\$ 1.0448
544.05	TD::I			4 4450	
FALSE	TRUE	Extended	\$	1.4152	\$ 1.0985
TRUE	TRUE	Extended	\$	1.1114	\$ 1.1114
TRUE	TRUE	Extended	\$	1.4939	\$ 1.1485
TRUE	TRUE	Extended	\$	1.1742	\$ 1.1742
TRUE	TRUE	Extended	\$	1.2244	\$ 1.2244
TRUE	TRUE	Extended	\$	1.2338	\$ 1.2338
IRUE	IRUE	Extended	Φ	1.2330	<b>Φ</b> 1.2330
FALSE	TRUE	Extended	\$	1.7879	\$ 1.3015
TRUE	TRUE	Extended	\$	1.9258	\$ 1.4061
FALSE	TRUE	Extended	\$	1.9182	\$ 1.4076
FALSE	TRUE	Extended	\$	2.1106	\$ 1.5606
IALUL	INUL	LAIGHUGU	ΙΨ	۷. ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱ ۱	Ψ 1.5000

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TRUE	TRUE	Extended	\$	2.1621	\$	1.6045
TRUE	TRUE	Extended	\$	2.2242	\$	1.6076
TRUE	TRUE	Extended	\$	1.6506	\$	1.6506
TRUE	TRUE	Extended	\$	1.6807	\$	1.6807
11.02	11102	<u> </u>	<b>T</b>	1.0001	*	1.0001
FALSE	TRUE	Extended	\$	1.7364	\$	1.6894
TRUE	TRUE	Extended	\$	2.3121	\$	1.6909
TOUE	TOUE	Forton do d	Α	4 7074	φ	4 7074
TRUE	TRUE	Extended	\$	1.7271	\$	1.7271
TRUE	TRUE	Extended	\$	1.7795	\$	1.7795
TRUE	TRUE	Extended	\$	1.7800	\$	1.7800
TRUE	TRUE	Extended	\$	1.8136	\$	1.7803
TOUE	TOUE		φ.	0.4004	φ.	4 7000
TRUE	TRUE	Extended	\$	2.1364	\$	1.7803
TRUE	TRUE	Extended	\$	2.1076	\$	1.8015
IIIOE	TITOL	Exteriord	Ι Ψ	2.1070	Ψ	1.0010
TRUE	TRUE	Extended	\$	2.1318	\$	1.8227
		_				
TRUE	TRUE	Extended	\$	2.5682	\$	1.8576
TRUE	TRUE	Extended	\$	1.9250	\$	1.9250
TRUE	TRUE	Extended	\$	2.6848	\$	1.9439
TRUE	TRUE	Extended	\$	2.6621	\$	1.9500
TRUE	TRUE	Extended	\$	2.3530	\$	2.0364
			, ¥		, <del>v</del>	0001

TRUE	TRUE	Extended	\$	2.0545	\$	2.0545
TRUE	TRUE	Extended	\$	2.3955	\$	2.0621
TRUE	TRUE	Extended	\$	2.4348	\$	2.0924
					·	
TRUE TRUE	TRUE TRUE	Extended Extended	\$ \$	2.1591 2.5258	\$ \$	2.1591 2.1621
		Exteriord		2.0200	·	
TRUE	TRUE	Extended	\$	2.9167	\$	2.2273
TRUE	TRUE	Extended	\$	3.0045	\$	2.2712
TRUE	TRUE	Extended	\$	2.2793	\$	2.2793
TRUE	TRUE	Extended	\$	2.6303	\$	2.2803
TRUE	TRUE	Extended	\$	2.3008	\$	2.3008
TRUE	TRUE	Extended	\$	2.3798	\$	2.3798
TRUE	TRUE	Extended	\$	2.7879	\$	2.4136
TRUE	TRUE	Basic	\$	2.4223	\$	2.4223
				_:0		=::==0

TRUE	TRUE	Extended	\$	3.4924	\$	2.5970
TRUE	TRUE	Extended	\$	2.8470	\$	2.7773
TRUE	TRUE	Extended	\$	2.9018	\$	2.9018
FALSE	TRUE	Extended	\$	3.0818	\$	2.9985
TRUE TRUE	TRUE TRUE	Extended Extended	\$	3.0021 3.0545	\$	3.0021 3.0545
IRUE	IRUE	Extended	Ф	3.0545	Ф	3.0545
TRUE	TRUE	Extended	\$	3.5212	\$	3.0591
FALSE	TRUE	Extended	\$	3.5742	\$	3.0712
FALSE	TRUE	Extended	\$	4.0424	\$	3.0864
TRUE	TRUE	Extended	\$	3.7515	\$	3.2076
TRUE	TRUE	Extended	\$	3.8591	\$	3.3333
TRUE	TRUE	Extended	\$	3.9758	\$	3.4288

			I			
TRUE	TRUE	Extended	\$	3.6227	\$	3.5288
TRUE	TRUE	Extended	\$	4.1000	\$	3.5727
11102	INGL	Exteriord	<u> </u>	4.1000	Ψ	0.0121
TRUE	TRUE	Extended	\$	4.9424	\$	3.6758
TRUE	TRUE	Extended	\$	3.6852	\$	3.6852
TRUE	TRUE	Extended	\$	3.8561	\$	3.8561
TRUE	TRUE	Extended	\$	4.6803	\$	4.0227
TRUE	TRUE	Extended	\$	4.2267	\$	4.2267
TDUE	TDUE	Essida na da ad	φ.	4 5407	Ф	4 2055
TRUE TRUE	TRUE TRUE	Extended Extended	\$	4.5167 4.6758	\$	4.3955 4.6758
FALSE	TRUE	Extended	\$	5.0909	\$	4.9545
TALSE	INOL	LXterided	Ψ	3.0909	Ψ	4.3040
TRUE	TRUE	Extended	\$	5.5348	\$	5.3864
TRUE	TRUE	Basic	\$	5.6138	\$	5.6138
FALSE	TRUE	Extended	\$	5.7258	\$	5.7258
TRUE	TRUE	Basic	\$	5.9794	\$	5.9794
TRUE	TRUE	Extended	\$	6.0986	\$	6.0986

	Γ		1		1
TRUE	TRUE	Extended	\$	7.1621	\$ 6.1758
TRUE	TRUE	Extended	\$	6.5780	\$ 6.5780
TRUE	TRUE	Extended	\$	6.8023	\$ 6.8023
FALSE	TRUE	Extended	\$	7.1879	\$ 7.1879
FALSE	TRUE	Extended	\$	7.5000	\$ 7.5000
FALSE	TRUE	Extended	\$	8.1061	\$ 8.1061
FALSE	TRUE	Extended	\$	9.5348	\$ 8.2924
FALSE	TRUE	Extended	\$	8.4182	\$ 8.4182
FALSE	TRUE	Extended	\$	9.6803	\$ 9.6803
FALSE	TRUE	Extended	\$	9.9939	\$ 9.9939
FALSE	TRUE	Extended	\$	10.3212	\$ 10.3212
FALSE	FALSE	#N/A		#N/A	#N/A
TRUE	TRUE	Extended		#VALUE!	#VALUE!

		1		
FALSE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A #N/A	#N/A #N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TDUE	E41.05	// \ / / A	// 1./ 4	// 1/ 4
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALOE	EALOE	46146	415.17.5	418.17.6
FALSE	FALSE	#N/A	#N/A	#N/A

				•
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
		•	·	-
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A

FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
FALSE	FALSE	#N/A	#N/A	#N/A
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TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
INUE	FALSE	#IN/A	#IN/A	#IN/PA
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
-			·	
TRUE	FALSE	#N/A	#N/A	#N/A

TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TDUE	FALSE	401/0	#NI/A	#11/4
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A

TRUE	FALSE	#N/A	#N/A	#N/A
-				
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TROE	TALOL	HINDX	man	THAT Y
TDIJE	EALSE	#51/5	#N/A	#NI/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TDUE	EALSE	#61/4	#51/5	#N1/A
TRUE	FALSE	#N/A	#N/A	#N/A

TDUE	5.N.05	//2.//2	// // /	// // /
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
		<b>#</b>	<b>#</b>	<b>"</b>
TRUE	FALSE	#N/A	#N/A	#N/A
TRUE	FALSE	#N/A	#N/A	#N/A
TD.15	541.05	//2.//2	// // // // // // // // // // // // //	// // // // // // // // // // // // //
TRUE	FALSE	#N/A	#N/A	#N/A

## rmation:

nere: https://jlcpcb.com/componentSearch/uploadComponentInfo ary" with the new spreadsheet. Ensure the title remains unchanged

Stock	Flash	RAM	10	Freq.
7159	64 kBytes	8 kBytes		72 MHz
1353	32 kBytes	4 kBytes		48 MHz
		-		
1294	64 kBytes	8 kBytes		72 MHz
15	64 kBytes	8 kBytes		48 MHz
	, , , , , , , ,	J J		
1492	32 kBytes	4 kBytes		84 MHz
	, , , , , , , ,	)		
	00 kD: 40 c	O IsDuda a	20	20 MH I-
0	32 kBytes	8 kBytes	38	32 MHz
759	64 kBytes	8 kBytes	43	64 MHz
546	64 kBytes	8 kBytes		84 MHz
153	32 kBytes	4 kBytes	39	48 MHz

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1212	128 kBytes	16 kBytes		84 MHz
2007	22 kBytos	6 kPytos		72 MHz
2007	32 kBytes	6 kBytes		1∠ IVI⊓∠
1468	64 kBytes	8 kBytes		72 MHz
1400	04 KDyte3	0 KDytes		7 2 1011 12
1497	32 kBytes	6 kBytes		108 MHz
0	128 kBytes	36 kBytes	44	64 MHz
1358	64 kBytes	8 kBytes		108 MHz
0	128 kBytes	32 kBytes		108 MHz
1267	128 kBytes	16 kBytes		56 MHz
0	64 kBytes	20 kBytes		120 MHz
982	128 kBytes	16 kBytes		108 MHz
		i c ii jii c		
0	32 kBytes	8 kBytes	43	64 MHz
0	32 kBytos	1 kBytes	30	48 MHz
U	32 kBytes	4 kBytes	39	+O IVI∏Z
0	64 kBytes	8 kBytes	43	64 MHz
0	128 kBytes	36 kBytes	44	64 MHz

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3	64 kBytes	8 kBytes	39	48 MHz
769	32 kBytes	8 kBytes	37	32 MHz
0	64 kBytes	40 kBytes	38	80 MHz
0	64 kBytes	8 kBytes	37	32 MHz
0	16 kBytes	4 kBytes		24 MHz
0	64 kBytes	16 kBytes	37	72 MHz
2	64 kBytes	32 kBytes		32 MHz
0	64 kBytes	8 kBytes	37	24 MHz
0	256 kBytes	64 kBytes	38	80 MHz
0	400 LD 4	04 LD: 4	20	00 MIL
0	128 kBytes	64 kBytes	38	80 MHz
0	256 kBytes	48 kBytes	37	72 MHz
0	128 kBytes	40 kBytes	37	72 MHz
11476	64 kBytes	8 kBytes	39	48 MHz

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0	16 kBytes	16 kBytes	37	72 MHz
0	16 kBytes	6 kBytes	38	48 MHz
3	32 kBytes	4 kBytes	39	48 MHz
0	64 kBytes	36 kBytes	44	64 MHz
685	128 kBytes	32 kBytes	37	72 MHz
0	128 kBytes	20 kBytes		108 MHz
0	16 kBytes	4 kBytes	39	48 MHz
0	128 kBytes	32 kBytes	38	170 MHz
0	64 kBytes	16 kBytes	37	72 MHz
0	128 kBytes	64 kBytes	38	80 MHz
0	128 kBytes	16 kBytes	37	32 MHz
0	32 kBytes	16 kBytes	37	72 MHz

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0	129 kBytos	20 kBytos	27	32 MHz
U	128 kBytes	20 kBytes	31	SZ IVITIZ
4500	04 1-0-4	40 laDa 4	0.7	70 MI I-
1508	64 kBytes	16 kBytes	37	72 MHz
1435	32 kBytes	16 kBytes	37	72 MHz
0	64 kBytes	8 kBytes	39	48 MHz
0	64 kBytes	32 kBytes	37	32 MHz
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3	256 kBytes	64 kBytes	38	80 MHz
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1248	128 kBytes	32 kBytes	37	32 MHz
1240	120 KDytes	32 KDytes	31	JZ IVII IZ
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0	128 kBytes	40 kBytes		80 MHz
0	128 kBytes	16 kBytes	37	48 MHz
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0	32 kBytes	32 kBytes	38	170 MHz
0	192 kBytes	20 kBytes	37	32 MHz
32019	64 kBytes	20 kBytes	37	72 MHz
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0	64 kBytes	32 kBytes	38	170 MHz
5240	128 kBytes	20 kBytes	37	
	1			
0	32 kBytes	6 kBytes	38	48 MHz
<u> </u>	102	15 112 1100		

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0	192 kBytes	20 kBytes	37	32 MHz
2	64 kBytes	16 kBytes	37	48 MHz
0	256 kBytes	32 kBytes	37	48 MHz
0	128 kBytes	128 kBytes	38	170 MHz
0	128 kBytes	128 kBytes	38	170 MHz
0	256 kBytes	128 kBytes	38	170 MHz
0	256 kBytes	32 kBytes	37	72 MHz
0	256 kBytes	128 kBytes	38	170 MHz
0	512 kBytes	128 kBytes	38	170 MHz
0	512 kBytes	128 kBytes	38	170 MHz
0	512 kBytes	128 kBytes	38	170 MHz
#N/A	512 kBytes	160 kBytes	38	80 MHz
0	64 kBytes	8 kBytes	37	32 MHz

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#N/A	32 kBytes	4 kBytes	38	48 MHz
	-			
#N/A	16 kBytes	8 kBytes	39	48 MHz
#N/A	32 kBytes	6 kBytes	37	48 MHz
#11/7	32 KDytes	0 KDytes	31	40 IVII IZ
#N/A	128 kBytes	16 kBytes	36	48 MHz
				-
#N/A	256 kBytes	32 kBytes	37	48 MHz
#N/A	32 kBytes	4 kBytes	37	24 MHz
#N/A	16 kBytes	4 kBytes		36 MHz
#N/A	32 kBytes	6 kBytes		36 MHz
#N/A	16 kBytes	4 kBytes		48 MHz
#N/A	32 kBytes	6 kBytes		48 MHz
#N/A	64 kBytes	10 kBytes		48 MHz
#N/A	16 kBytes	6 kBytes	37	72 MHz
4481/4	20 kD: 4	40 kD: 4	07	70 MIL
#N/A	32 kBytes	16 kBytes	37	72 MHz
#N/A	256 kBytes	40 kBytes	37	72 MHz
πιν/Δ	200 KDytes	40 KDytes	- 51	1 2 1011 12
#N/A	32 kBytes	16 kBytes	37	72 MHz
	,			
#N/A	64 kBytes	16 kBytes	36	72 MHz
//5.//5	0415	1015		70 14:
#N/A	64 kBytes	16 kBytes	36	72 MHz

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#N/A	256 kBytes	48 kBytes	36	72 MHz
#N/A	128 kBytes	24 kBytes	37	72 MHz
#N/A	256 kBytes	32 kBytes	36	72 MHz
#N/A	128 kBytes	32 kBytes	35	100 MHz
#N/A	16 kBytes	8 kBytes	43	64 MHz
#N/A	32 kBytes	8 kBytes	43	64 MHz
#N/A	64 kBytes	8 kBytes	43	64 MHz

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#N/A	32 kBytes	36 kBytes	44	64 MHz
#N/A	128 kBytes	36 kBytes	44	64 MHz
#N/A	128 kBytes	32 kBytes	38	170 MHz
#N/A	256 kBytes	128 kBytes	38	170 MHz
#N/A	512 kBytes	128 kBytes	38	170 MHz
#N/A	512 kBytes	128 kBytes	38	170 MHz
#N/A	128 kBytes	32 kBytes	42	170 MHz
#N/A	16 kBytes	8 kBytes	38	32 MHz
#N/A	32 kBytes	8 kBytes	38	32 MHz
#N/A	32 kBytes	8 kBytes	38	32 MHz
#N/A	32 kBytes	8 kBytes	37	32 MHz
ni vi v	oz naytos	o naytee	<u> </u>	02 1111 12
#N/A	32 kBytes	8 kBytes	37	32 MHz
#N/A	64 kBytes	8 kBytes	37	32 MHz
#N/A	64 kBytes	20 kBytes	37	32 MHz
#N/A	128 kBytes	20 kBytes	37	32 MHz

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#N/A	192 kBytes	20 kBytes	37	32 MHz
#N/A	128 kBytes	20 kBytes	37	32 MHz
#N/A	192 kBytes	20 kBytes	37	32 MHz
#N/A	128 kBytes	20 kBytes	37	32 MHz
HINIX	120 KDytos	_0 1103100		22 IVII IZ
#N/A	102 kBytos	20 kBytos	27	32 MHz
#1N/A	192 kBytes	20 kBytes	31	JZ IVII IZ
#N/A	32 kBytes	10 kBytes	37	32 MHz
#N/A	32 kBytes	16 kBytes	37	32 MHz
#N/A	64 kBytes	10 kBytes	37	32 MHz
#N/A	128 kBytes	32 kBytes	37	32 MHz
#N/A	256 kBytes	32 kBytes	37	32 MHz
#N/A	32 kBytes	10 kBytes	37	32 MHz
#N/A	32 kBytes	16 kBytes		32 MHz
#N/A	64 kBytes	10 kBytes	37	32 MHz
#N/A	128 kBytes	16 kBytes	37	32 MHz
#N/A	256 kBytes	32 kBytes	37	32 MHz
#N/A	128 kBytes	40 kBytes	36	80 MHz

451/5	120 kD to a	40 kDutoo	20	00 MLI=
#N/A	128 kBytes	40 kBytes	36	80 MHz
#N/A	256 kBytes	64 kBytes	38	80 MHz
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#N/A	512 kBytes	160 kBytes	38	80 MHz
mi vii	012 KBytoo	100 KBytoo	- 00	00 WII 12
#N/A	512 kBytes	160 kBytes	38	80 MHz
		•		
#N/A	512 kBytes	320 kBytes	38	120 MHz
#N/A	1024 kBytes	320 kBytes	38	120 MHz
#N/A	1024 kBytes	320 kBytes	36	120 MHz
	1			
#N/A	1024 kBytes	320 kBytes	38	120 MHz
#N/A	1024 kBytes	320 kBytes	36	120 MHz

#N/A	256 kBytes	256 kBytes	38	110 MHz
#N/A	512 kBytes	256 kBytes	38	110 MHz
#N/A	512 kBytes	256 kBytes	36	110 MHz
#N/A	512 kBytes	256 kBytes	38	110 MHz
#N/A	512 kBytes	256 kBytes	36	110 MHz

```
Notes
Cortex-M23 instead of Cortex-M3
Pin 1: "VBAT" on F103 becomes "VDD" (requires shorting resistor/solder blob on R2);
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Cortex-M23 instead of Cortex-M3
Pin 1: "VBAT" on F103 becomes "OSC32-IN" (loss of VBAT functionality; loss of OSC32 functionality);
Pin 2: "PC13" on F103 becomes "OSC32-OUT" (unusable as GPIO; loss of OSC32 functionality)
Pin 3: "OSC32-IN" on F103 becomes "OSC-IN" (precludes use of 32 kHz oscillator input);
Pin 4: "OSC32-OUT" on F103 becomes "OSC-OUT" (precludes use of 32 kHz oscillator input);
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO):
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Appears to have internal op-amps
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO):
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Cortex-M4 instead of Cortex-M3
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Pin 1: "VBAT" on F103 becomes "PC0" on L010 (no degradation);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L010 (loss of wake-up pin; no hardware changes
required):
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Cortex-M4 instead of Cortex-M3
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F031 (loss of wake-up pin; no hardware changes
Pin 35: "VSS on F103 becomes "PF6" on F031 (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" on F031 (unusable as GPIO);
```

```
Cortex-M4 instead of Cortex-M3
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No JTAG; No SWO
Includes cap touch peripheral
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No JTAG; No SWO
Includes cap touch peripheral
Cortex-M4 instead of Cortex-M3
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No JTAG; No SWO
Includes cap touch peripheral
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Cortex-M4 instead of Cortex-M3
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No JTAG; No SWO
Includes cap touch peripheral
Cortex-M4 instead of Cortex-M3
No USB
Cortex-M4 instead of Cortex-M3
Cortex-M4 instead of Cortex-M3
Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);
No BOOT1; No JTAG; No SWO
Includes cap touch peripheral
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F031 (loss of wake-up pin; no hardware changes
required);
Pin 35: "VSS on F103 becomes "PF6" on F031 (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" on F031 (unusable as GPIO);
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
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Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F051 (loss of wake-up pin; no hardware changes required):
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Pin 35: "VSS on F103 becomes "PF6" on F051 (unusable as GPIO);

Pin 36: "VDD" on F103 becomes "PF7" on F051 (unusable as GPIO);

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F302 (unusable as VREF-);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F302 (loss of wake-up pin; no hardware changes required);

Pin 1: "VBAT" on F103 becomes "VDD" on L052 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L052 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD\_USB" on L052 (no degradation);

Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36 instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F301 (unusable as VREF-);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F301 (loss of wake-up pin; no hardware changes required);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F072 (loss of wake-up pin; no hardware changes required);

Pin 35: "VSS on F103 becomes "PF6" (unusable as GPIO);

Pin 36: "VDD" on F103 becomes "PF7" (unusable as GPIO);

No BOOT1; No USB; No JTAG; No SWO

Includes cap touch peripheral

No USB

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F091 (loss of wake-up pin; no hardware changes required):

Pin 1: "VBAT" on F103 becomes "VDD" on F070 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F070 (loss of wake-up pin; no hardware changes required);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F091 (loss of wake-up pin; no hardware changes required);

Pin 1: "VBAT" on F103 becomes "VDD" on L072 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L072 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD USB" on L072 (no degradation);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F071 (loss of wake-up pin; no hardware changes required);

Cortex-M4 instead of Cortex-M3

No USE

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F071 (loss of wake-up pin; no hardware changes required);

No USB

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Pin 1: "VBAT" on F103 becomes "VDD" on F030 (requires shorting resistor/solder blob on R2);
```

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F030 (loss of wake-up pin; no hardware changes required);

Pin 35: "VSS on F103 becomes "PF6" on F030 (unusable as GPIO);

Pin 36: "VDD" on F103 becomes "PF7" on F030 (unusable as GPIO);

Pin 1: "VBAT" on F103 becomes "VDD" on L051 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L051 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDDIO" on L051 (no degradation);

Pin 10: "PA0-WKUP" on F103 becomes "PA0/CK\_IN" on L412 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD USB" on L412 (no degradation);

## No BOOT1

Pin 1: "VBAT" on F103 becomes "VDD" on L051 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L051 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDDIO" on L051 (no degradation);

## No USB

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F303 (unusable as VREF-);

Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F303 (unusable as VREF+);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F303 (loss of wake-up pin; no hardware changes required);

Pin 1: "VBAT" on F103 becomes "VLCD" on L151 (same hardware requirements, different function); No SWO

## No USB

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L431 (unusable as VREF-);

Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L431 (unusable as VREF+);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L431 (loss of wake-up pin; no hardware changes required);

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L431 (unusable as VREF-);

Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L431 (unusable as VREF+);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L431 (loss of wake-up pin; no hardware changes required);

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F303 (unusable as VREF-);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F303 (loss of wake-up pin; no hardware changes required);

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F303 (unusable as VREF-);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F303 (loss of wake-up pin; no hardware changes required);

Pin 1: "VBAT" on F103 becomes "VDD" on F030 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F030 (loss of wake-up pin; no hardware changes required);

Pin 35: "VSS on F103 becomes "PF6" on F030 (unusable as GPIO);

Pin 36: "VDD" on F103 becomes "PF7" on F030 (unusable as GPIO);

```
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F334 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F334 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F334 (loss of wake-up pin; no hardware changes
reauired):
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F042 (loss of wake-up pin; no hardware changes
Pin 1: "VBAT" on F103 becomes "VDD" on F030 (requires shorting resistor/solder blob on R2);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F030 (loss of wake-up pin; no hardware changes
required):
Pin 35: "VSS on F103 becomes "PF6" on F030 (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" on F030 (unusable as GPIO);
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F302 (unusable as VREF-);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F302 (loss of wake-up pin; no hardware changes
reauired):
Pin 35: "VSS on F103 becomes "PF6" on F031 (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" on F031 (unusable as GPIO);
No BOOT1; No USB; No JTAG; No SWO
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F373 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F373 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F373 (loss of wake-up pin; no hardware changes
required):
Pin 17: "PA7" on F103 becomes "VDD" on F373 (possibly requires jumper wire);
Pin 23: "VSS" on F103 becomes "VSSSD/VREFSD-" on F373 (unusable as VREFSD-);
Pin 24: "VDD" on F103 becomes "VDDSD" on F373 (VDDSD is tied to VDD);
Pin 25: "PB12" on F103 becomes "VREFSD+" on F373 (no degradation);
Pin 26: "PB13" on F103 becomes "PB14" on F373 (no degradation);
Pin 27: "PB14" on F103 becomes "PB15" on F373 (no degradation);
Pin 28: "PB15" on F103 becomes "PD8" on F373 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L433 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L433 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L433 (loss of wake-up pin; no hardware changes
reauired):
Pin 36: "VDD" on F103 becomes "VDD USB" on L433 (no degradation);
Pin 1: "VBAT" on F103 becomes "VLCD" on L151 (same hardware requirements, different function);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F301 (unusable as VREF-);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F301 (loss of wake-up pin; no hardware changes
required);
```

Pin 1: "VBAT" on F103 becomes "VDD" on L071 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L071 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDDIO" on L071 (no degradation);

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F334 (unusable as VREF-);

Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F334 (unusable as VREF+);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F334 (loss of wake-up pin; no hardware changes required);

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F334 (unusable as VREF-);

Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F334 (unusable as VREF+);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F334 (loss of wake-up pin; no hardware changes required);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F051 (loss of wake-up pin; no hardware changes required);

Pin 35: "VSS on F103 becomes "PF6" on F051 (unusable as GPIO);

Pin 36: "VDD" on F103 becomes "PF7" on F051 (unusable as GPIO);

Pin 1: "VBAT" on F103 becomes "VLCD" on L151 (same hardware requirements, different function); No SWO

Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L433 (unusable as VREF-);

Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L433 (unusable as VREF+);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L433 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD\_USB" on L433 (no degradation);

Pin 1: "VBAT" on F103 becomes "VLCD" on L151 (same hardware requirements, different function); No SWO

Pin 10: "PA0-WKUP" on F103 becomes "PA0/CK\_IN" on L412 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD USB" on L412 (no degradation);

No BOOT1

Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36 instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues

Pin 1: "VBAT" on F103 becomes "VDD" on L072 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L072 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD USB" on L072 (no degradation);

Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36 instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F042 (loss of wake-up pin; no hardware changes required);

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Pin 1: "VBAT" on F103 becomes "VDD" on L071 (requires shorting resistor/solder blob on R2):
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L071 (loss of wake-up pin; no hardware changes
required);
Pin 36: "VDD" on F103 becomes "VDDIO" on L071 (no degradation);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F072 (loss of wake-up pin; no hardware changes
required):
Pin 1: "VBAT" on F103 becomes "VDD" on F030 (requires shorting resistor/solder blob on R2);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F030 (loss of wake-up pin; no hardware changes
reauired):
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
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instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F373 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F373 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F373 (loss of wake-up pin; no hardware changes
required):
Pin 17: "PA7" on F103 becomes "VDD" on F373 (possibly requires jumper wire);
Pin 23: "VSS" on F103 becomes "VSSSD/VREFSD-" on F373 (unusable as VREFSD-);
Pin 24: "VDD" on F103 becomes "VDDSD" on F373 (VDDSD is tied to VDD);
Pin 25: "PB12" on F103 becomes "VREFSD+" on F373 (no degradation);
Pin 26: "PB13" on F103 becomes "PB14" on F373 (no degradation);
Pin 27: "PB14" on F103 becomes "PB15" on F373 (no degradation);
Pin 28: "PB15" on F103 becomes "PD8" on F373 (no degradation);
Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
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Near as I can tell, all of the STM32G-series MCUs map the SWD pins (SWDIO/SWCLK) to pins 35/36
instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L452 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L452 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L452 (loss of wake-up pin; no hardware changes
required);
Pin 36: "VDD" on F103 becomes "VDD USB" on L452 (no degradation);
Pin 1: "VBAT" on F103 becomes "VLCD" on L053 (same hardware requirements, different function);
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Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L053 (loss of wake-up pin; no hardware changes

required);
Pin 36: "VDD" on F103 becomes "VDD\_USB" on L053 (no degradation);

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Pin 20: "PB2" on F103 becomes "NPOR" on F038 (requires external device to manage power-on reset)
Pin 35: "VSS on F103 becomes "PF6" on F038 (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" on F038 (unusable as GPIO);
No BOOT1: No USB: No JTAG: No SWO
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F051 (loss of wake-up pin; no hardware changes
required):
Pin 35: "VSS on F103 becomes "PF6" on F051 (unusable as GPIO):
Pin 36: "VDD" on F103 becomes "PF7" on F051 (unusable as GPIO);
Pin 1: "VBAT" on F103 becomes "VDD" on F070 (requires shorting resistor/solder blob on R2):
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F070 (loss of wake-up pin; no hardware changes
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F078 (loss of wake-up pin; no hardware changes
required);
Pin 20: "PB2" on F103 becomes "NPOR" on F038 (requires external device to manage power-on reset);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F098 (loss of wake-up pin; no hardware changes
reauired):
Pin 20: "PB2" on F103 becomes "NPOR" on F038 (requires external device to manage power-on reset);
No USB
No USB
No USB
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F302 (unusable as VREF-);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F302 (loss of wake-up pin; no hardware changes
required);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F302 (unusable as VREF-);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F302 (loss of wake-up pin; no hardware changes
required);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F303 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F303 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F303 (loss of wake-up pin: no hardware changes
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F318 (unusable as VREF-):
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F0318 (loss of wake-up pin; no hardware changes
required);
Pin 20: "PB2" on F103 becomes "NPOR" on F318 (requires external device to manage power-on reset);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F328 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F328 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F328 (loss of wake-up pin; no hardware changes
required):
Pin 20: "PB2" on F103 becomes "NPOR" on F328 (requires external device to manage power-on reset);
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Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F358 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F358 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F358 (loss of wake-up pin; no hardware changes
required):
Pin 20: "PB2" on F103 becomes "NPOR" on F358 (requires external device to manage power-on reset);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F373 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F373 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F373 (loss of wake-up pin; no hardware changes
required):
Pin 17: "PA7" on F103 becomes "VDD" on F373 (possibly requires jumper wire);
Pin 23: "VSS" on F103 becomes "VSSSD/VREFSD-" on F373 (unusable as VREFSD-);
Pin 24: "VDD" on F103 becomes "VDDSD" on F373 (VDDSD is tied to VDD);
Pin 25: "PB12" on F103 becomes "VREFSD+" on F373 (no degradation);
Pin 26: "PB13" on F103 becomes "PB14" on F373 (no degradation):
Pin 27: "PB14" on F103 becomes "PB15" on F373 (no degradation);
Pin 28: "PB15" on F103 becomes "PD8" on F373 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F378 (unusable as VREF-):
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F378 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on F378 (loss of wake-up pin; no hardware changes
required);
Pin 17: "PA7" on F103 becomes "VDD" on F373 (possibly requires jumper wire);
Pin 20: "PB2" on F103 becomes "NPOR" on F378 (requires external device to manage power-on reset);
Pin 21: "PB10" on F103 becomes "PE8" on F378 (no degradation);
Pin 22: "PB11" on F103 becomes "PE9" on F378 (no degradation);
Pin 23: "VSS" on F103 becomes "VSSSD/VREFSD-" on F378 (unusable as VREFSD-);
Pin 24: "VDD" on F103 becomes "VDDSD" on F378 (VDDSD is tied to VDD);
Pin 25: "PB12" on F103 becomes "VREFSD+" on F378 (no degradation);
Pin 26: "PB13" on F103 becomes "PB14" on F378 (no degradation);
Pin 27: "PB14" on F103 becomes "PB15" on F378 (no degradation):
Pin 28: "PB15" on F103 becomes "PD8" on F378 (no degradation);
Pin 35: "VSS on F103 becomes "PF6" on F378 (unusable as GPIO);
Pin 36: "VDD" on F103 becomes "PF7" on F378 (unusable as GPIO);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on F410 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on F410 (unusable as VREF+);
Pin 22: "PB10" on F103 becomes "VCAP 1" on F410 (requires external capacitor);
Pin 46: "PB9" on F103 becomes "VSS" on F410 (possibly required jumper wire);
Pin 47: "VSS" on F103 becomes "PDR ON" on F410 (Power supply supervisor permanently turned off;
requires external supervisor);
No USB
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instead of pins 34/37, making them unsuitable for this breakout board (in addition to any other issues
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- Pin 1: "VBAT" on F103 becomes "PC0" on L031 (no degradation);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L031 (loss of wake-up pin; no hardware changes required);
- Pin 1: "VBAT" on F103 becomes "PC0" on L031 (no degradation);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L031 (loss of wake-up pin; no hardware changes required):
- Pin 1: "VBAT" on F103 becomes "PC0" on L041 (no degradation);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L041 (loss of wake-up pin; no hardware changes required);
- Pin 1: "VBAT" on F103 becomes "VDD" on L052 (requires shorting resistor/solder blob on R2);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L052 (loss of wake-up pin; no hardware changes required):
- Pin 36: "VDD" on F103 becomes "VDD USB" on L052 (no degradation);
- Pin 1: "VBAT" on F103 becomes "VLCD" on L053 (same hardware requirements, different function);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L053 (loss of wake-up pin; no hardware changes required);
- Pin 36: "VDD" on F103 becomes "VDD USB" on L053 (no degradation);
- Pin 1: "VBAT" on F103 becomes "VLCD" on L063 (same hardware requirements, different function);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L063 (loss of wake-up pin; no hardware changes required);
- Pin 36: "VDD" on F103 becomes "VDD USB" on L063 (no degradation);
- Pin 1: "VBAT" on F103 becomes "VDD" on L071 (requires shorting resistor/solder blob on R2);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L071 (loss of wake-up pin; no hardware changes required);
- Pin 36: "VDD" on F103 becomes "VDDIO" on L071 (no degradation);
- Pin 1: "VBAT" on F103 becomes "VLCD" on L073 (same hardware requirements, different function);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L073 (loss of wake-up pin; no hardware changes required);
- Pin 36: "VDD" on F103 becomes "VDD\_USB" on L073 (no degradation);

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Pin 1: "VBAT" on F103 becomes "VLCD" on L073 (same hardware requirements, different function);
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Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L073 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD\_USB" on L073 (no degradation);

Pin 1: "VBAT" on F103 becomes "VDD" on L081 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L081 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDDIO" on L081 (no degradation);

Pin 1: "VBAT" on F103 becomes "VDD" on L081 (requires shorting resistor/solder blob on R2);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L081 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDDIO" on L081 (no degradation);

Pin 1: "VBAT" on F103 becomes "VLCD" on L083 (same hardware requirements, different function);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L083 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD USB" on L083 (no degradation);

Pin 1: "VBAT" on F103 becomes "VLCD" on L083 (same hardware requirements, different function);

Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L083 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD USB" on L083 (no degradation);

Pin 1: "VBAT" on F103 becomes "VLCD" on L151 (same hardware requirements, different function); No SWO

Pin 1: "VBAT" on F103 becomes "VLCD" on L151 (same hardware requirements, different function); No SWO

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Pin 1: "VBAT" on F103 becomes "VLCD" on L151 (same hardware requirements, different function); No SWO

Pin 10: "PA0-WKUP" on F103 becomes "PA0/CK\_IN" on L412 (loss of wake-up pin; no hardware changes required);

Pin 36: "VDD" on F103 becomes "VDD USB" on L412 (no degradation);

No BOOT1

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Pin 10: "PA0-WKUP" on F103 becomes "PA0/CK IN" on L412 (loss of wake-up pin: no hardware
changes required);
Pin 36: "VDD" on F103 becomes "VDD USB" on L412 (no degradation);
No BOOT1
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L443 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L443 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L443 (loss of wake-up pin; no hardware changes
required):
Pin 36: "VDD" on F103 becomes "VDD USB" on L443 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L451 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L451 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L451 (loss of wake-up pin; no hardware changes
required):
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L462 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L462 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L462 (loss of wake-up pin: no hardware changes
required):
Pin 36: "VDD" on F103 becomes "VDD USB" on L462 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L4P5 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L4P5 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L4P5 (loss of wake-up pin; no hardware changes
required);
Pin 36: "VDD" on F103 becomes "VDD_USB" on L4P5 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L4P5 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L4P5 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L4P5 (loss of wake-up pin; no hardware changes
required);
Pin 36: "VDD" on F103 becomes "VDD_USB" on L4P5 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L4P5 (unusable as VREF-):
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L4P5 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L4P5 (loss of wake-up pin; no hardware changes
required);
Pin 22: "PB11" on F103 becomes "VDD12" on L4P5 (no degradation):
Pin 46: "PB9" on F103 becomes "VDD12" on L4P5 (no degradation);
Pin 36: "VDD" on F103 becomes "VDD USB" on L4P5 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L4Q5 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L4Q5 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L4Q5 (loss of wake-up pin; no hardware changes
required);
Pin 36: "VDD" on F103 becomes "VDD_USB" on L4Q5 (no degradation);
Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L4Q5 (unusable as VREF-);
Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L4Q5 (unusable as VREF+);
Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L4Q5 (loss of wake-up pin; no hardware changes
required);
Pin 36: "VDD" on F103 becomes "VDD USB" on L4Q5 (no degradation);
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Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L552 (unusable as VREF-);
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- Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L552 (unusable as VREF+);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L552 (loss of wake-up pin; no hardware changes required);
- Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L552 (unusable as VREF-);
- Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L552 (unusable as VREF+);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L552 (loss of wake-up pin; no hardware changes required);
- Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L552 (unusable as VREF-);
- Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L552 (unusable as VREF+);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L552 (loss of wake-up pin; no hardware changes required);
- Pin 22: "PB11" on F103 becomes "VDD12 1" on L562 (no degradation);
- Pin 46: "PB9" on F103 becomes "VDD12 2" on L462 (no degradation);
- Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L562 (unusable as VREF-);
- Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L562 (unusable as VREF+):
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L562 (loss of wake-up pin; no hardware changes required);
- Pin 8: "VSS" on F103 becomes "VSS/VREF-" on L562 (unusable as VREF-);
- Pin 9: "VSS" on F103 becomes "VDD/VREF+" on L562 (unusable as VREF+);
- Pin 10: "PA0-WKUP" on F103 becomes "PA0" on L562 (loss of wake-up pin; no hardware changes required);
- Pin 22: "PB11" on F103 becomes "VDD12 1" on L562 (no degradation);
- Pin 46: "PB9" on F103 becomes "VDD12 2" on L462 (no degradation);