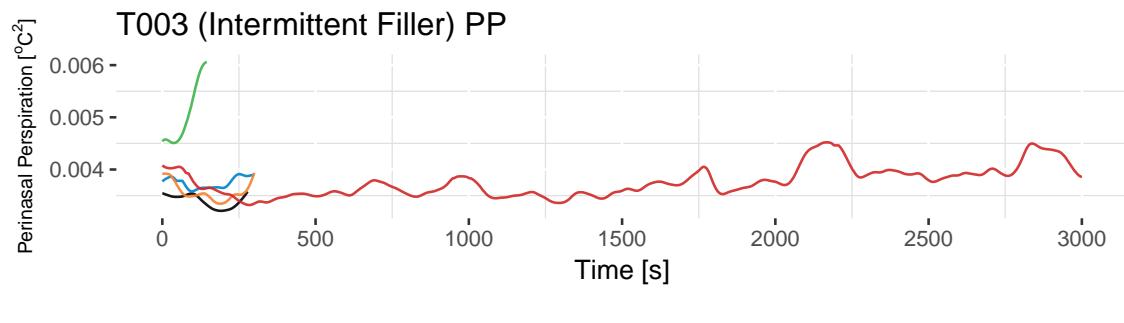
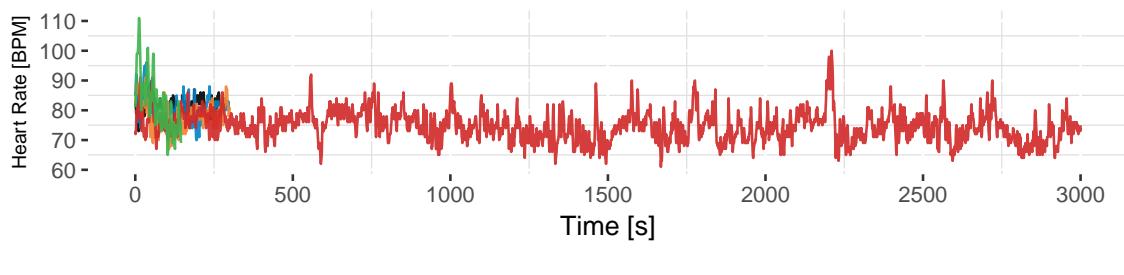


Sensor Channel per Subject - All Activities

Intermittent-Filler (IF) Subjects



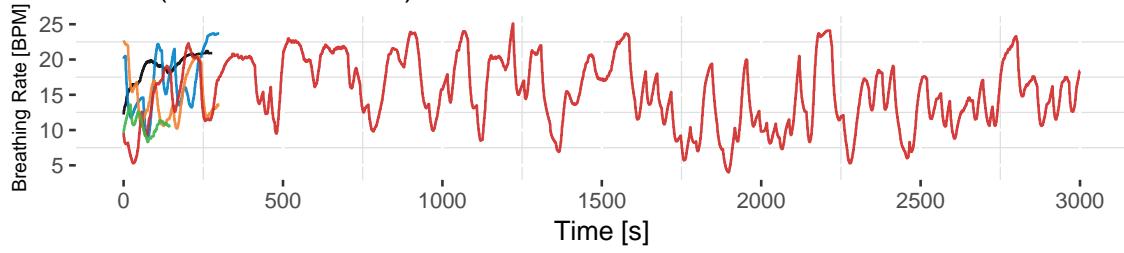
T003 (Intermittent Filler) HR



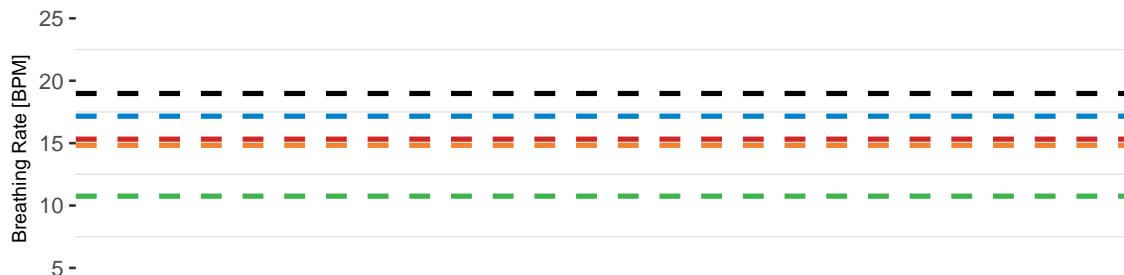
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



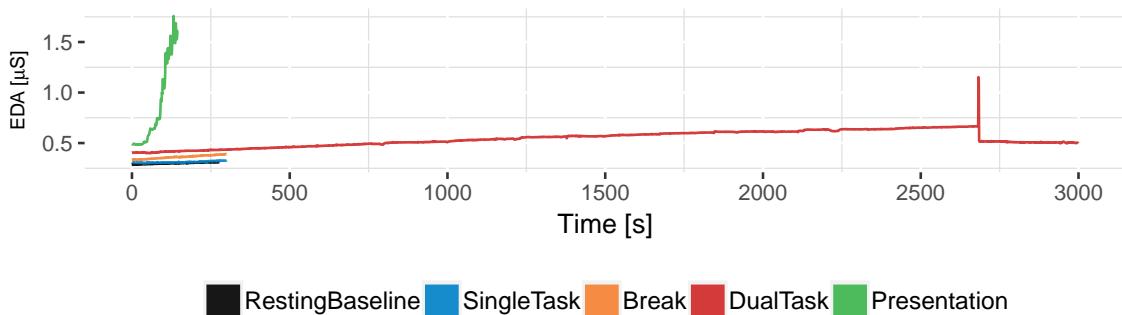
T003 (Intermittent Filler) BR



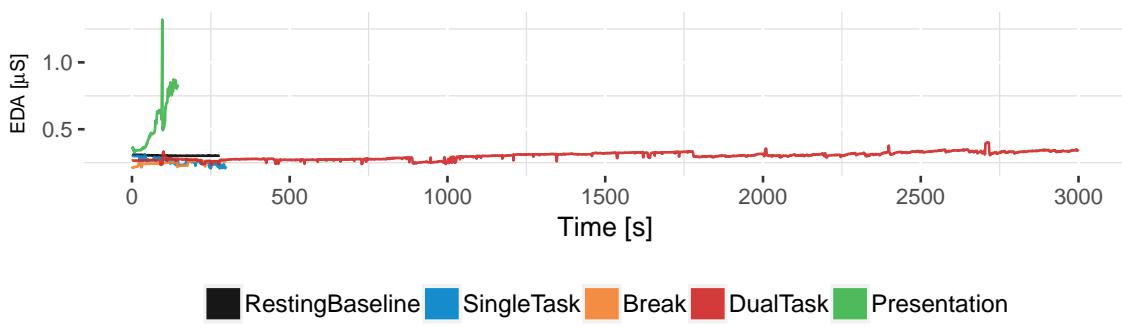
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



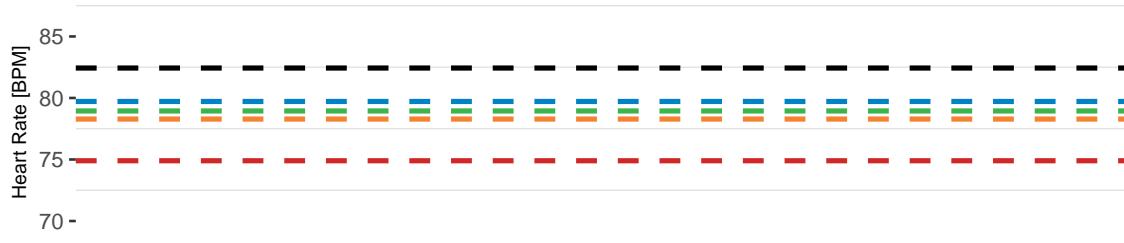
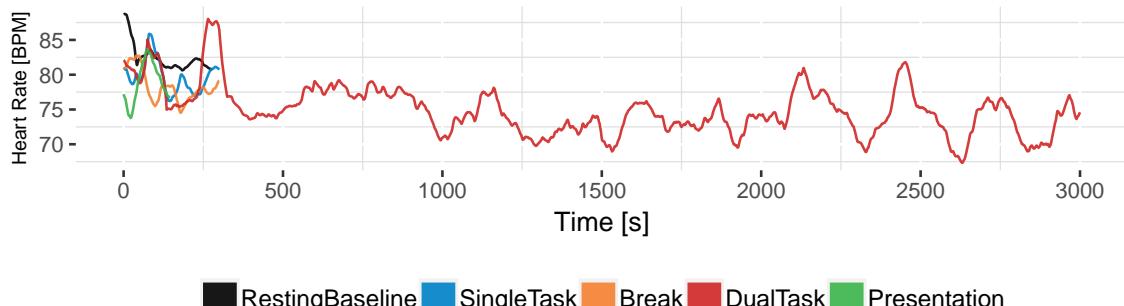
T003 (Intermittent Filler) D.EDA



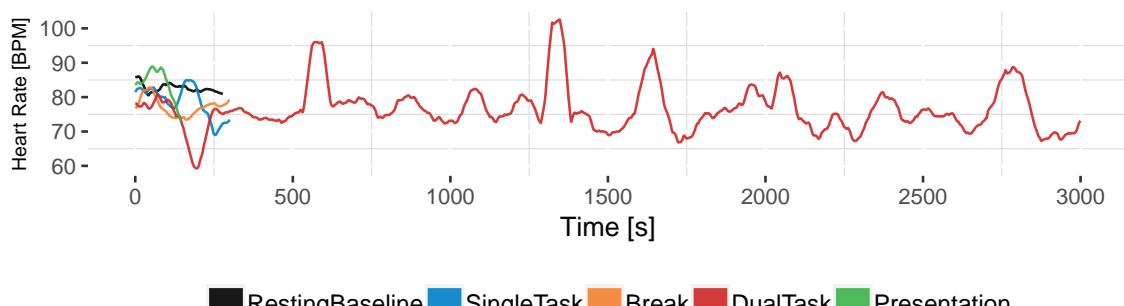
T003 (Intermittent Filler) N.EDA

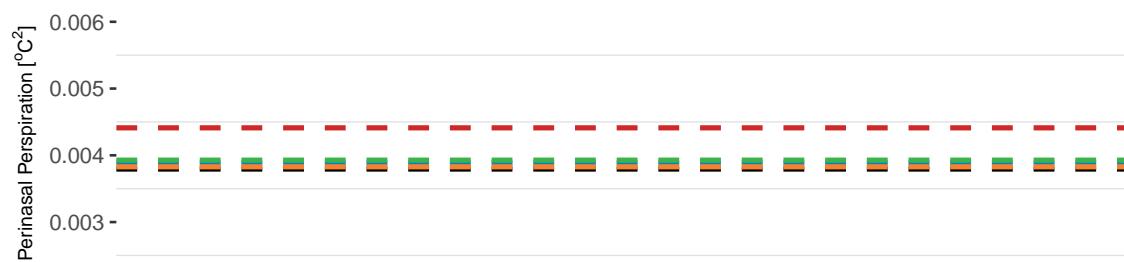
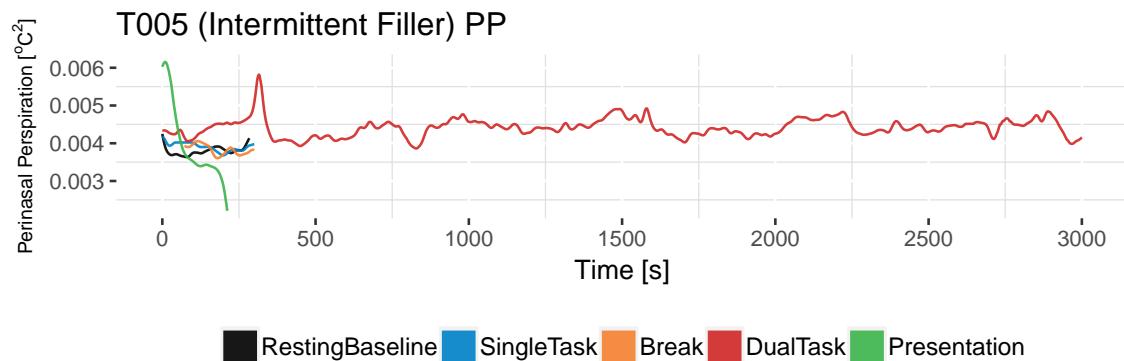


T003 (Intermittent Filler) D.HR

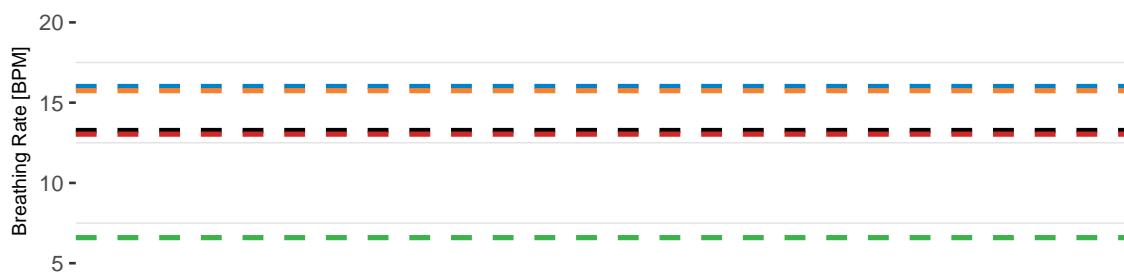
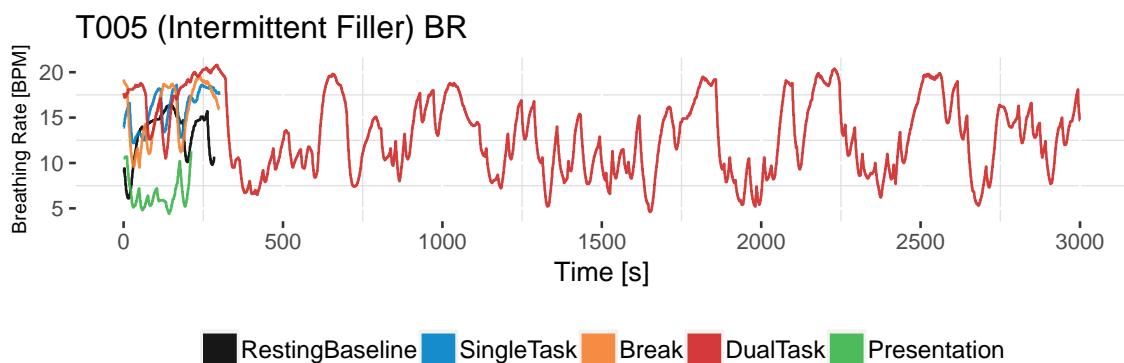


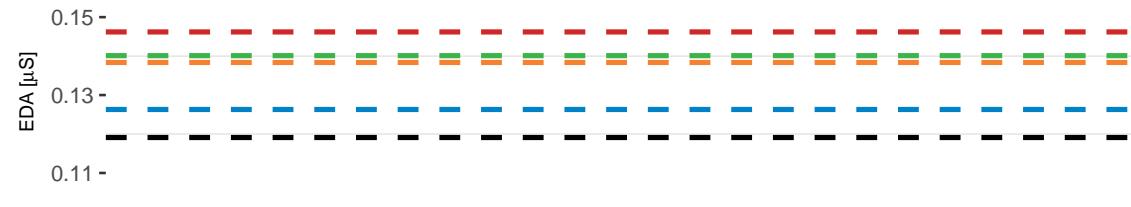
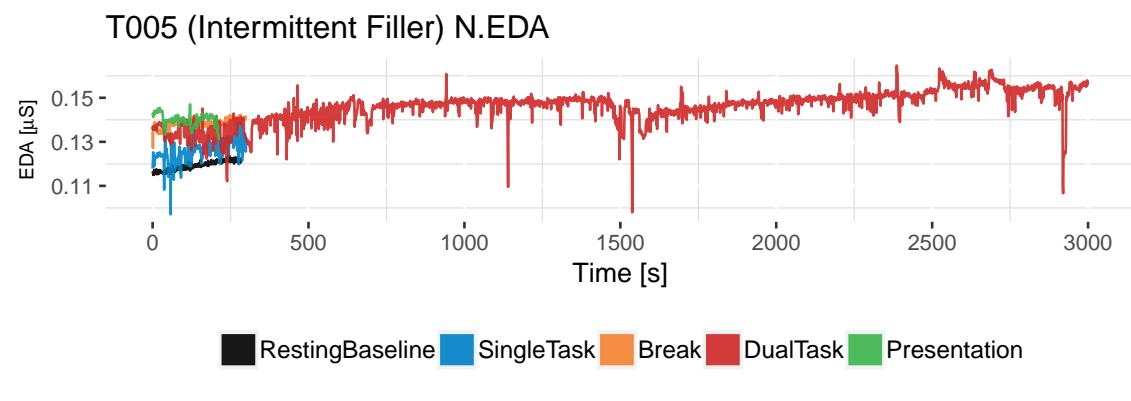
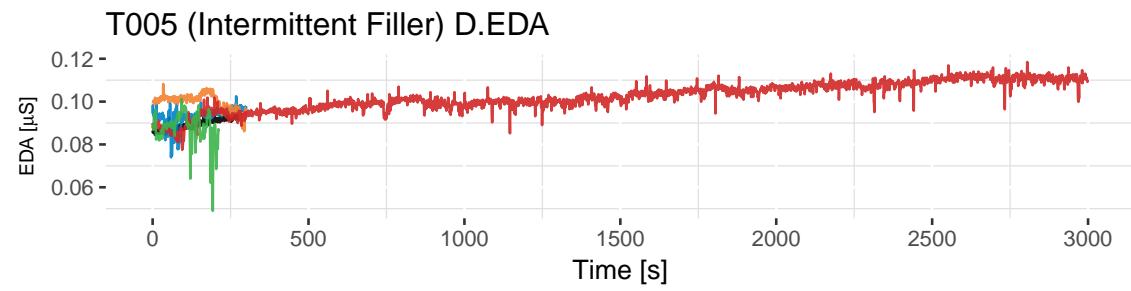
T003 (Intermittent Filler) N.HR



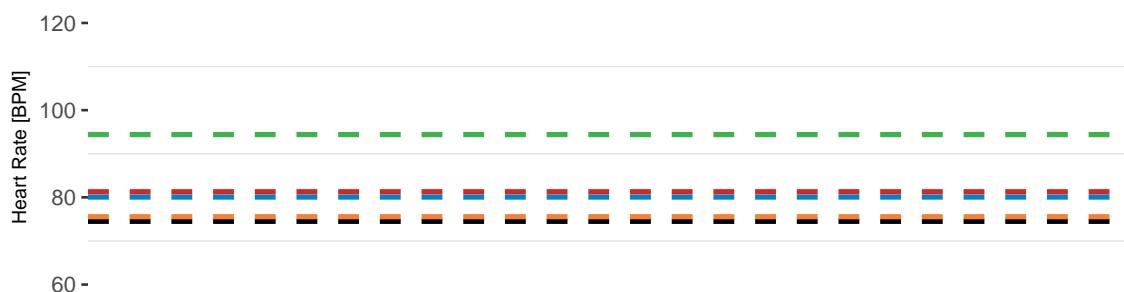
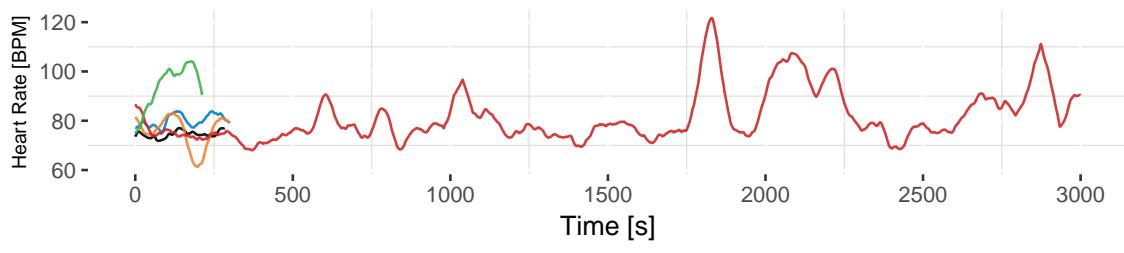


```
##  
##  
## Subject T005 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```

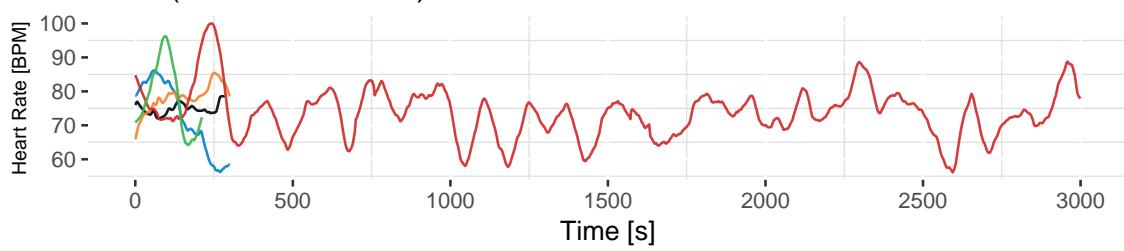


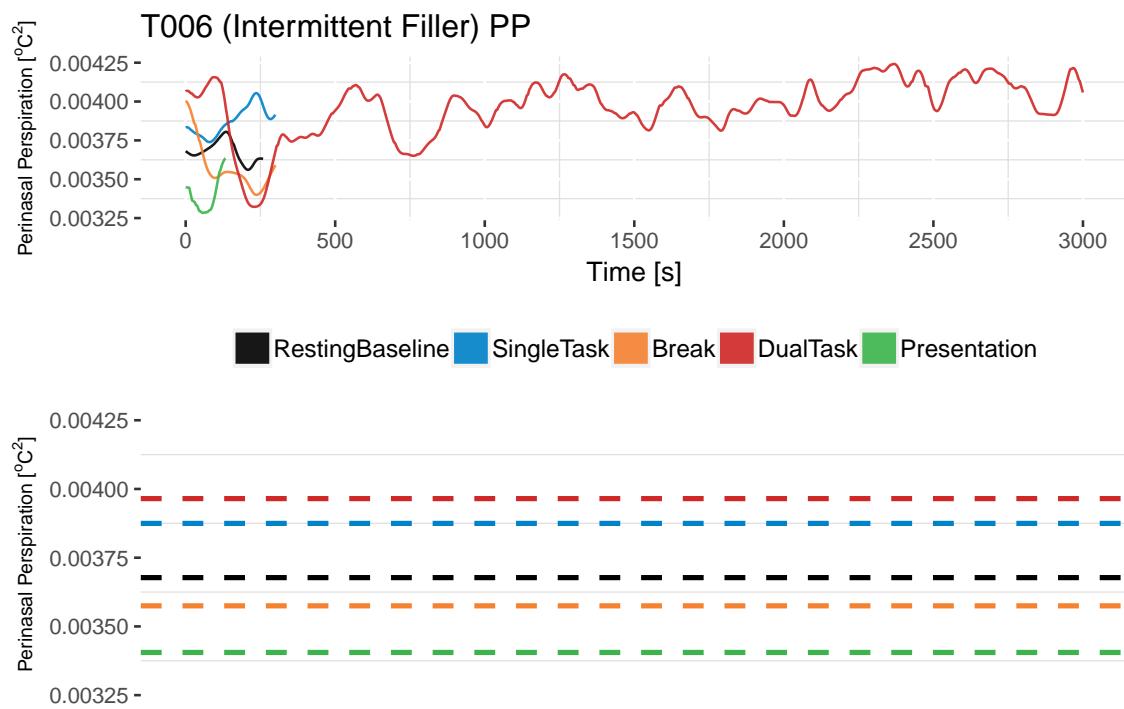


T005 (Intermittent Filler) D.HR

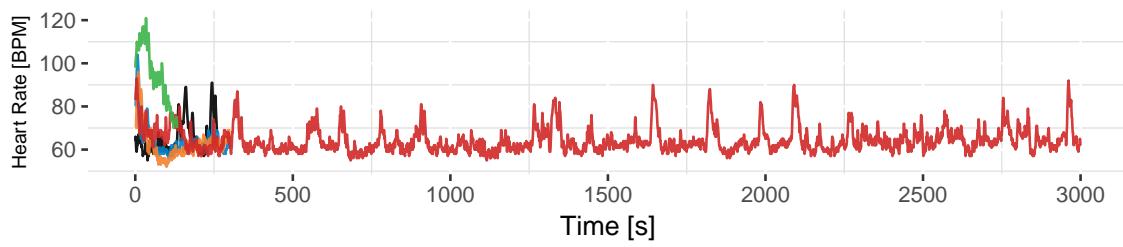


T005 (Intermittent Filler) N.HR

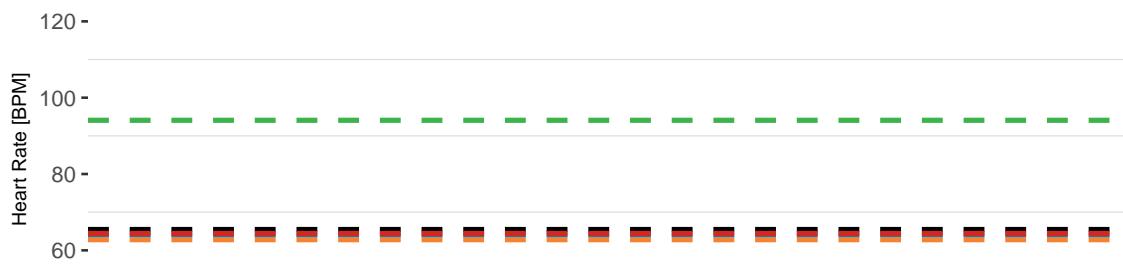




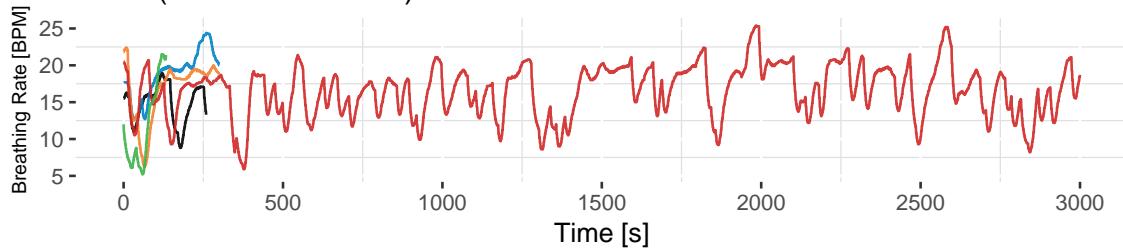
T006 (Intermittent Filler) HR



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



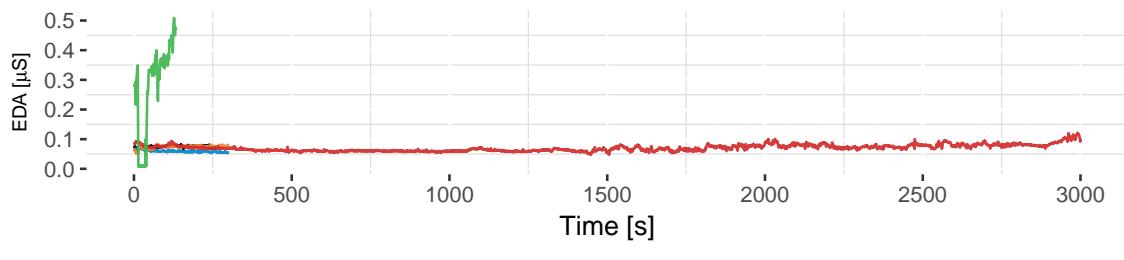
T006 (Intermittent Filler) BR



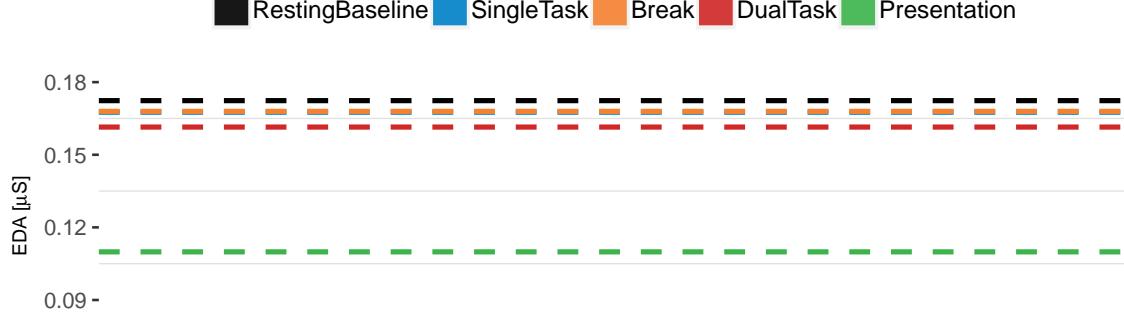
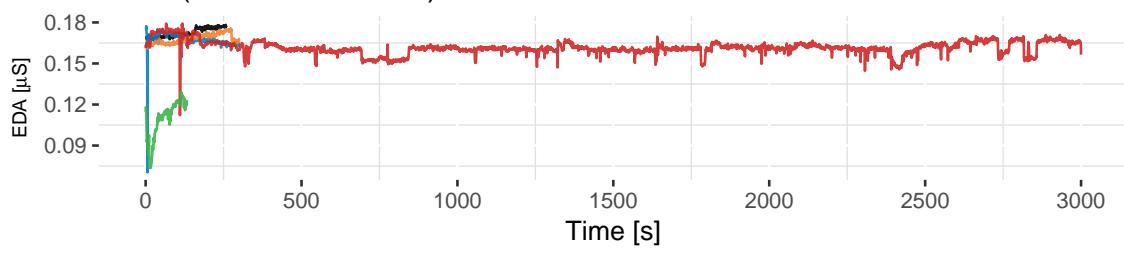
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



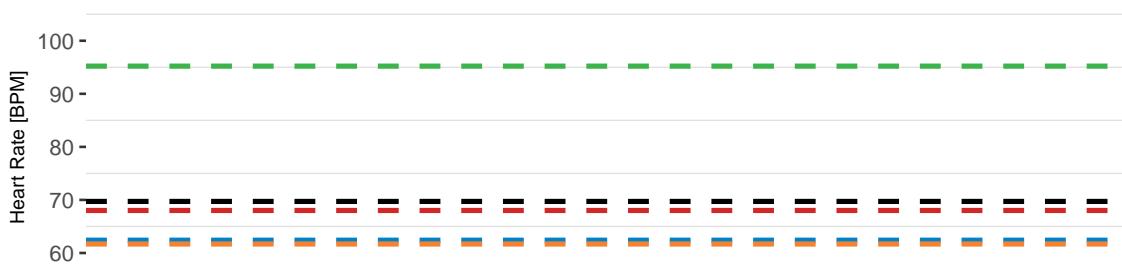
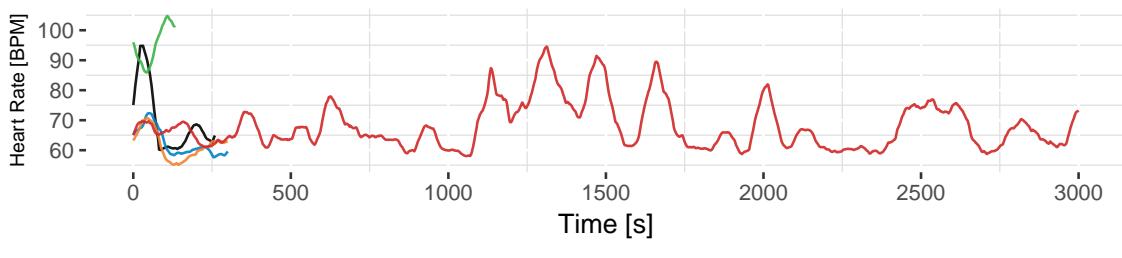
T006 (Intermittent Filler) D.EDA



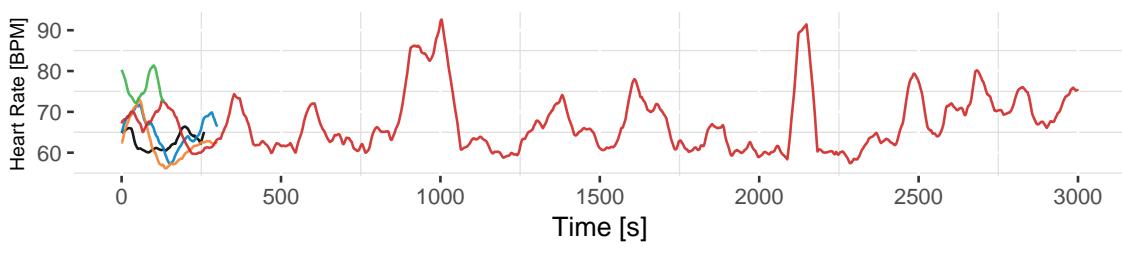
T006 (Intermittent Filler) N.EDA

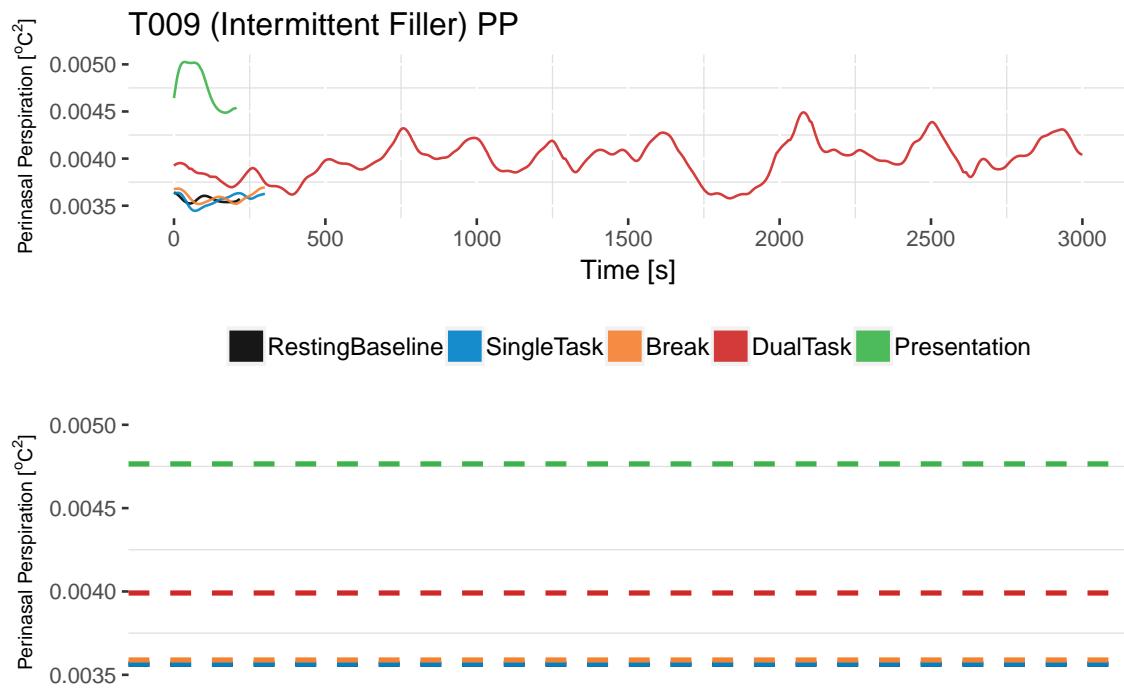


T006 (Intermittent Filler) D.HR

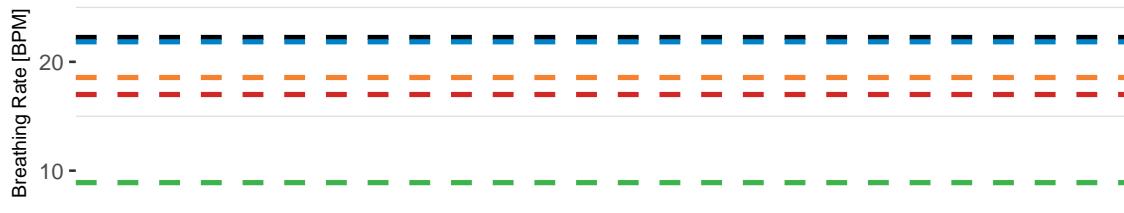
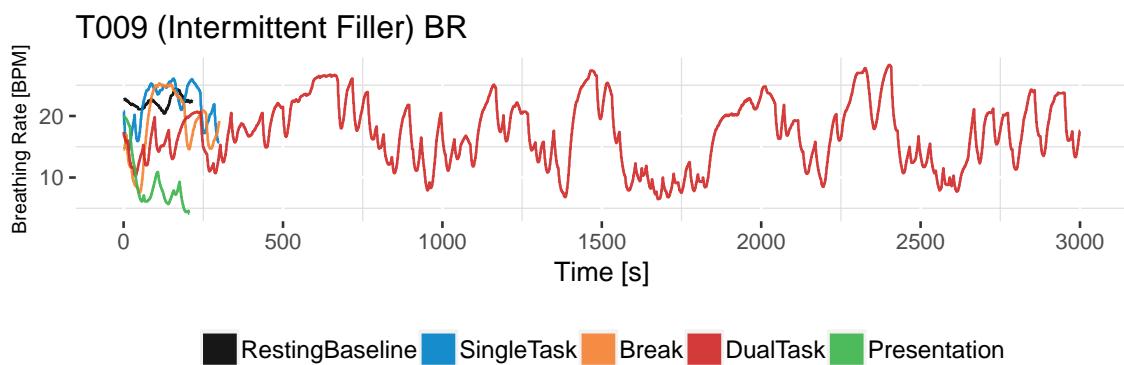


T006 (Intermittent Filler) N.HR

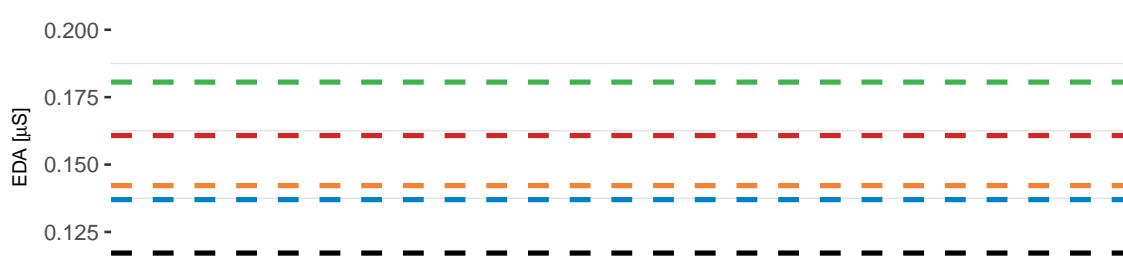
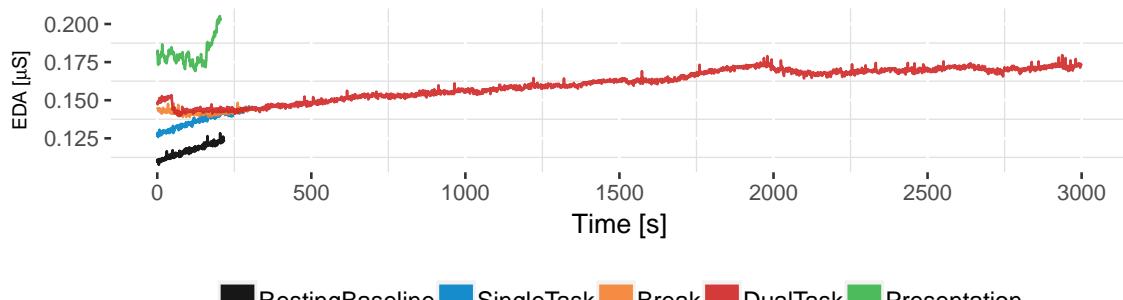




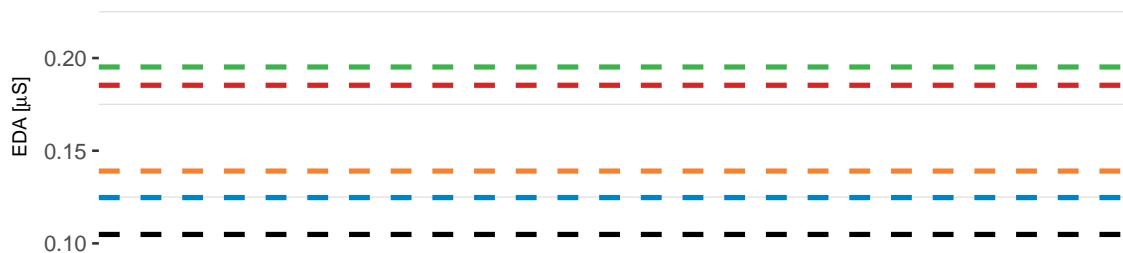
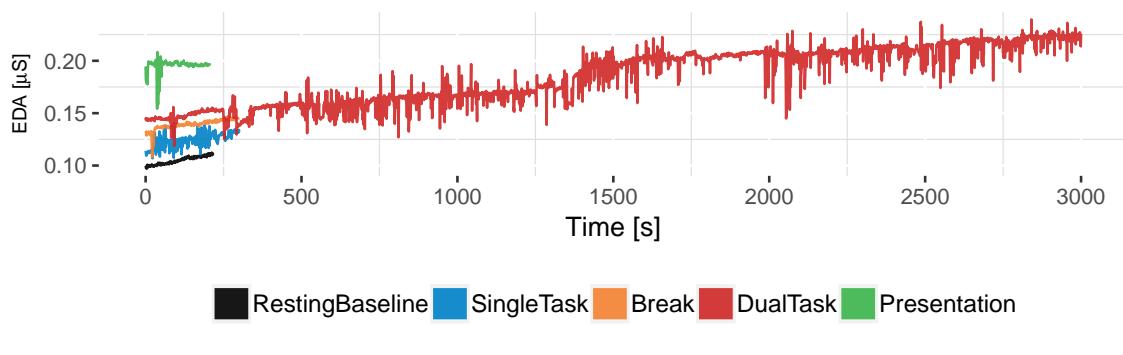
```
##  
##  
## Subject T009 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```



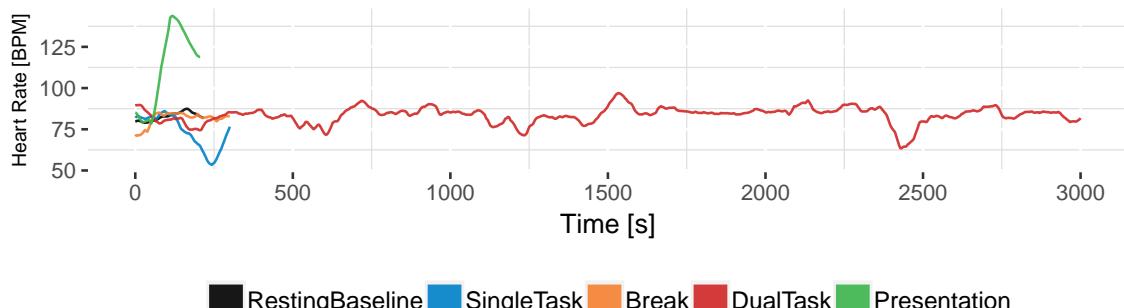
T009 (Intermittent Filler) D.EDA



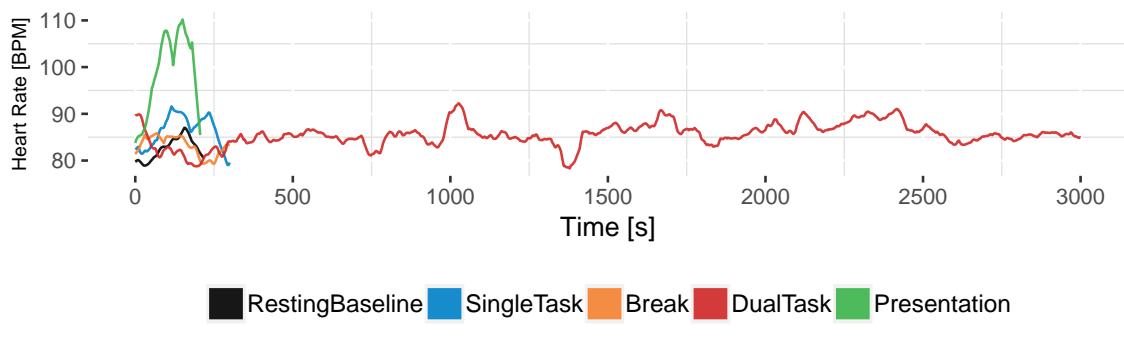
T009 (Intermittent Filler) N.EDA

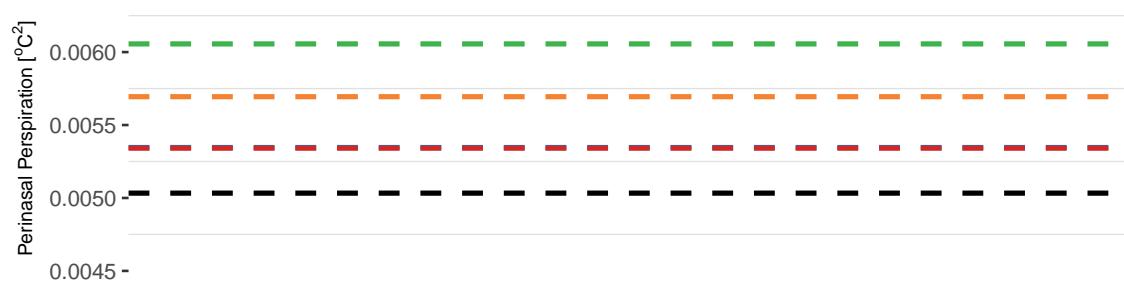
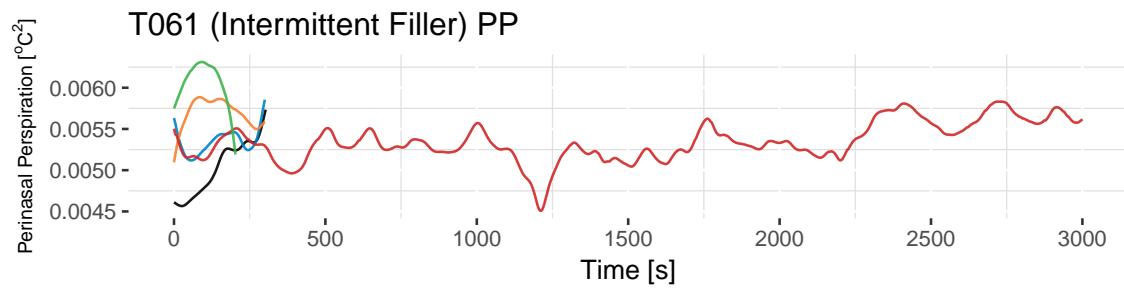


T009 (Intermittent Filler) D.HR

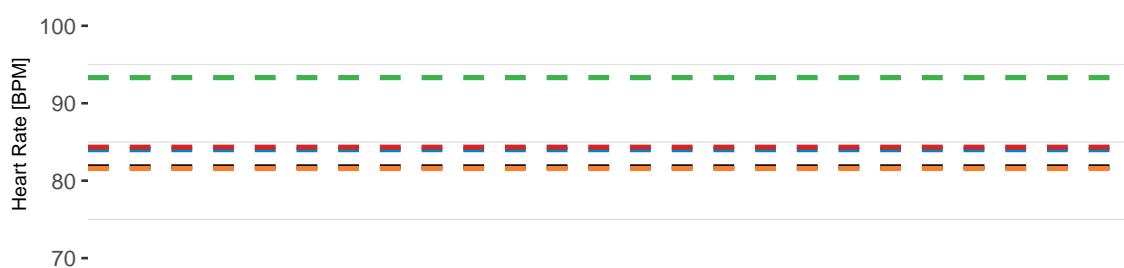


T009 (Intermittent Filler) N.HR

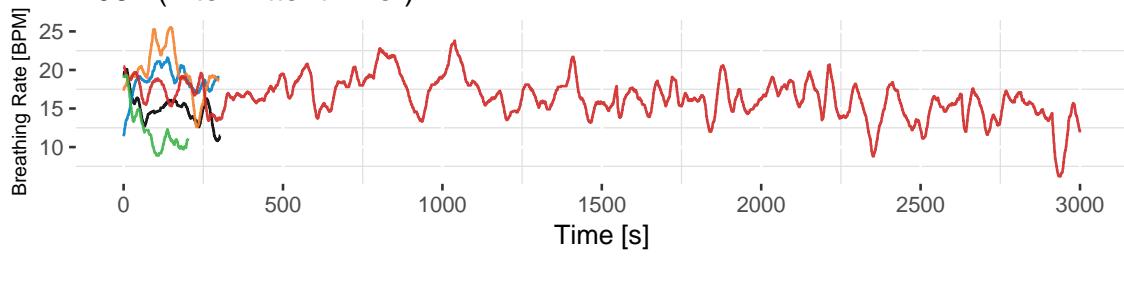


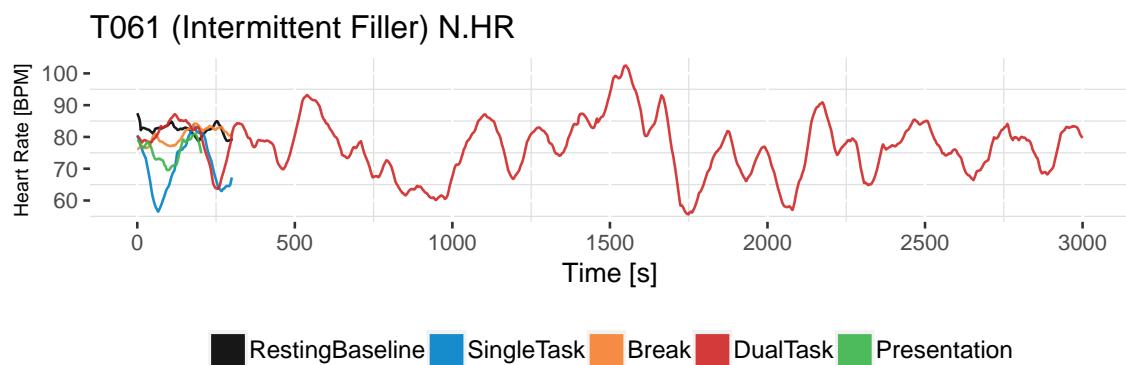
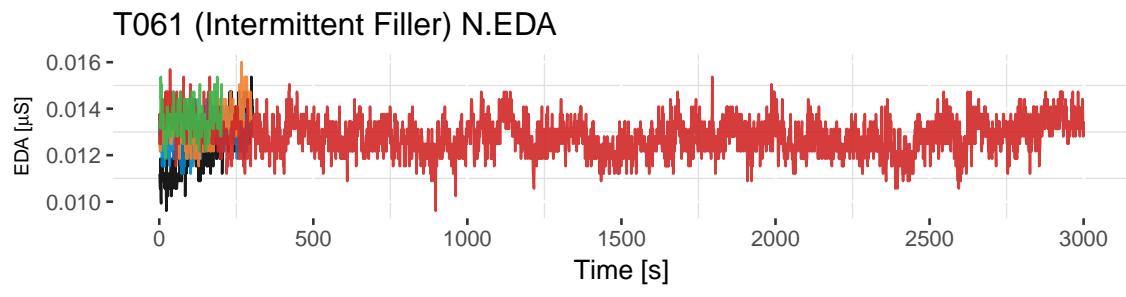


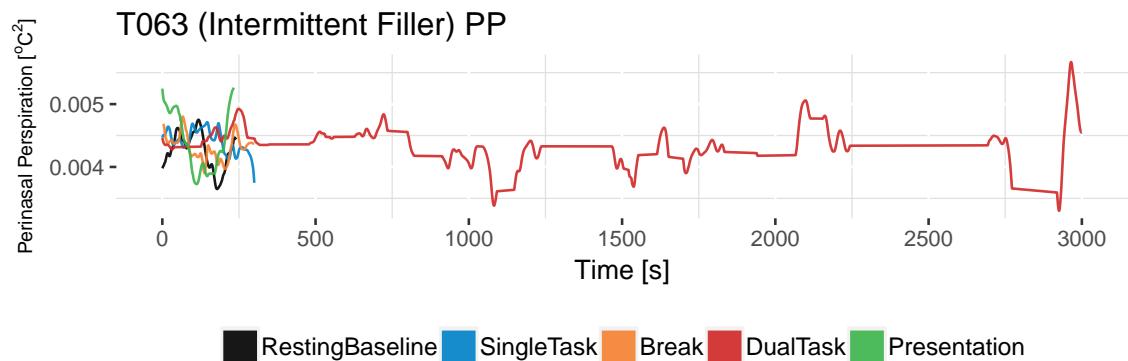
T061 (Intermittent Filler) HR



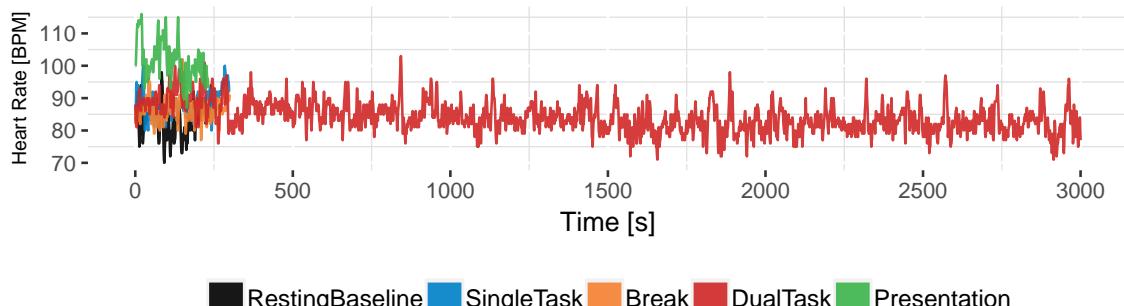
T061 (Intermittent Filler) BR







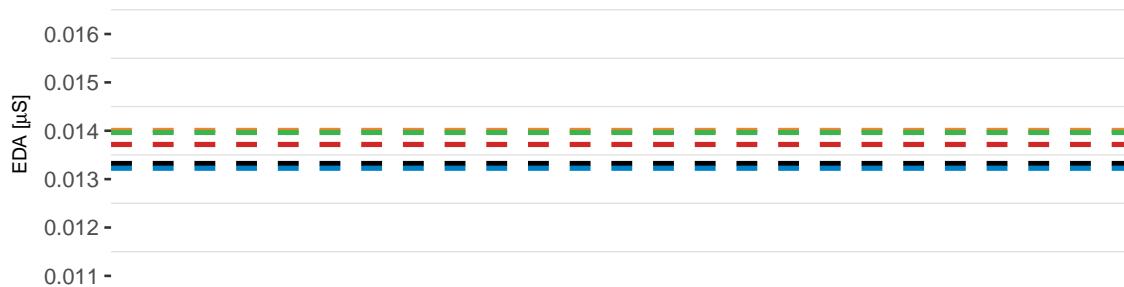
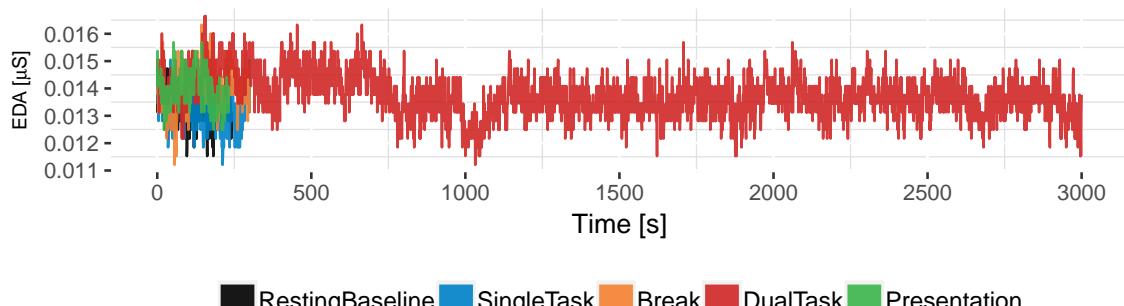
T063 (Intermittent Filler) HR



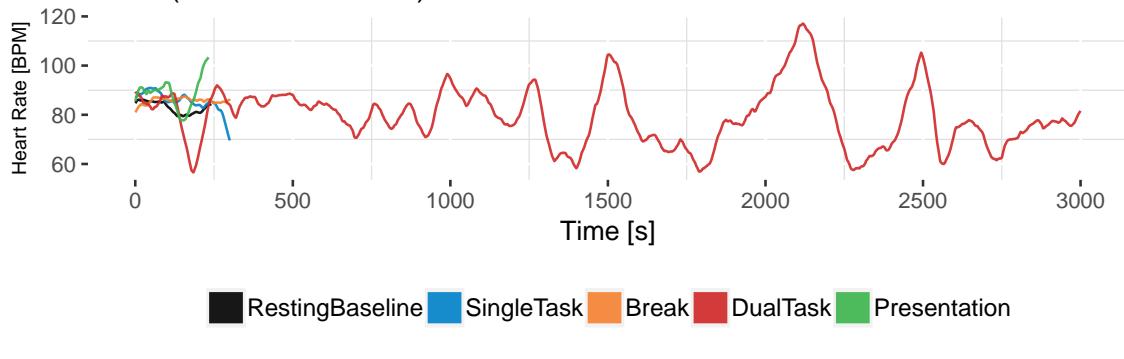
T063 (Intermittent Filler) BR

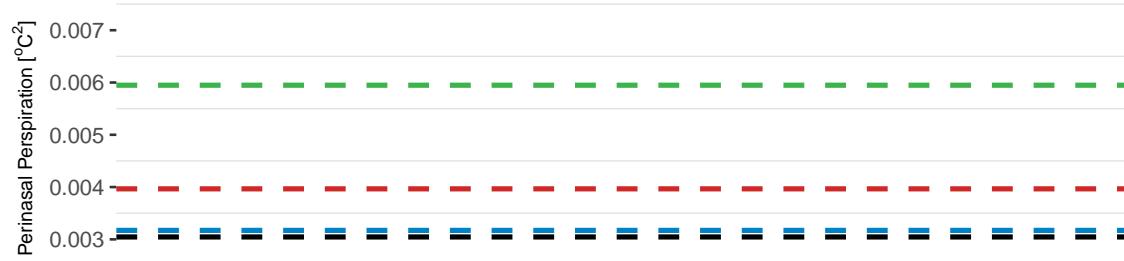
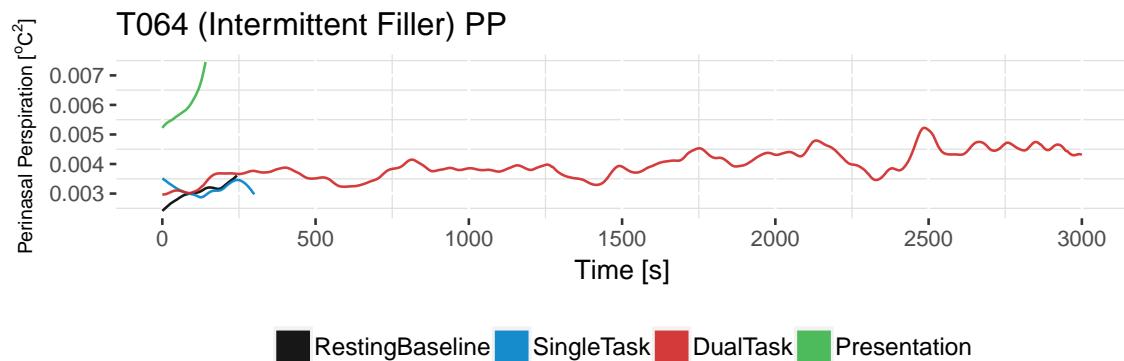


T063 (Intermittent Filler) N.EDA

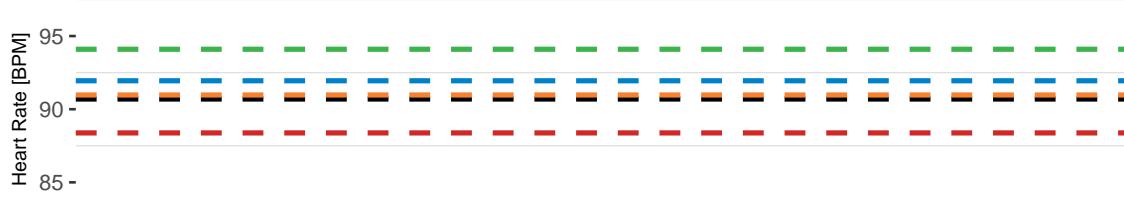
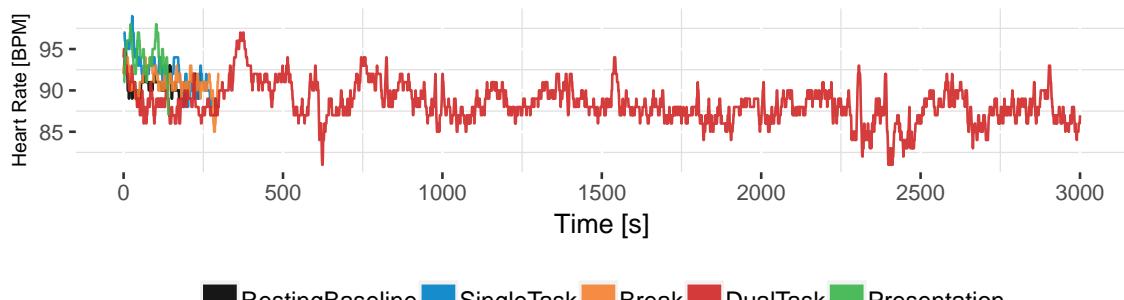


T063 (Intermittent Filler) N.HR

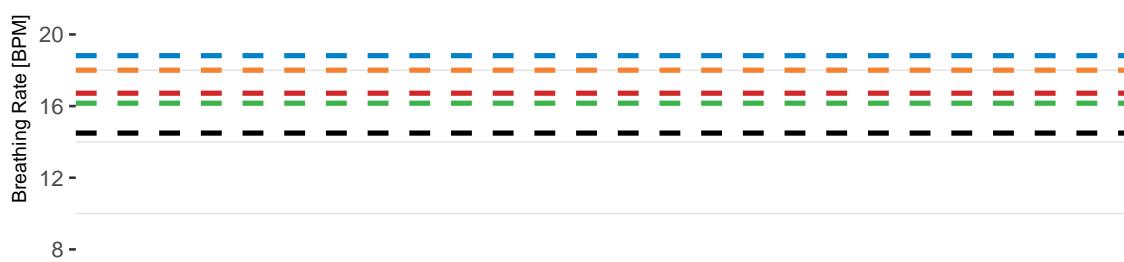




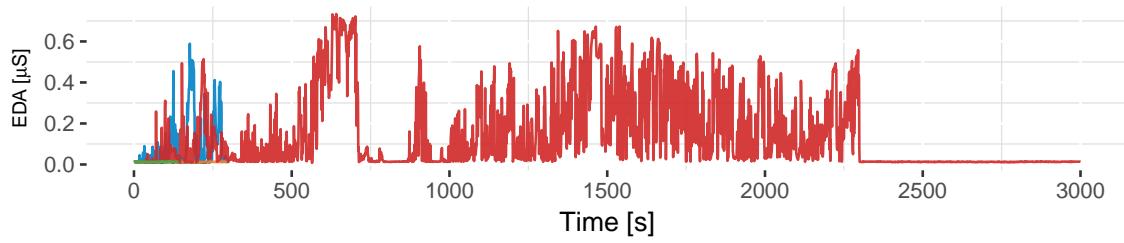
T064 (Intermittent Filler) HR



T064 (Intermittent Filler) BR



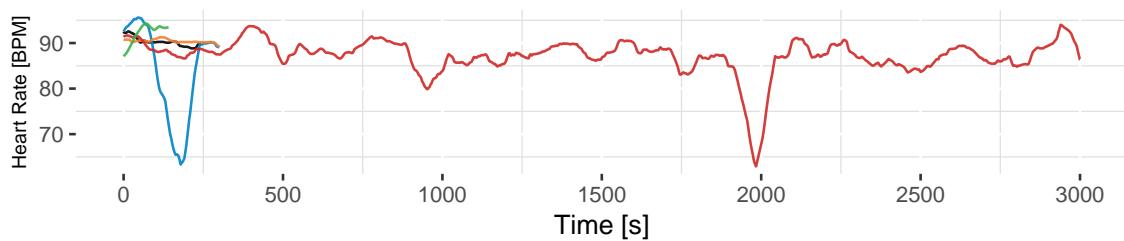
T064 (Intermittent Filler) N.EDA



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

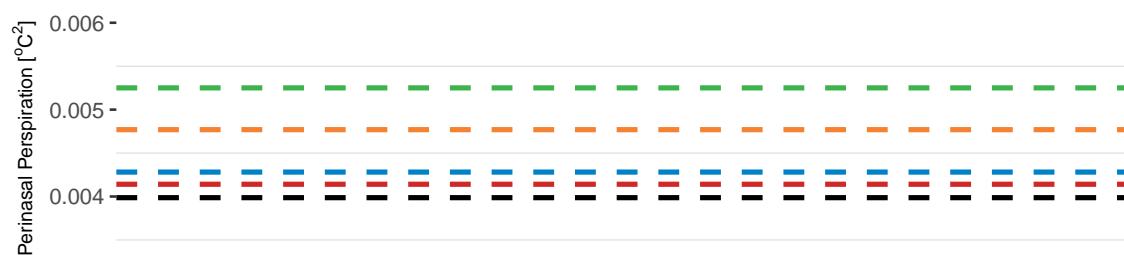
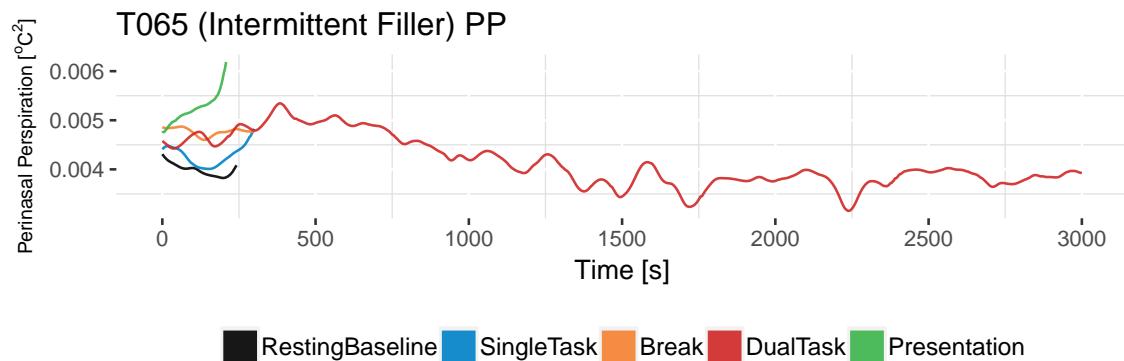


T064 (Intermittent Filler) N.HR

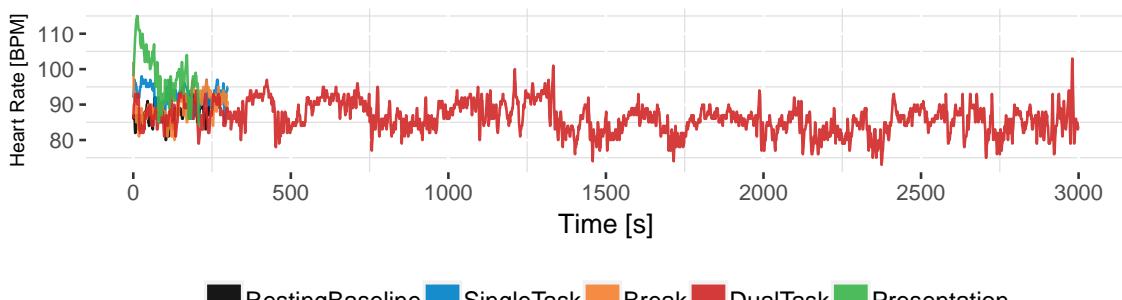


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

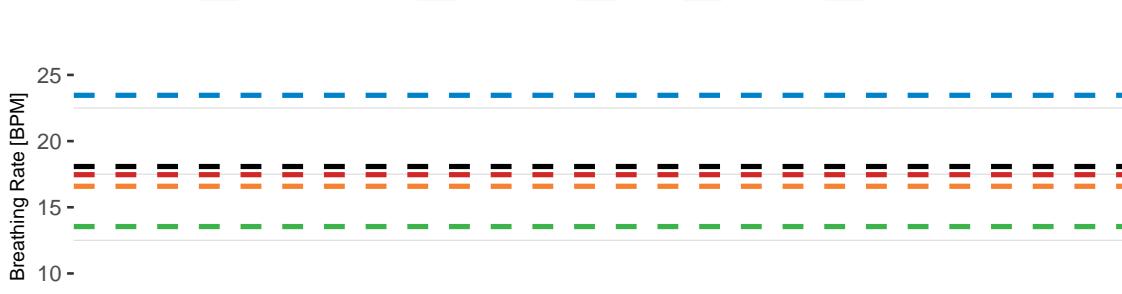
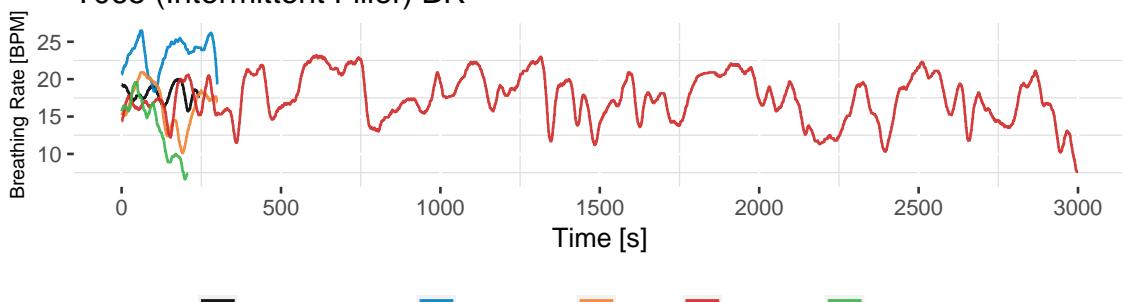




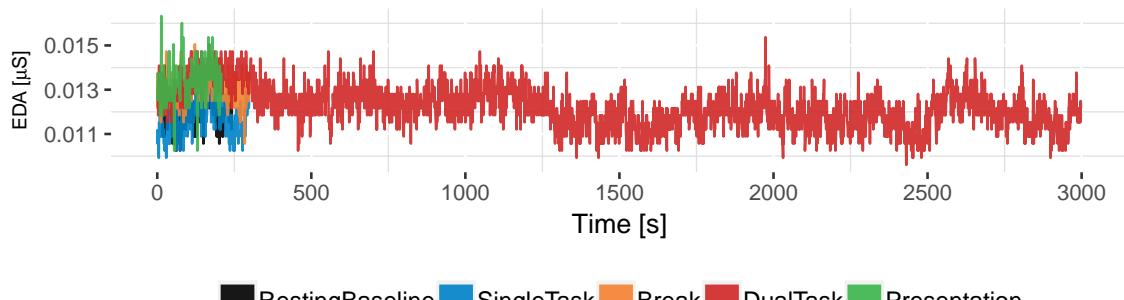
T065 (Intermittent Filler) HR



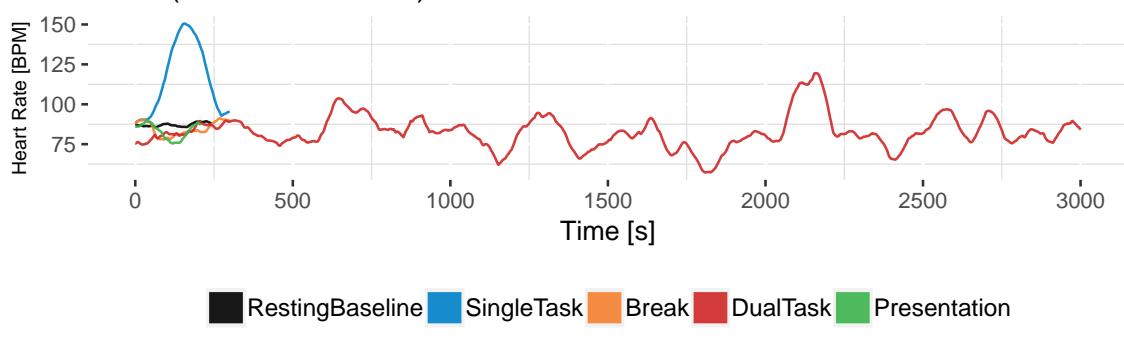
T065 (Intermittent Filler) BR

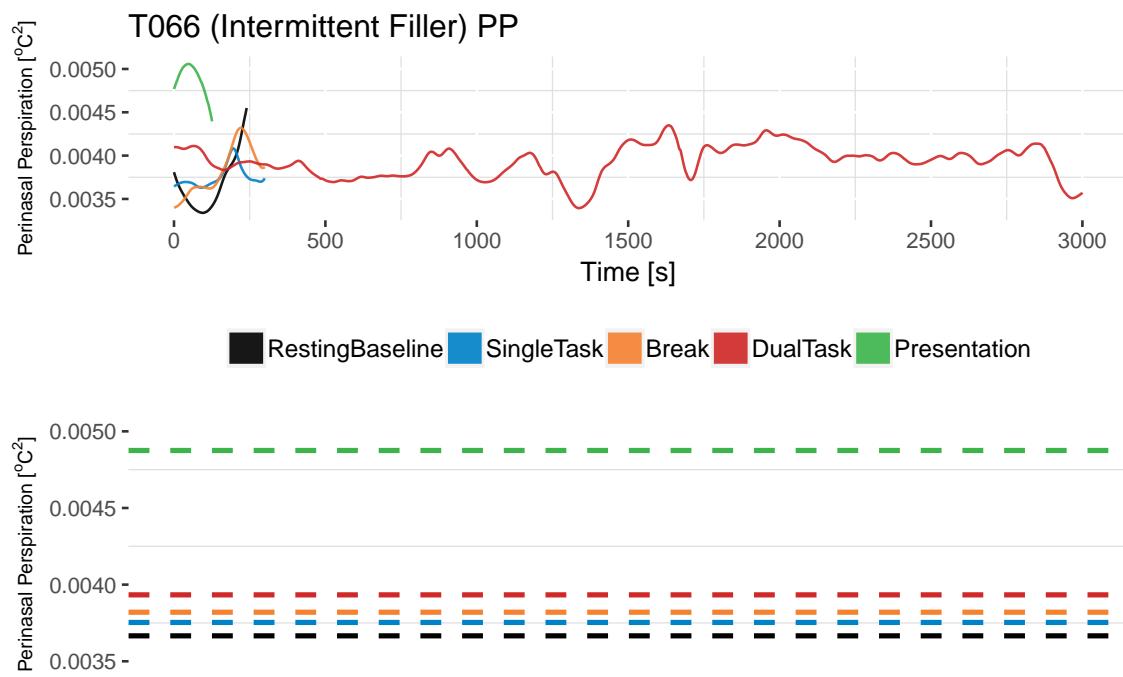


T065 (Intermittent Filler) N.EDA

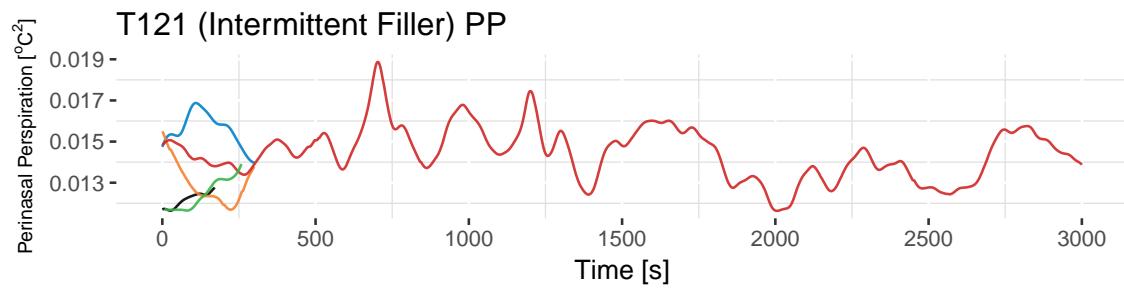


T065 (Intermittent Filler) N.HR





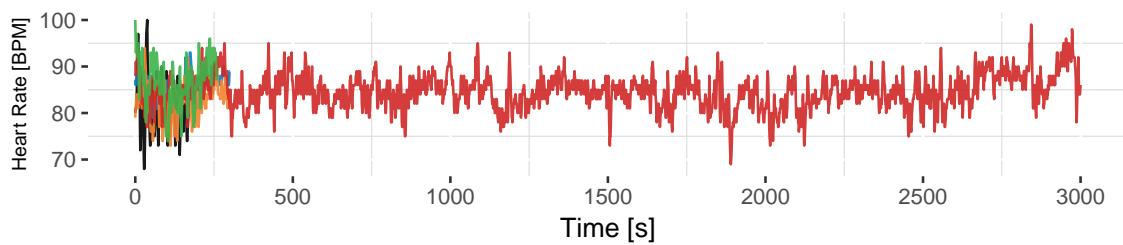
```
##  
##  
## Subject T066 does not have HR signals.  
##  
##  
##  
##  
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##  
##  
## Subject T066 does not have BR signals.  
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##  
##  
##
```



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



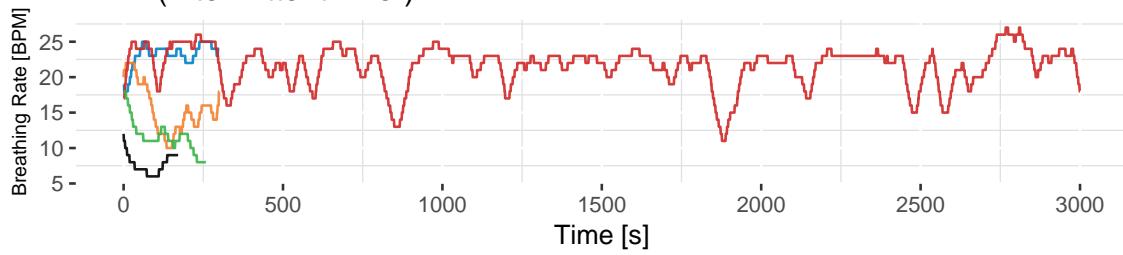
T121 (Intermittent Filler) HR



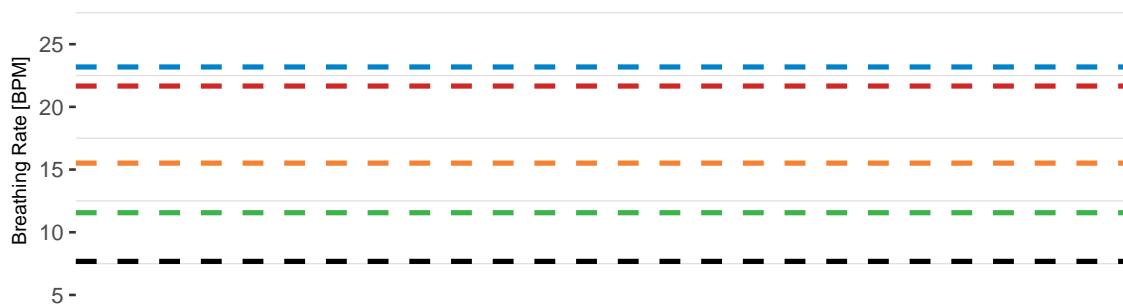
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

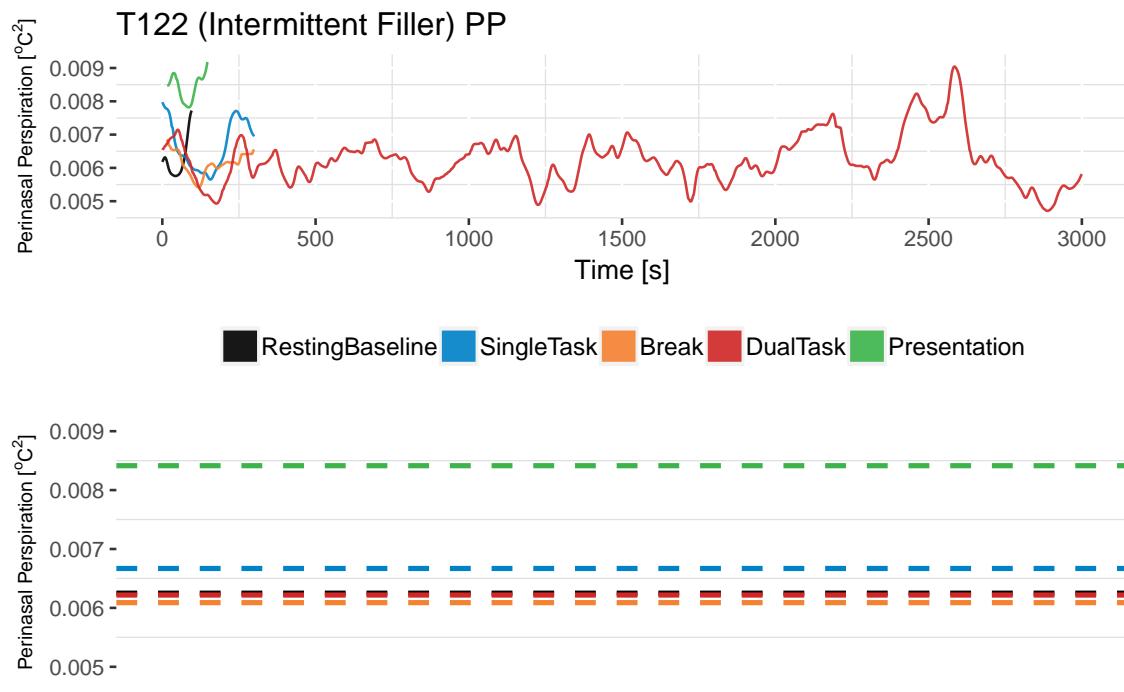


T121 (Intermittent Filler) BR

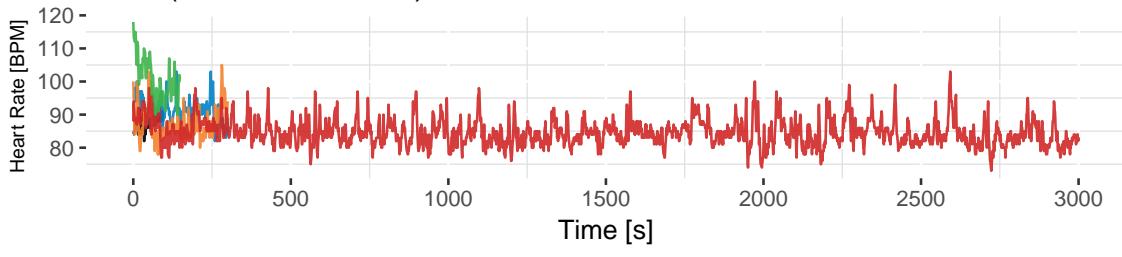


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation





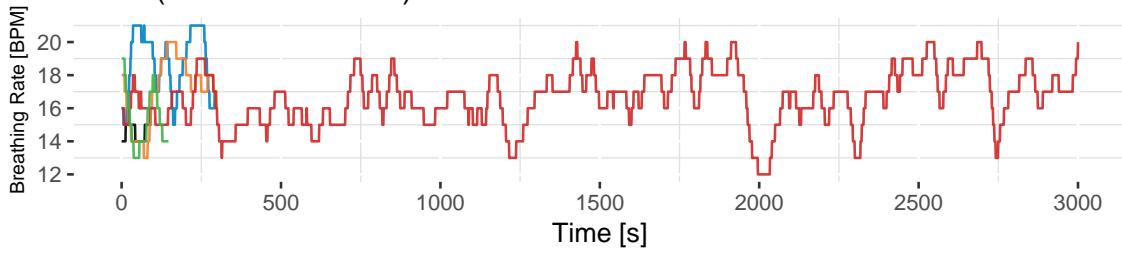
T122 (Intermittent Filler) HR



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

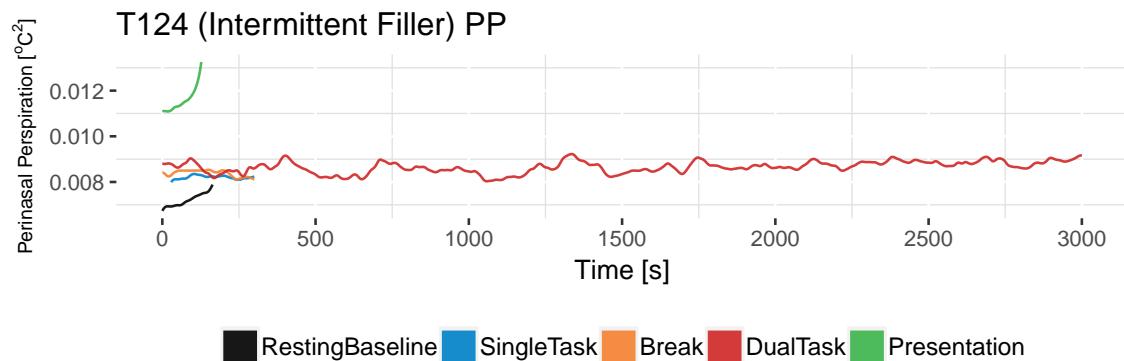


T122 (Intermittent Filler) BR

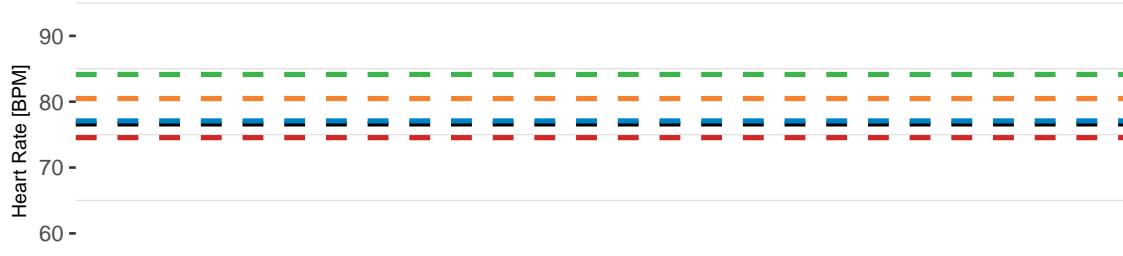
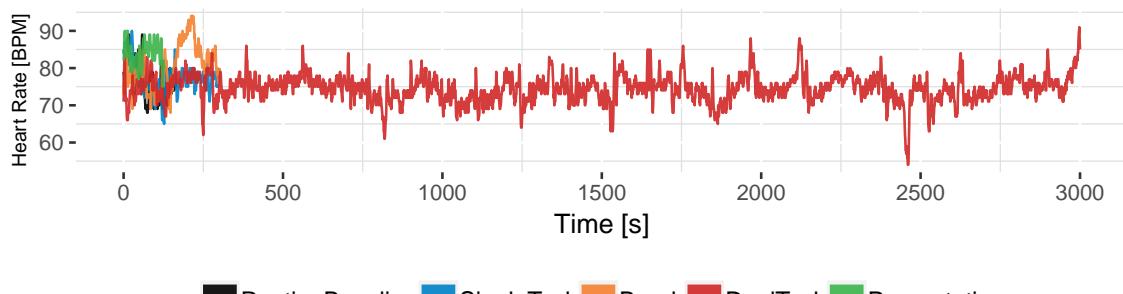


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

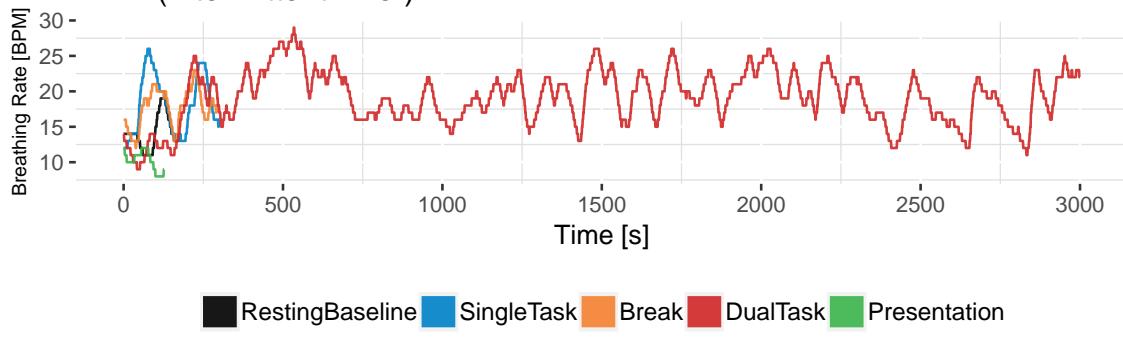




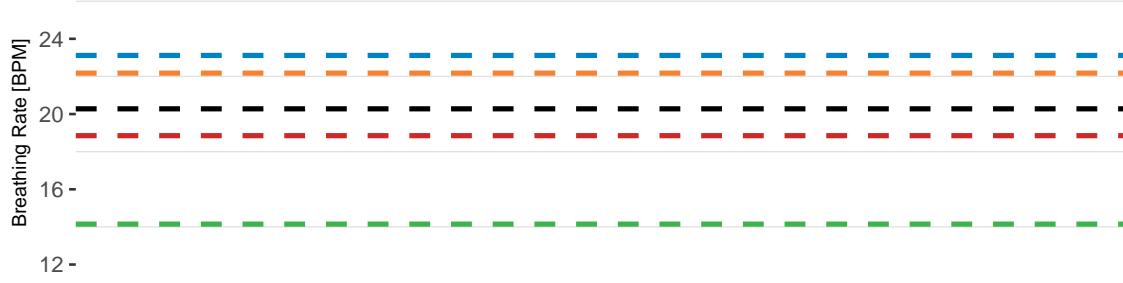
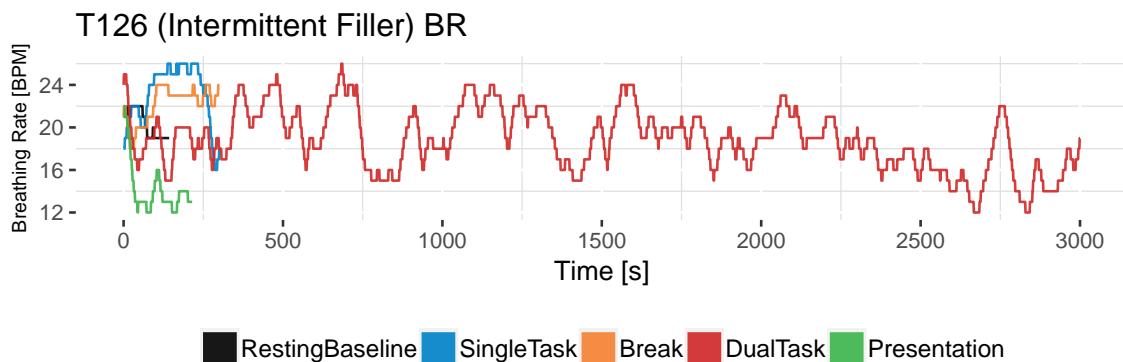
T124 (Intermittent Filler) HR

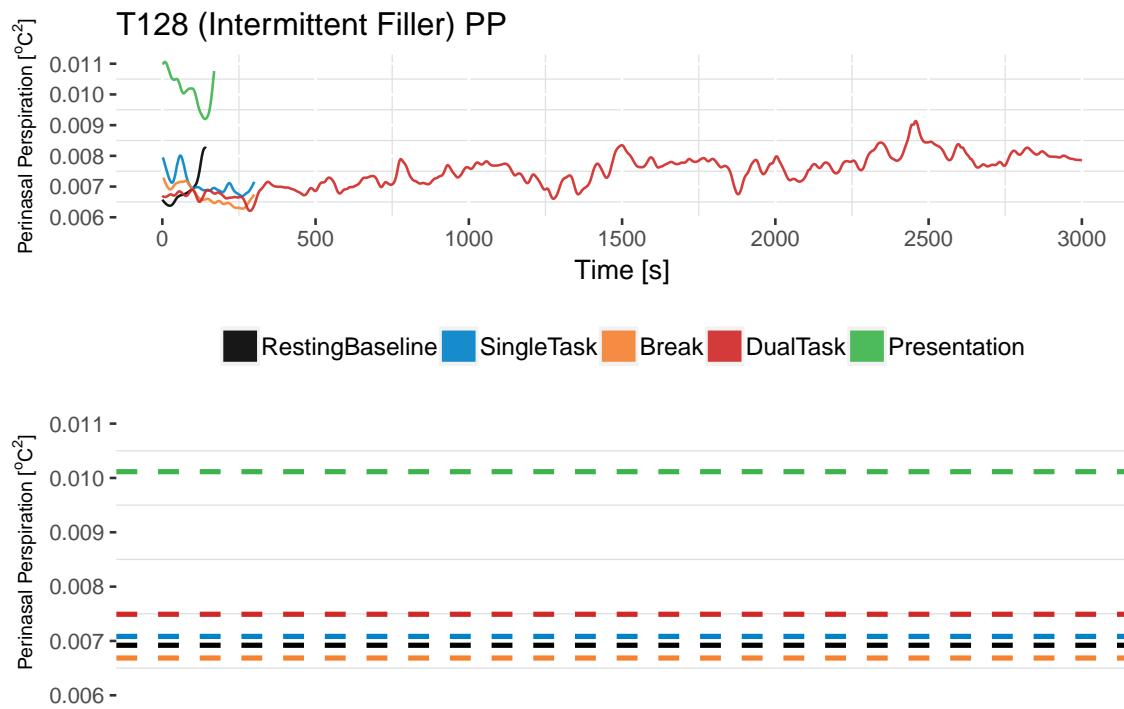


T124 (Intermittent Filler) BR

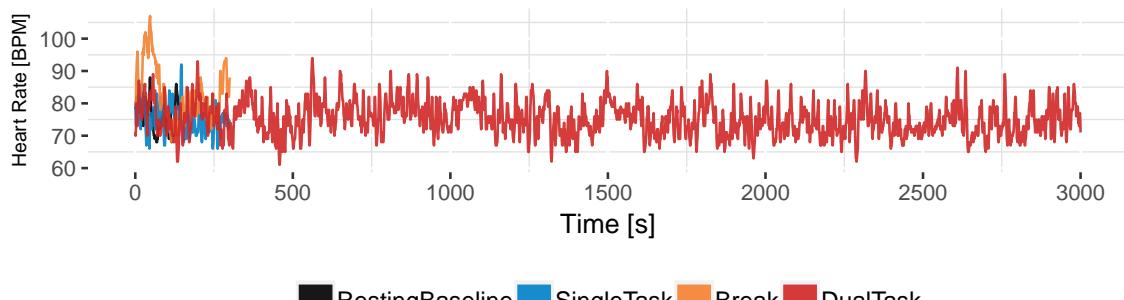


```
##  
##  
## Subject T126 does not have PP signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
## Subject T126 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##
```

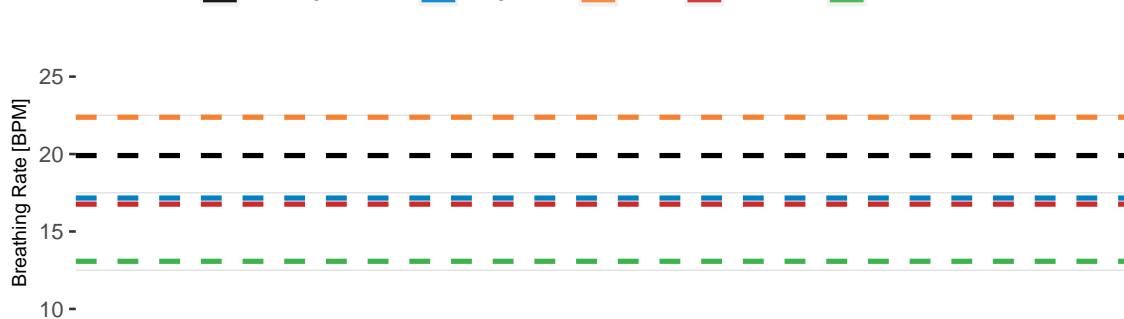
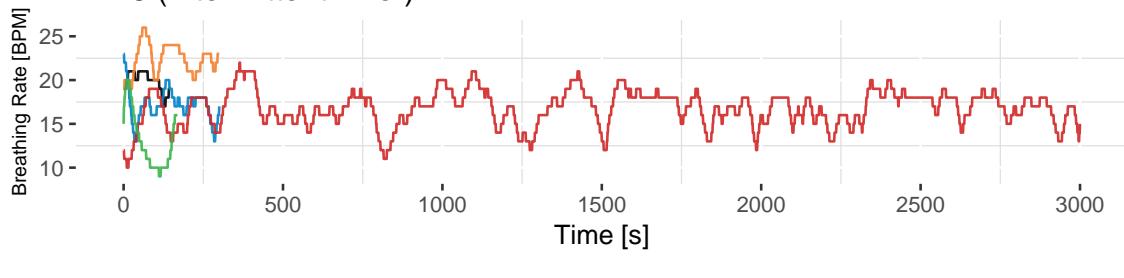


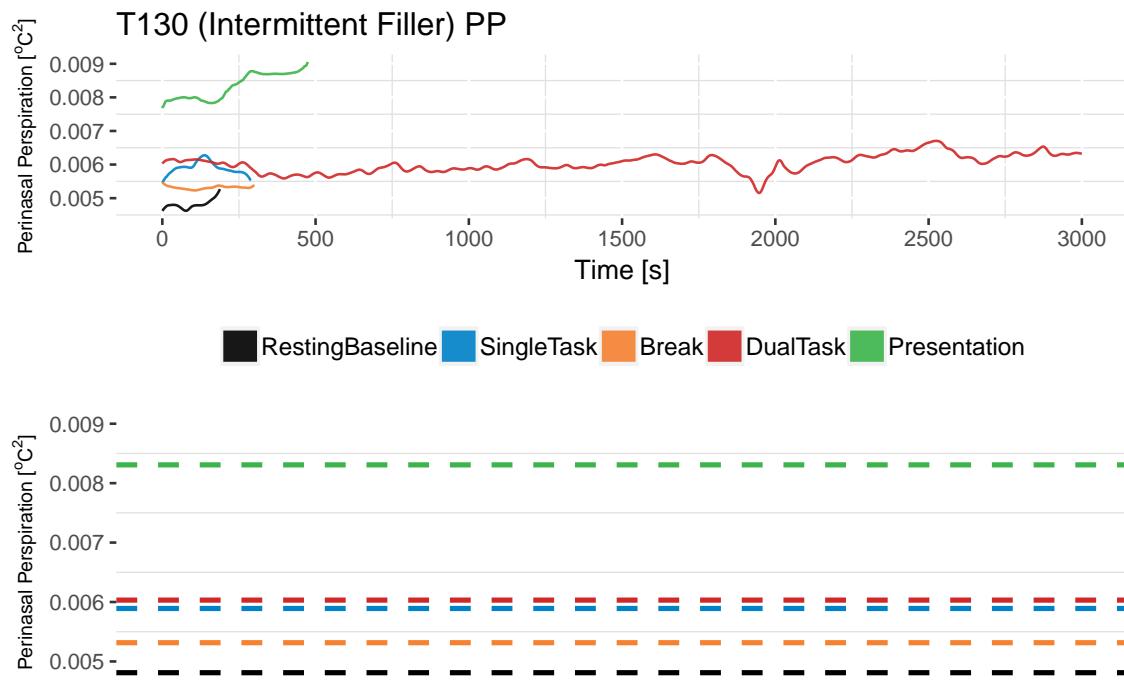


T128 (Intermittent Filler) HR



T128 (Intermittent Filler) BR





```

##  

##  

## Subject T130 does not have HR signals.  

##  

##  

##  

##  

##  

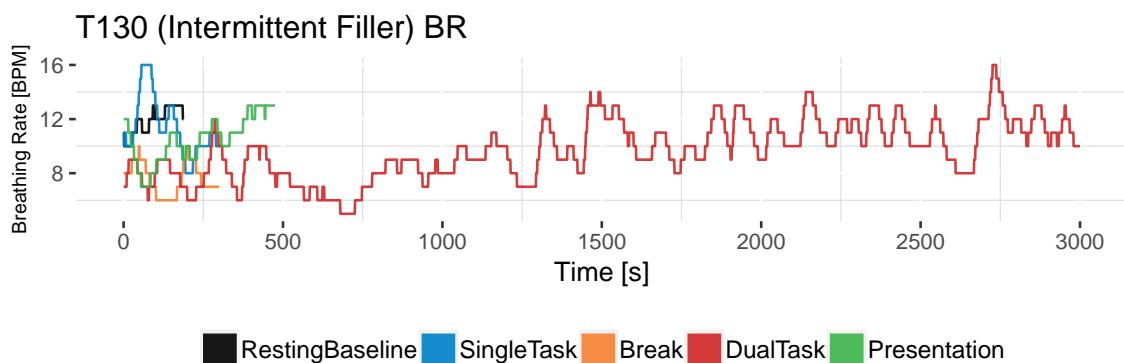
##  

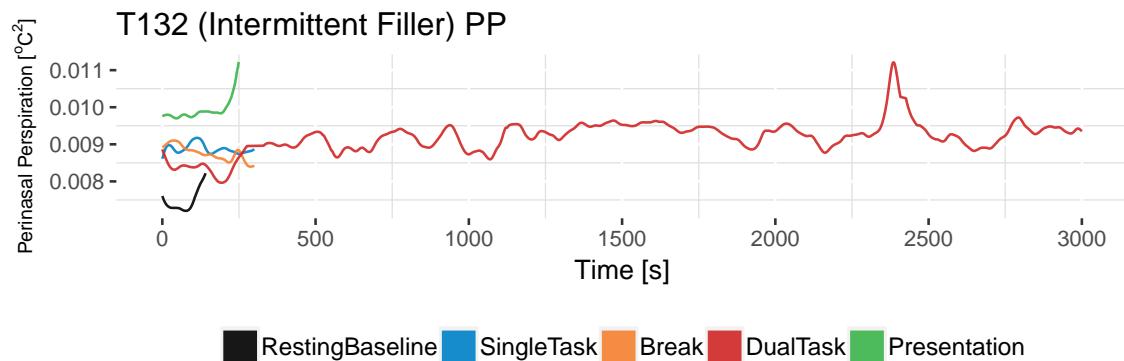
##  

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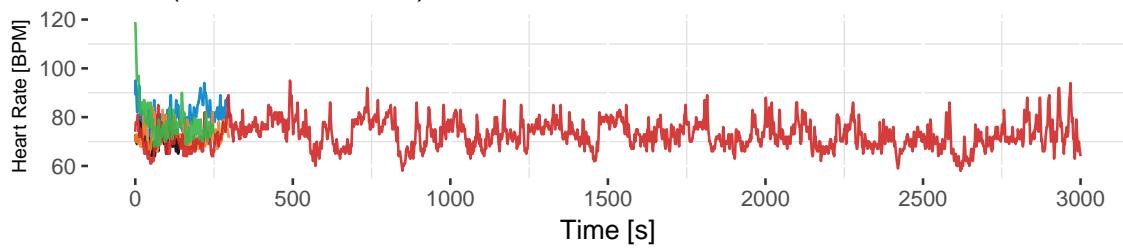
##  

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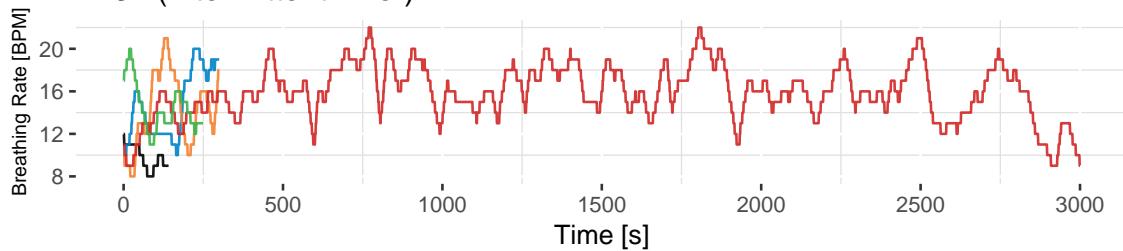
T132 (Intermittent Filler) HR



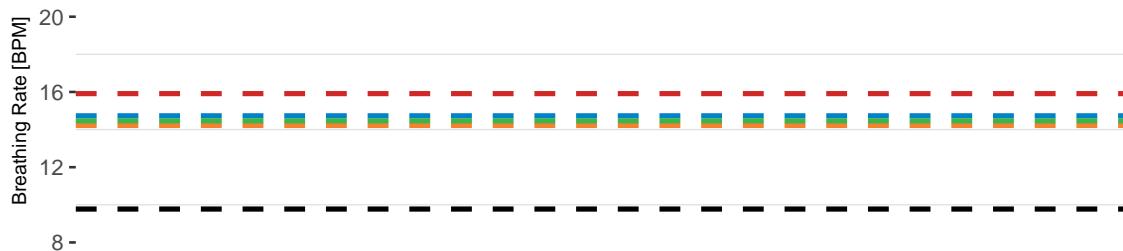
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



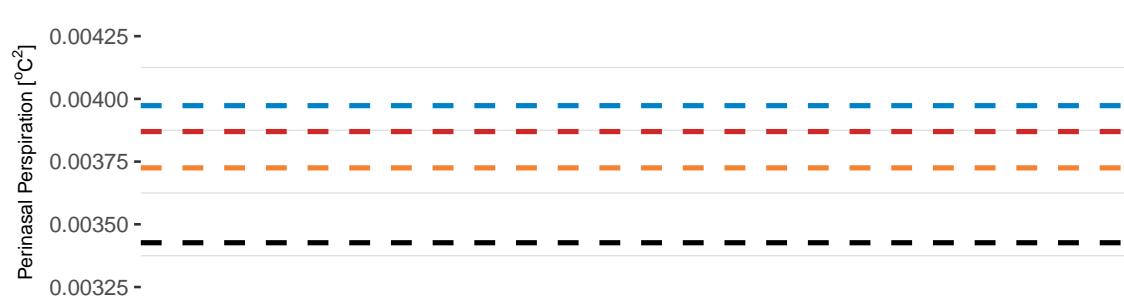
T132 (Intermittent Filler) BR



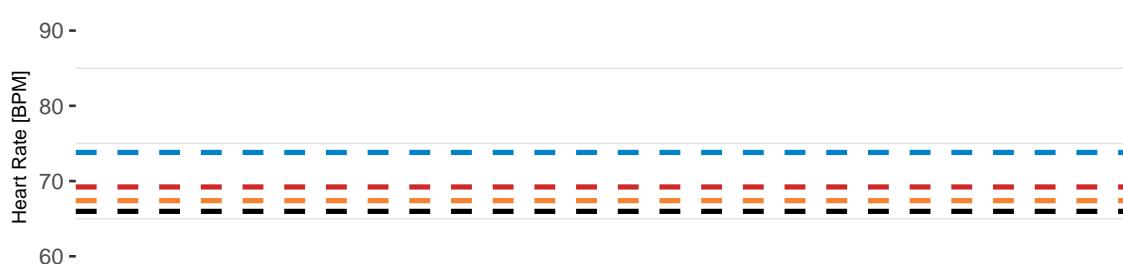
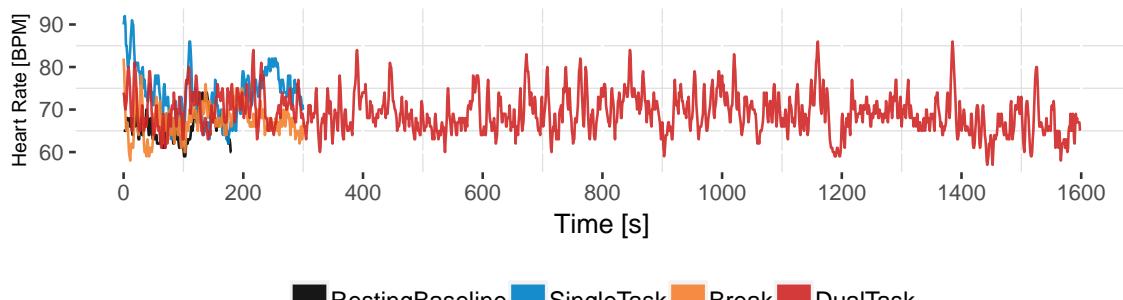
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



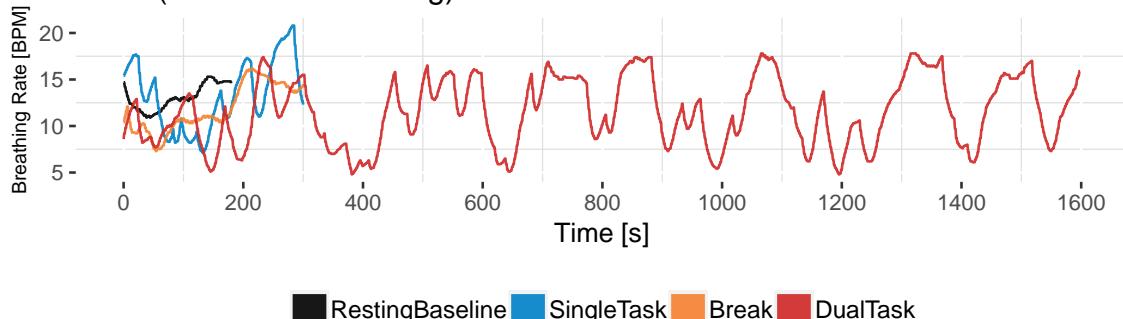
Intermittent-Nothing (IN) Subjects



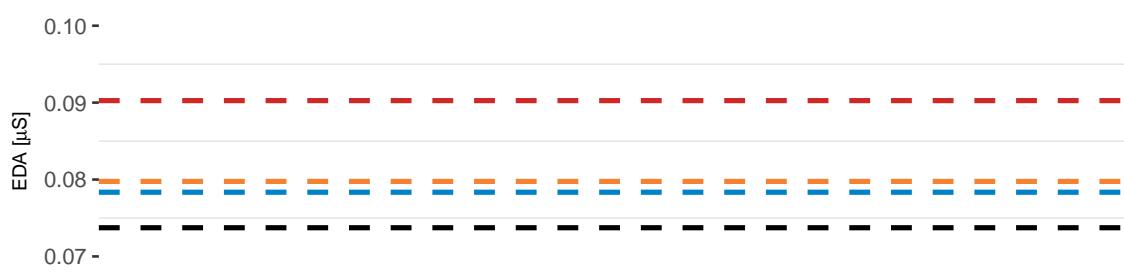
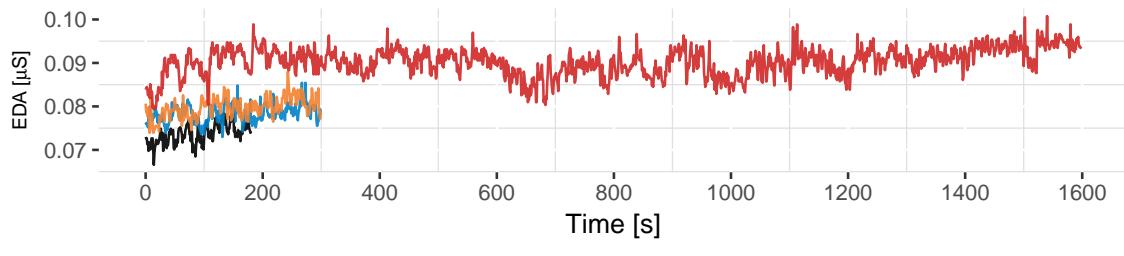
T031 (Intermittent Nothing) HR



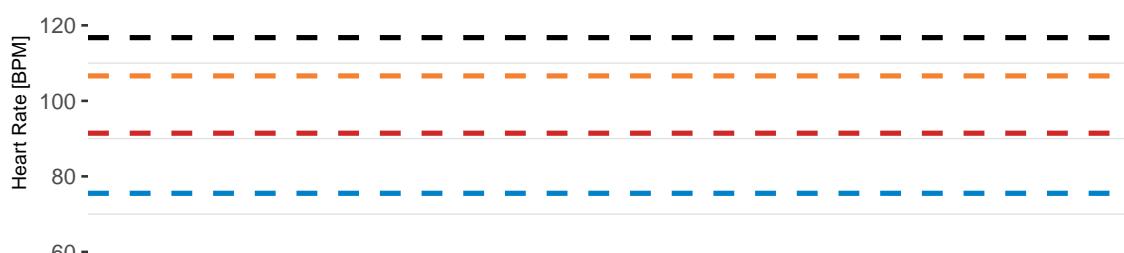
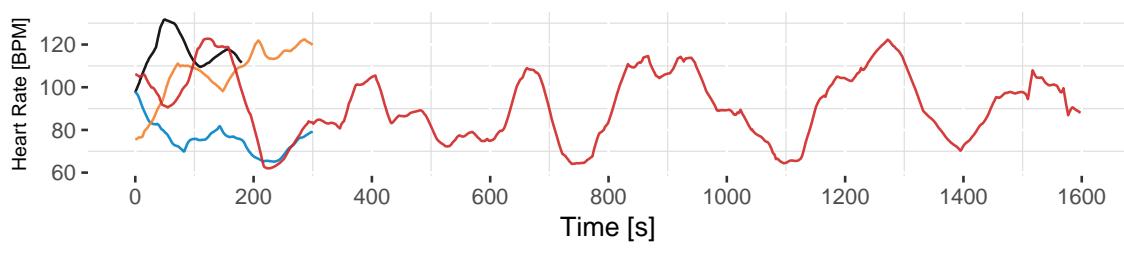
T031 (Intermittent Nothing) BR

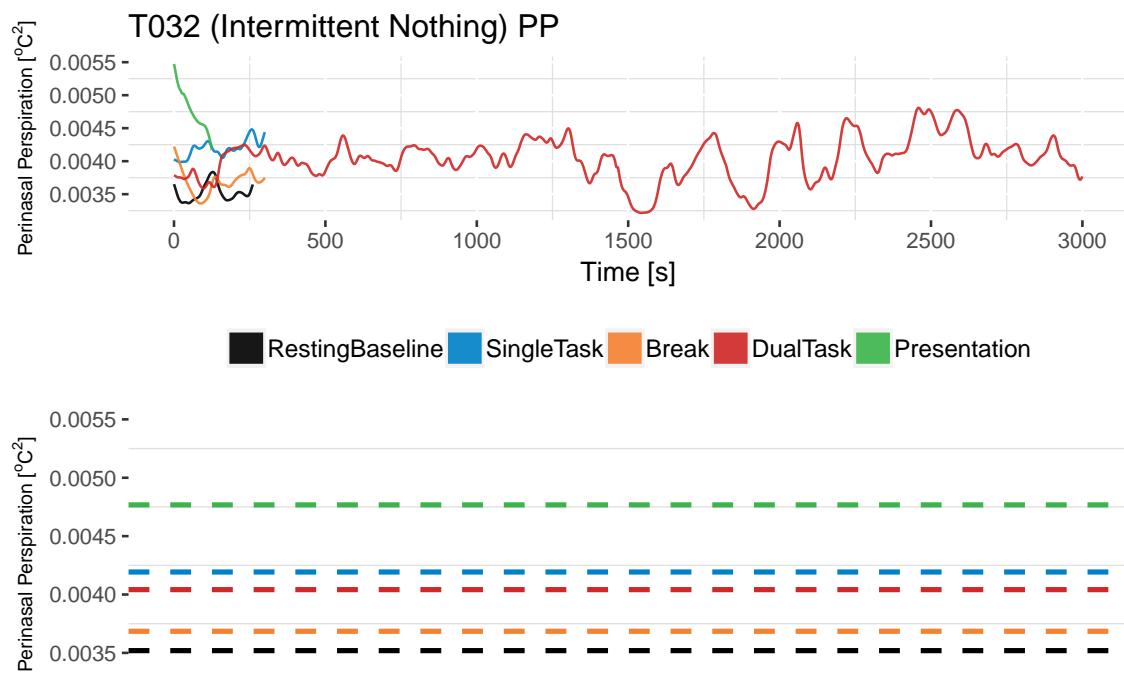


T031 (Intermittent Nothing) N.EDA

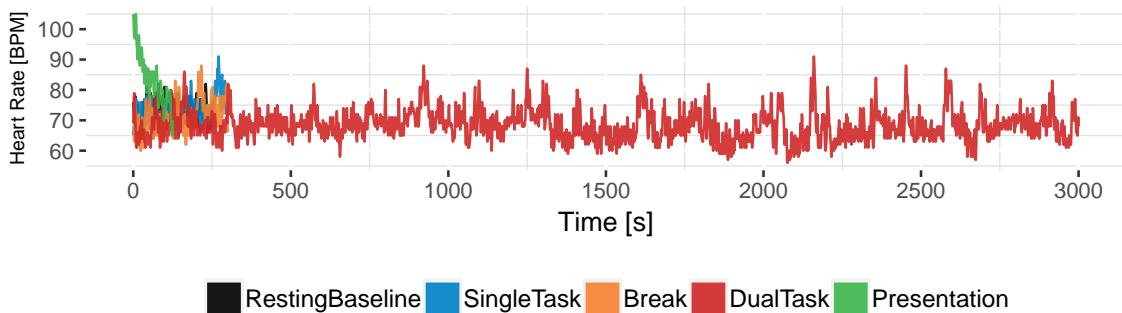


T031 (Intermittent Nothing) N.HR

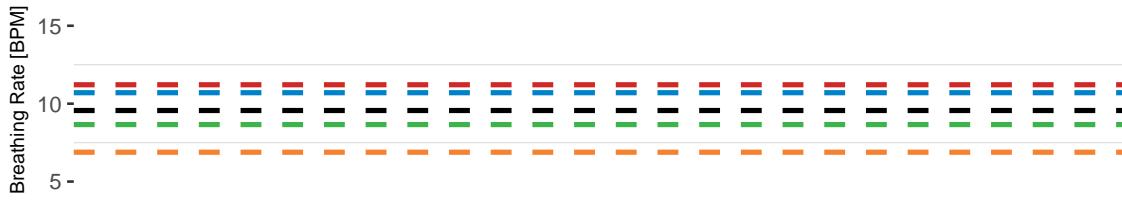
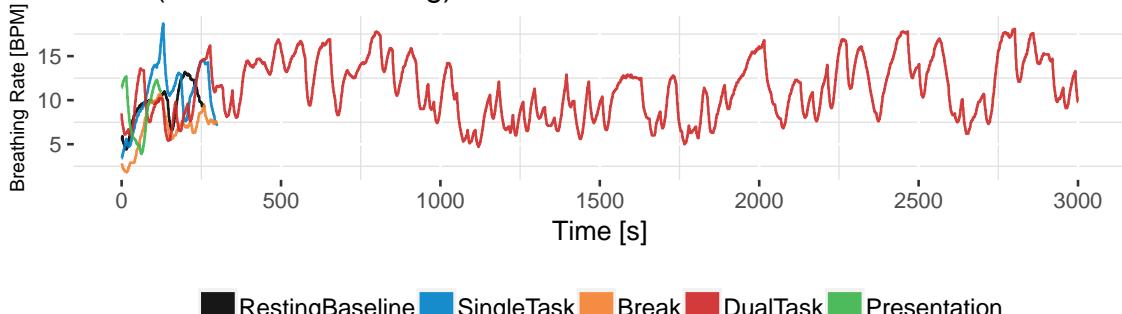




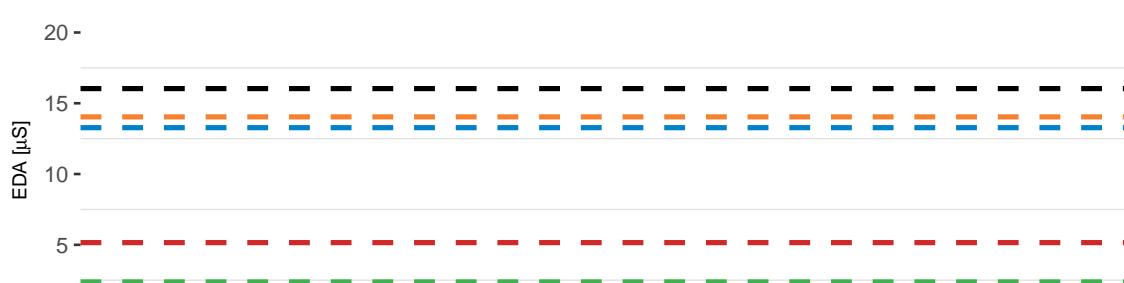
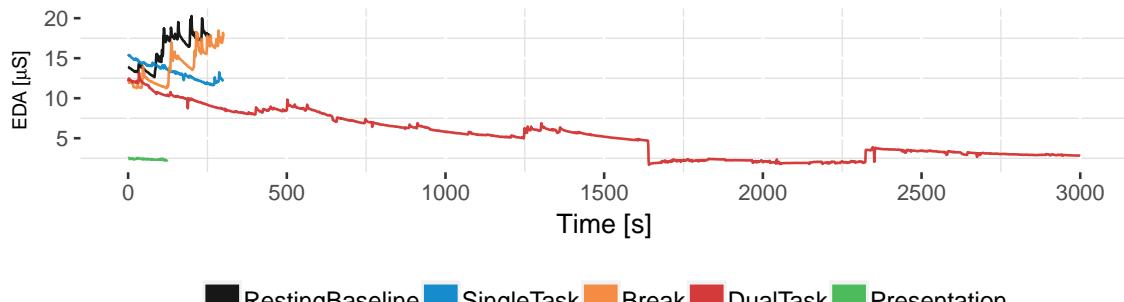
T032 (Intermittent Nothing) HR



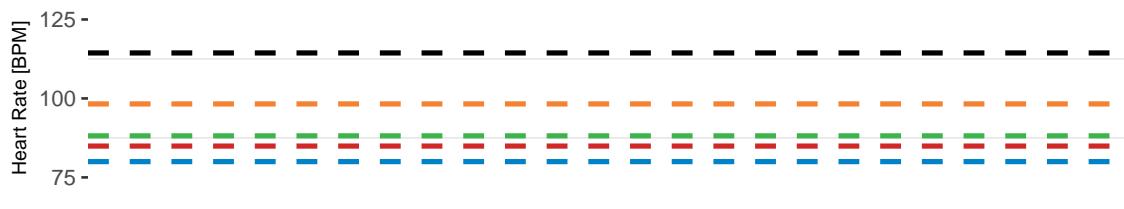
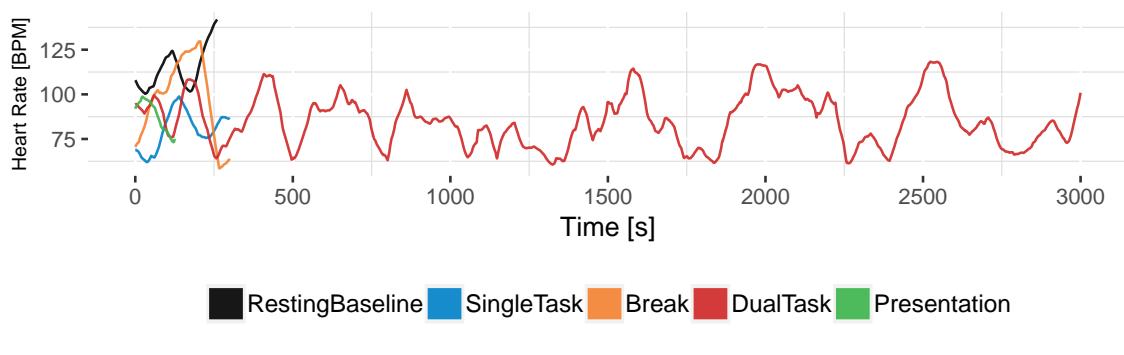
T032 (Intermittent Nothing) BR

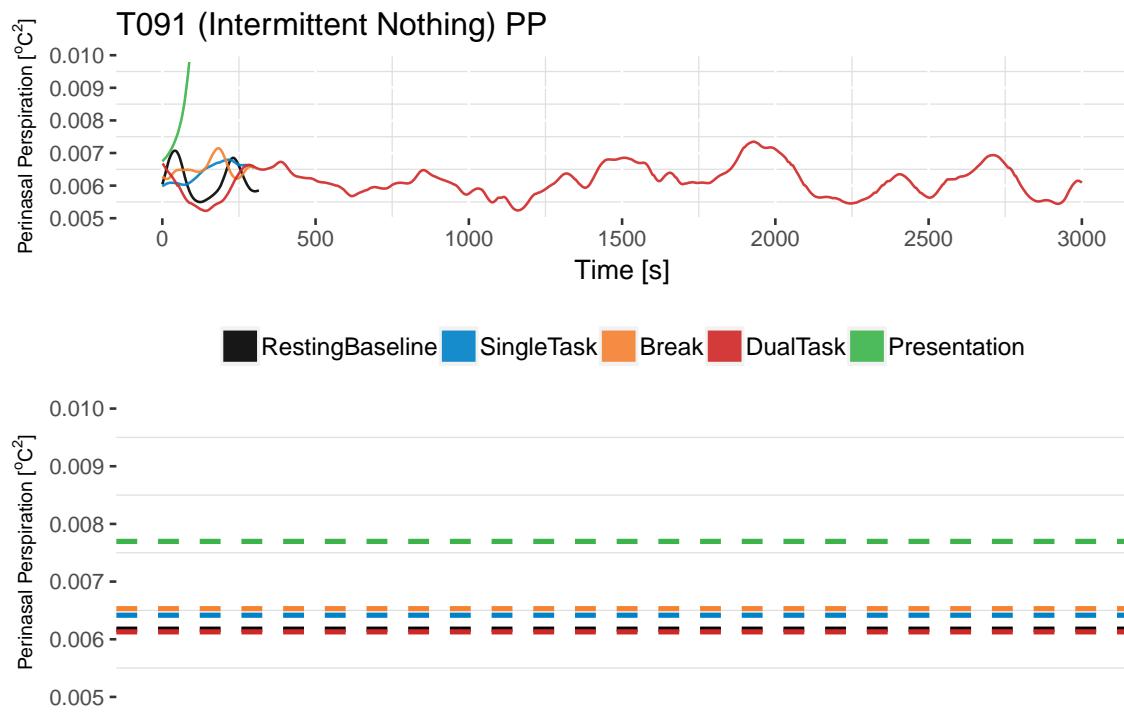


T032 (Intermittent Nothing) N.EDA

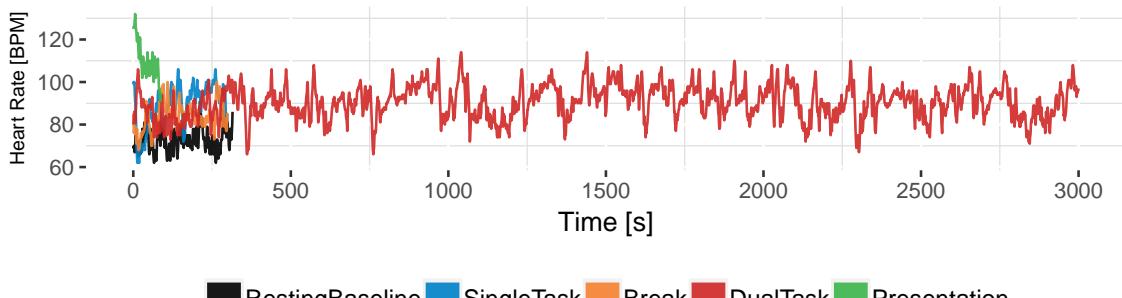


T032 (Intermittent Nothing) N.HR

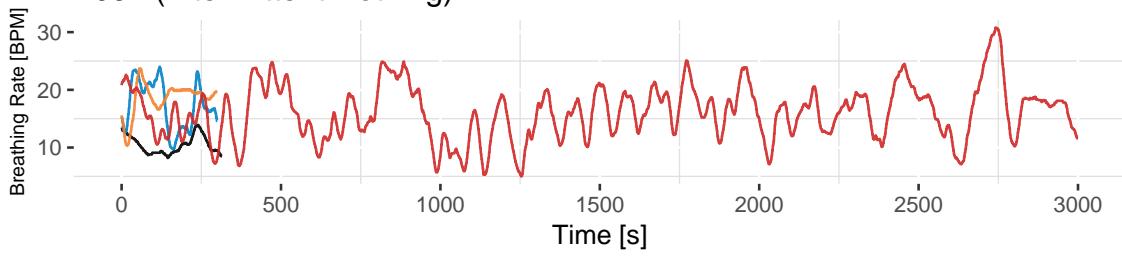




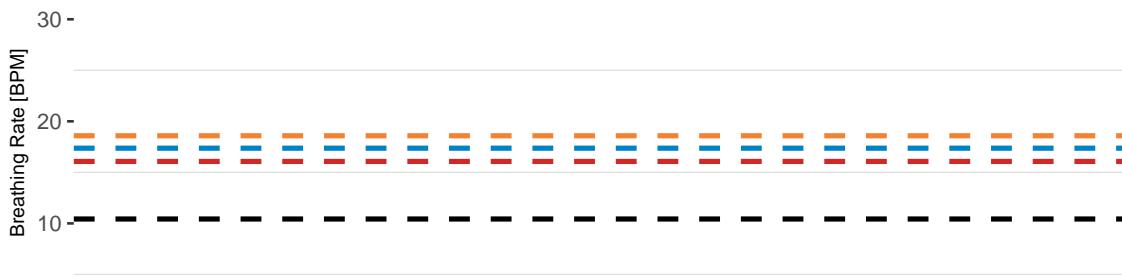
T091 (Intermittent Nothing) HR



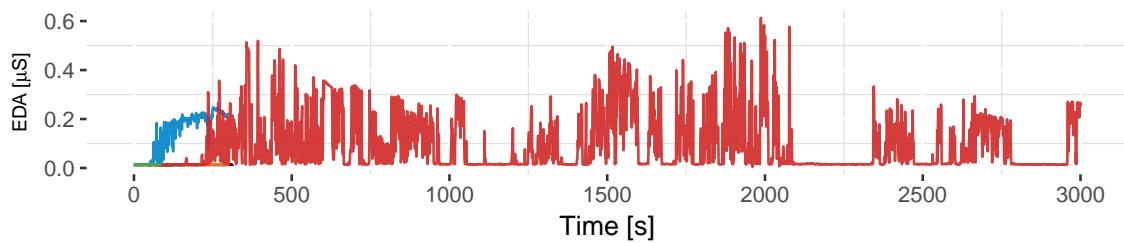
T091 (Intermittent Nothing) BR



RestingBaseline SingleTask Break DualTask



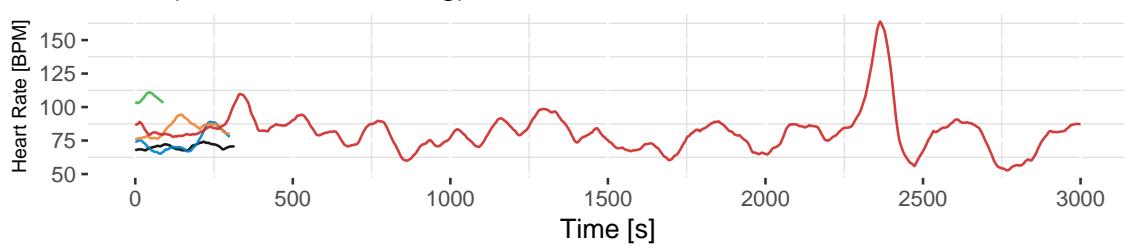
T091 (Intermittent Nothing) N.EDA



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

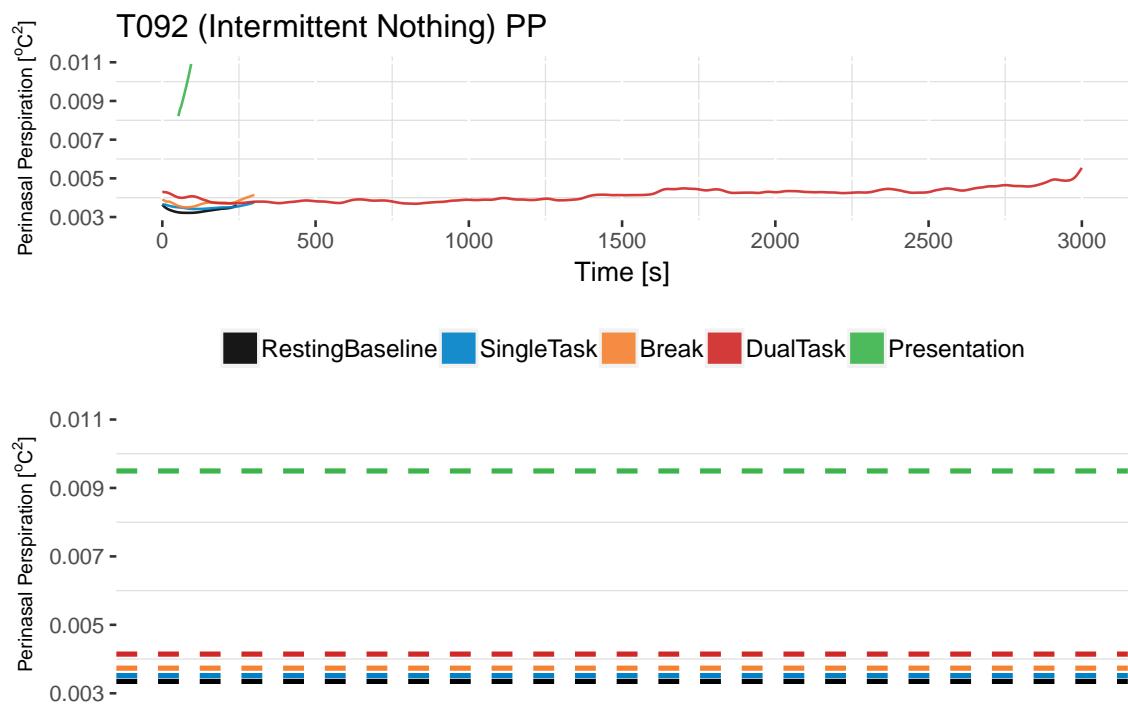


T091 (Intermittent Nothing) N.HR



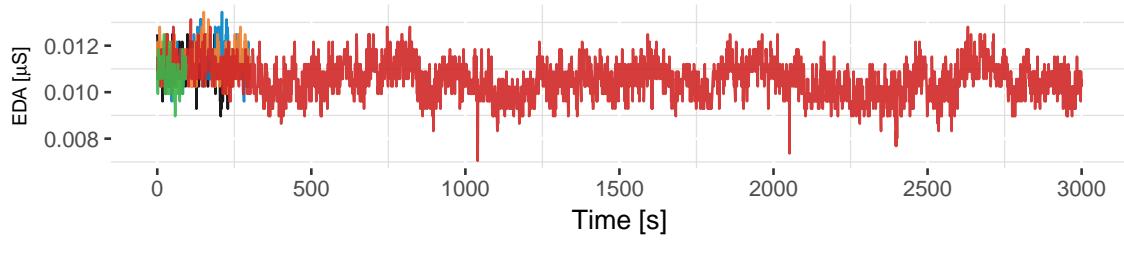
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



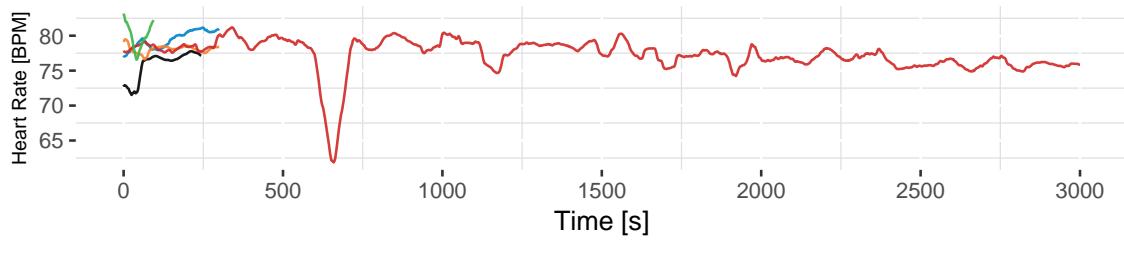


```
##  
##  
## Subject T092 does not have HR signals.  
##  
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##  
## Subject T092 does not have BR signals.  
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T092 (Intermittent Nothing) N.EDA

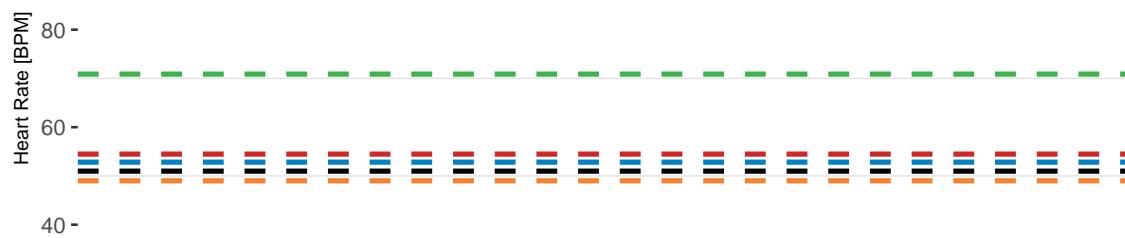
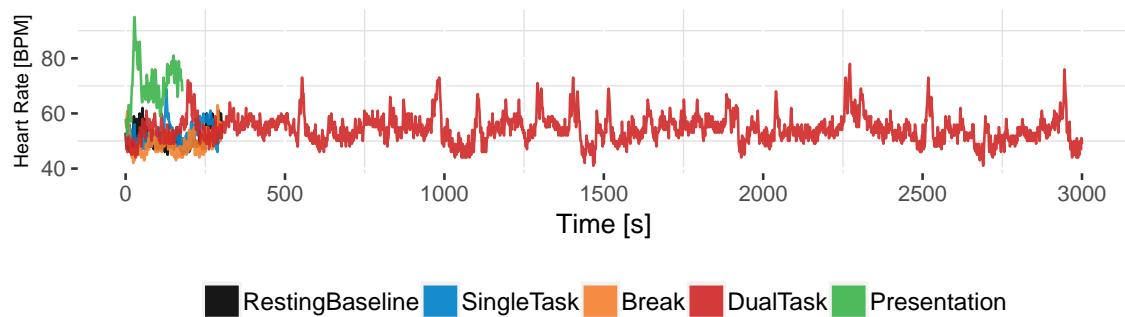


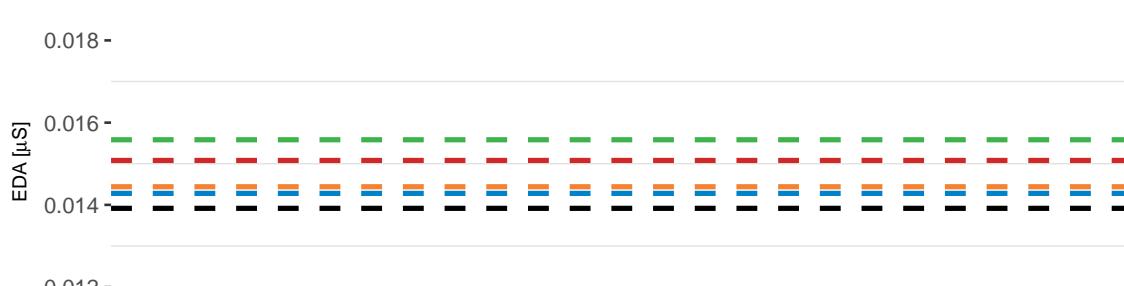
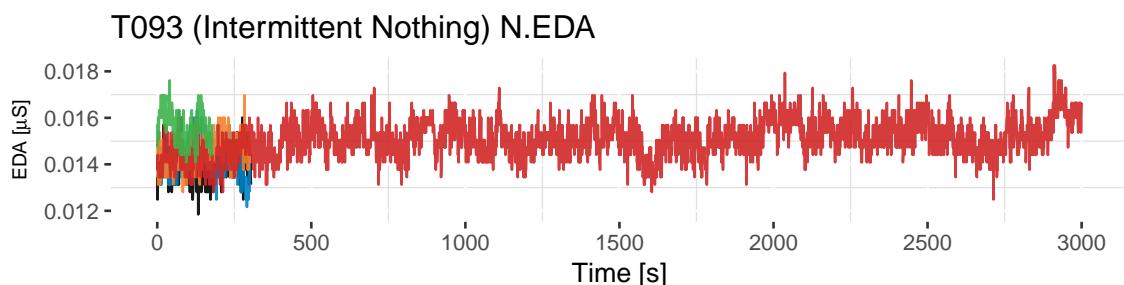
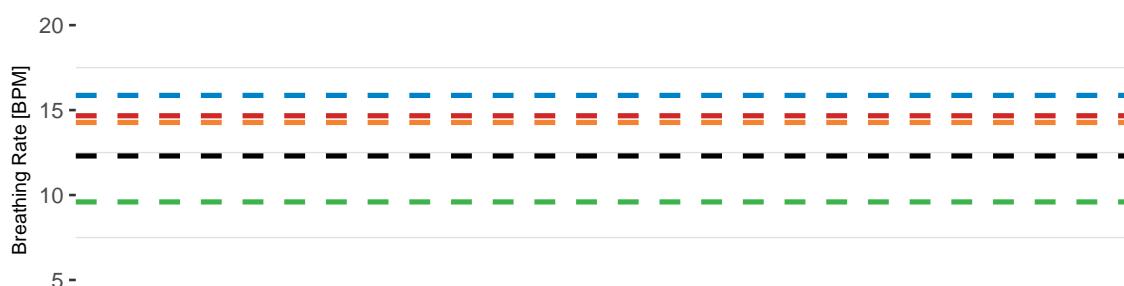
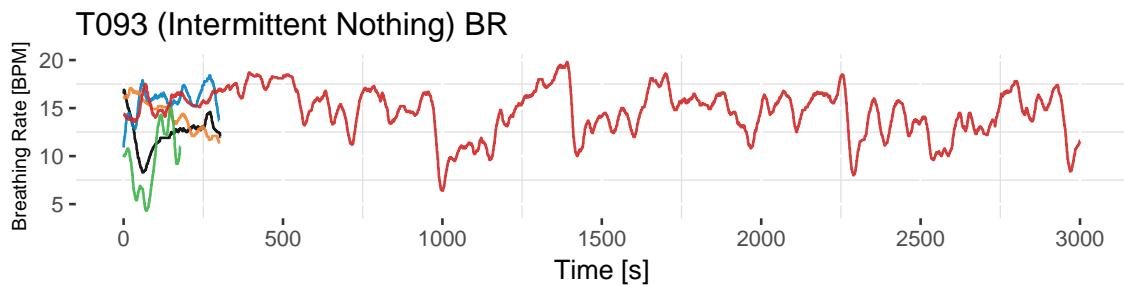
T092 (Intermittent Nothing) N.HR



```
##  
##  
## Subject T093 does not have PP signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```

T093 (Intermittent Nothing) HR





T093 (Intermittent Nothing) N.HR



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



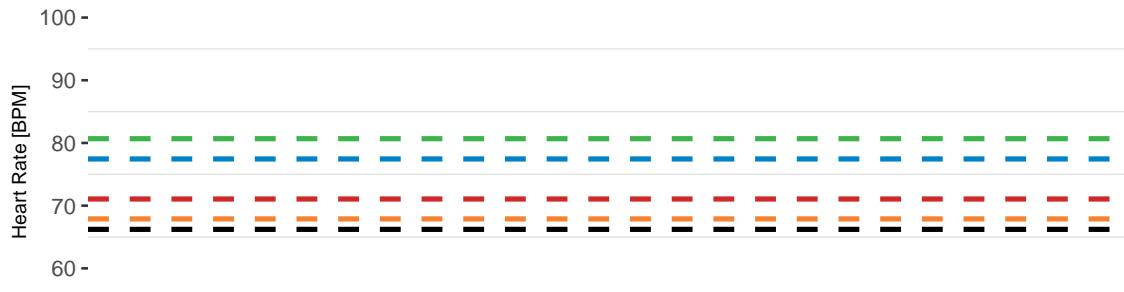
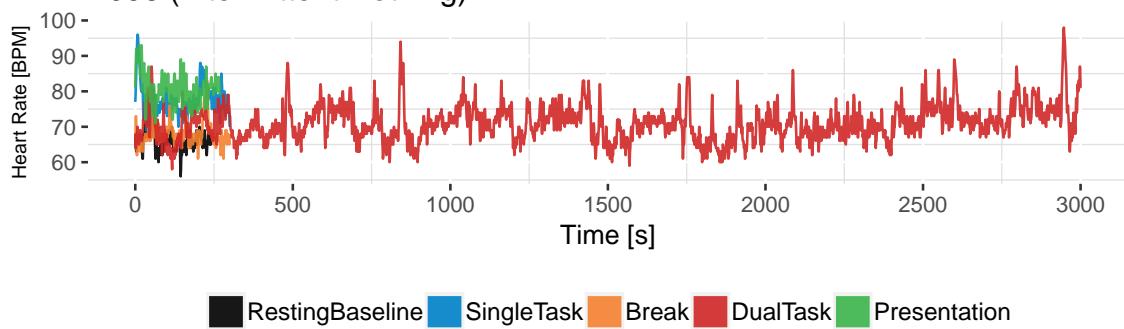
T096 (Intermittent Nothing) PP



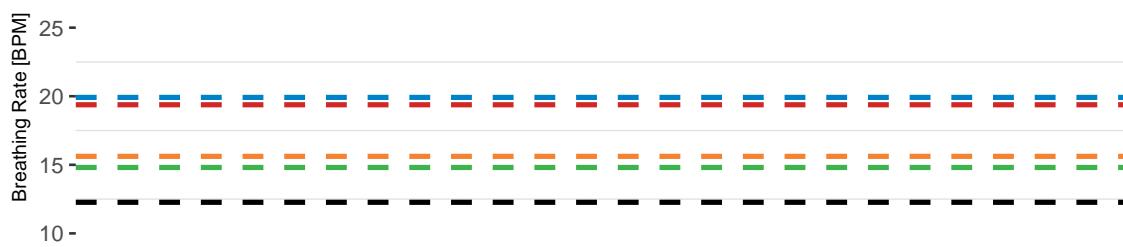
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



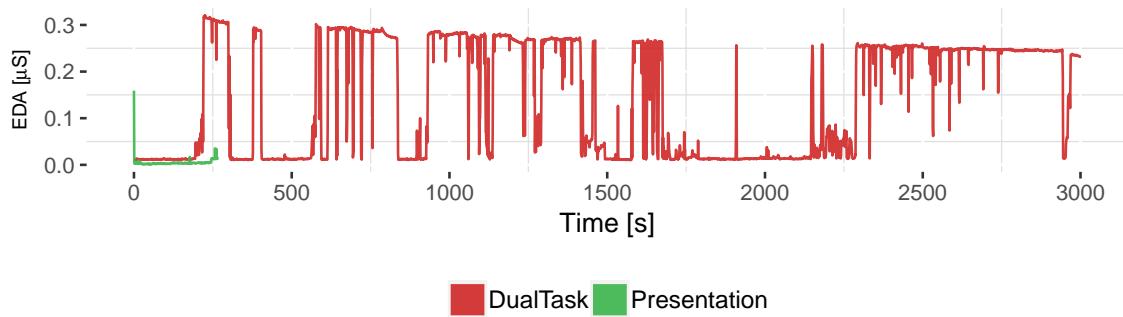
T096 (Intermittent Nothing) HR



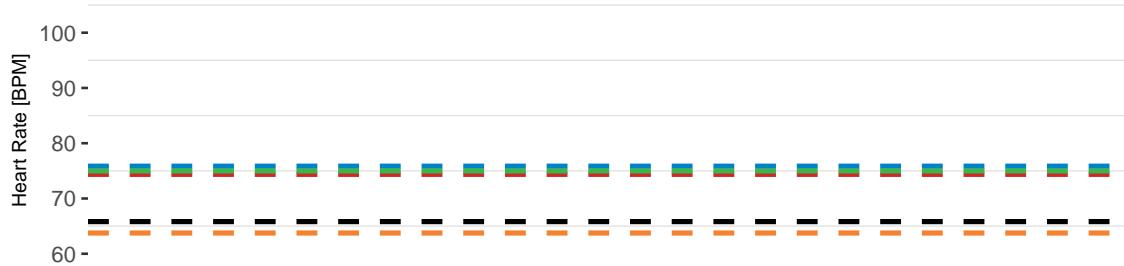
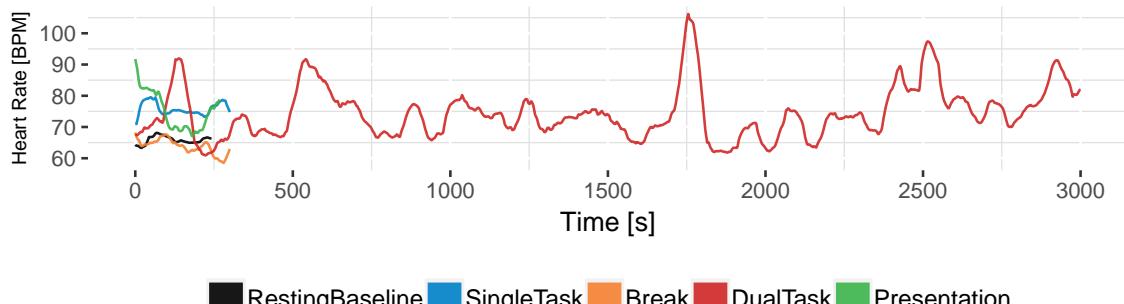
T096 (Intermittent Nothing) BR



T096 (Intermittent Nothing) N.EDA

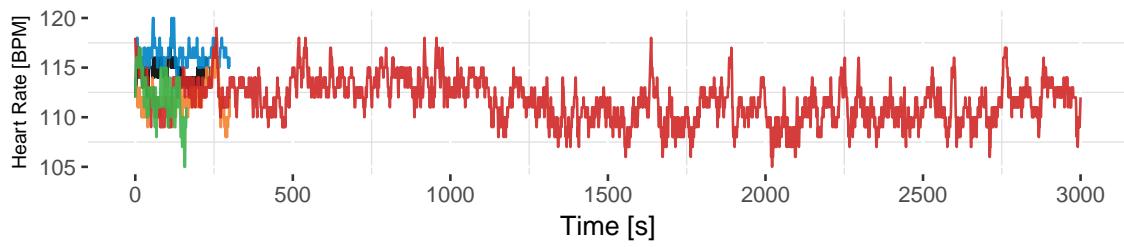


T096 (Intermittent Nothing) N.HR



```
##  
##  
## Subject T097 does not have PP signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##
```

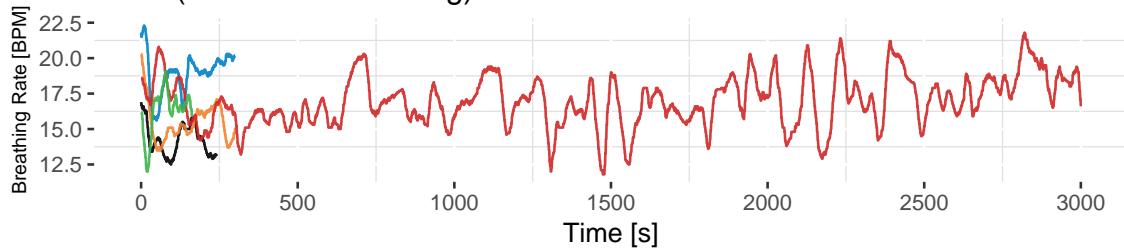
T097 (Intermittent Nothing) HR



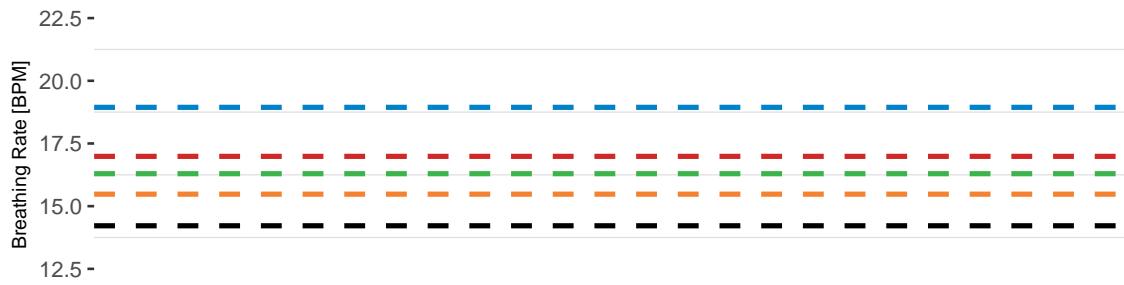
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



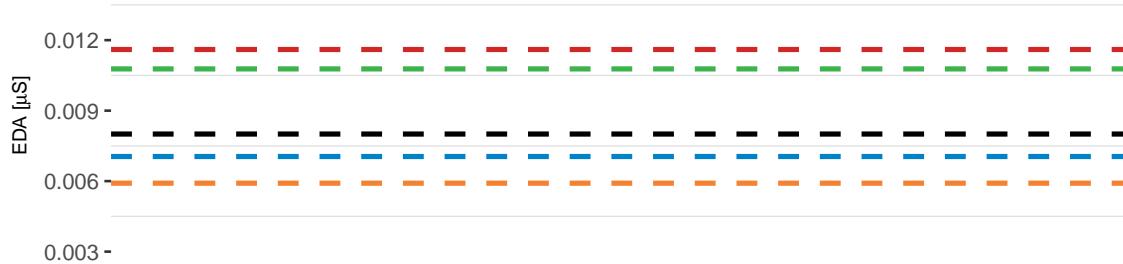
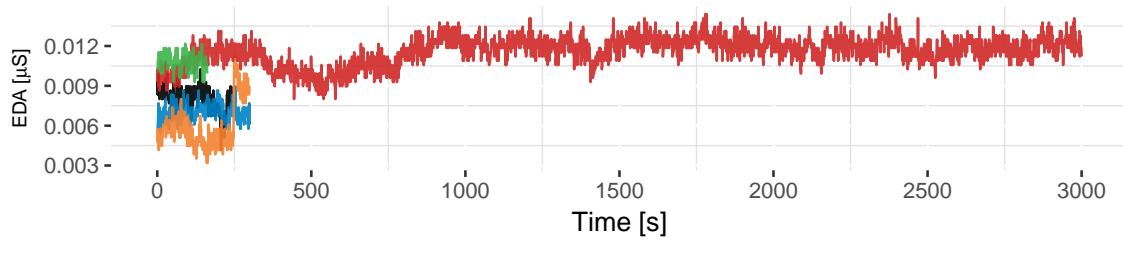
T097 (Intermittent Nothing) BR



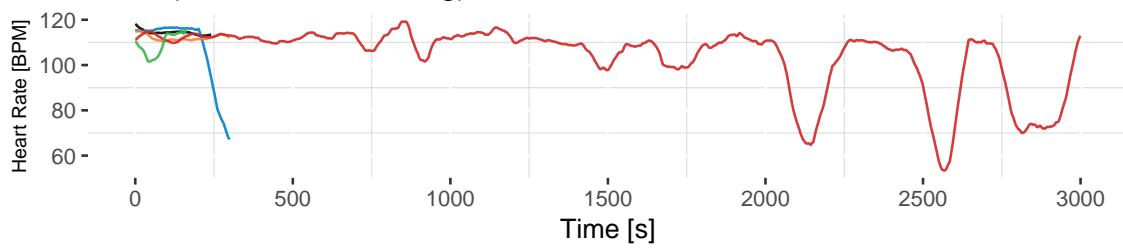
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

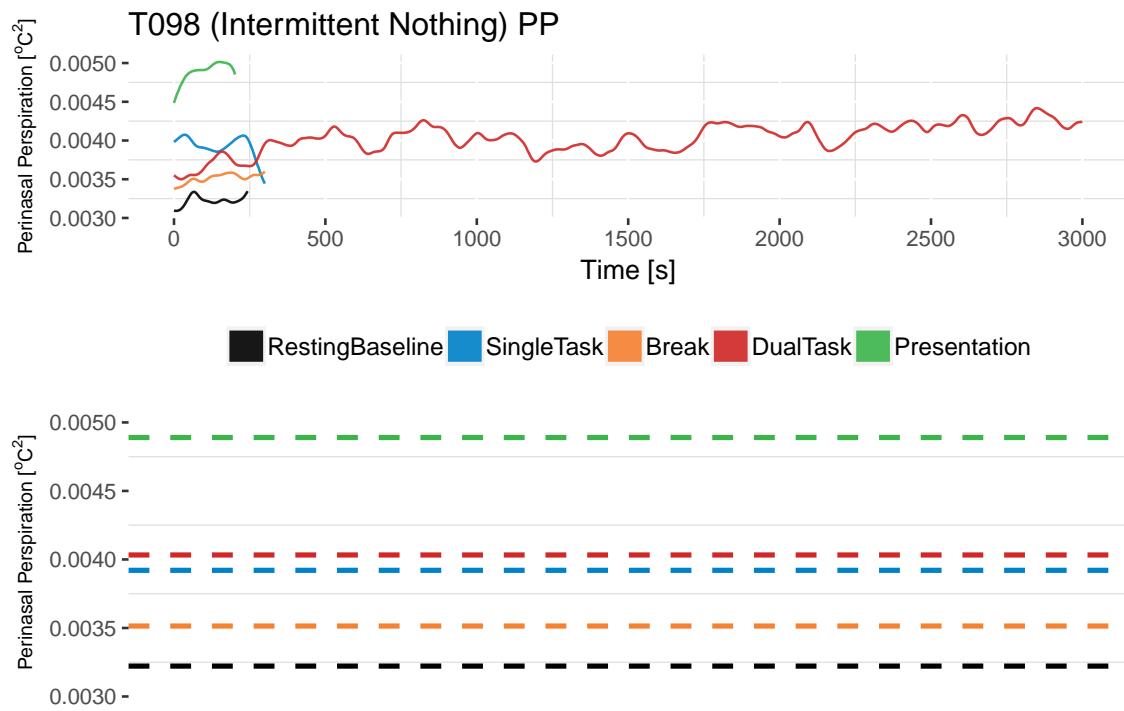


T097 (Intermittent Nothing) N.EDA

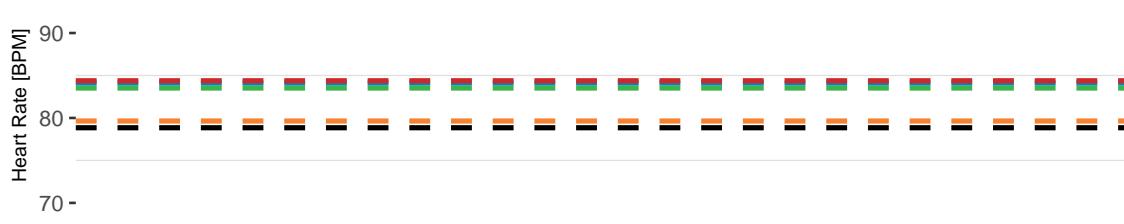
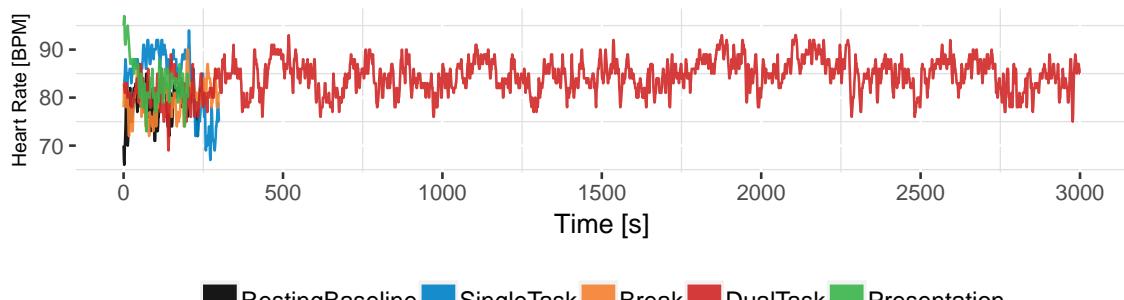


T097 (Intermittent Nothing) N.HR

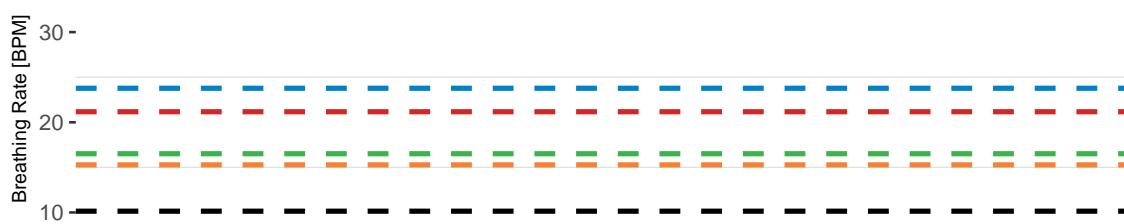
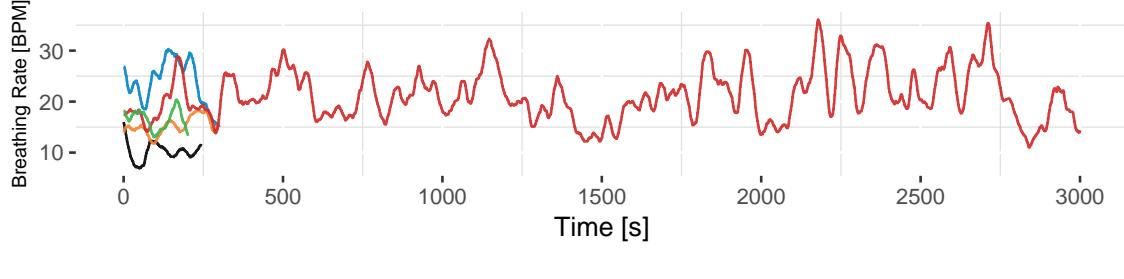


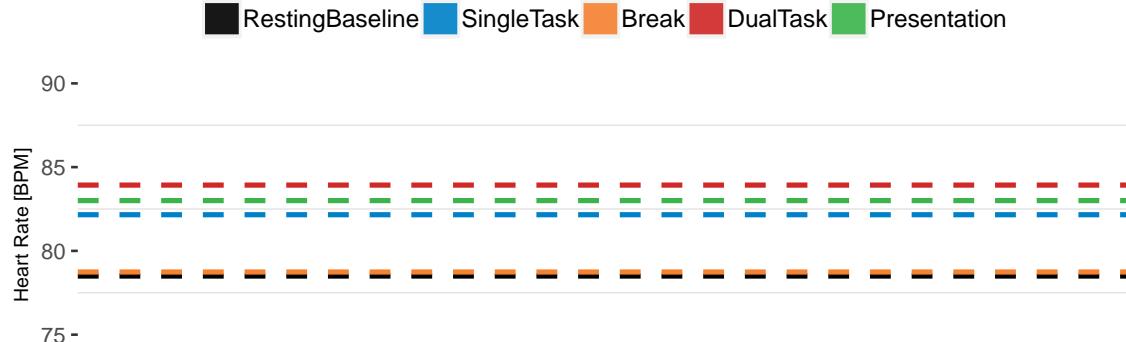
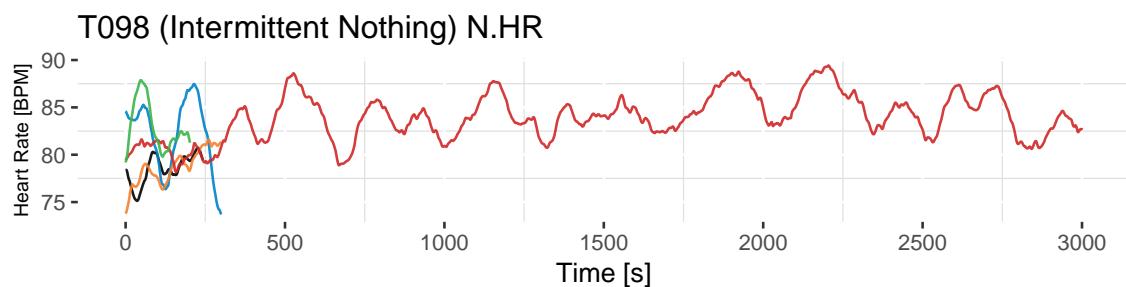
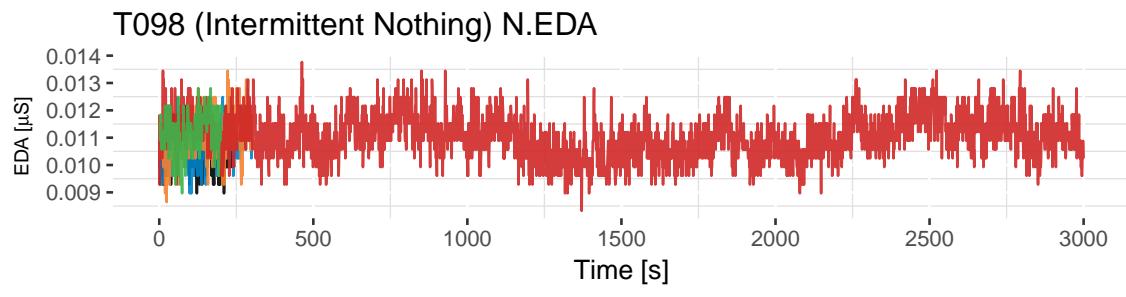


T098 (Intermittent Nothing) HR



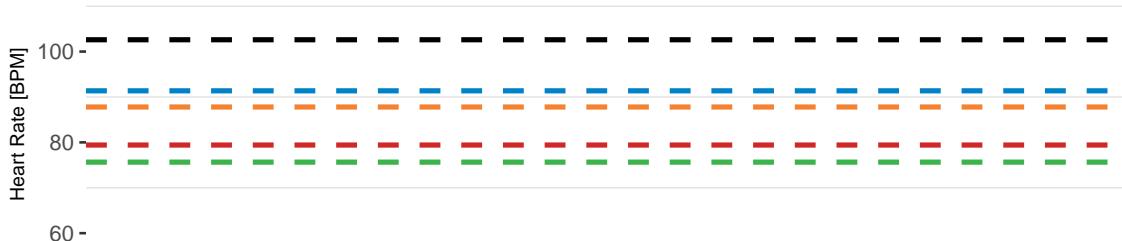
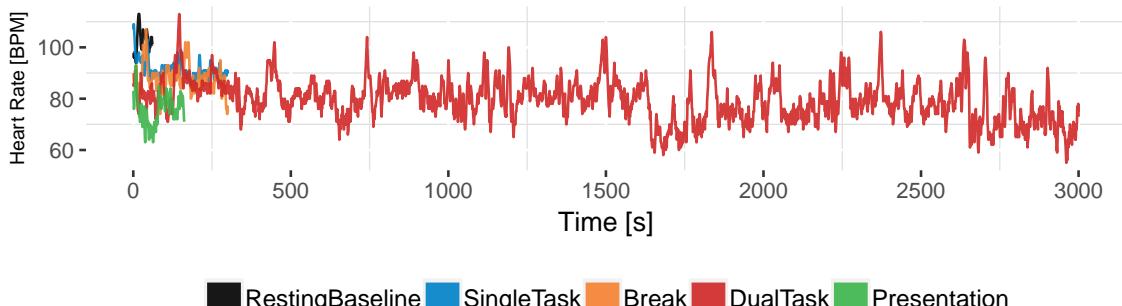
T098 (Intermittent Nothing) BR



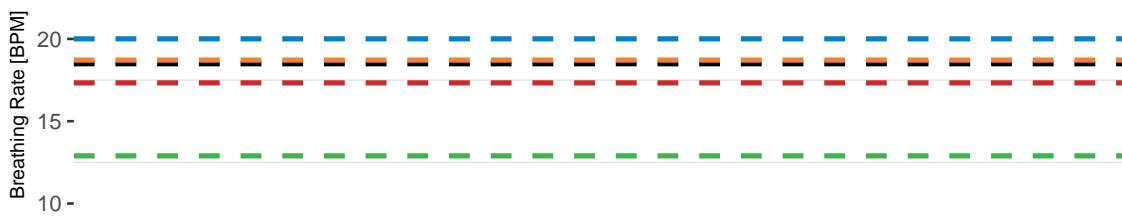
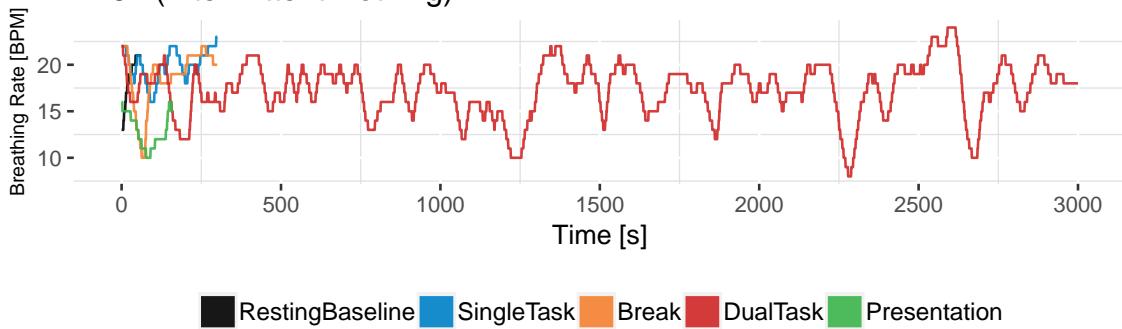




T151 (Intermittent Nothing) HR

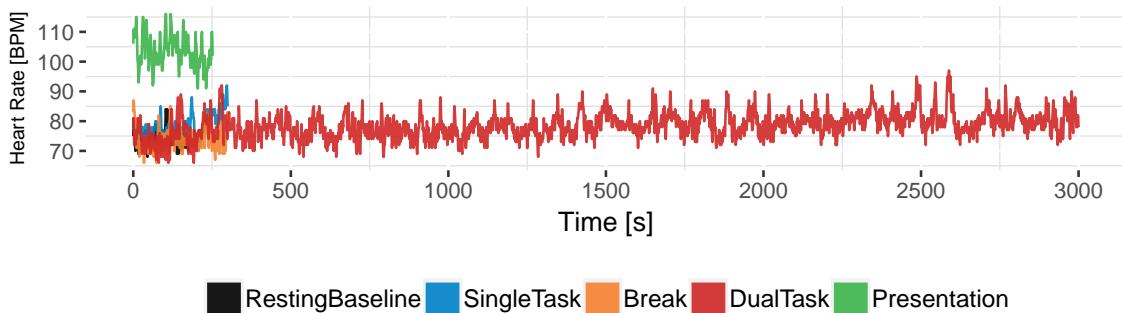


T151 (Intermittent Nothing) BR

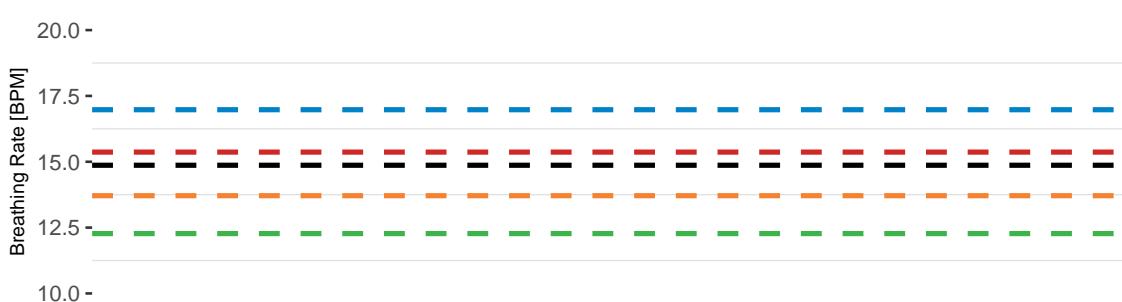
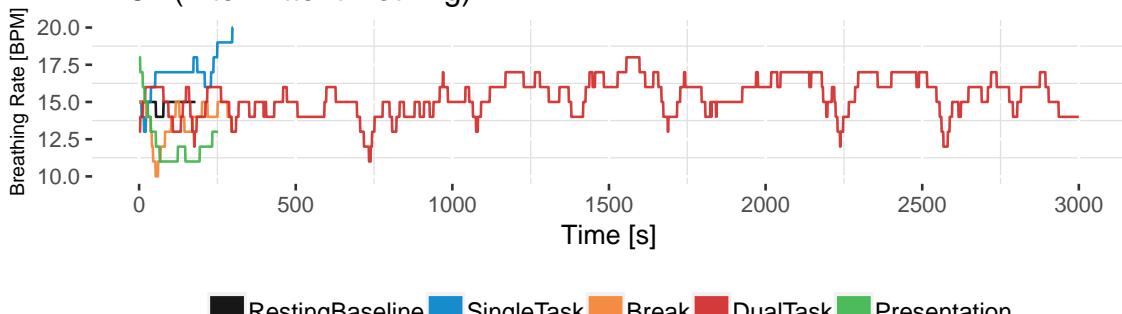




T152 (Intermittent Nothing) HR

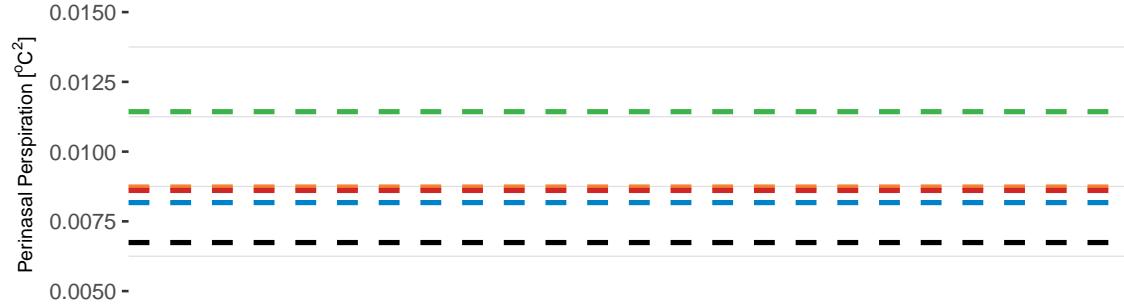
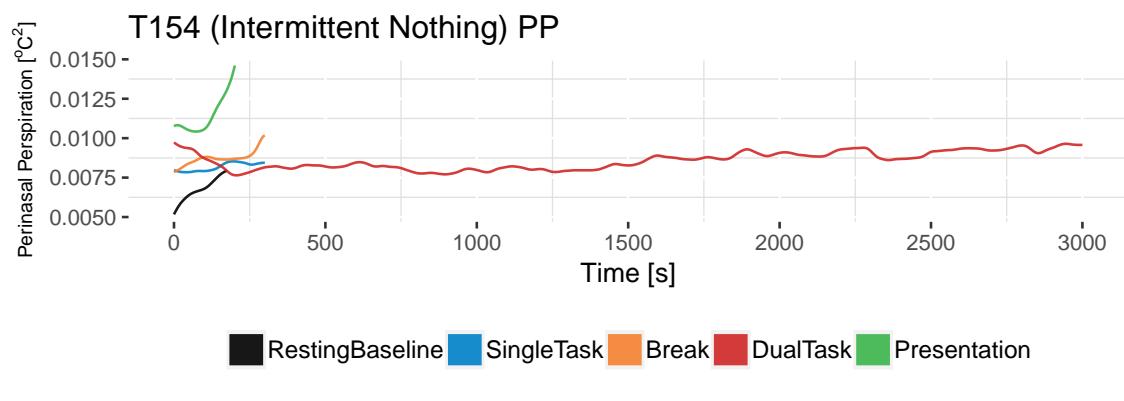
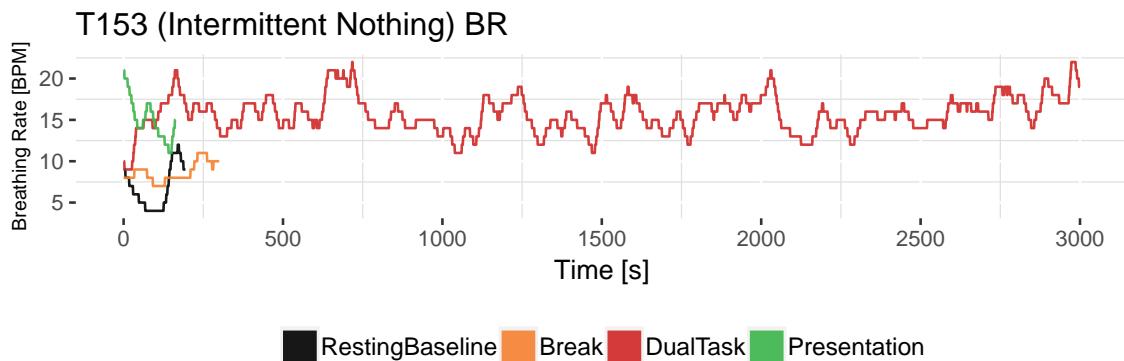


T152 (Intermittent Nothing) BR

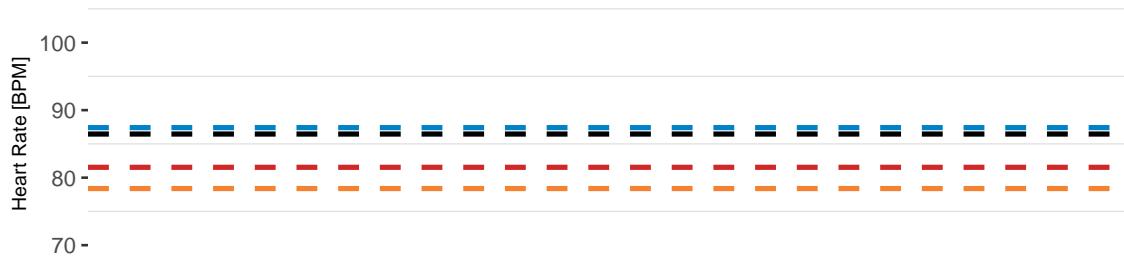
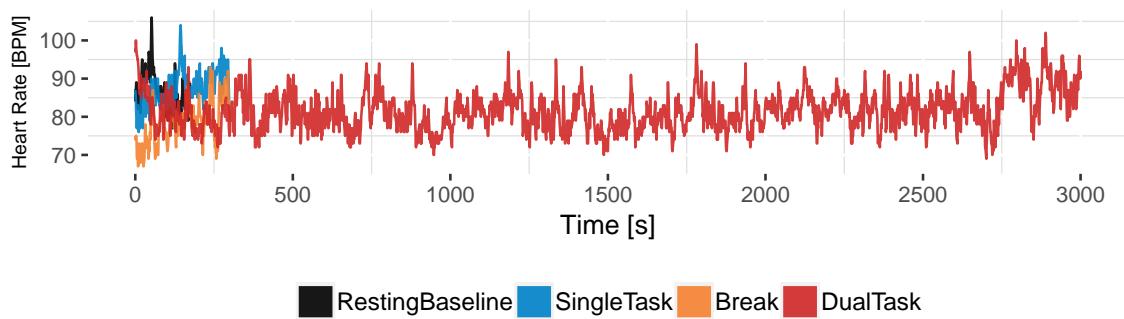


```
##  
##  
## Subject T153 does not have PP signals.  
##  
##  
##  
##  
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##  
##  
##  
##
```

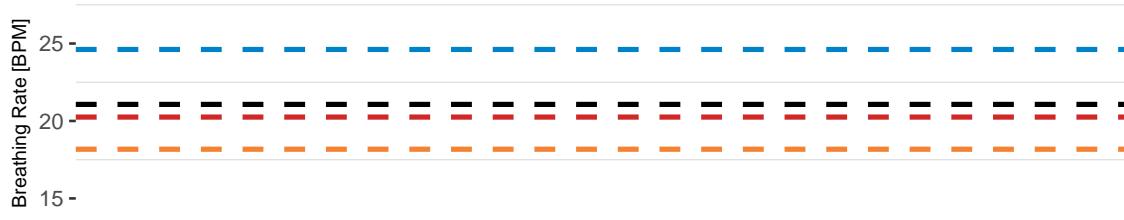
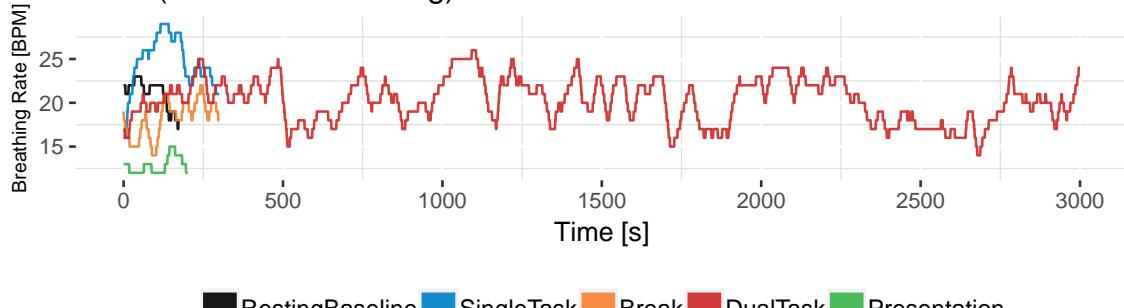




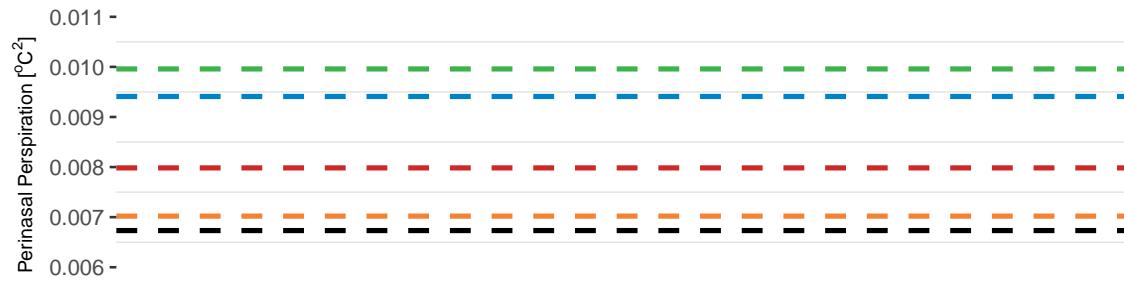
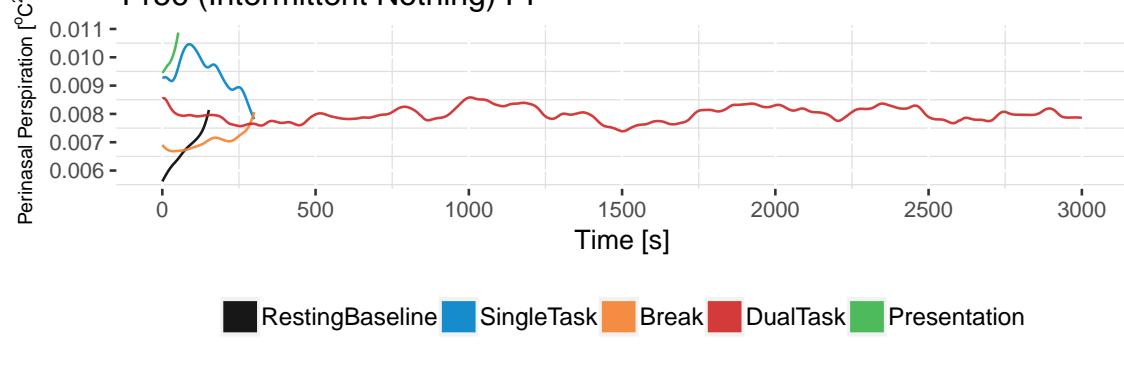
T154 (Intermittent Nothing) HR



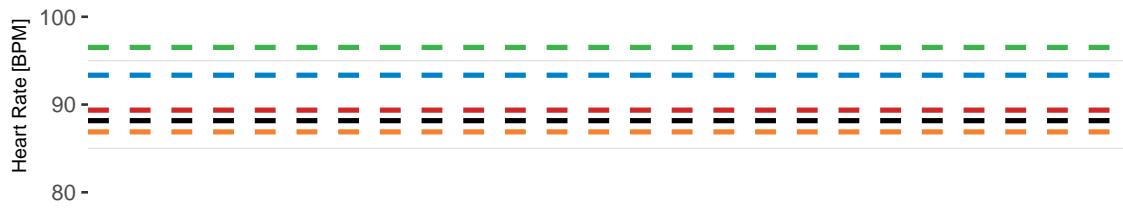
T154 (Intermittent Nothing) BR

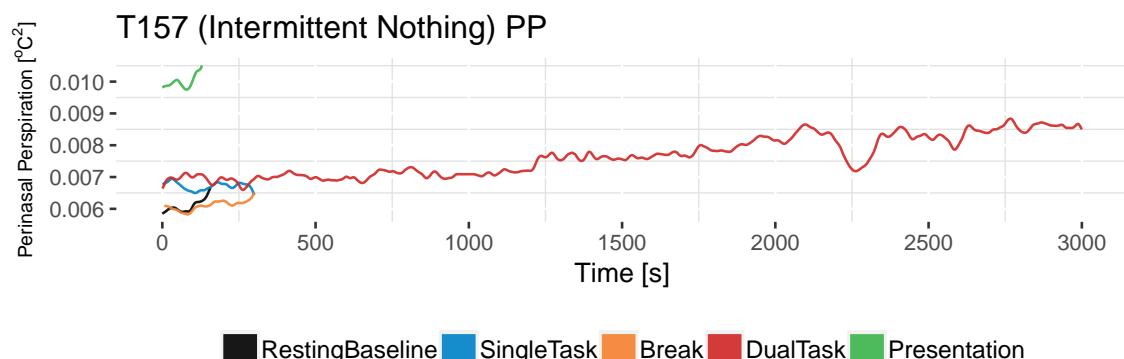
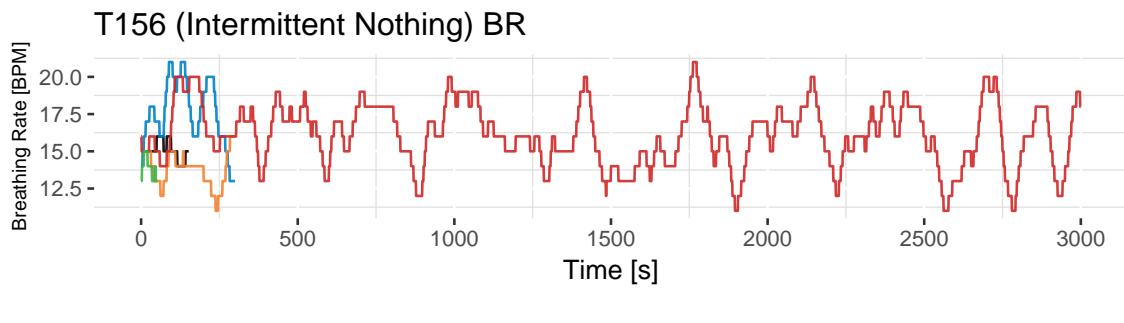


T156 (Intermittent Nothing) PP

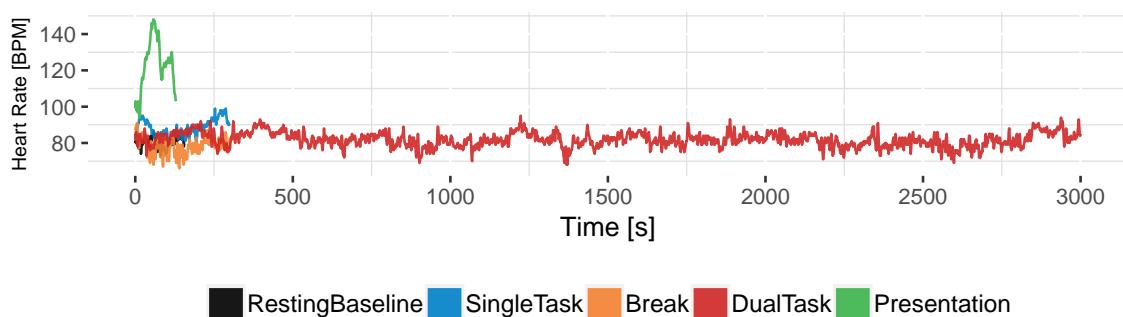


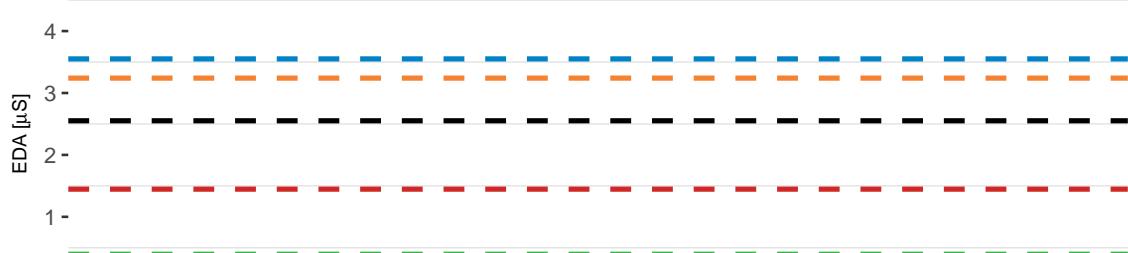
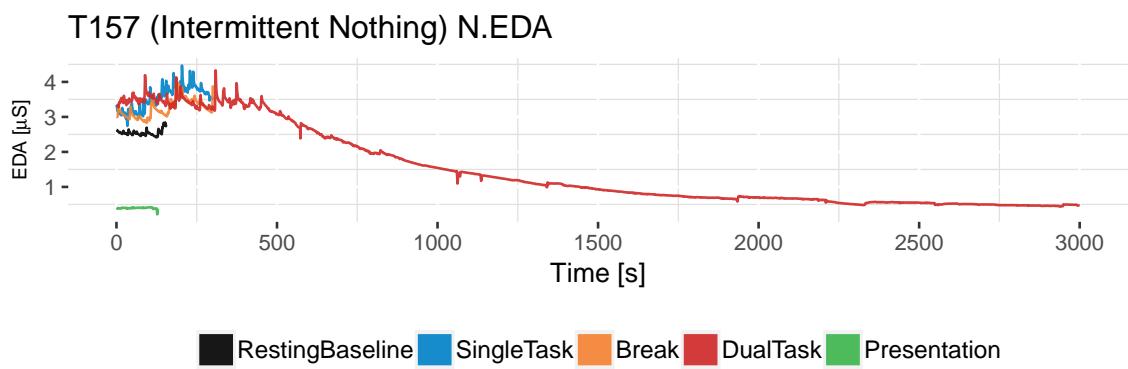
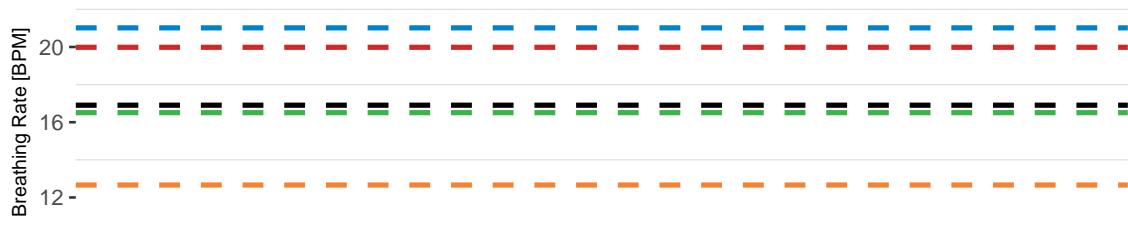
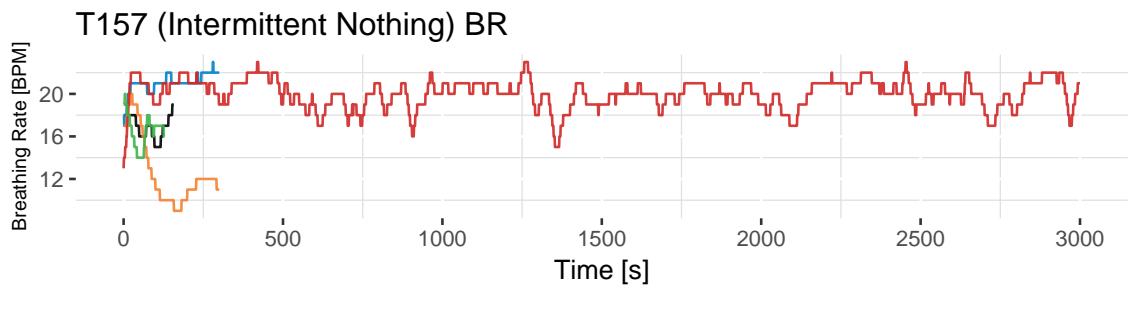
T156 (Intermittent Nothing) HR





T157 (Intermittent Nothing) HR

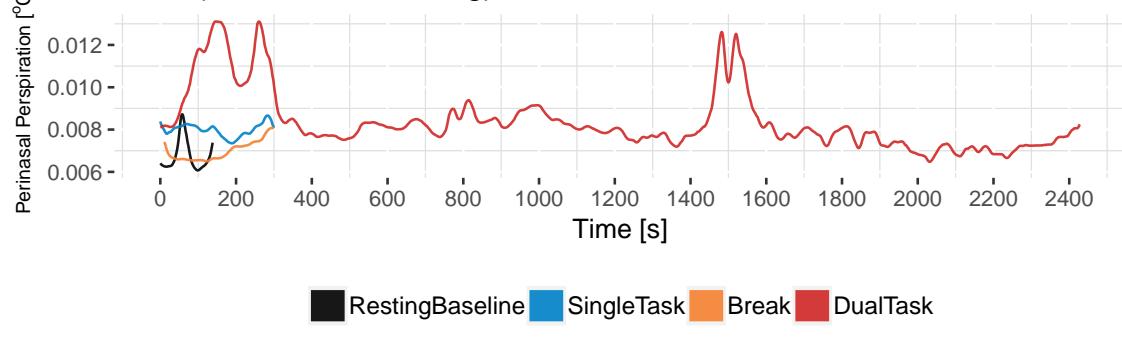




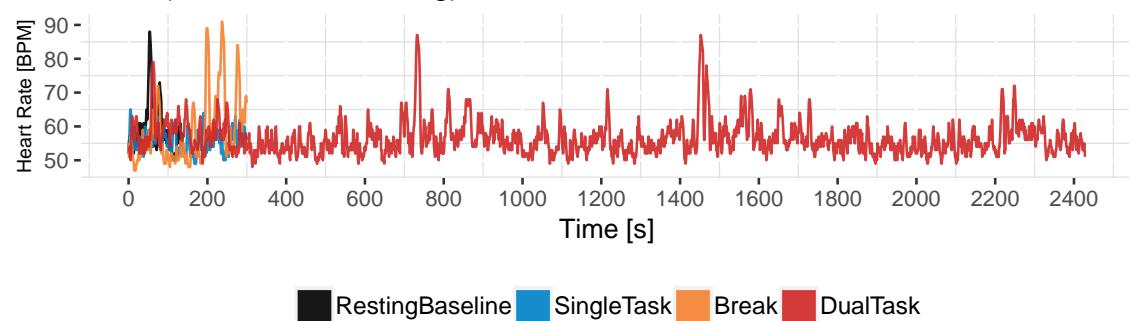
T157 (Intermittent Nothing) N.HR

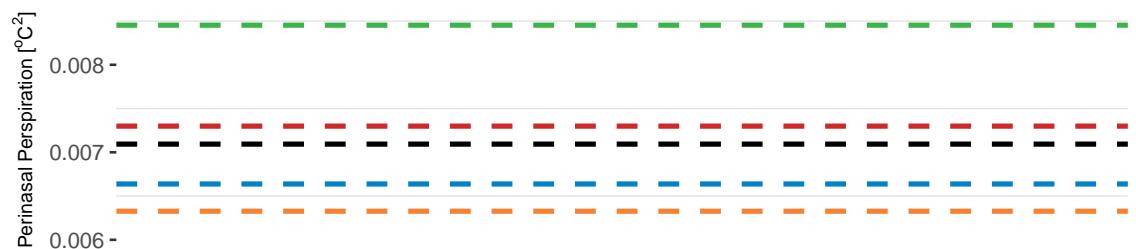
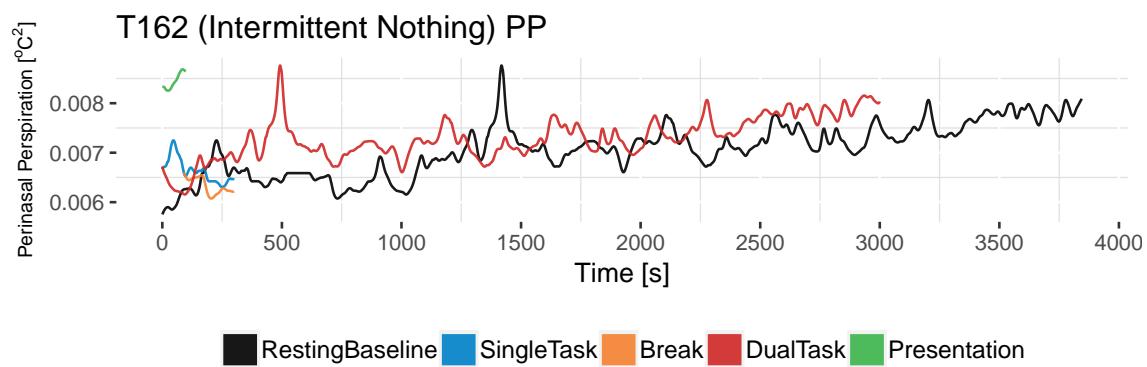
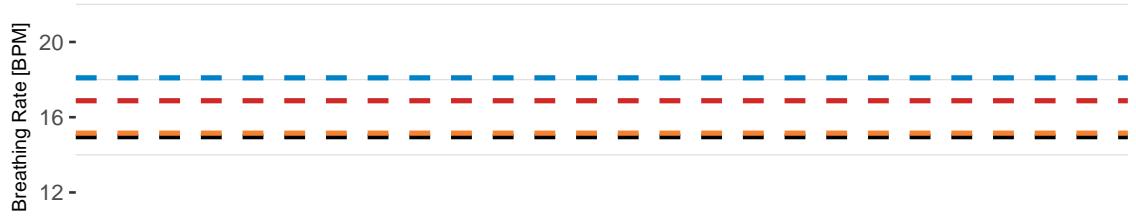
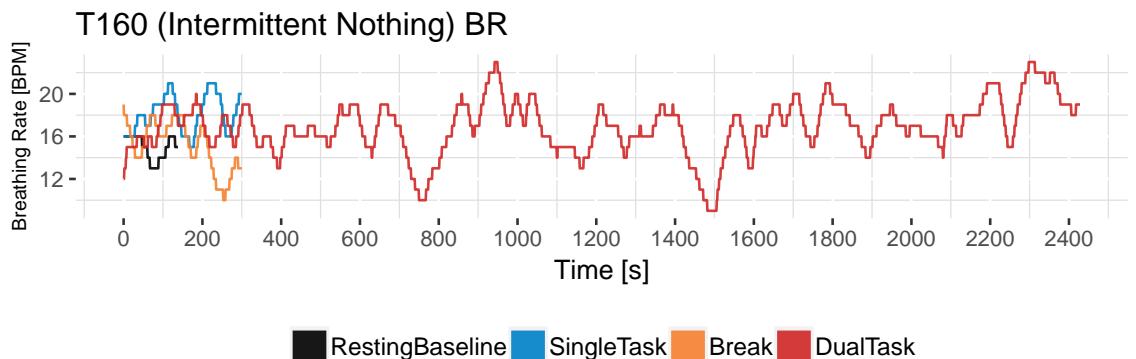


T160 (Intermittent Nothing) PP



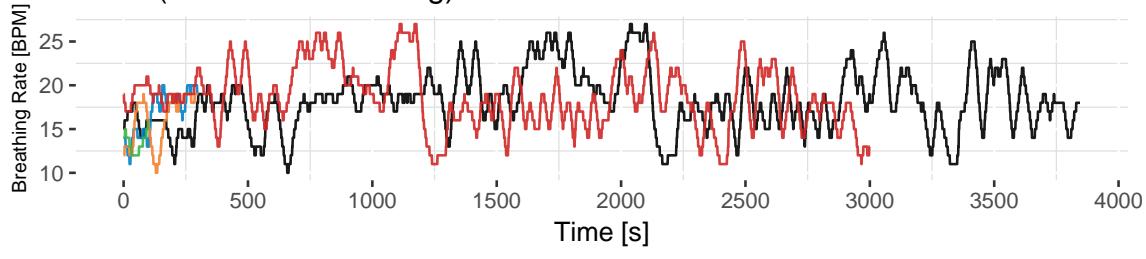
T160 (Intermittent Nothing) HR





```
##  
##  
## Subject T162 does not have HR signals.  
##  
##  
##  
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##  
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```

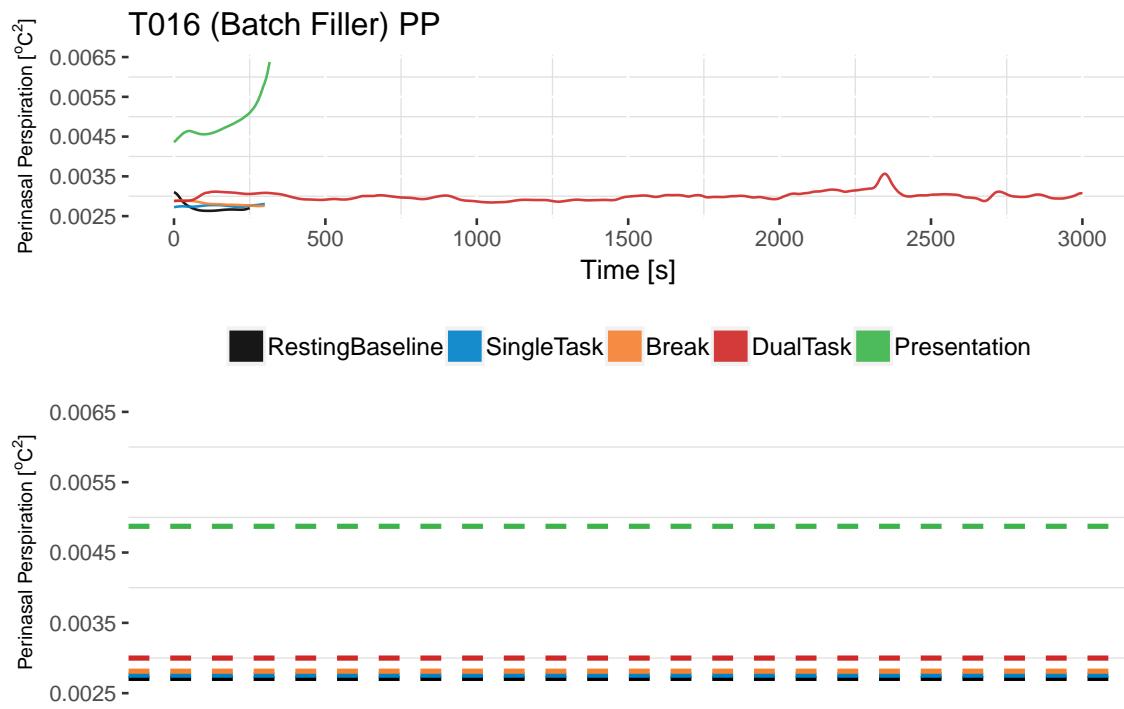
T162 (Intermittent Nothing) BR



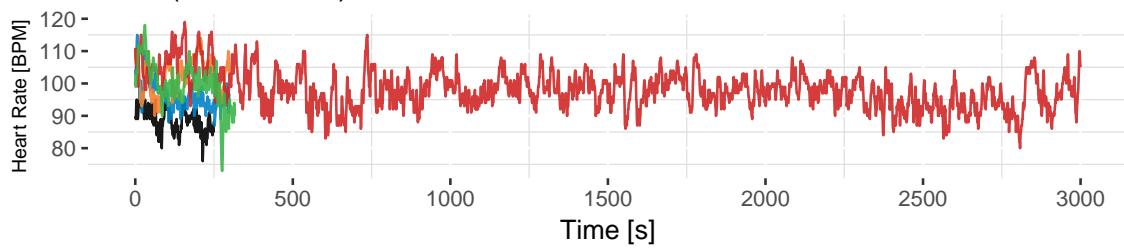
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



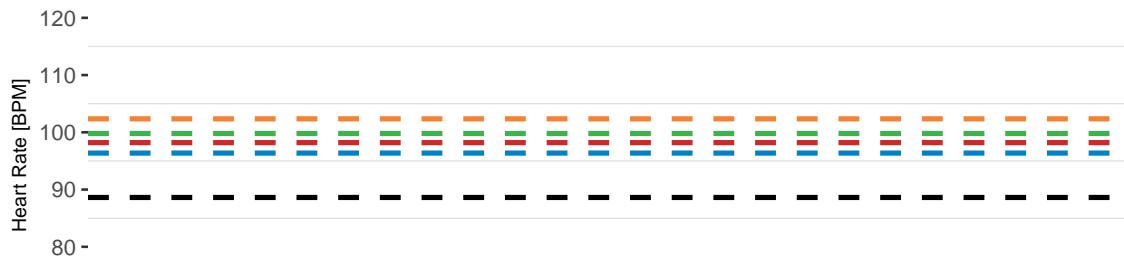
Batch-Filler (BF) Subjects



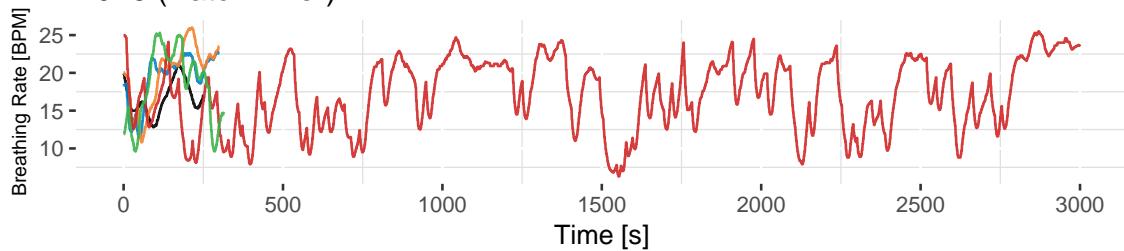
T016 (Batch Filler) HR



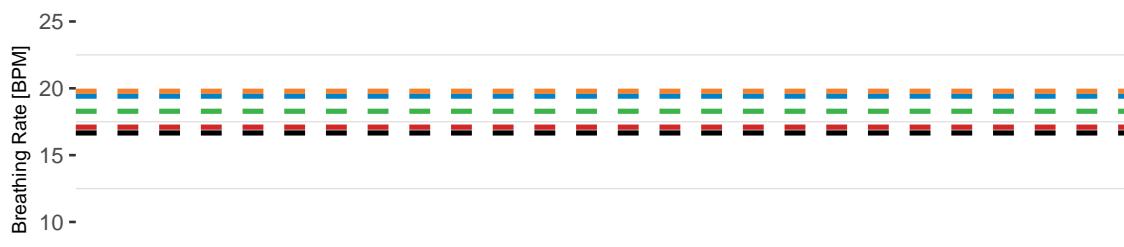
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



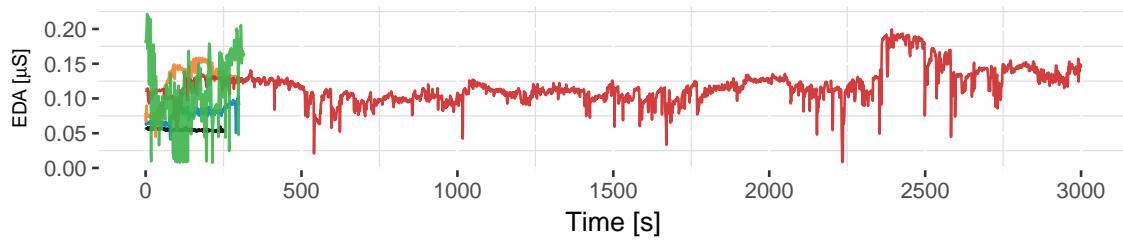
T016 (Batch Filler) BR



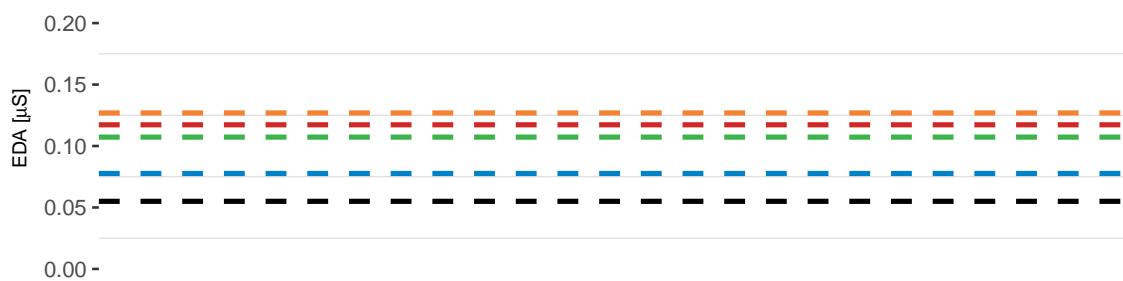
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



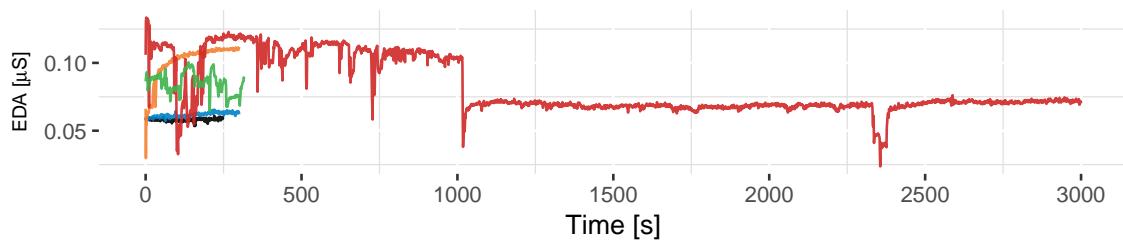
T016 (Batch Filler) D.EDA



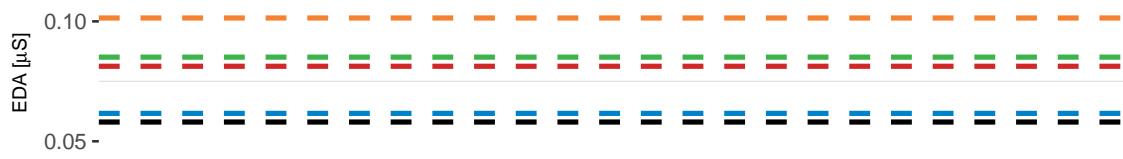
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



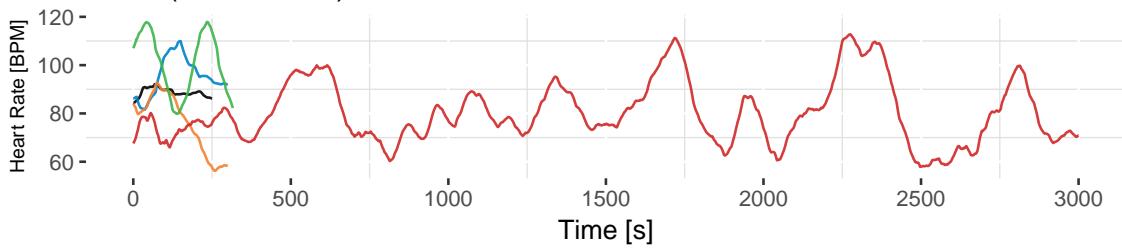
T016 (Batch Filler) N.EDA



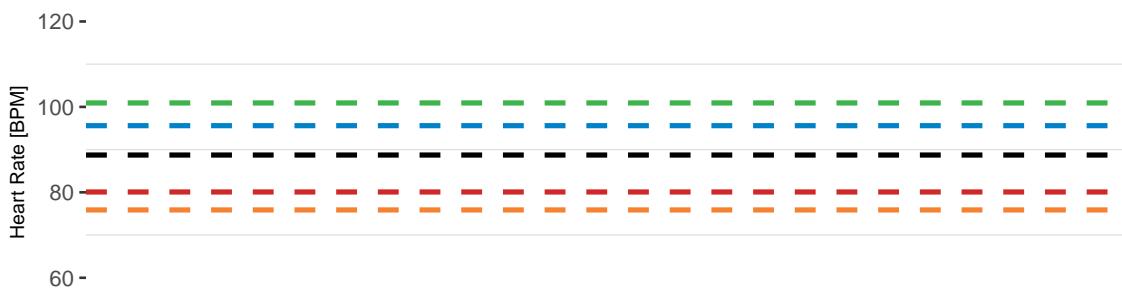
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



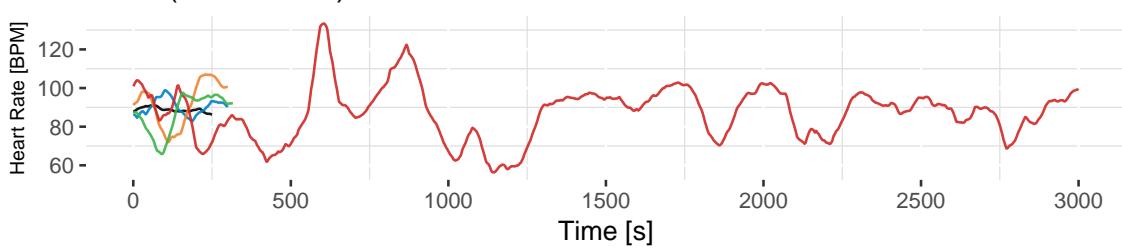
T016 (Batch Filler) D.HR



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

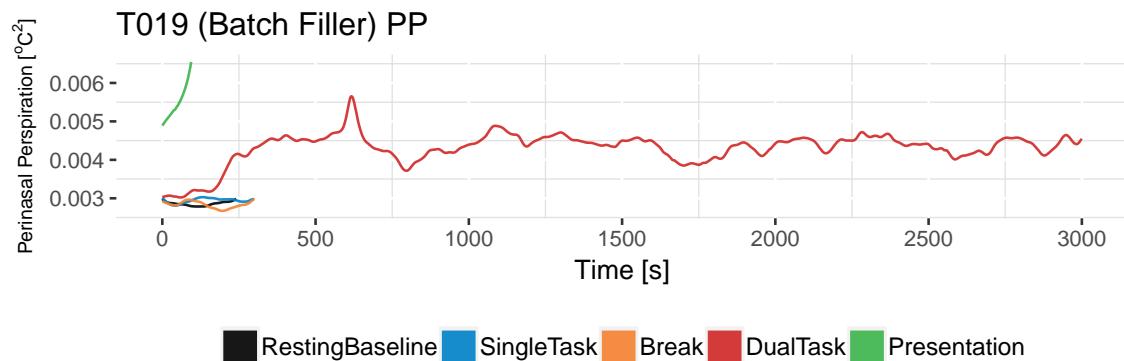


T016 (Batch Filler) N.HR

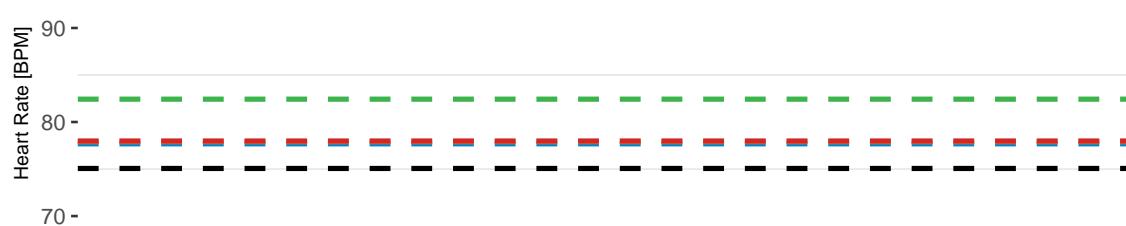
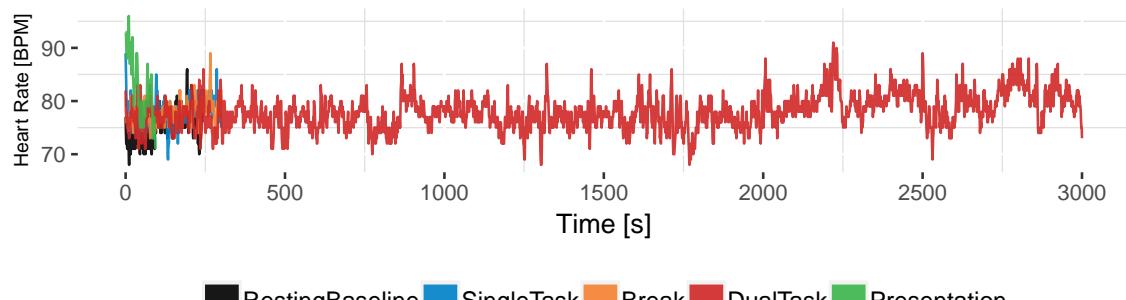


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

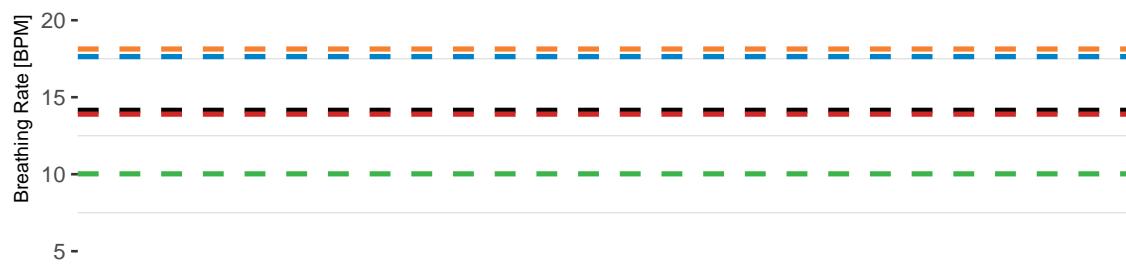
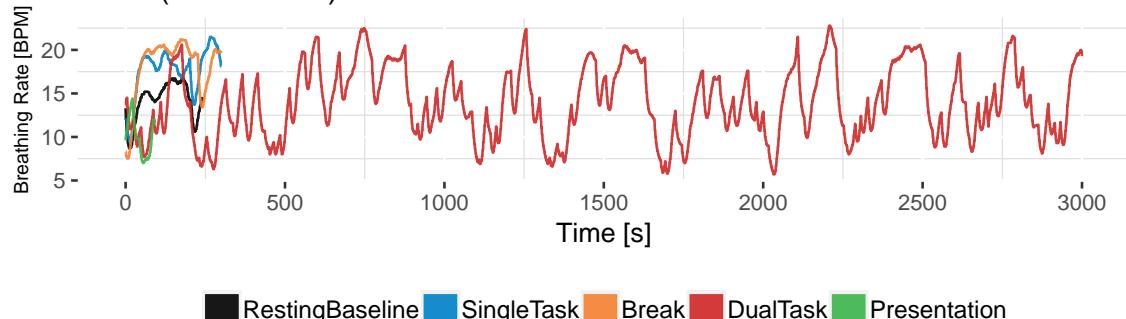




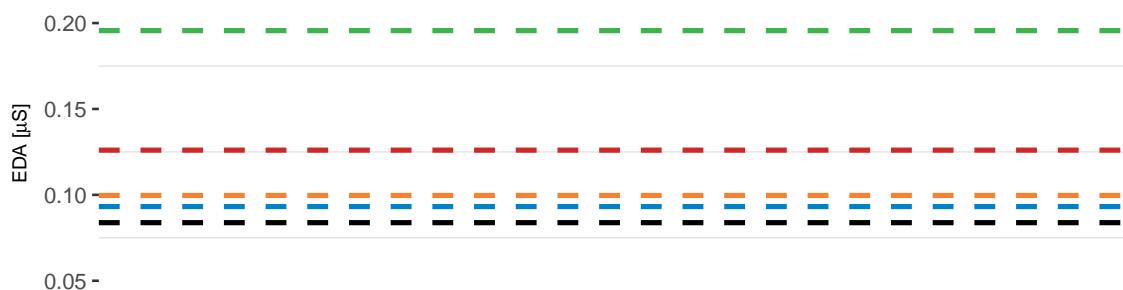
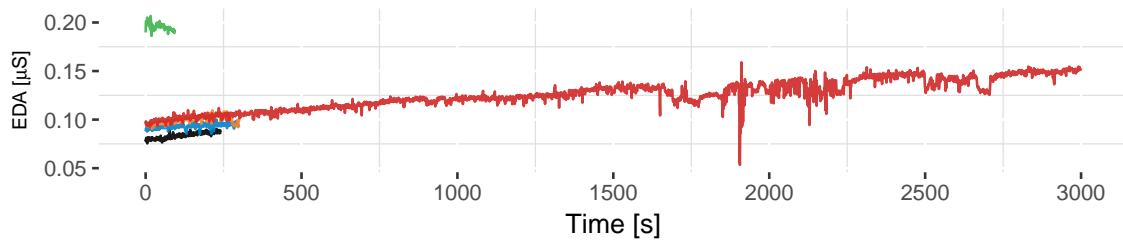
T019 (Batch Filler) HR



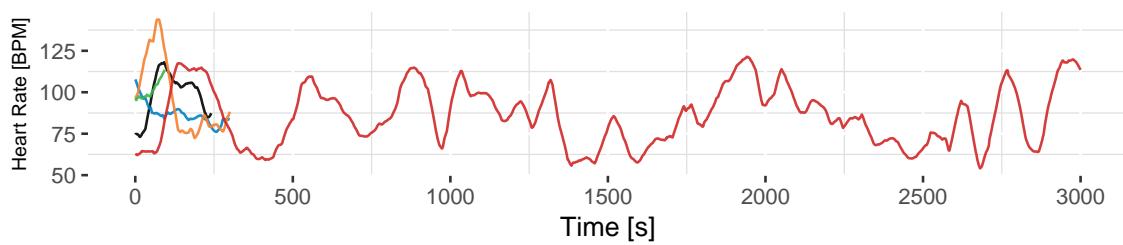
T019 (Batch Filler) BR

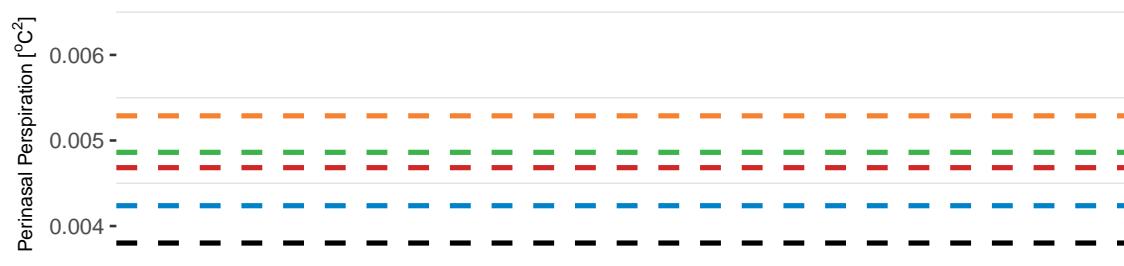
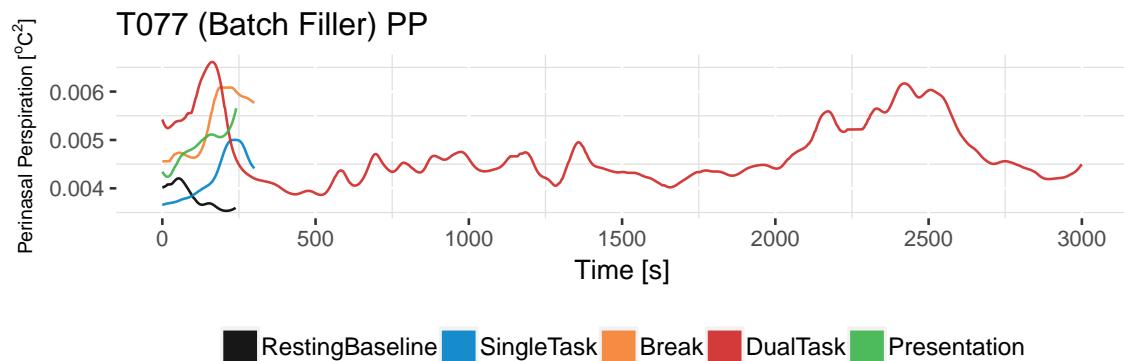


T019 (Batch Filler) N.EDA

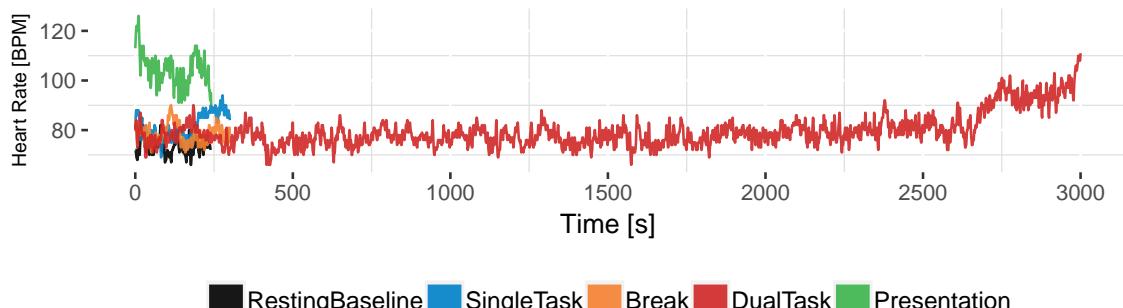


T019 (Batch Filler) N.HR

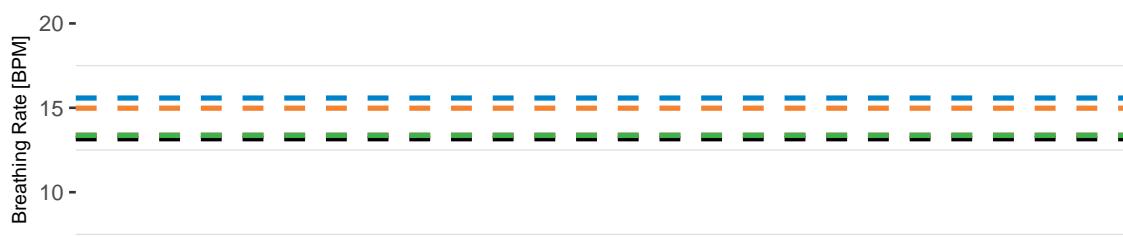




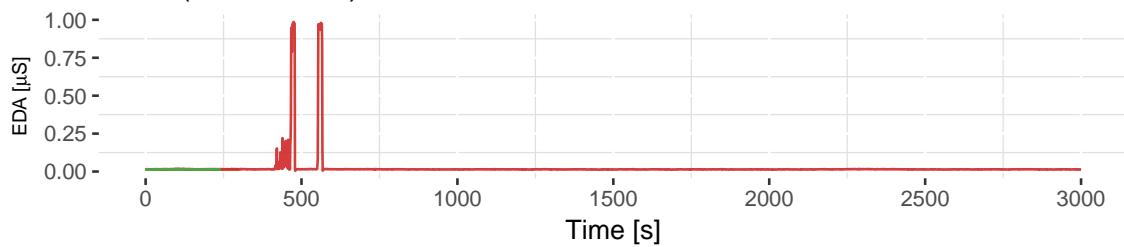
T077 (Batch Filler) HR



T077 (Batch Filler) BR



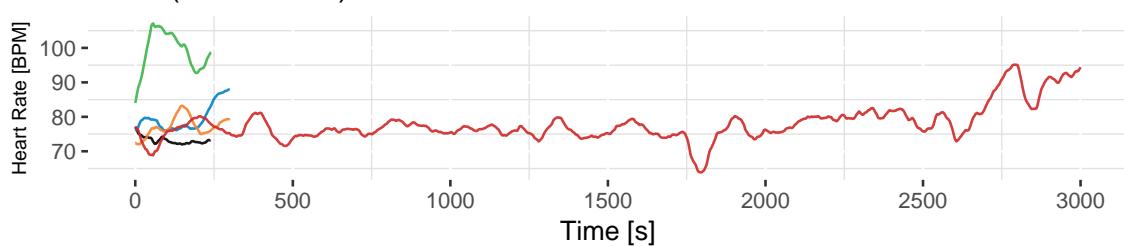
T077 (Batch Filler) N.EDA



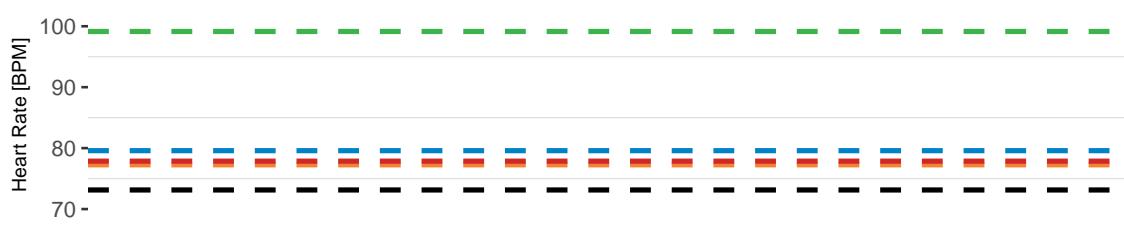
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

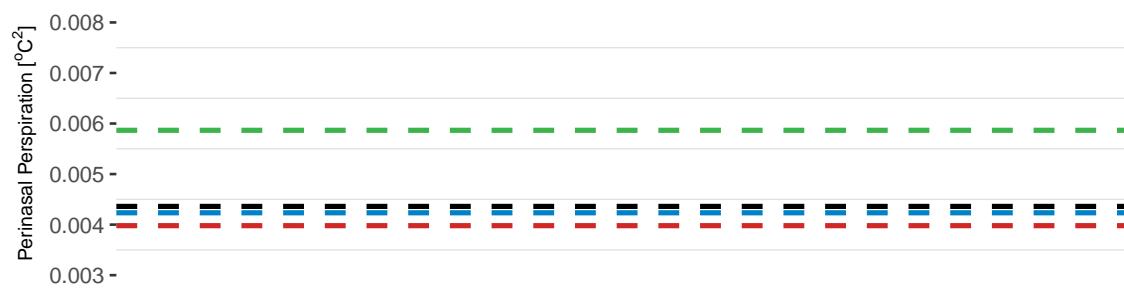
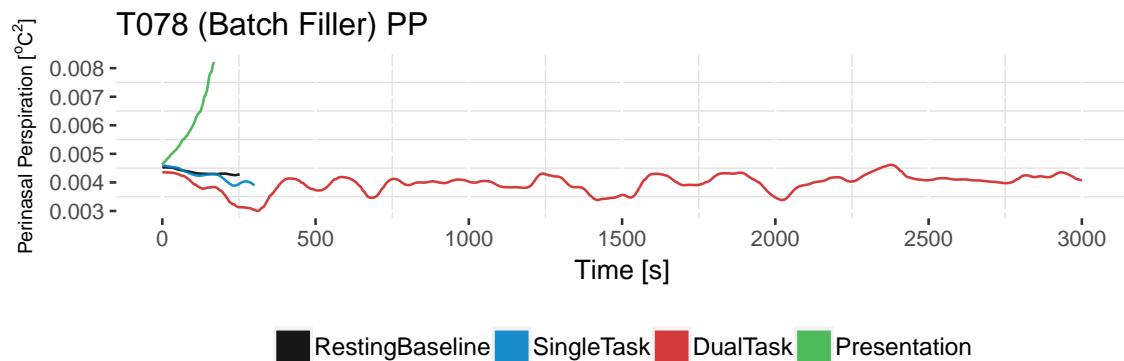


T077 (Batch Filler) N.HR

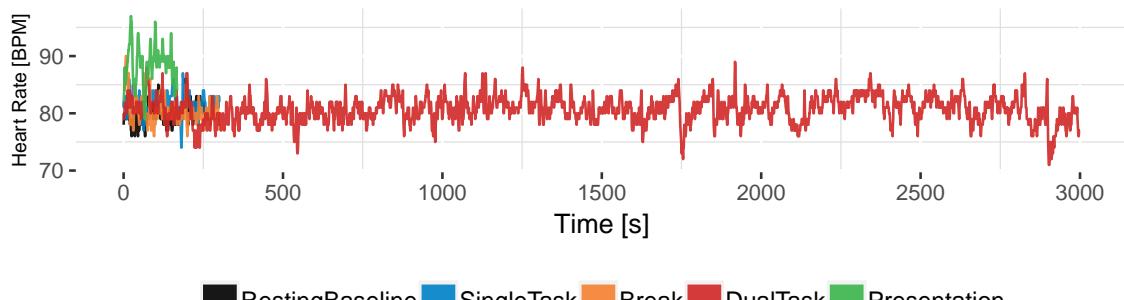


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation





T078 (Batch Filler) HR



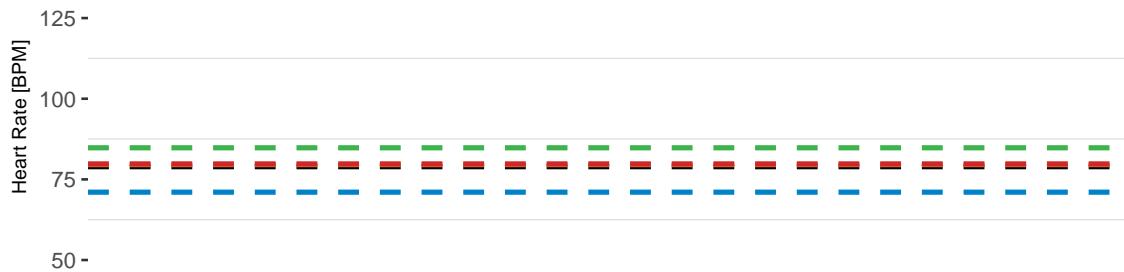
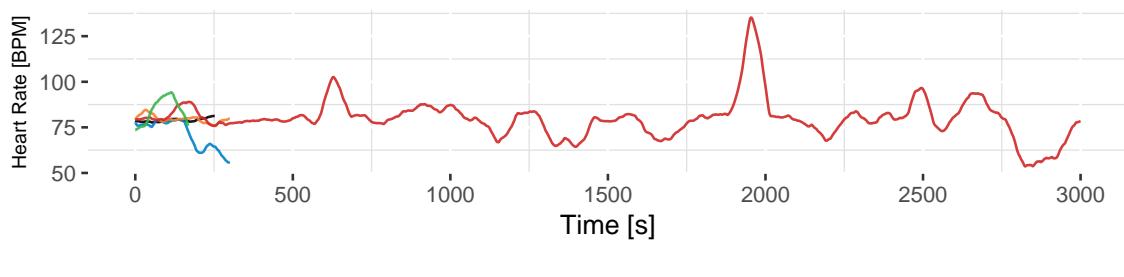
T078 (Batch Filler) BR

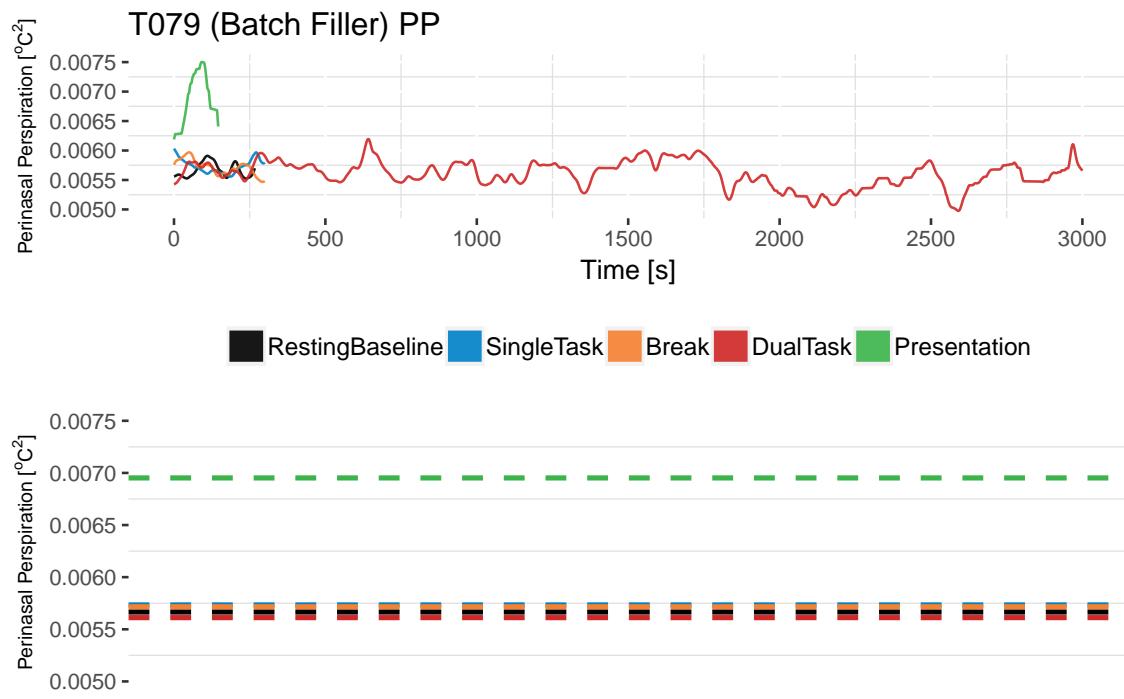


T078 (Batch Filler) N.EDA

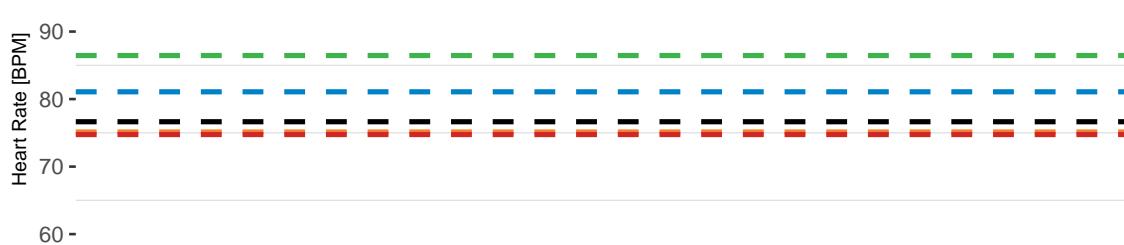
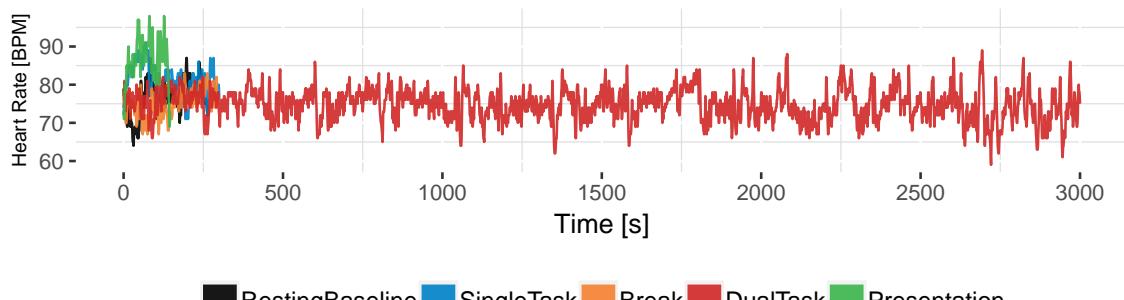


T078 (Batch Filler) N.HR

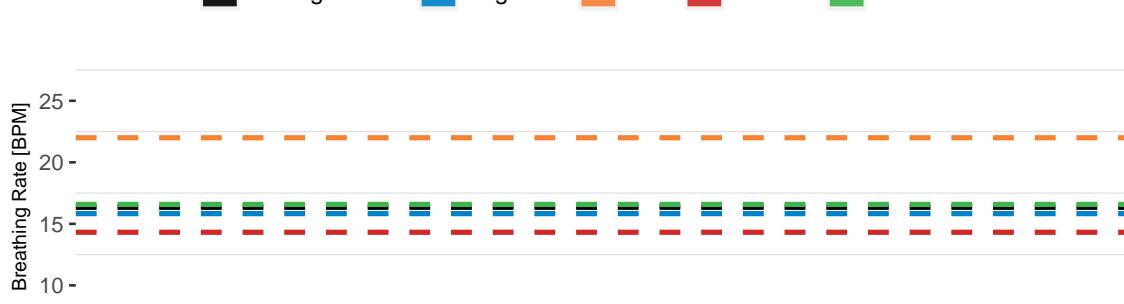
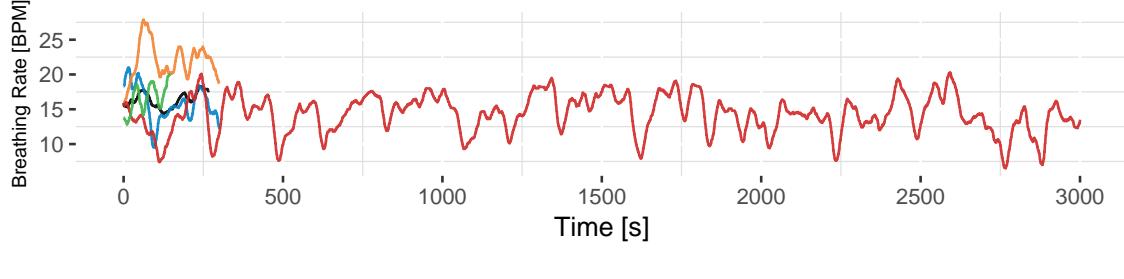




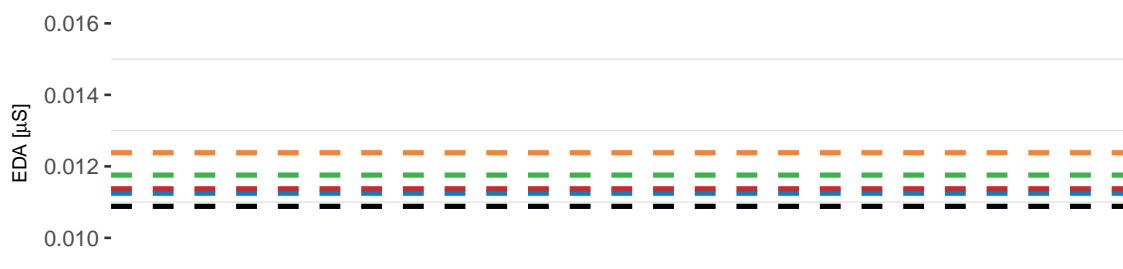
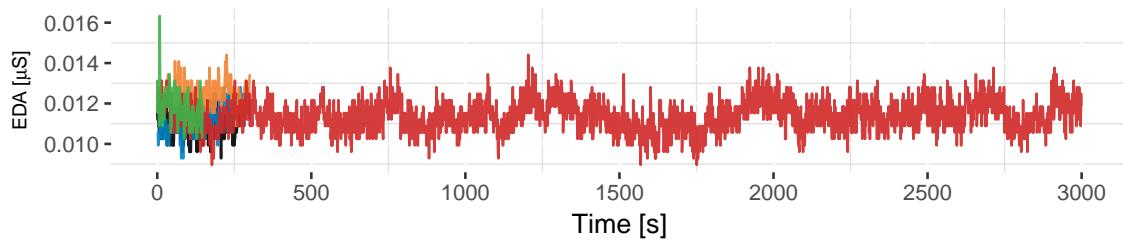
T079 (Batch Filler) HR



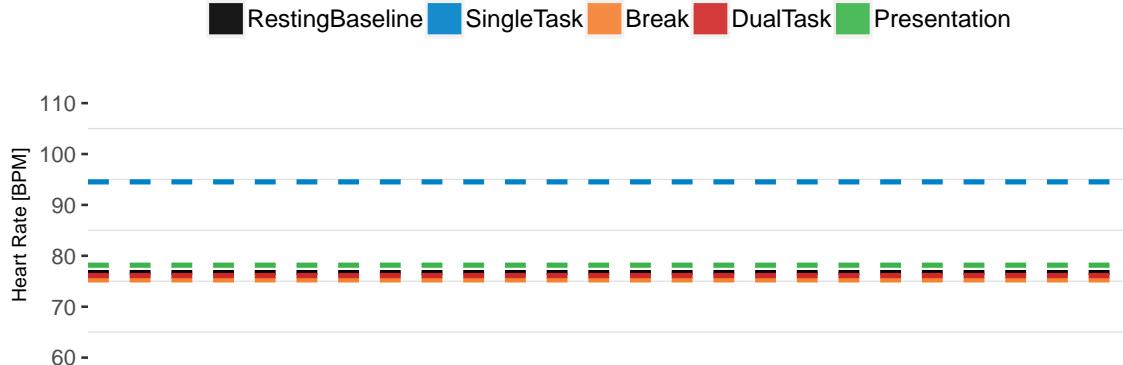
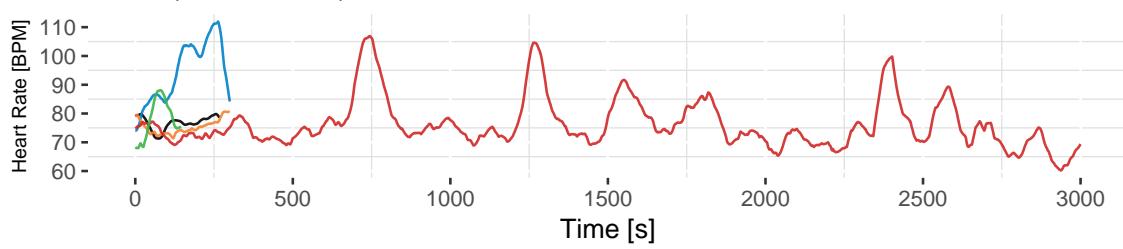
T079 (Batch Filler) BR

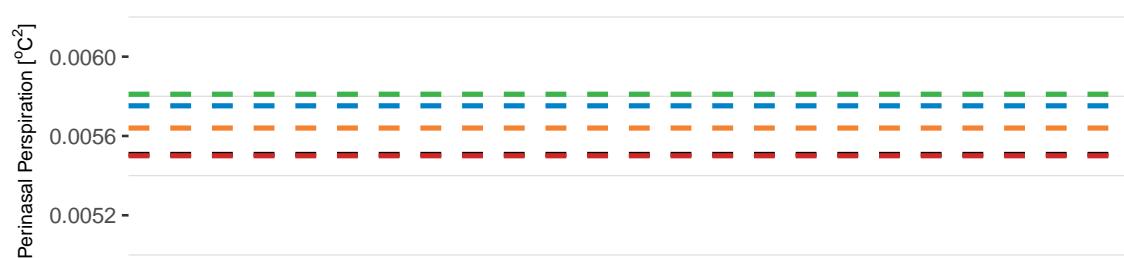
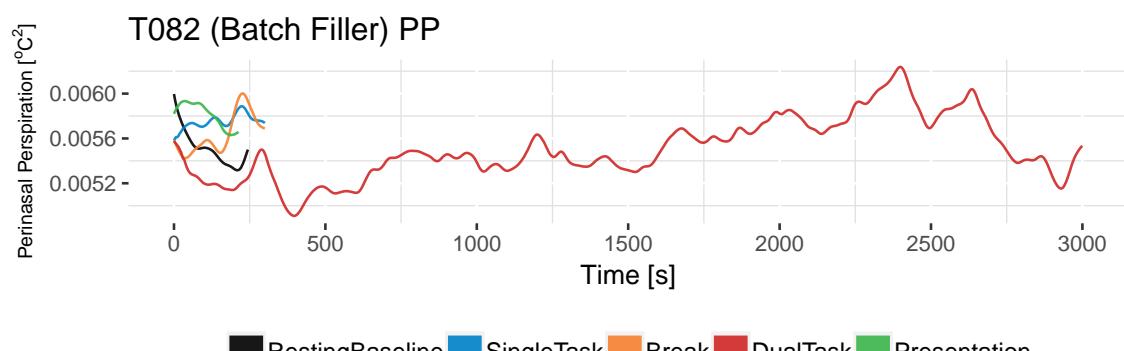


T079 (Batch Filler) N.EDA

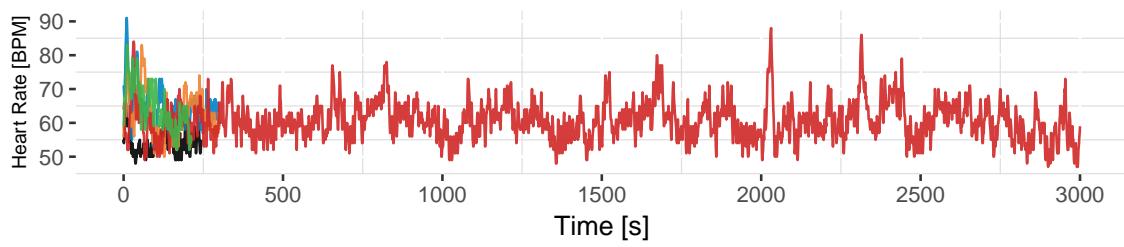


T079 (Batch Filler) N.HR

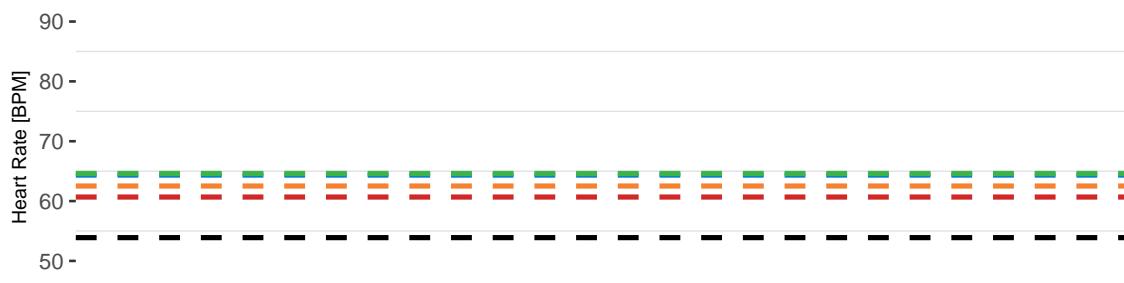




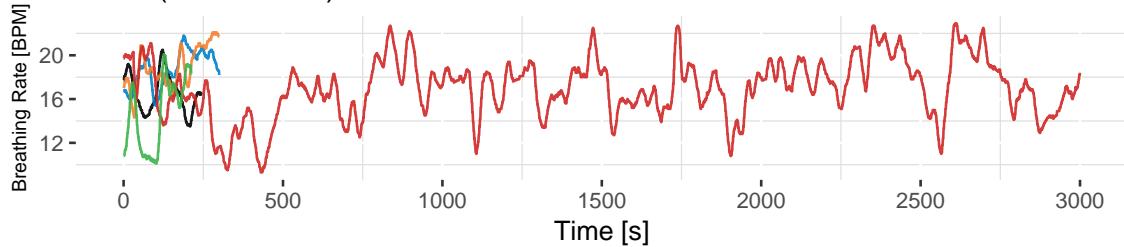
T082 (Batch Filler) HR



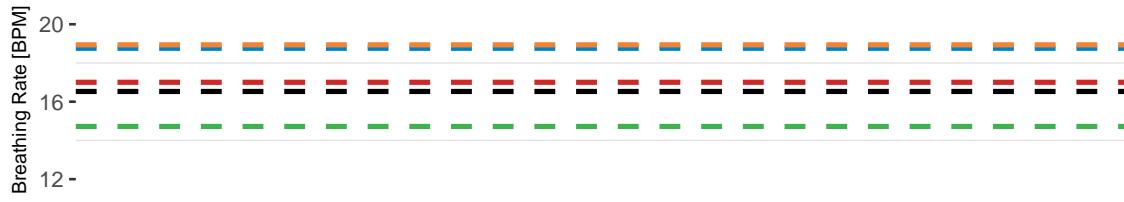
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



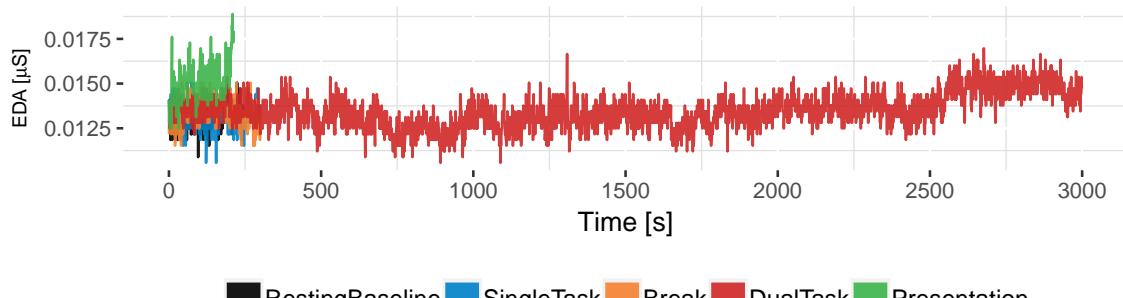
T082 (Batch Filler) BR



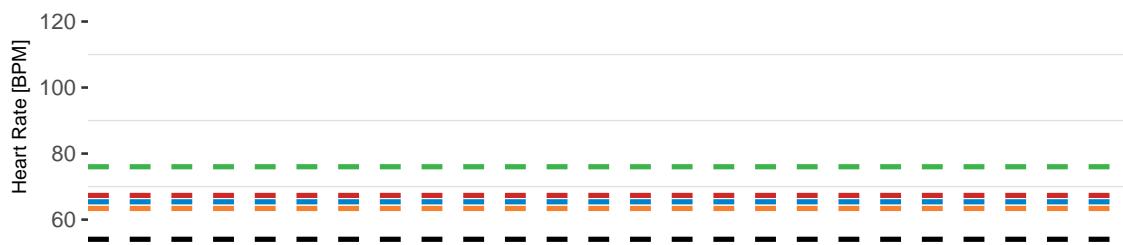
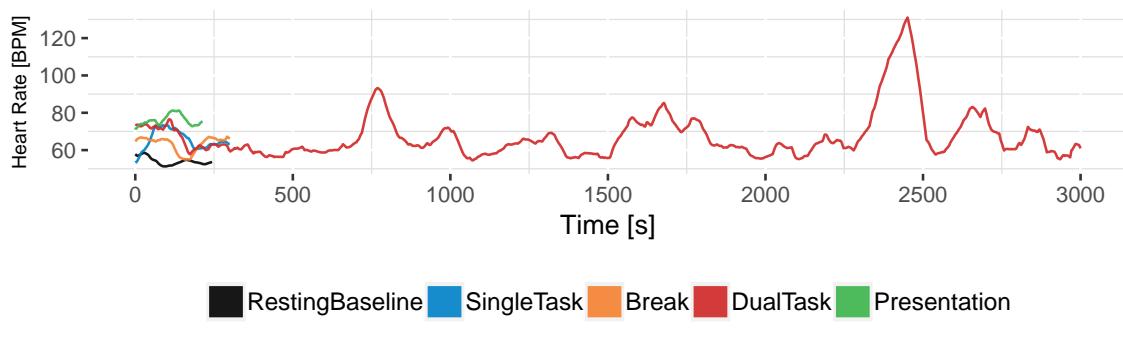
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

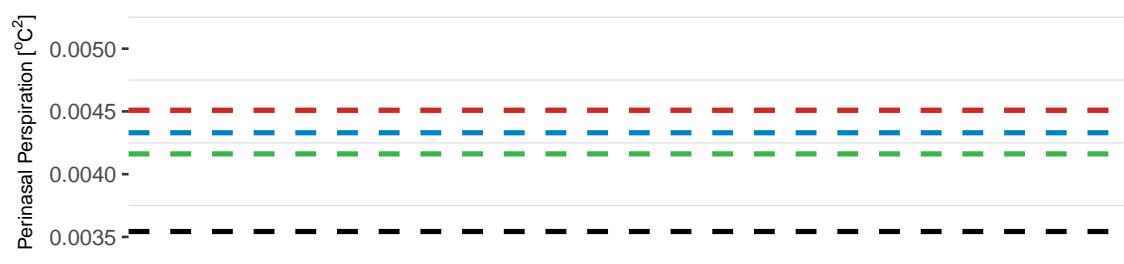
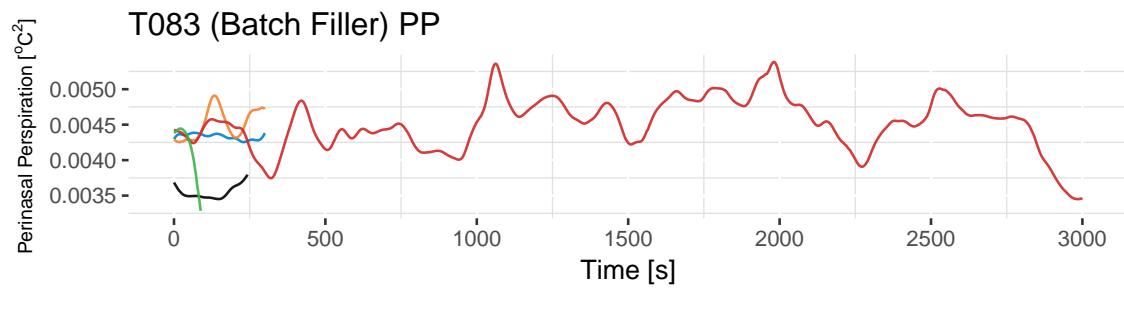


T082 (Batch Filler) N.EDA

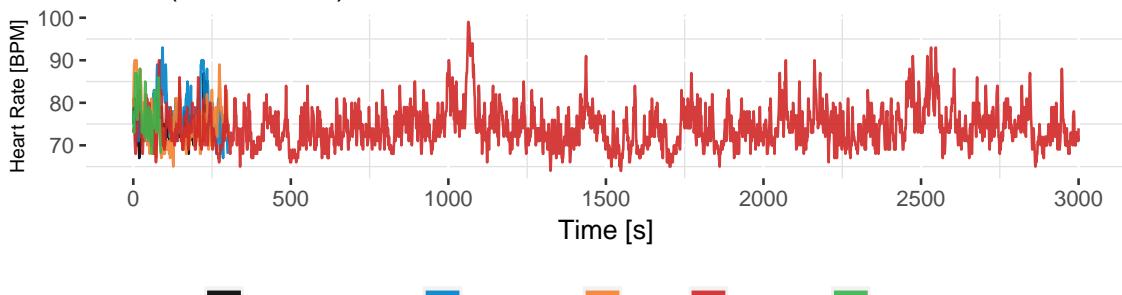


T082 (Batch Filler) N.HR





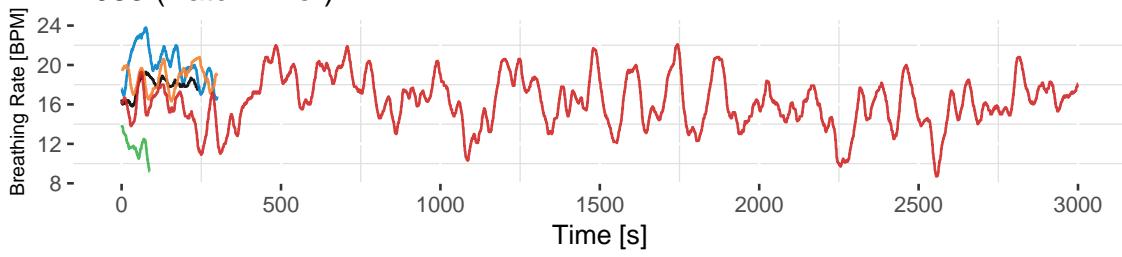
T083 (Batch Filler) HR



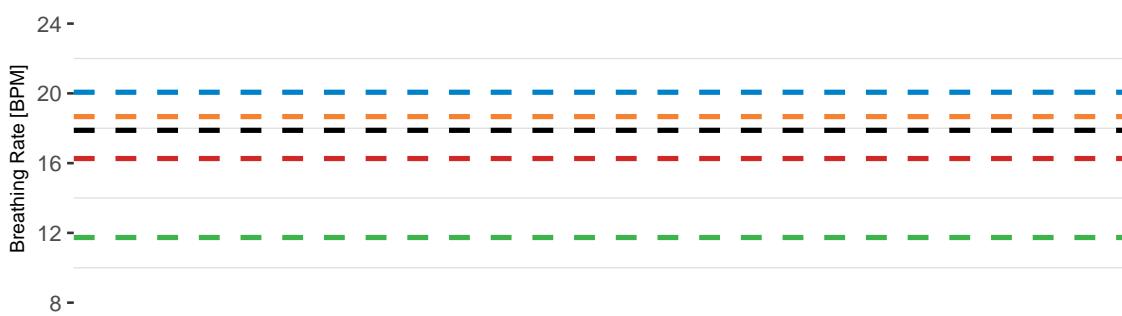
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

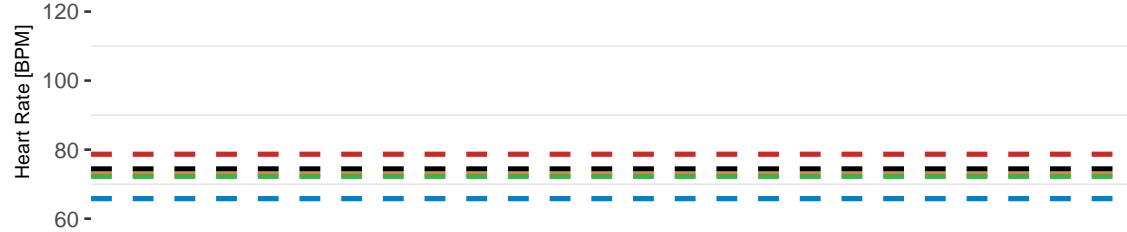
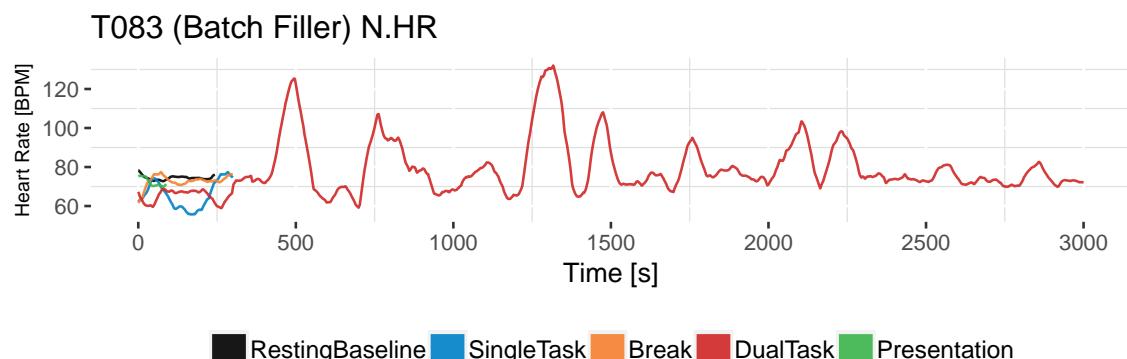
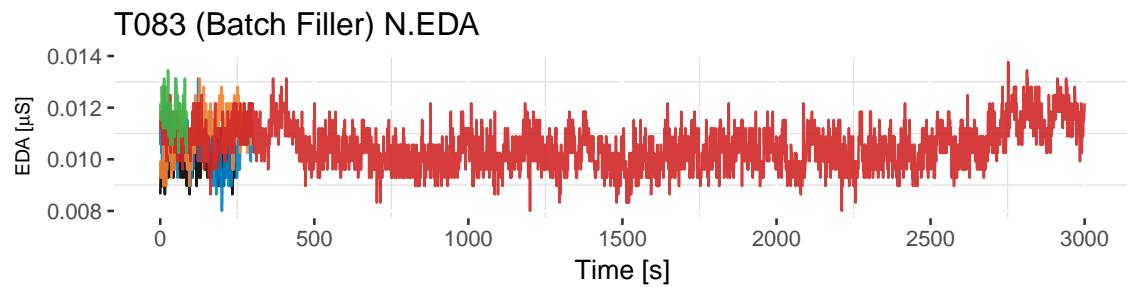


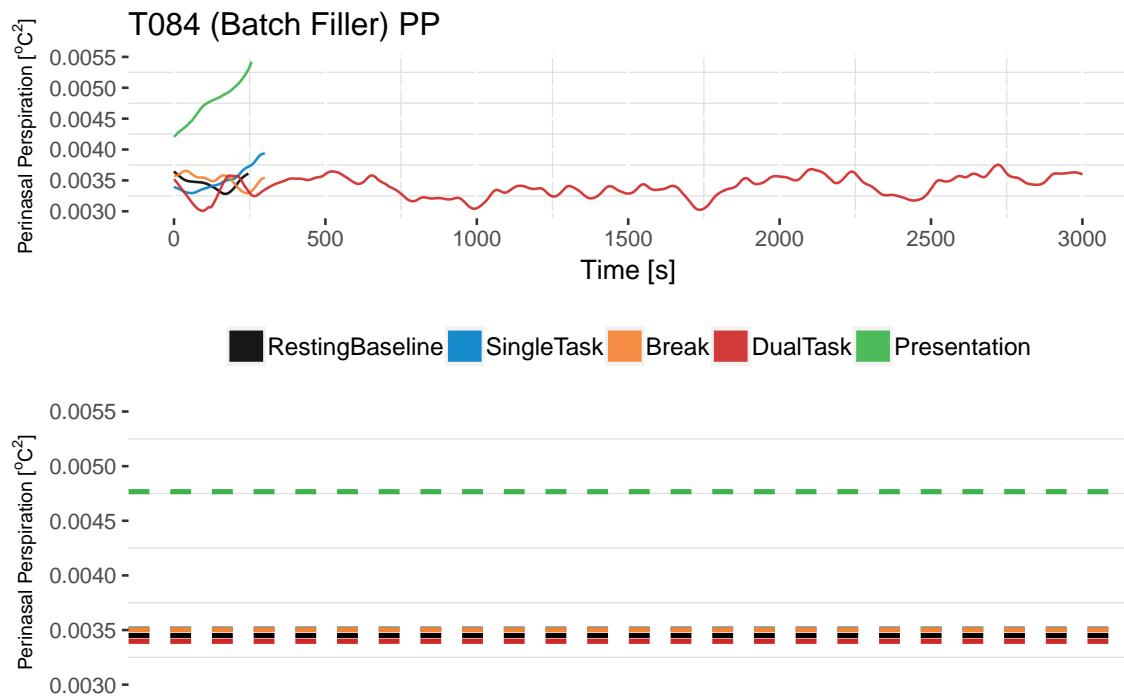
T083 (Batch Filler) BR



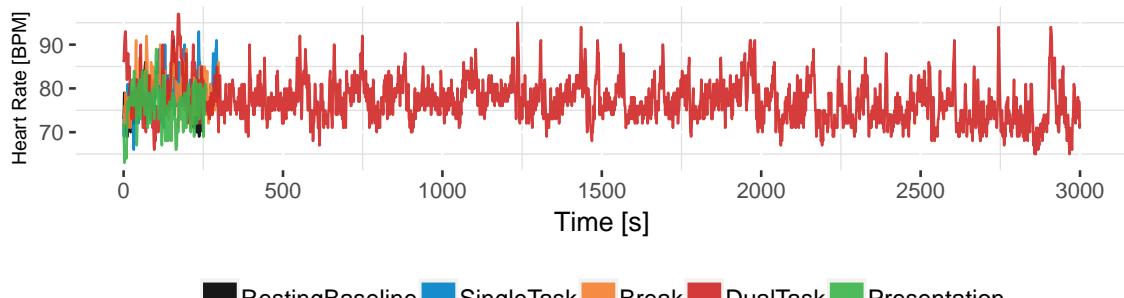
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



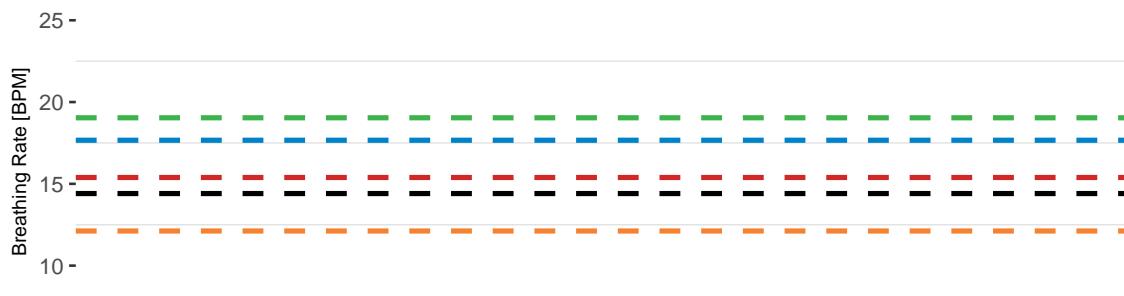
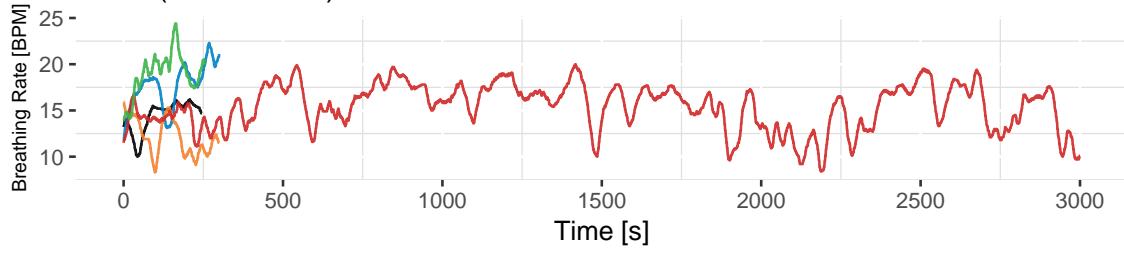




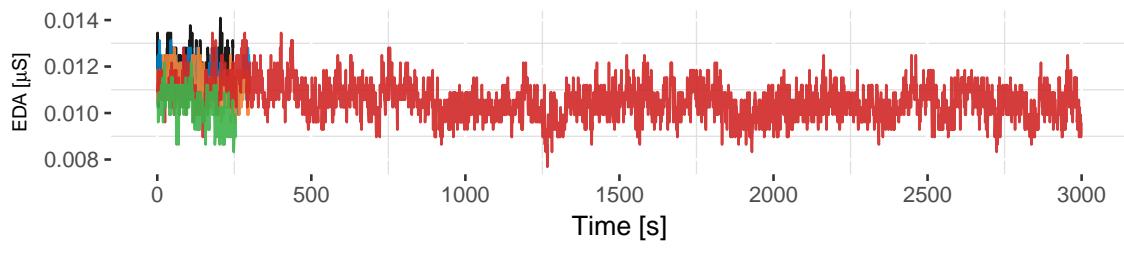
T084 (Batch Filler) HR



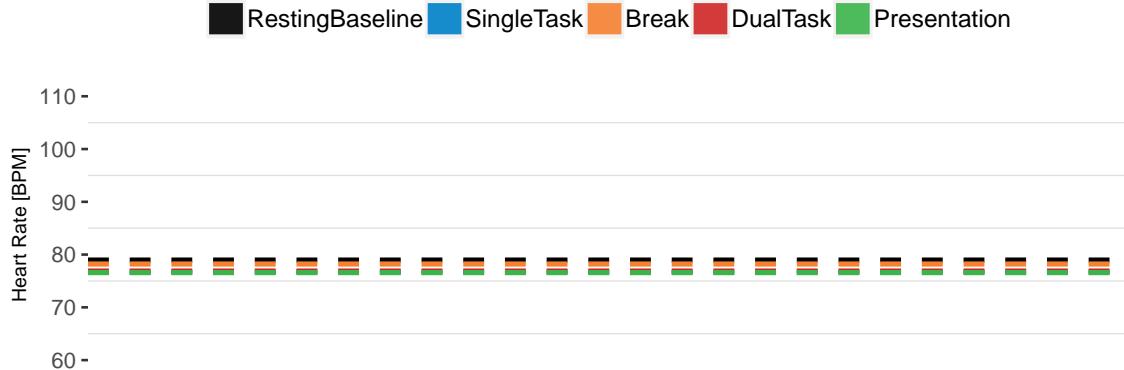
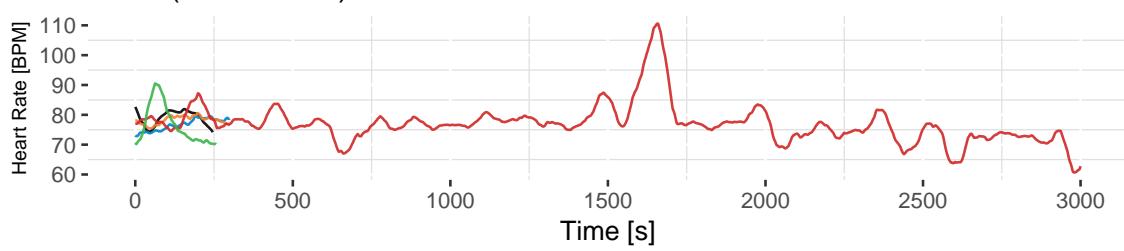
T084 (Batch Filler) BR



T084 (Batch Filler) N.EDA

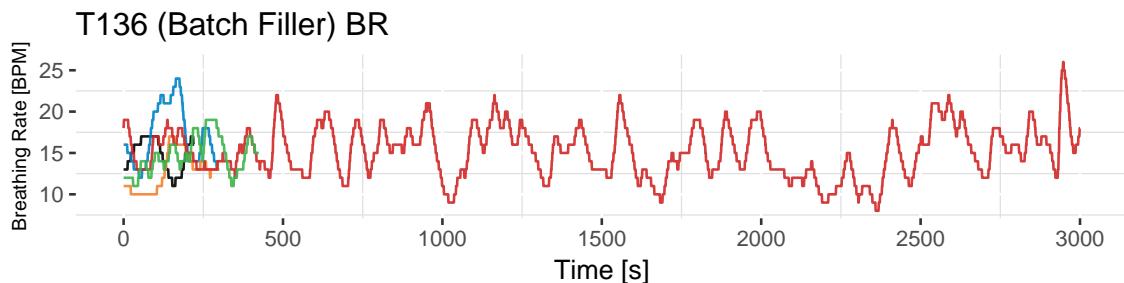


T084 (Batch Filler) N.HR

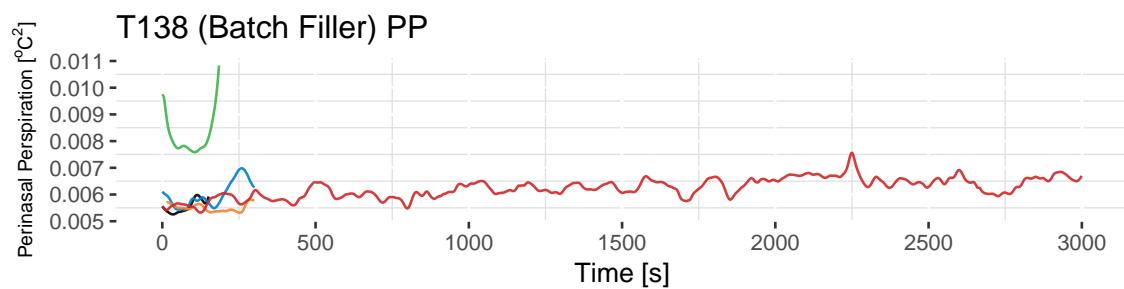


```
##  
##  
## Subject T136 does not have PP signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```

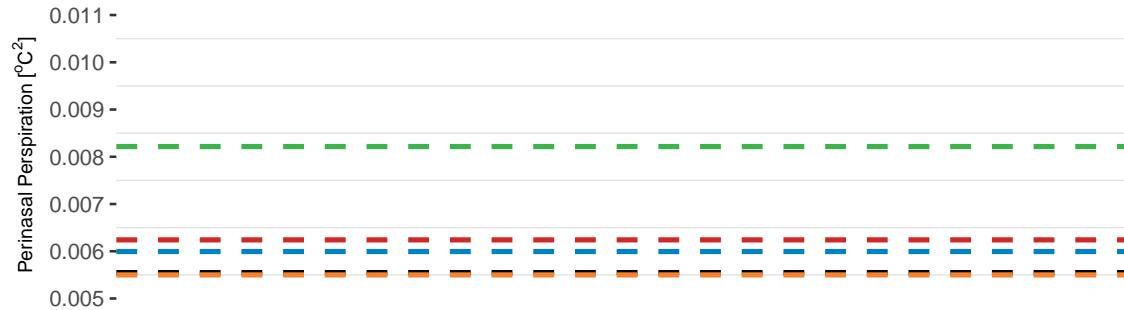




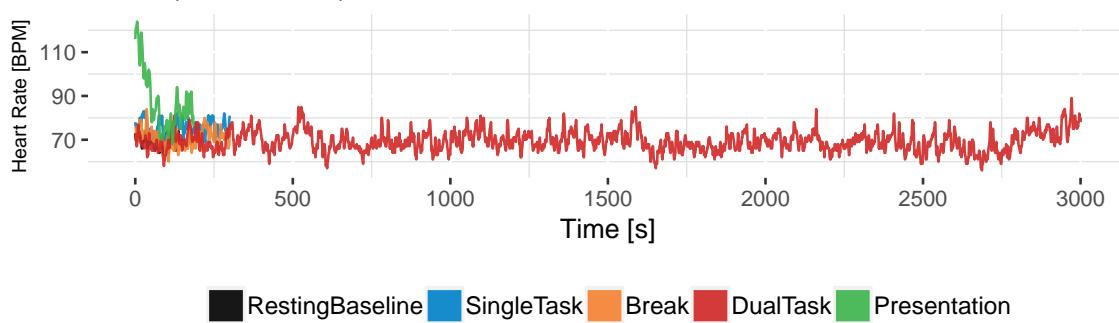
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

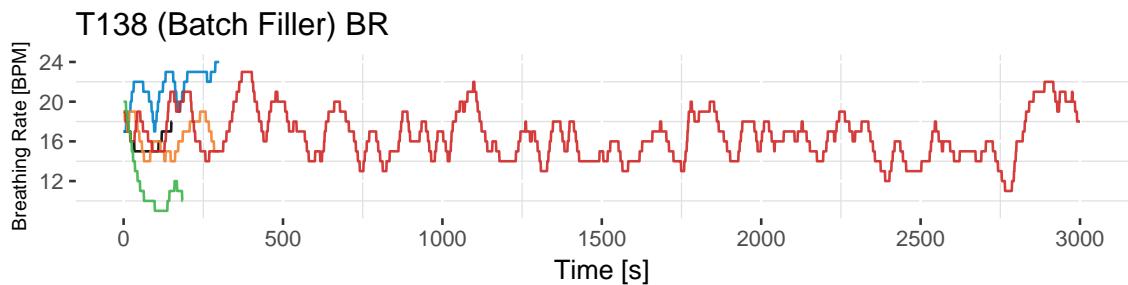


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

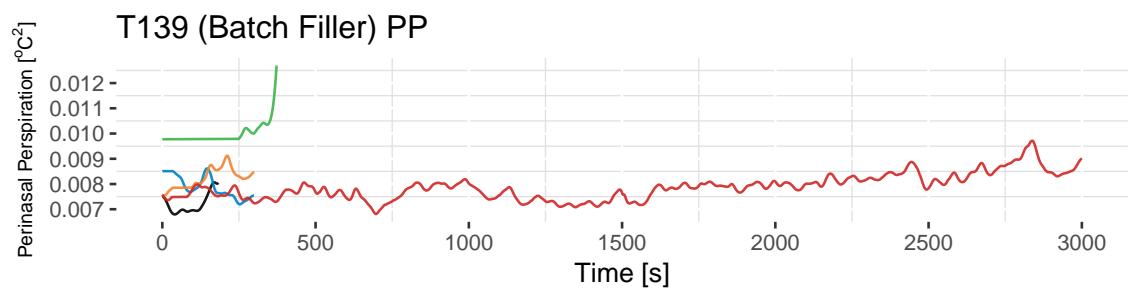
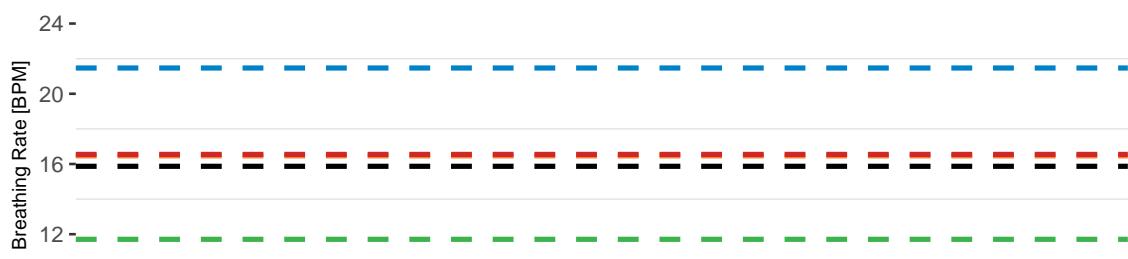


T138 (Batch Filler) HR

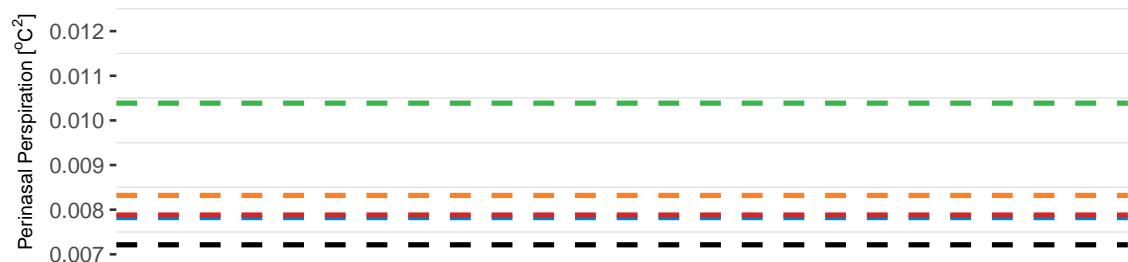




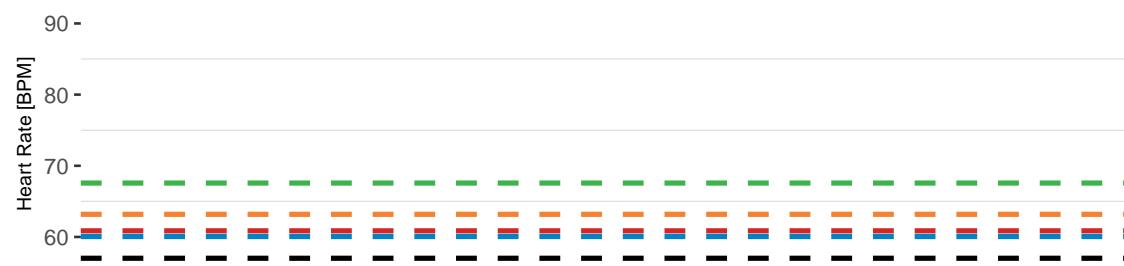
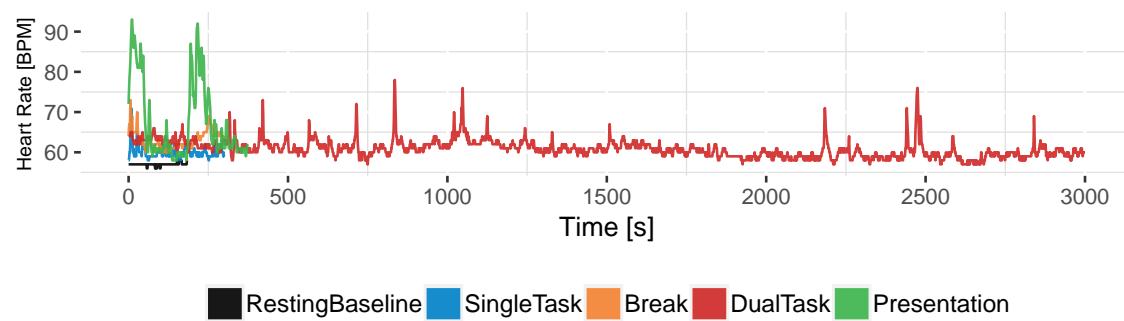
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

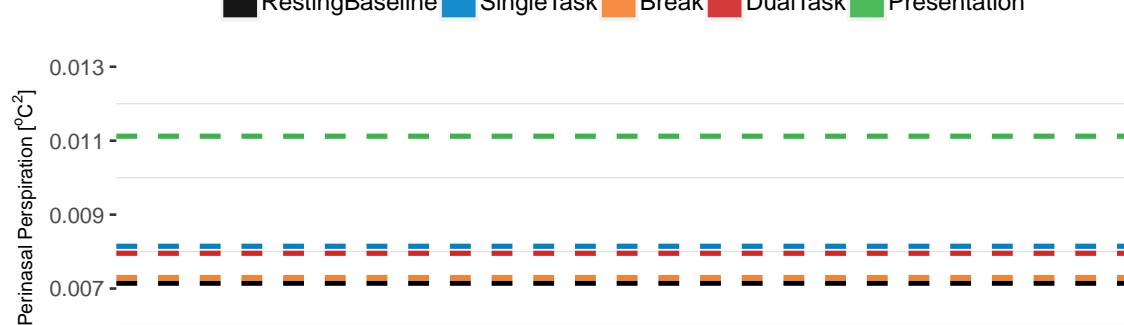
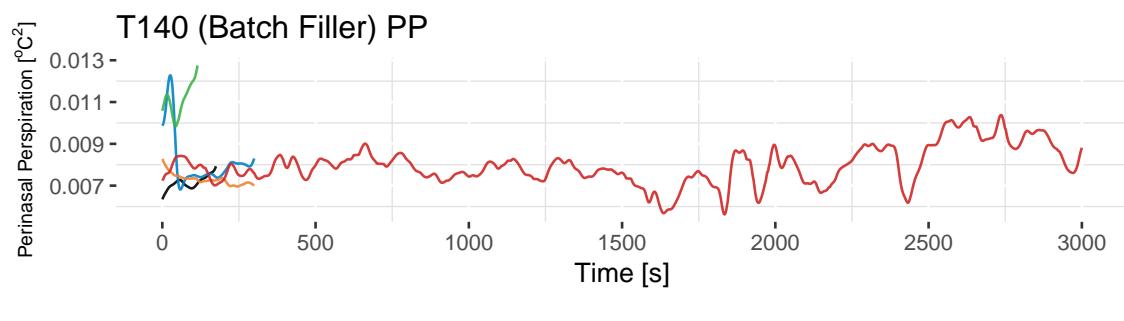
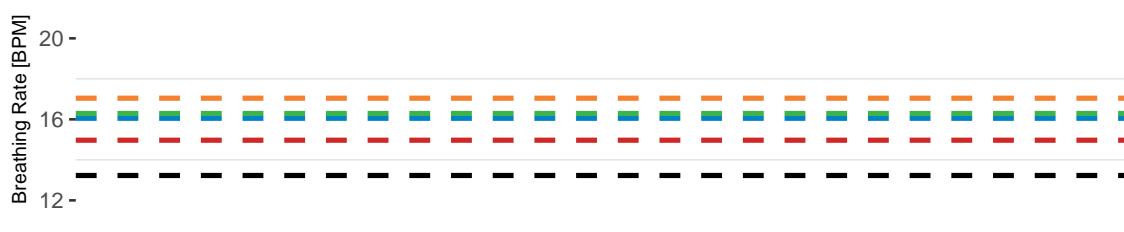
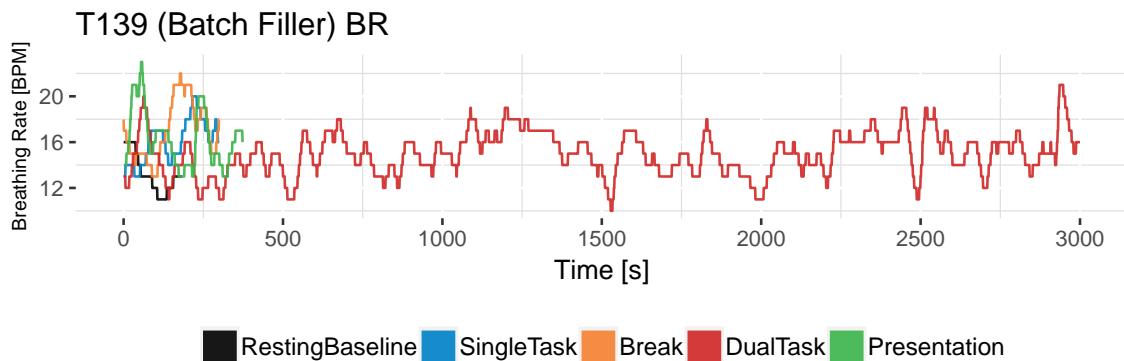


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

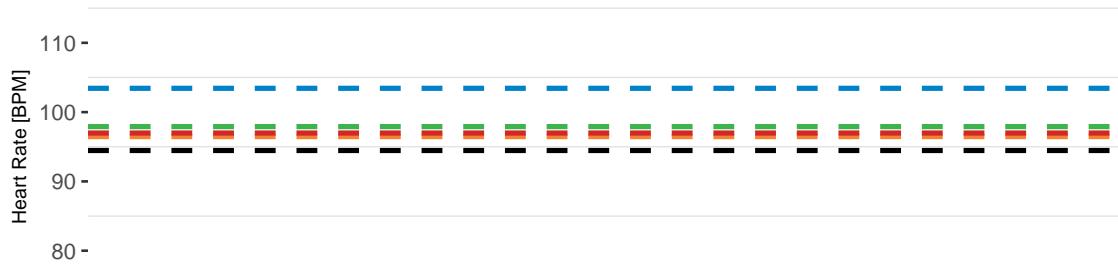
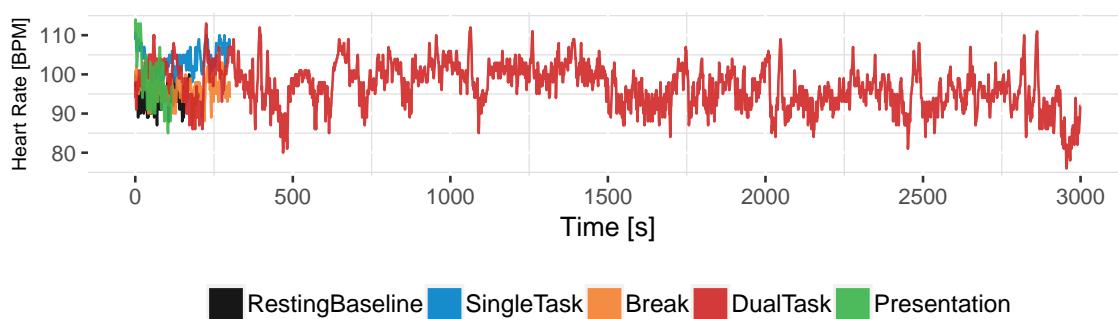


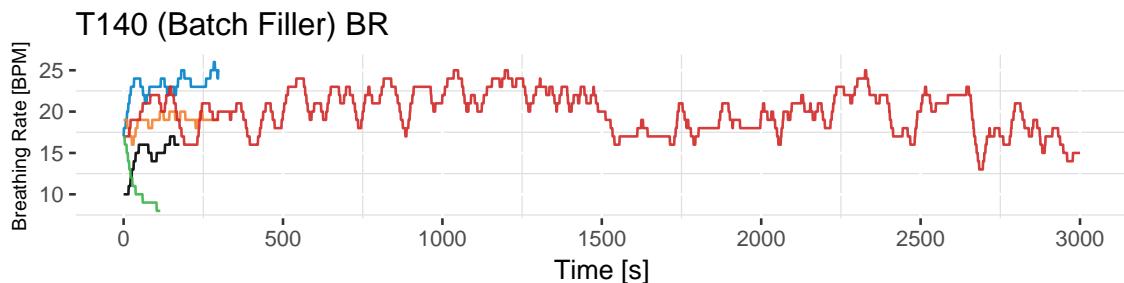
T139 (Batch Filler) HR



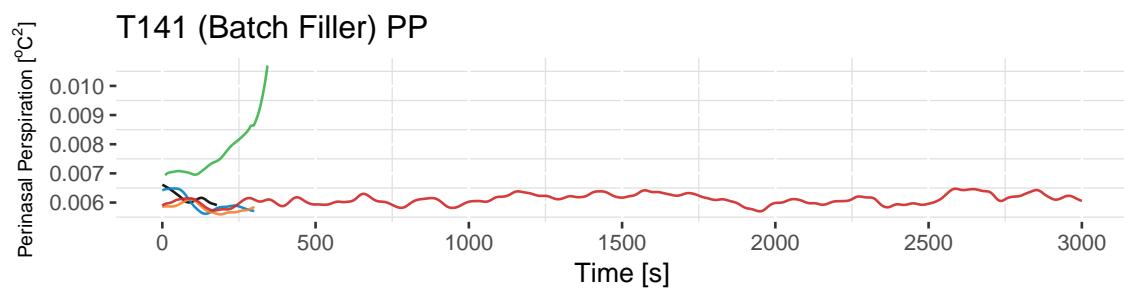


T140 (Batch Filler) HR





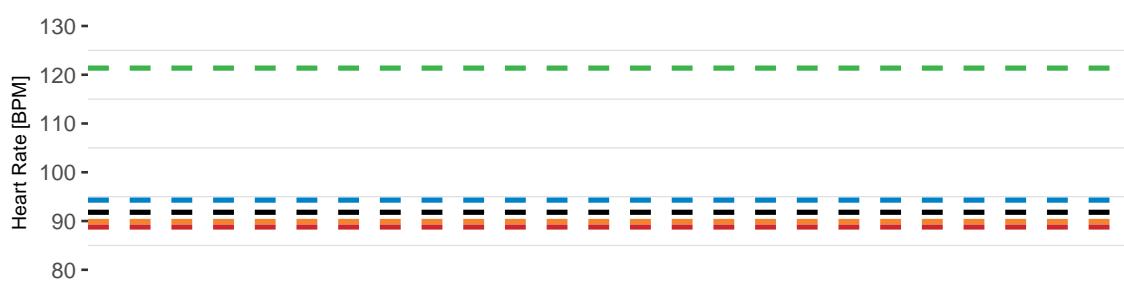
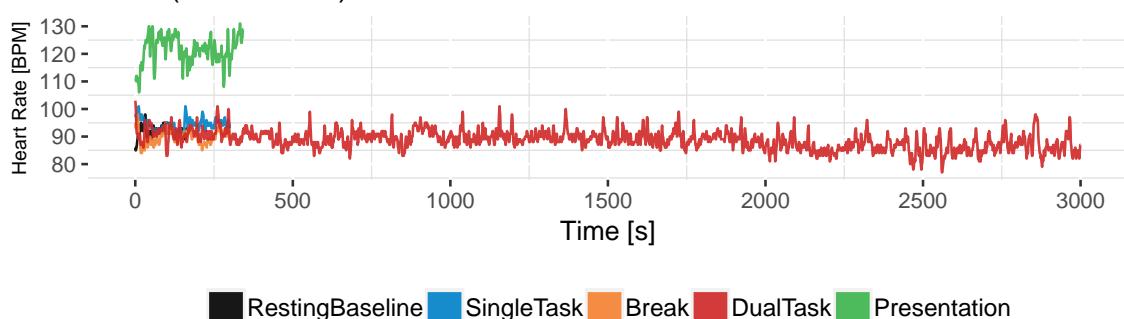
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

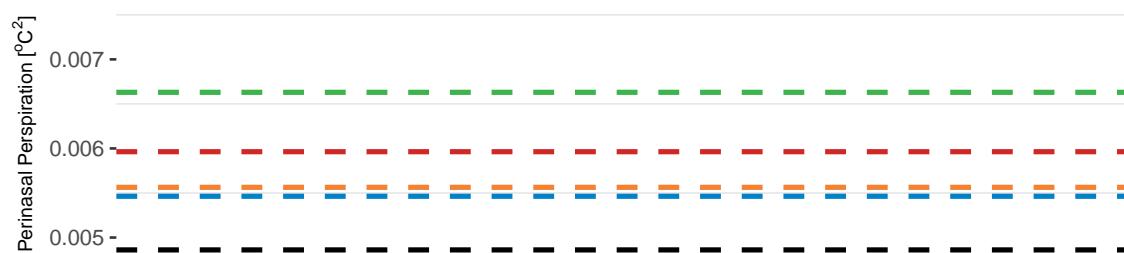
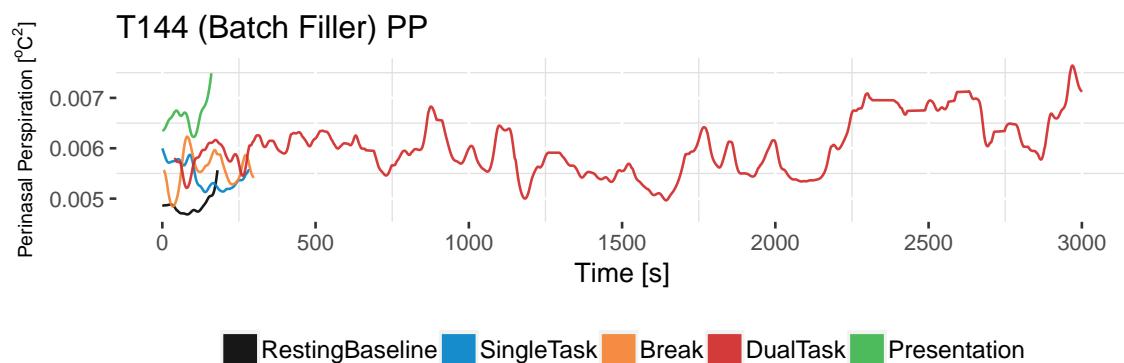
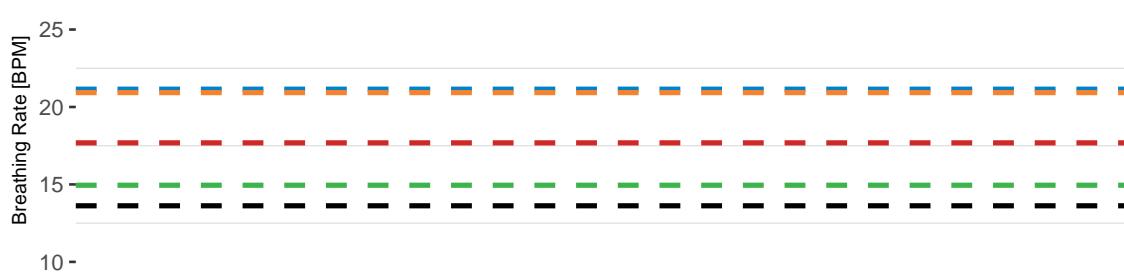
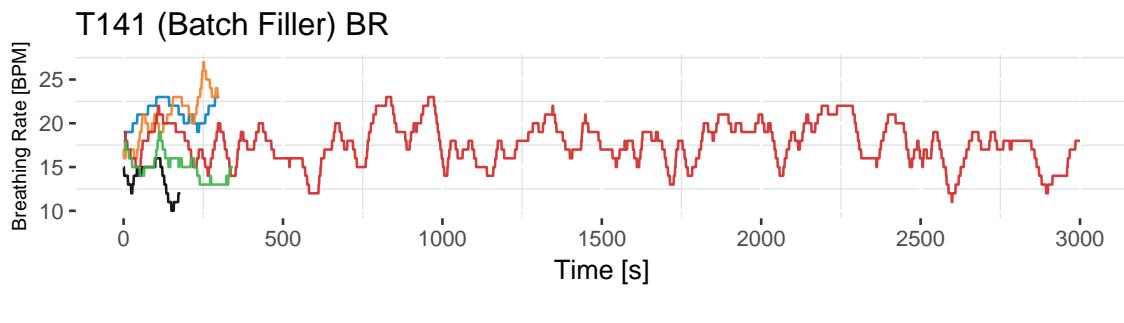


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

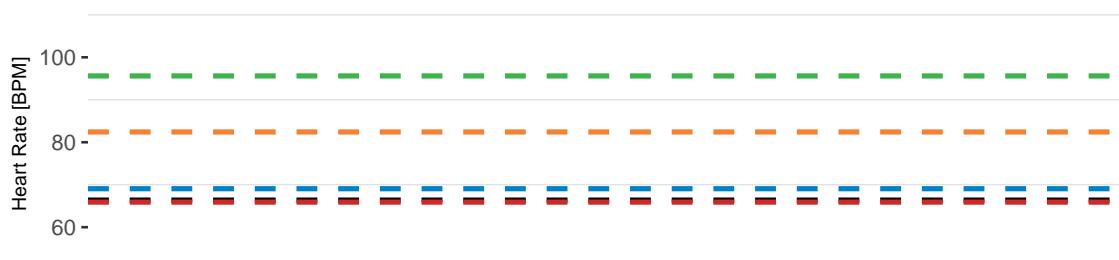
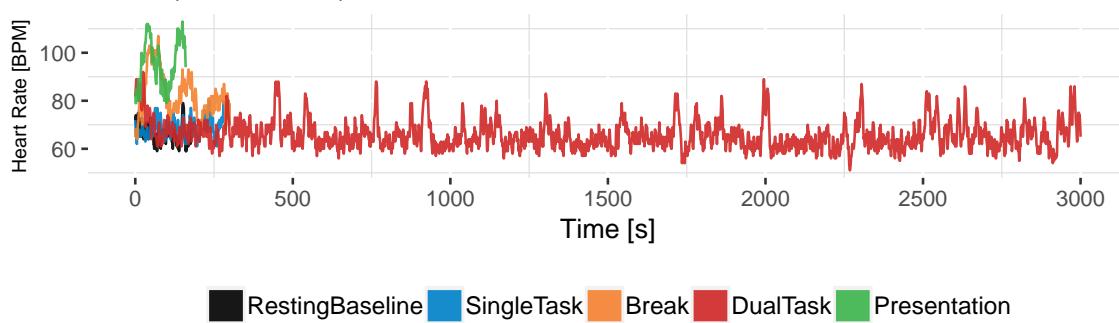


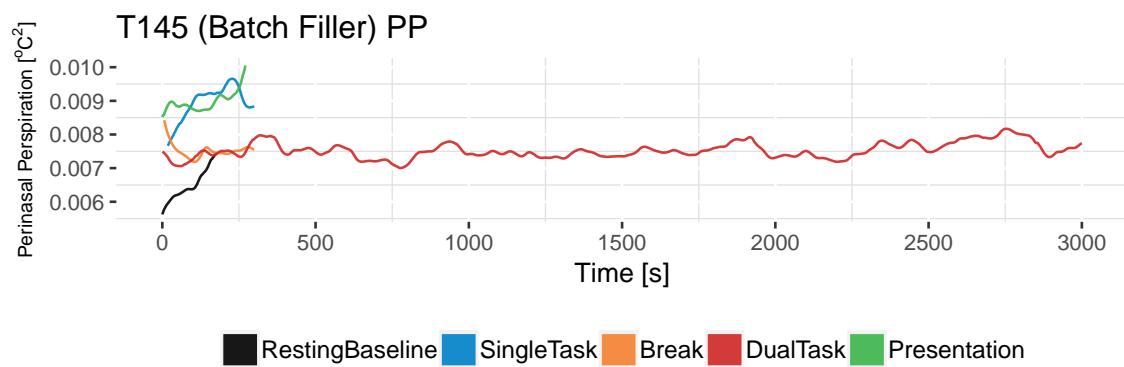
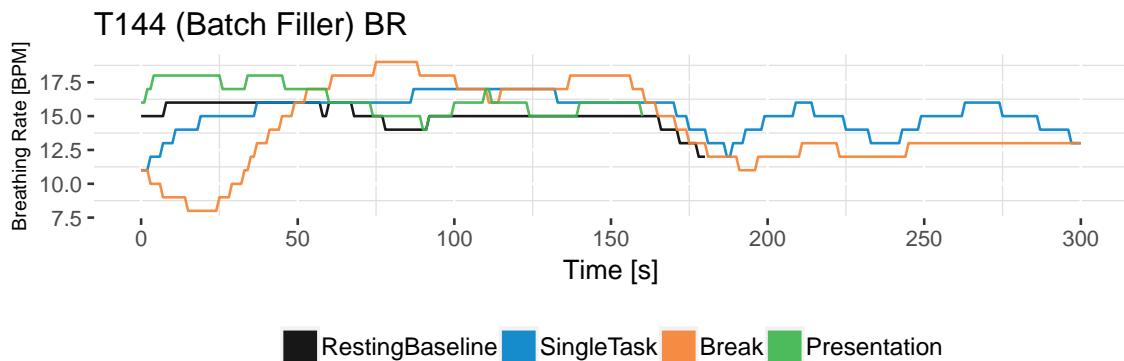
T141 (Batch Filler) HR



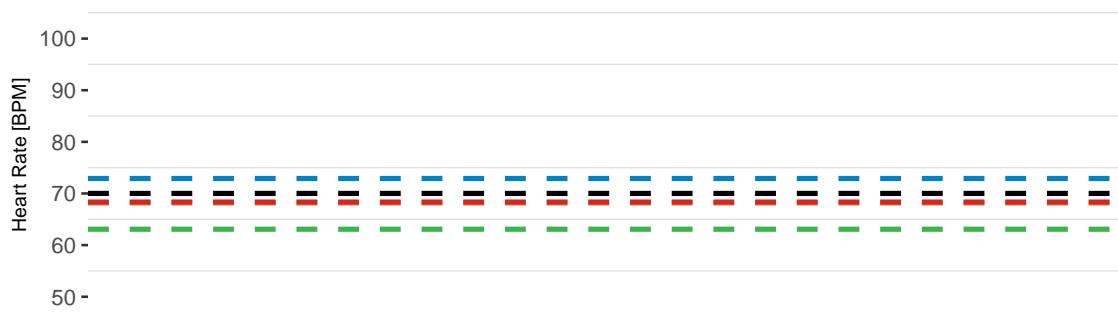
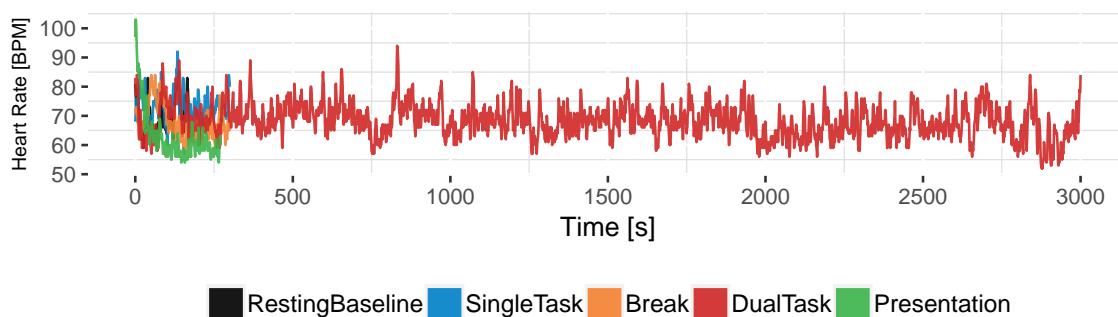


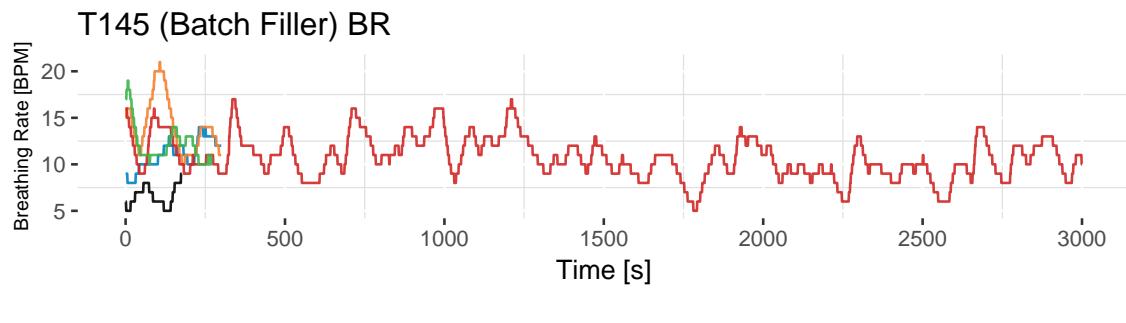
T144 (Batch Filler) HR



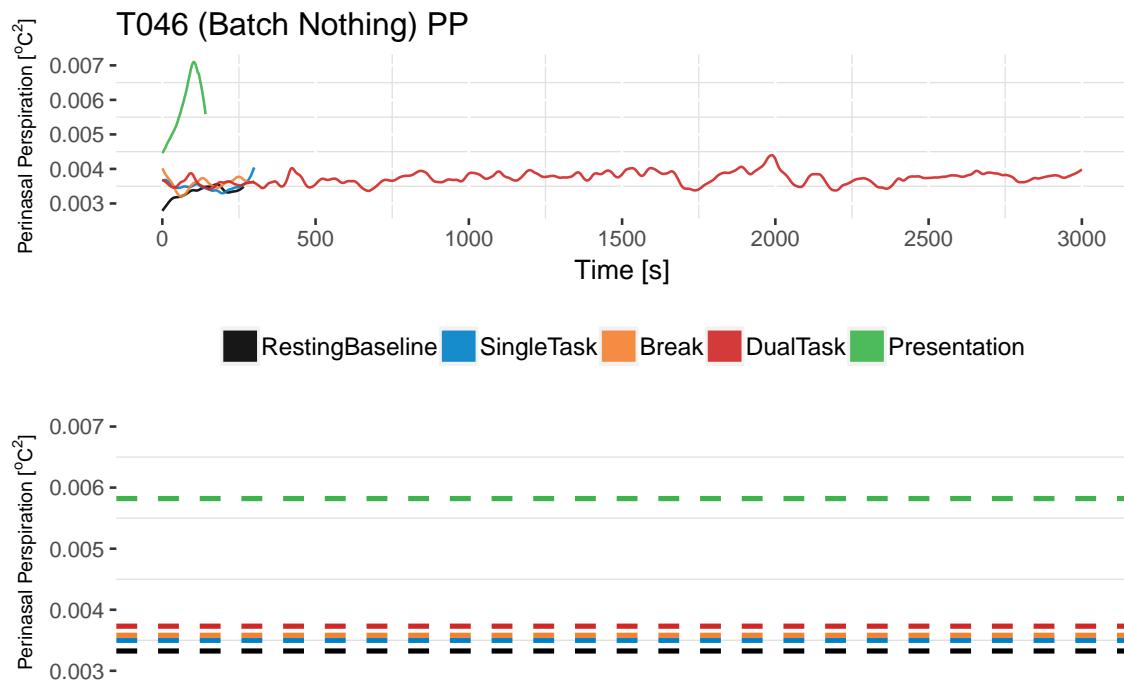


T145 (Batch Filler) HR

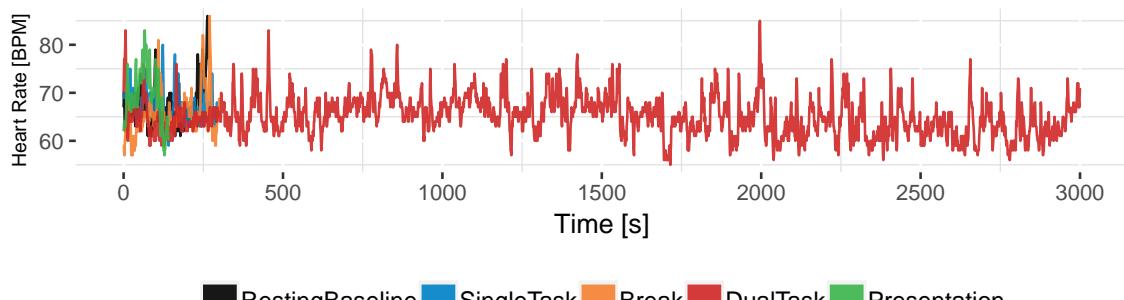




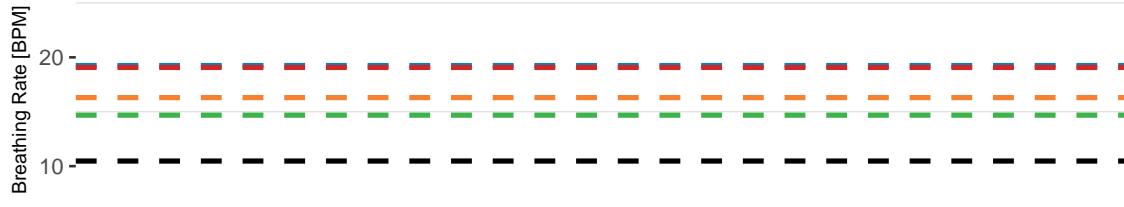
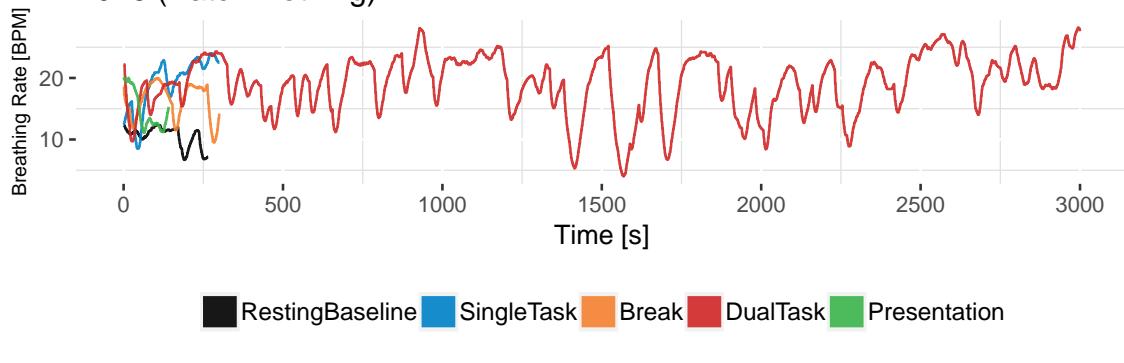
Batch-Nothing (BN) Subjects



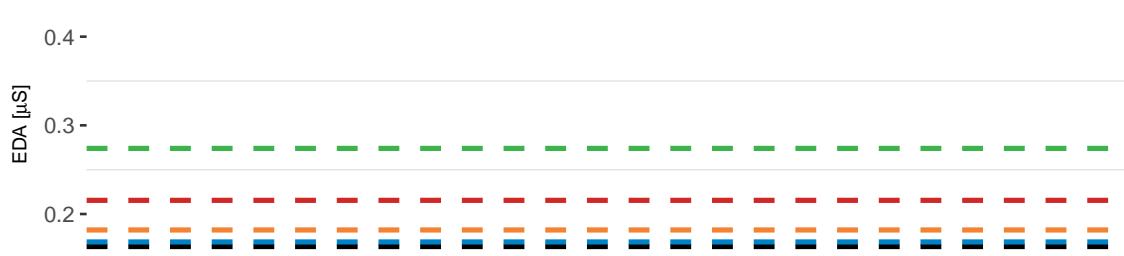
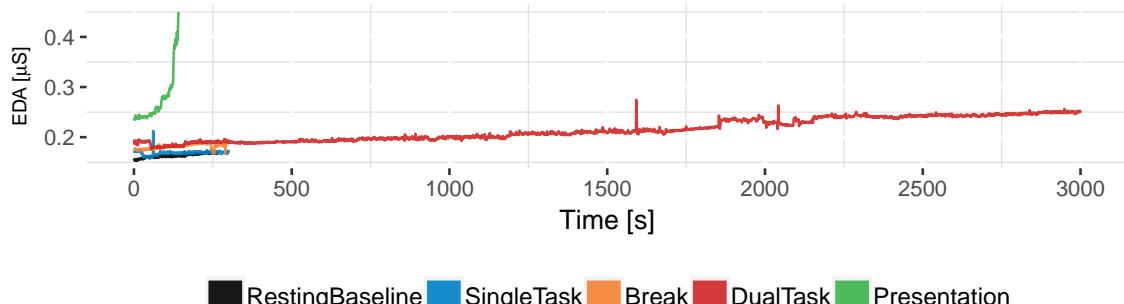
T046 (Batch Nothing) HR



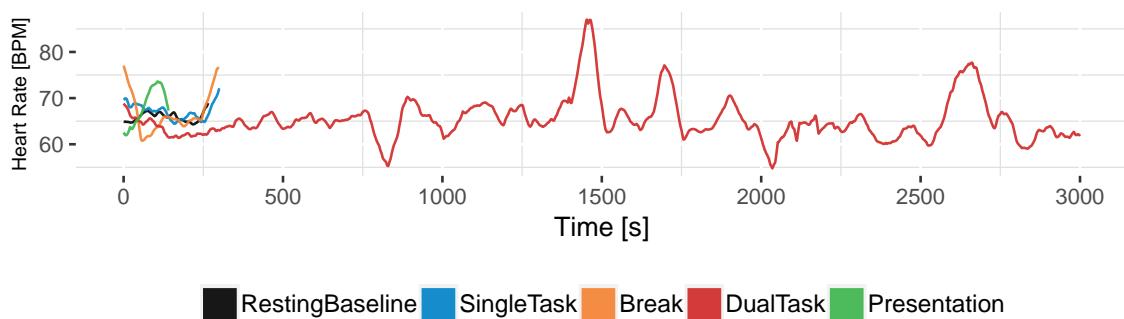
T046 (Batch Nothing) BR



T046 (Batch Nothing) N.EDA

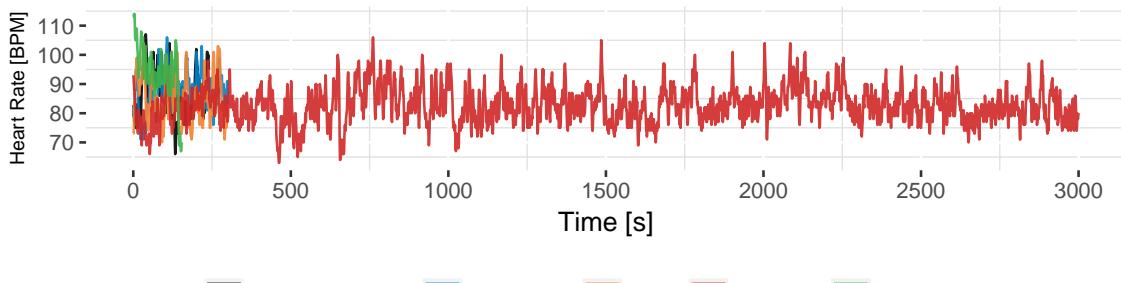


T046 (Batch Nothing) N.HR

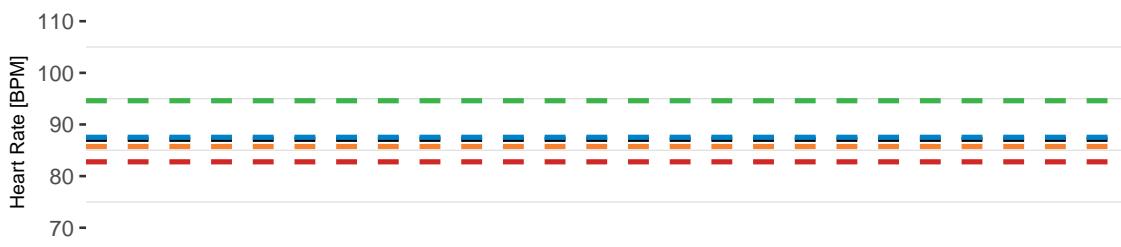




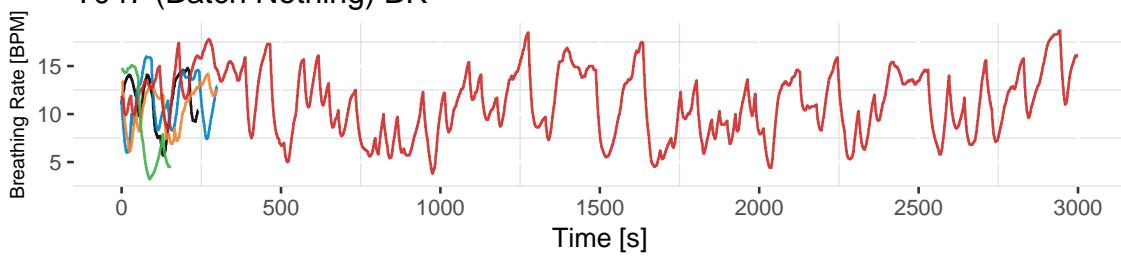
T047 (Batch Nothing) HR



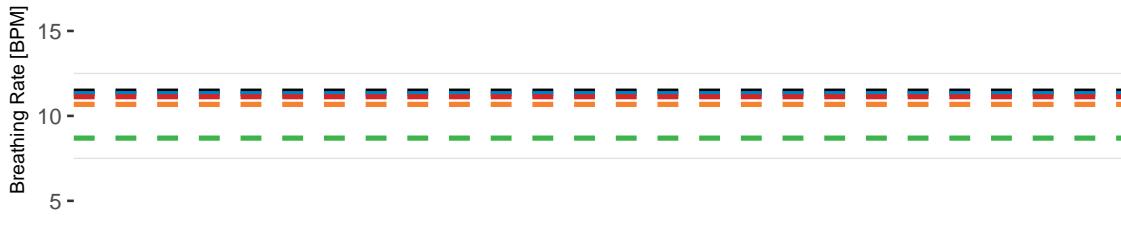
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



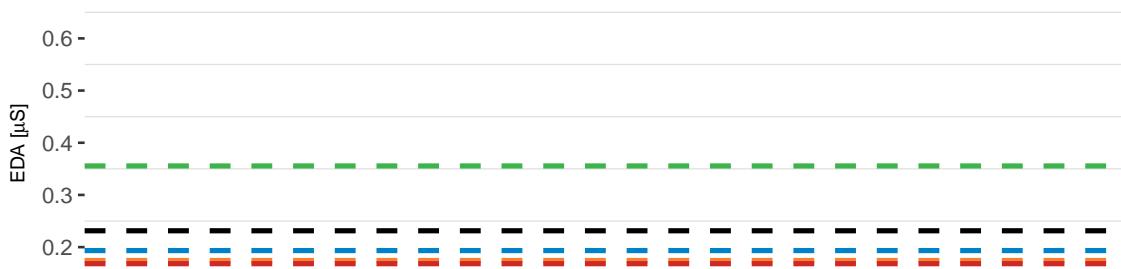
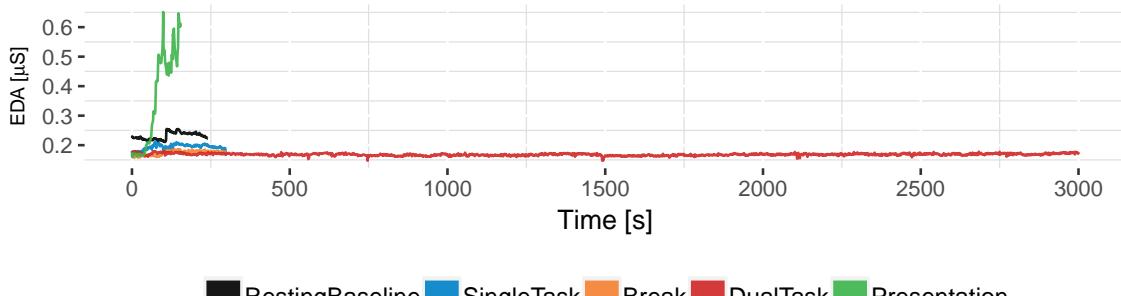
T047 (Batch Nothing) BR



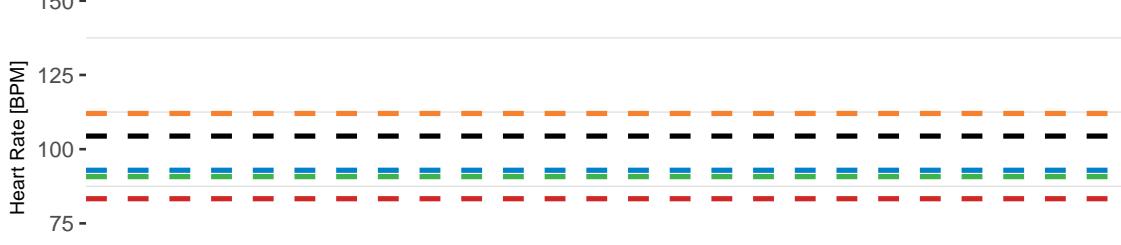
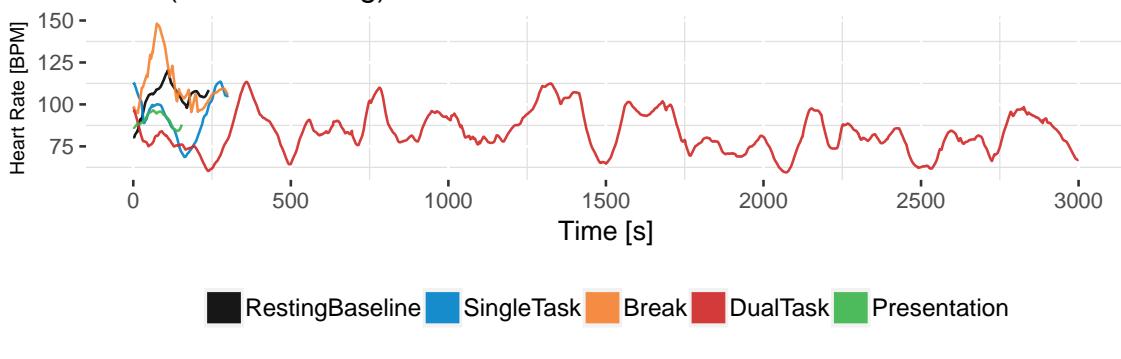
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

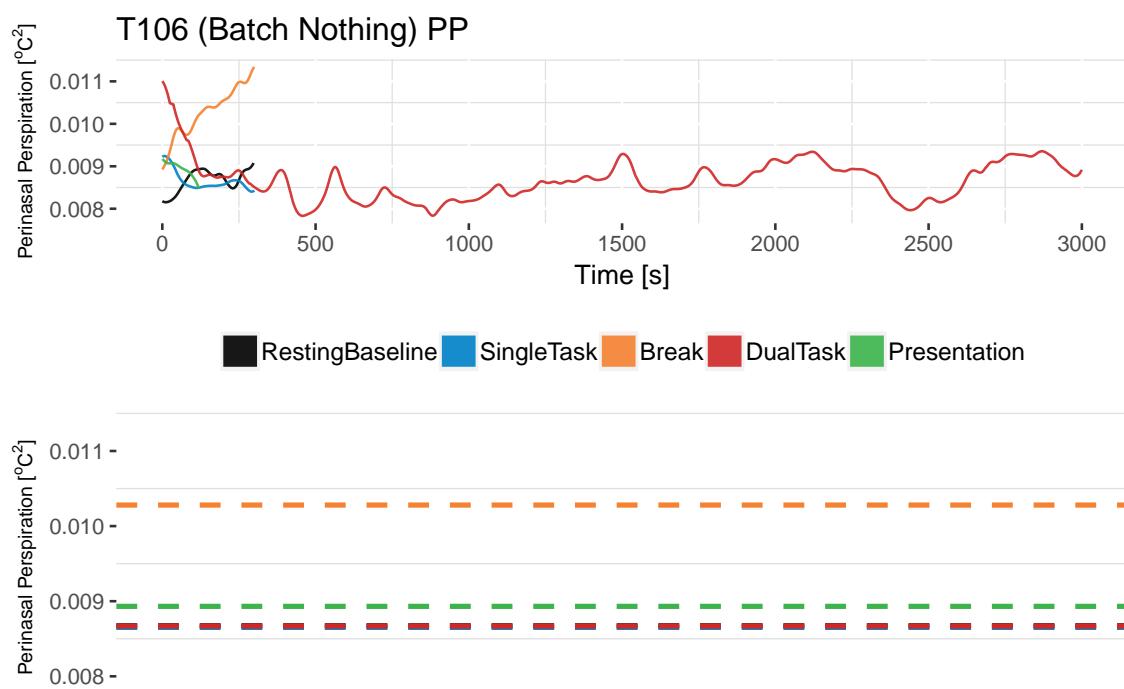


T047 (Batch Nothing) N.EDA

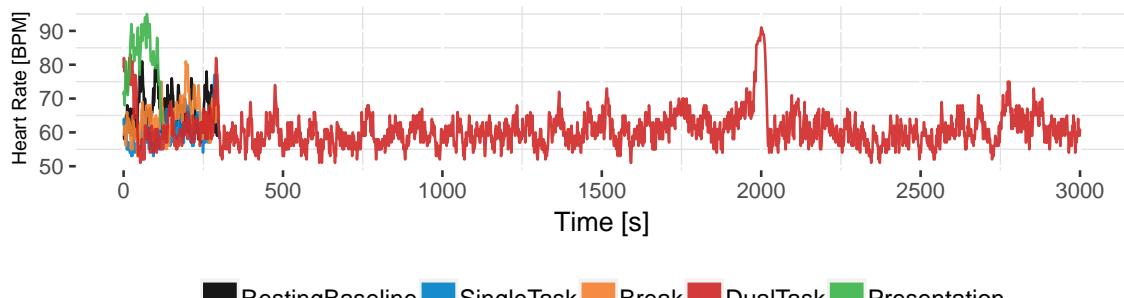


T047 (Batch Nothing) N.HR

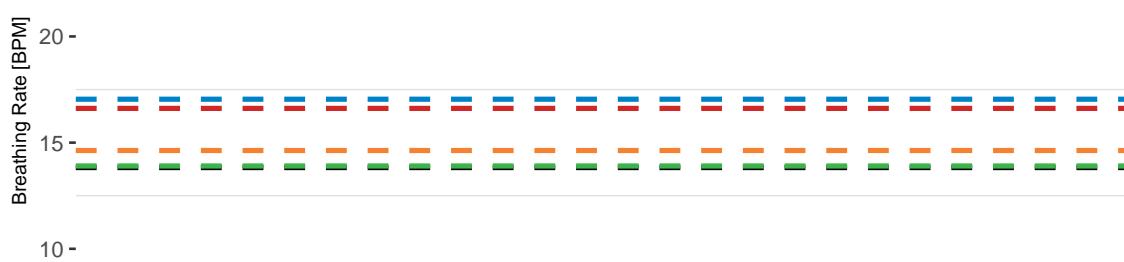
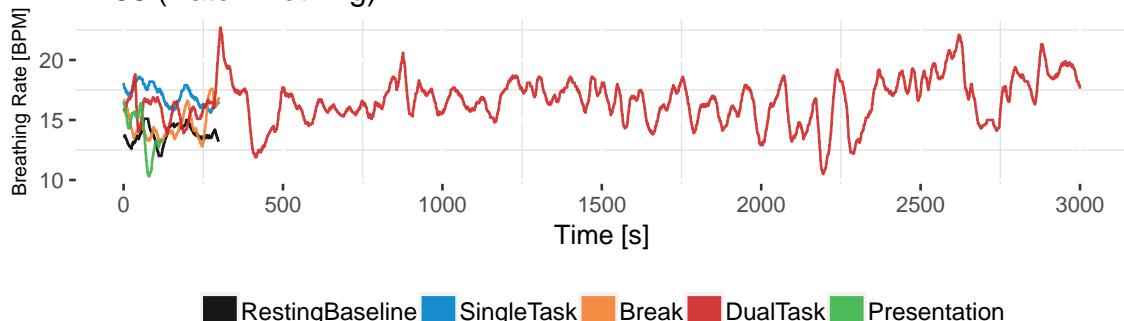




T106 (Batch Nothing) HR



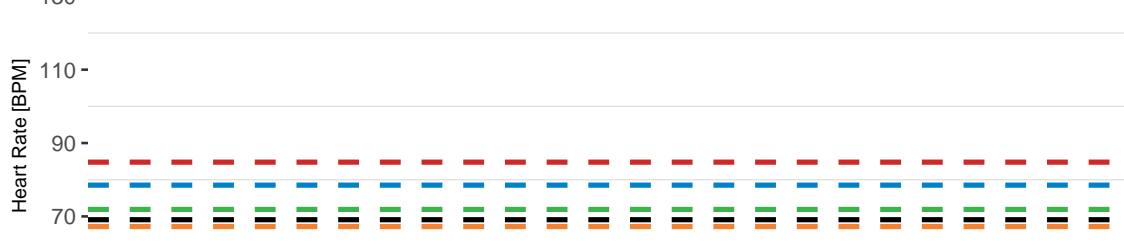
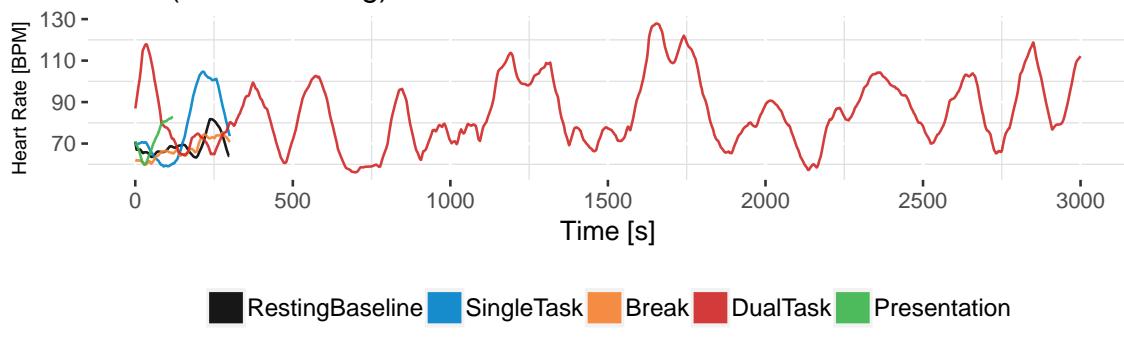
T106 (Batch Nothing) BR



T106 (Batch Nothing) N.EDA



T106 (Batch Nothing) N.HR



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## Subject T108 does not have PP signals.  

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## Subject T108 does not have HR signals.  

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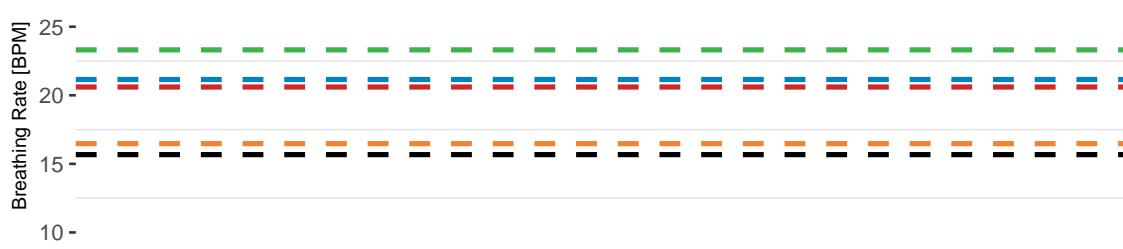
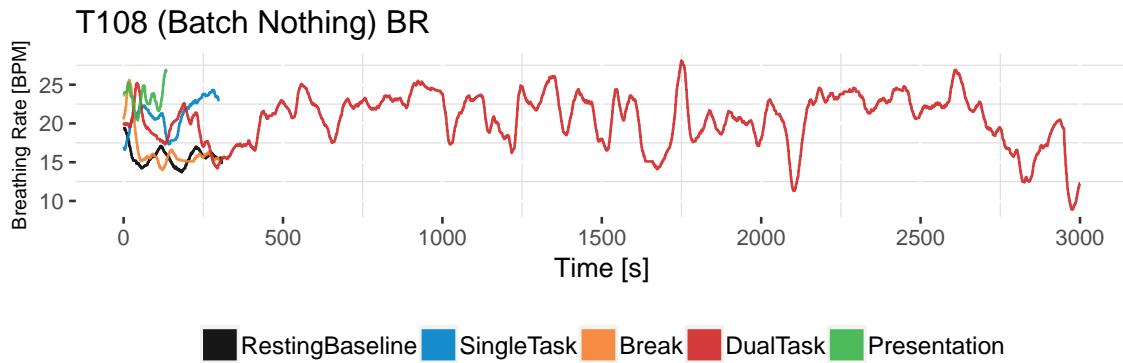
##  

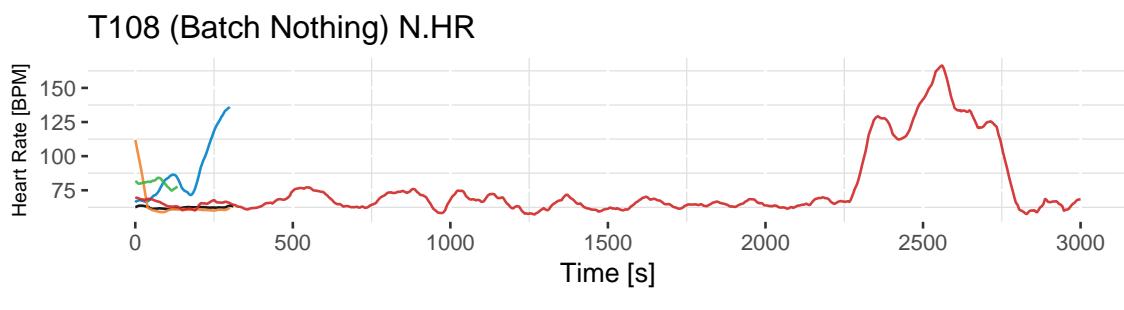
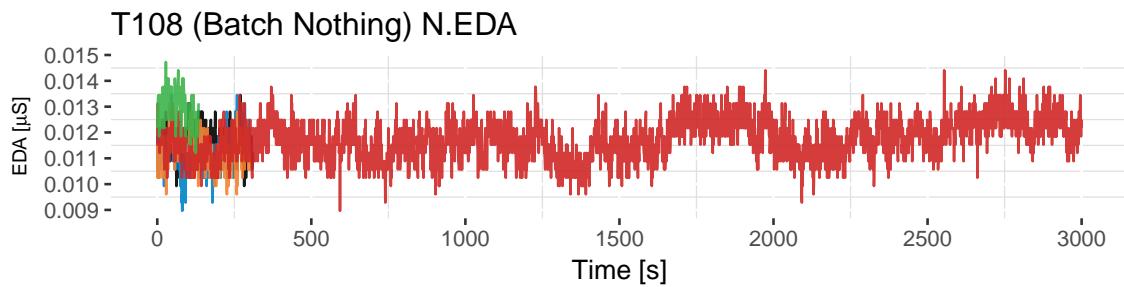
##  

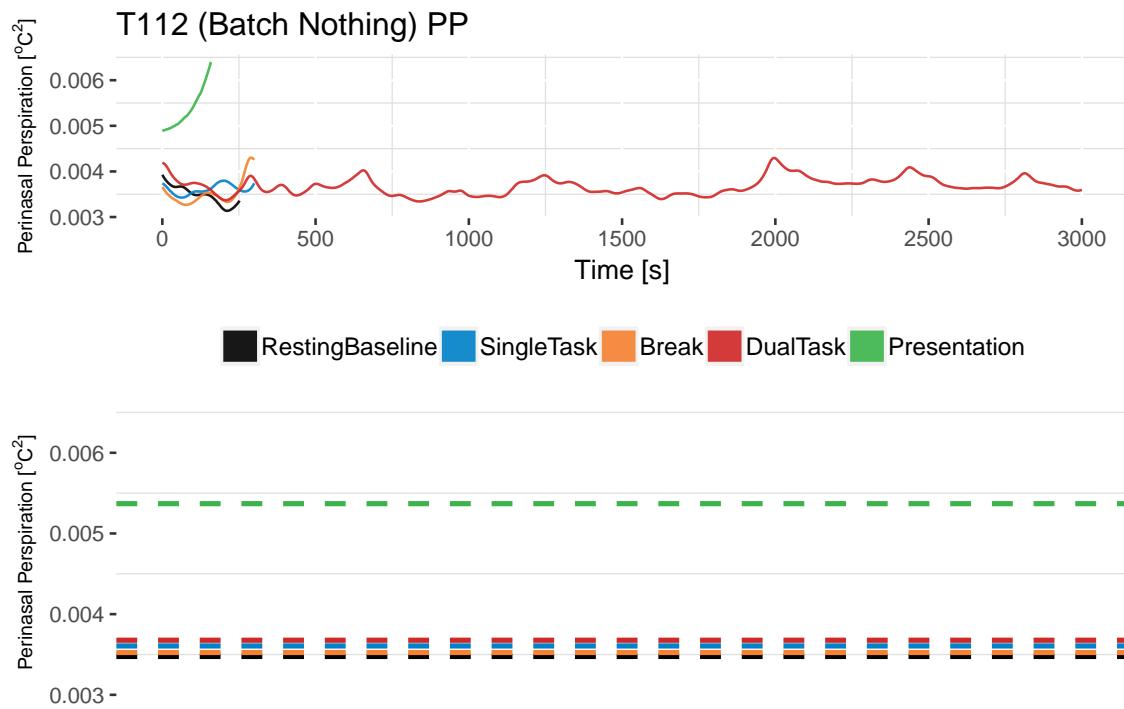
##  

##  

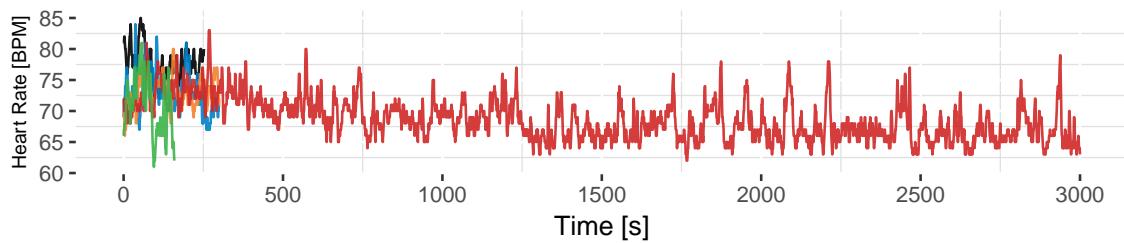
##
```



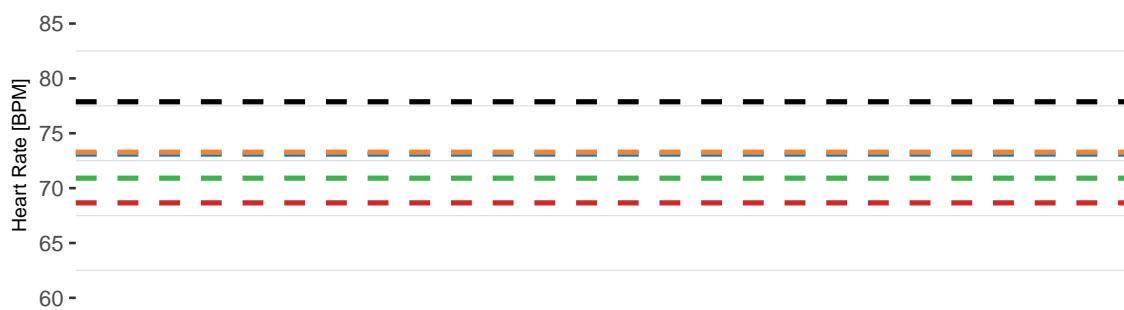




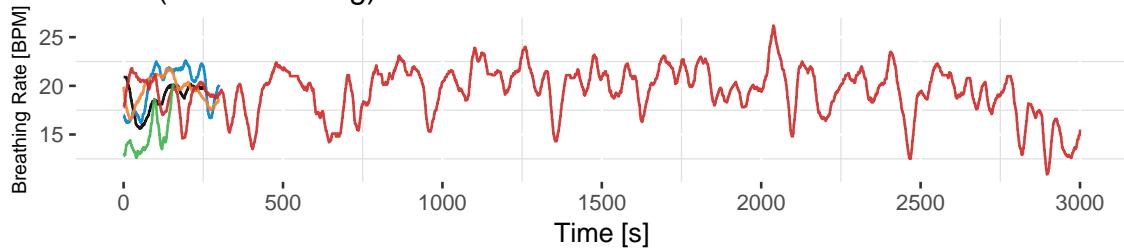
T112 (Batch Nothing) HR



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



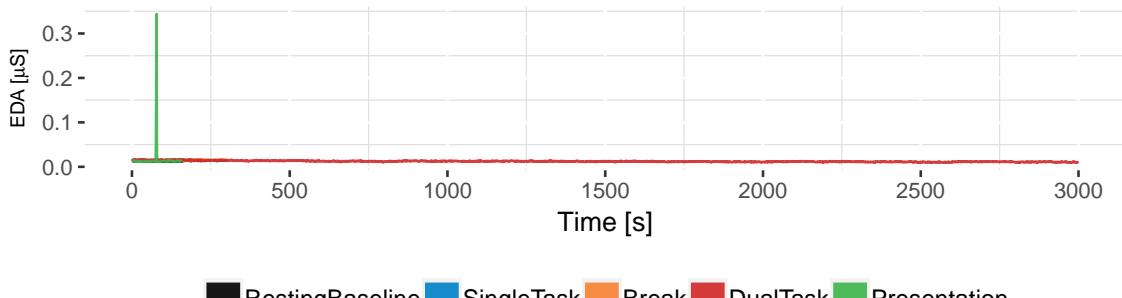
T112 (Batch Nothing) BR



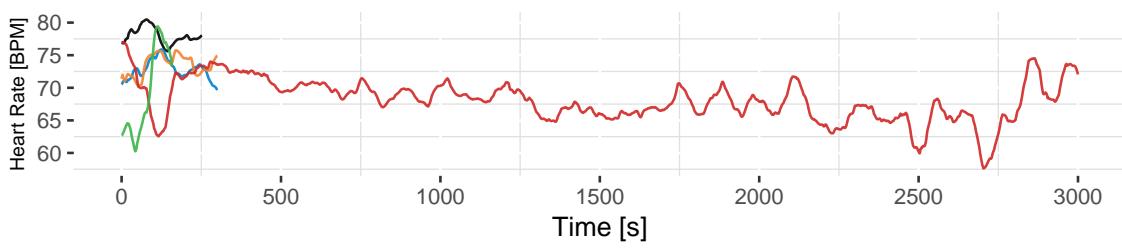
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



T112 (Batch Nothing) N.EDA



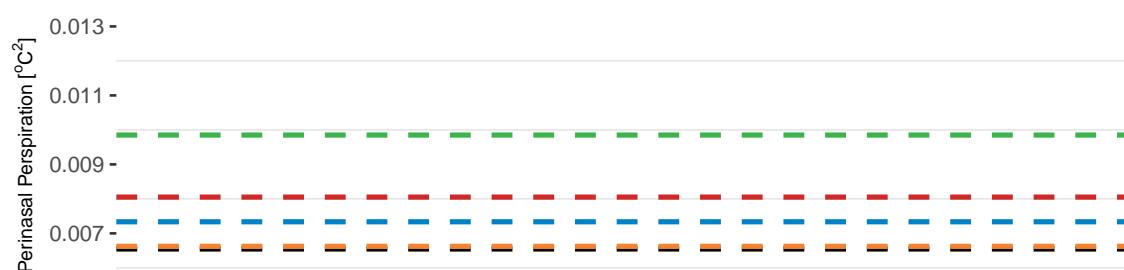
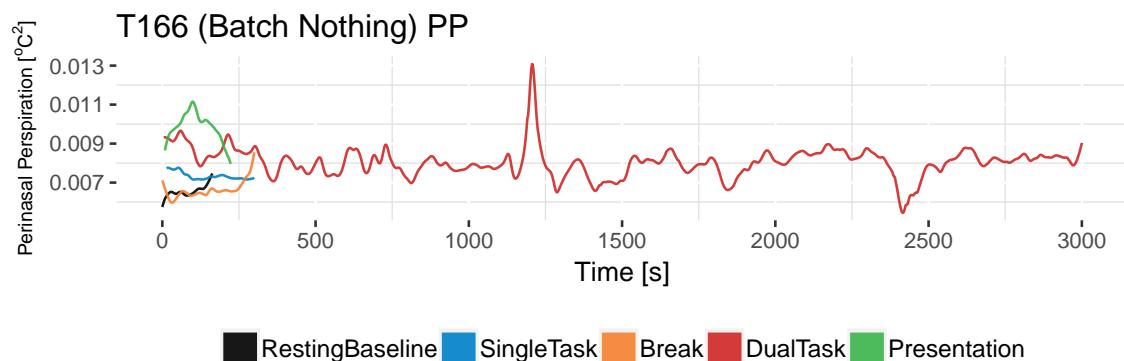
T112 (Batch Nothing) N.HR



Legend:

- RestingBaseline (black square)
- SingleTask (blue square)
- Break (orange square)
- DualTask (red square)
- Presentation (green square)





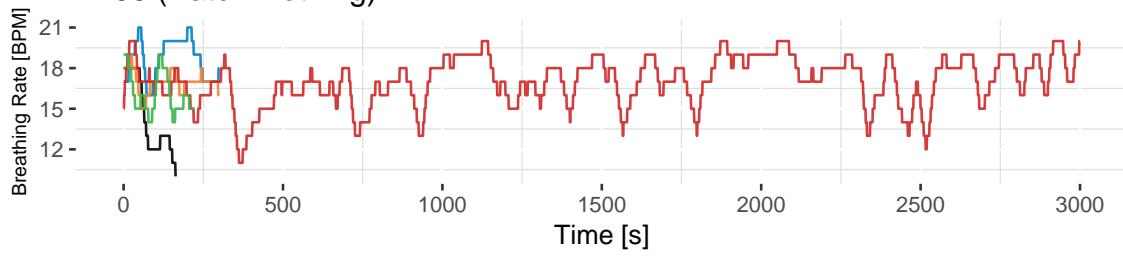
T166 (Batch Nothing) HR



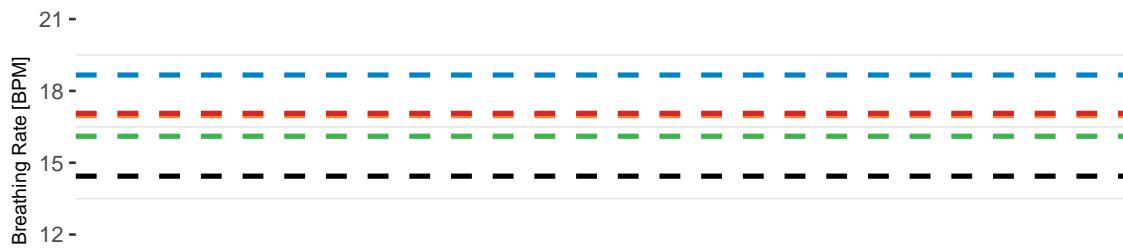
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

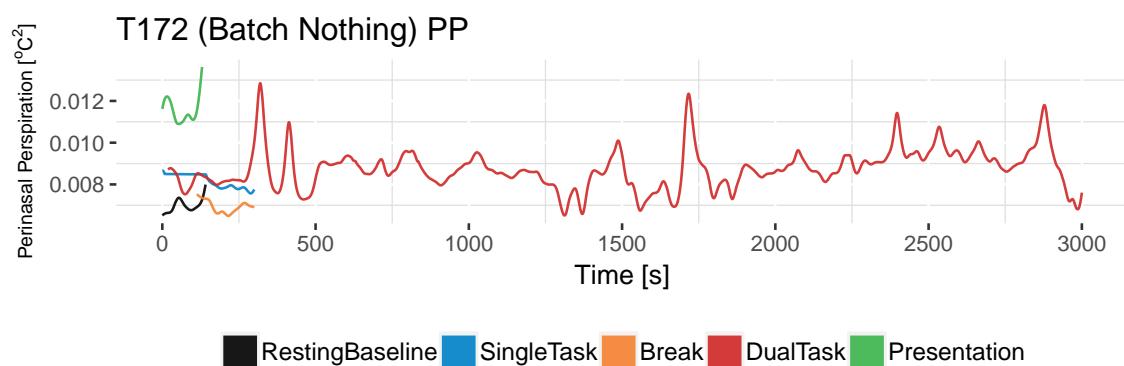


T166 (Batch Nothing) BR

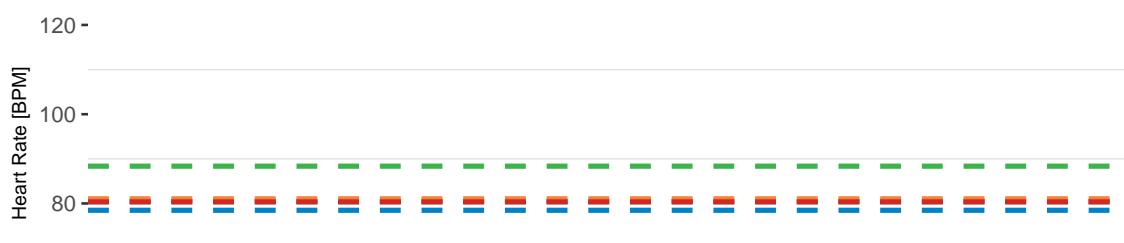
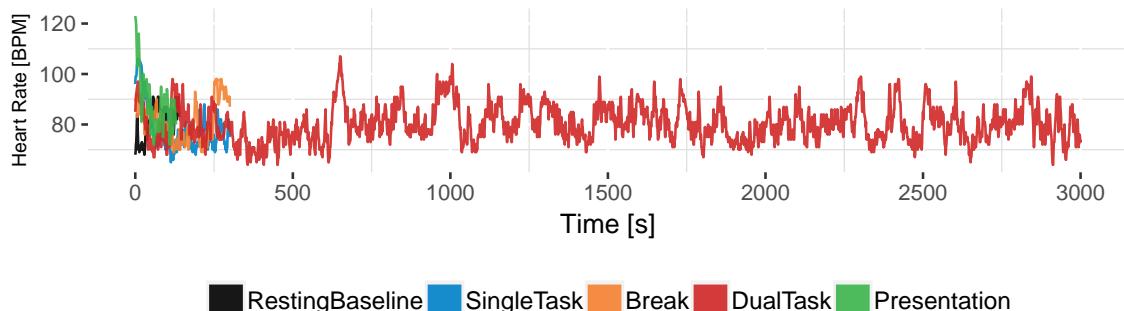


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

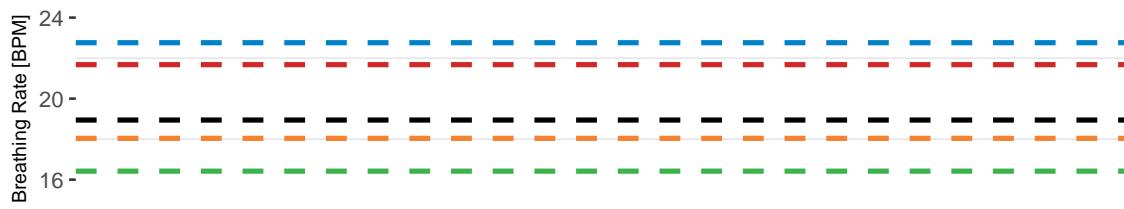
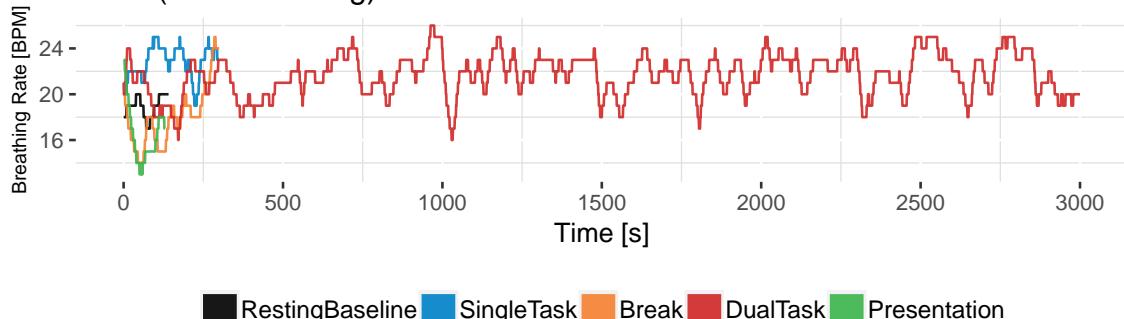




T172 (Batch Nothing) HR

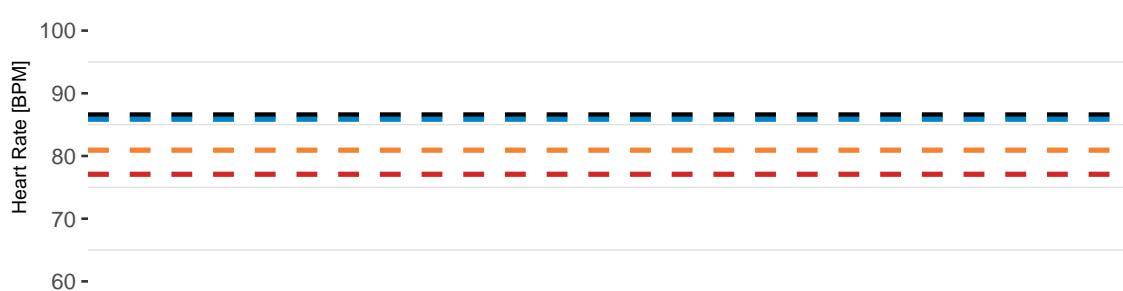
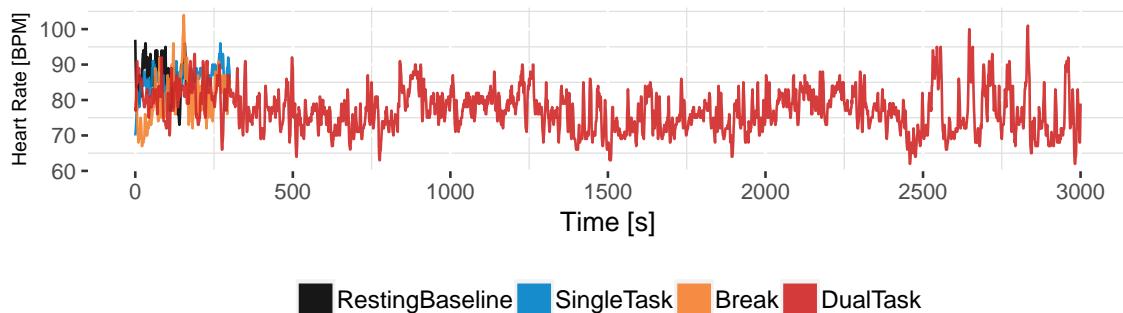


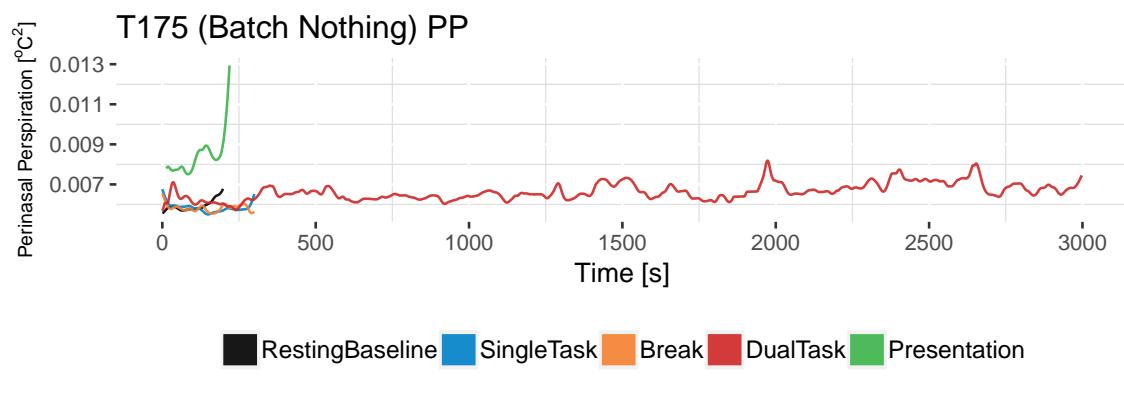
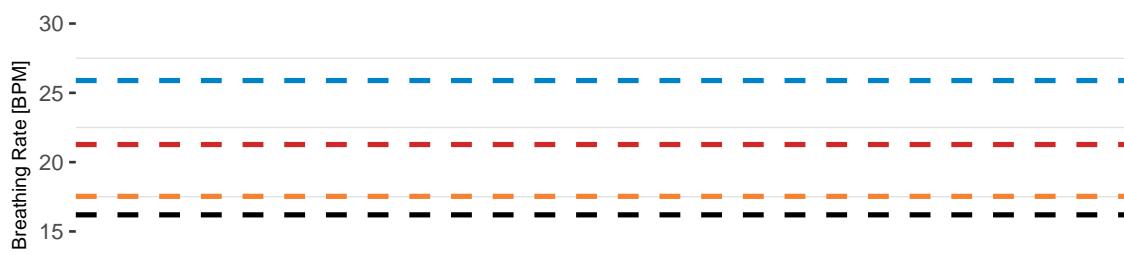
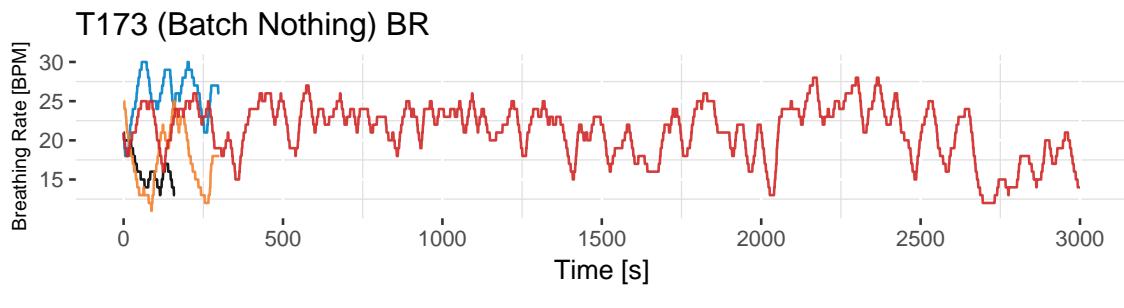
T172 (Batch Nothing) BR



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##  
##  
## Subject T173 does not have PP signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##
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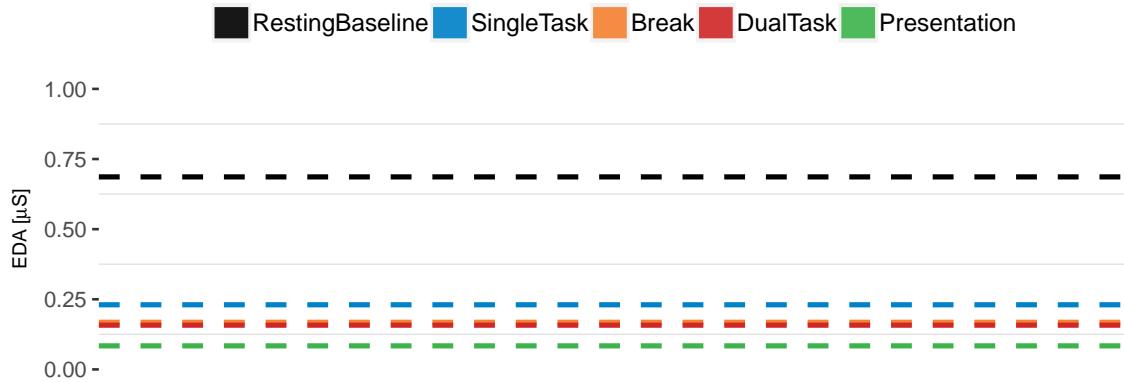
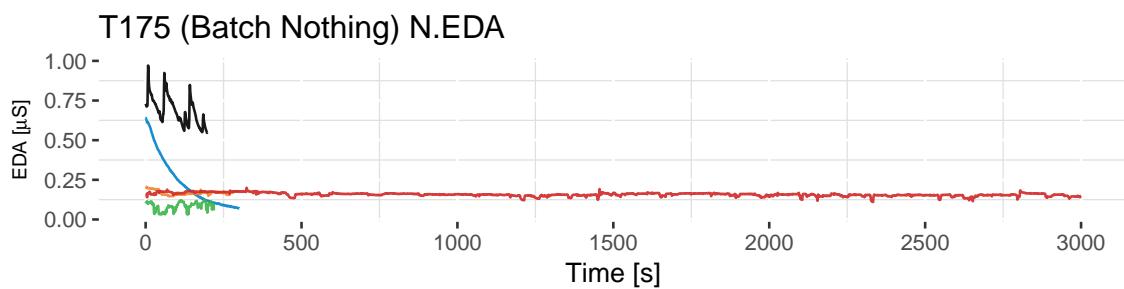
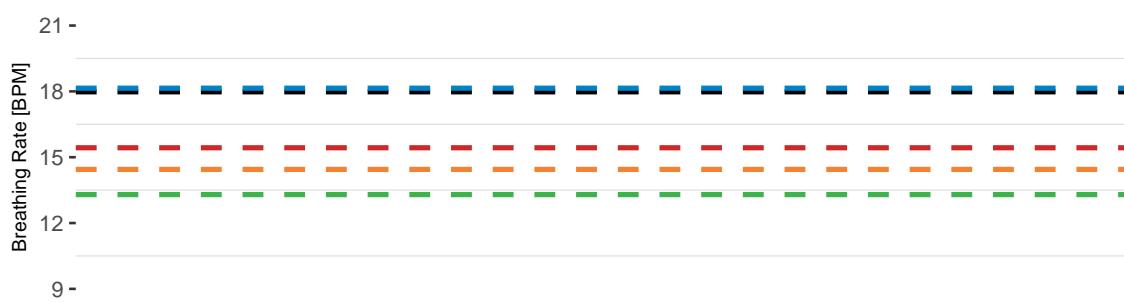
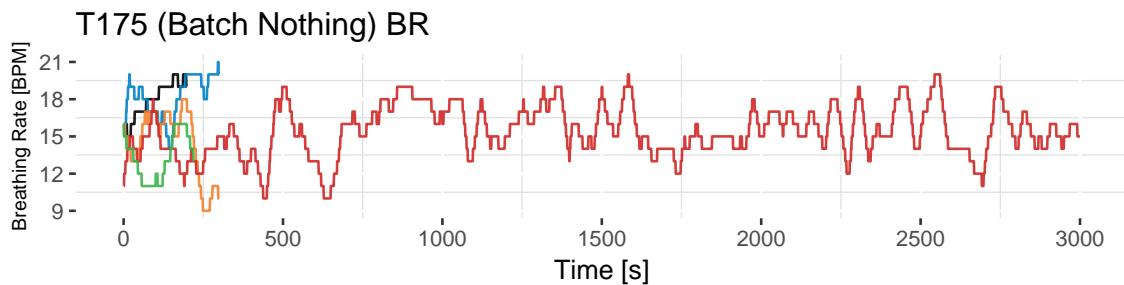
T173 (Batch Nothing) HR



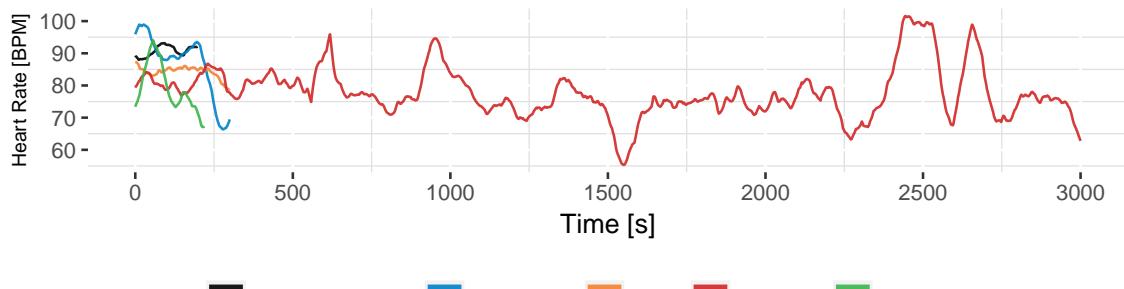


T175 (Batch Nothing) HR

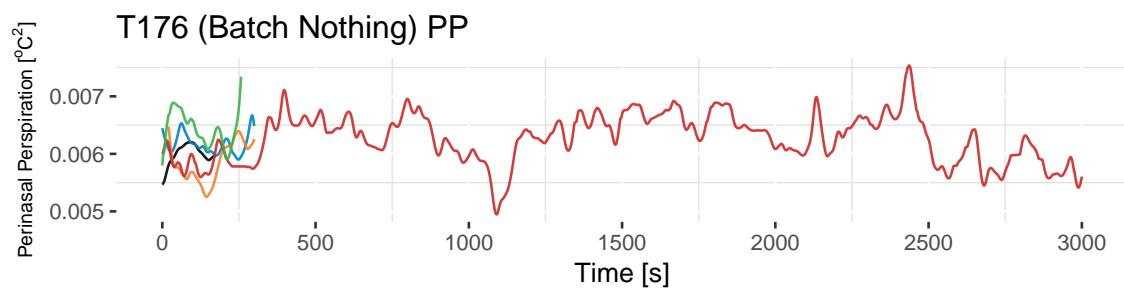




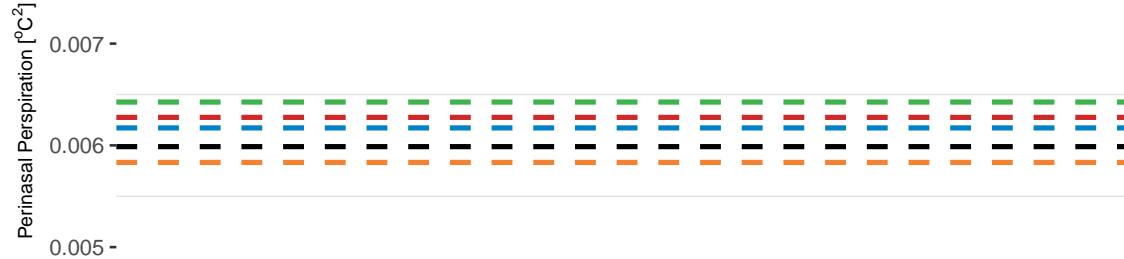
T175 (Batch Nothing) N.HR



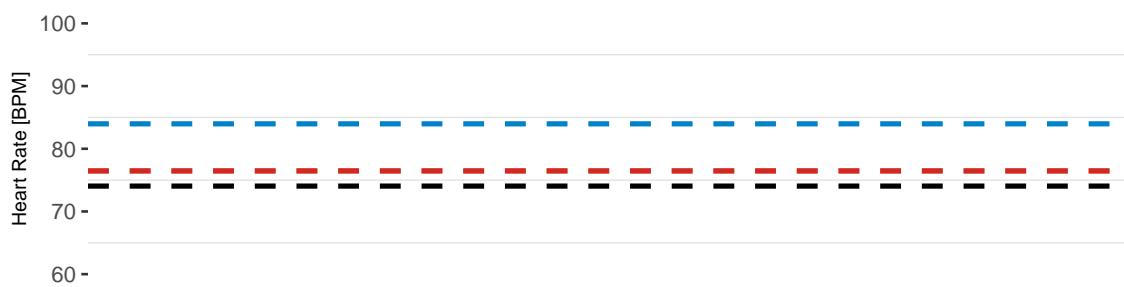
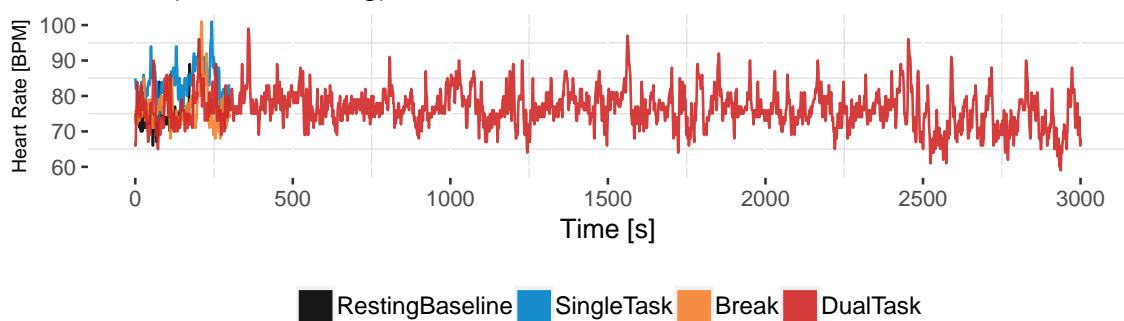
T176 (Batch Nothing) PP

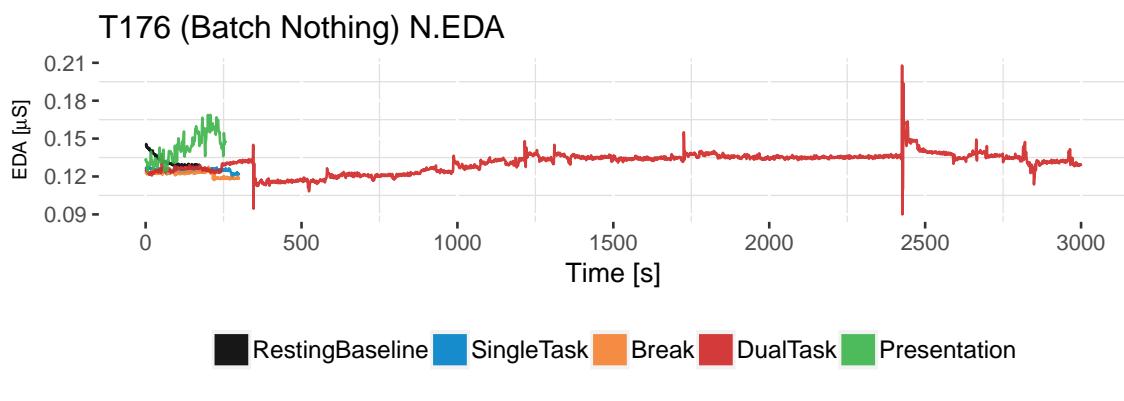
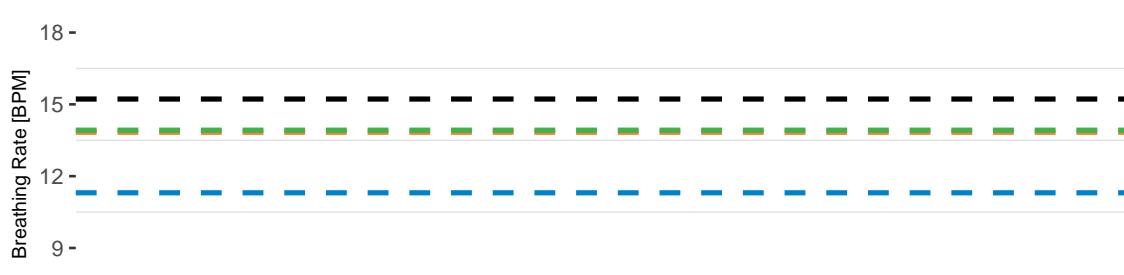
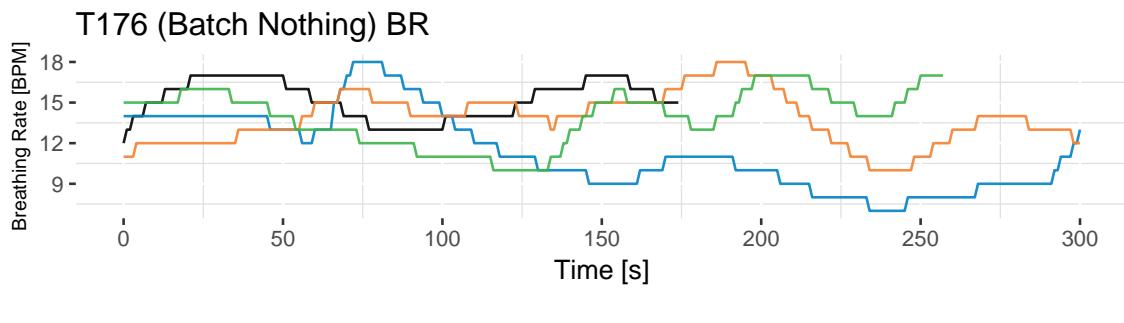


RestingBaseline SingleTask Break DualTask Presentation

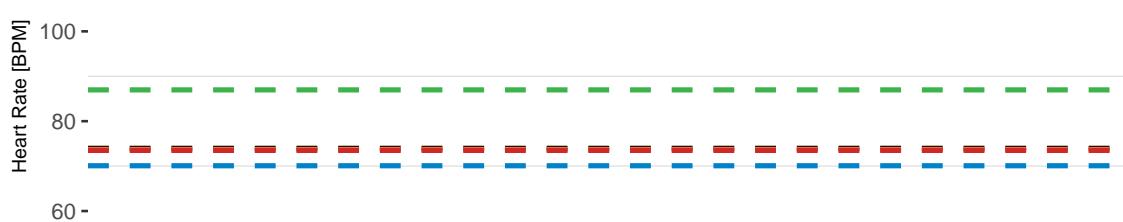
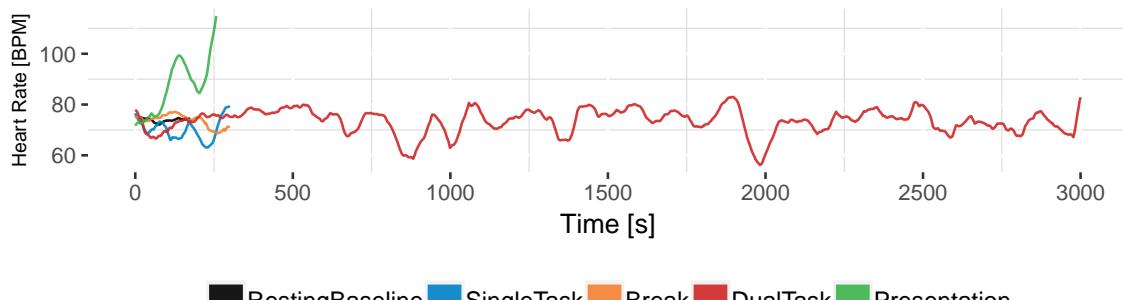


T176 (Batch Nothing) HR

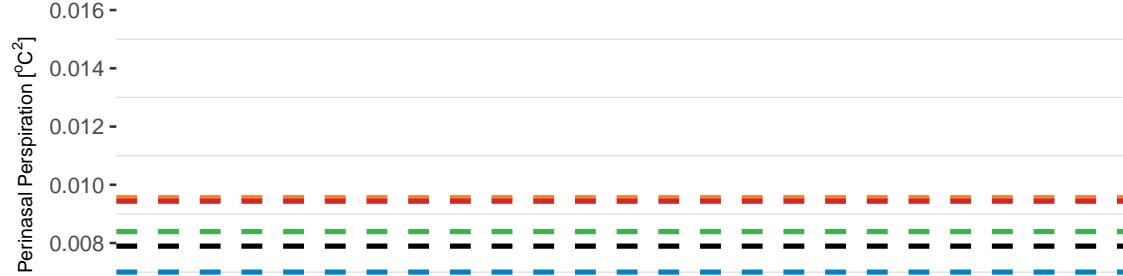
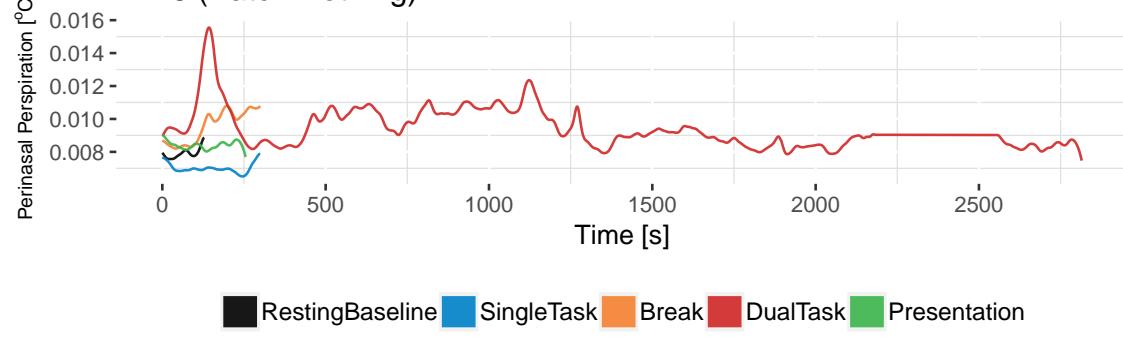




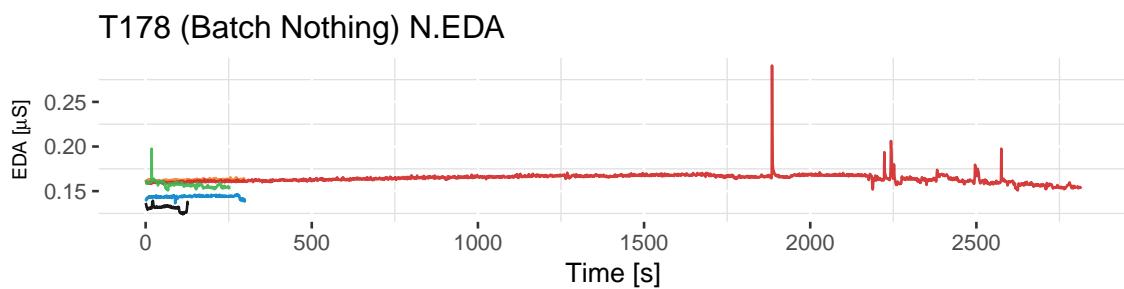
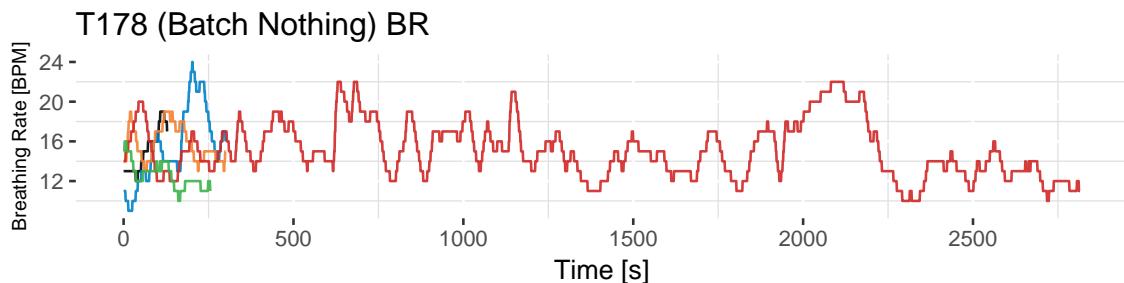
T176 (Batch Nothing) N.HR



T178 (Batch Nothing) PP



```
##  
##  
## Subject T178 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##
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T178 (Batch Nothing) N.HR

