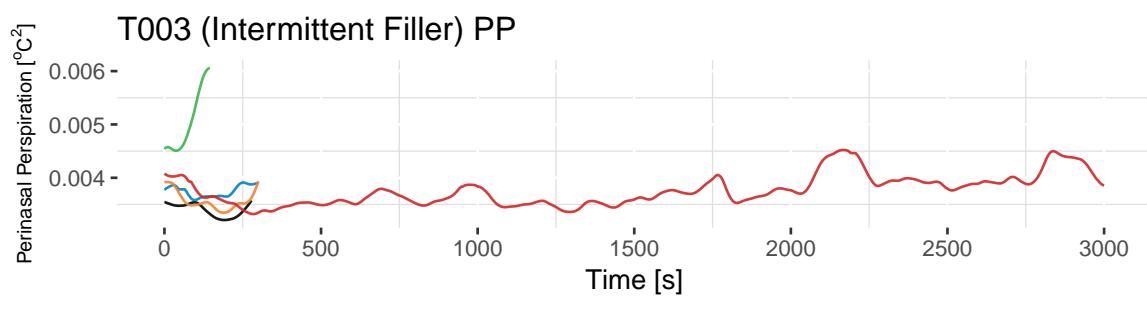
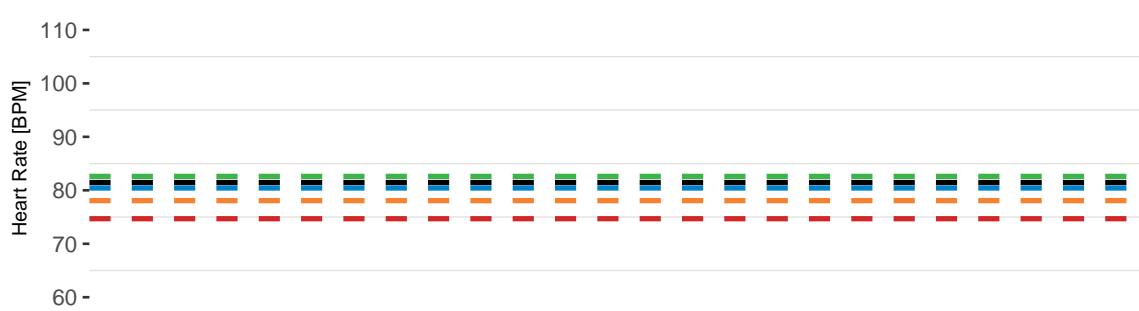


## Sensor Channel per Subject - All Activities

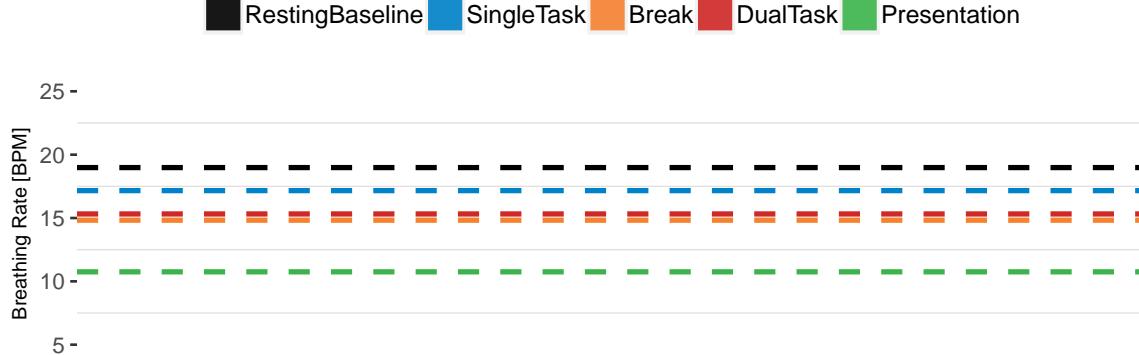
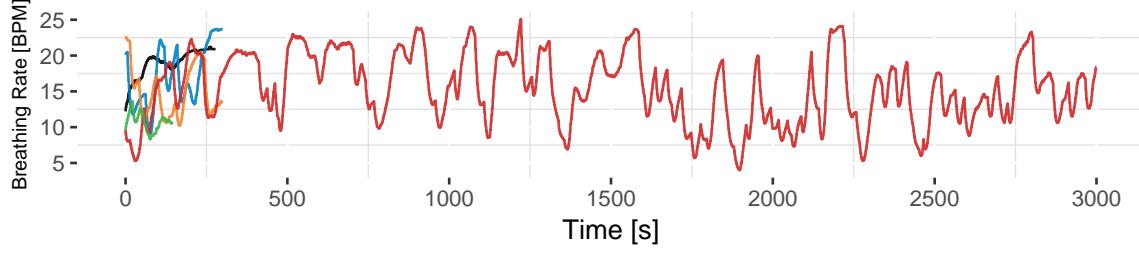
## **Intermittent-Filler (IF) Subjects**



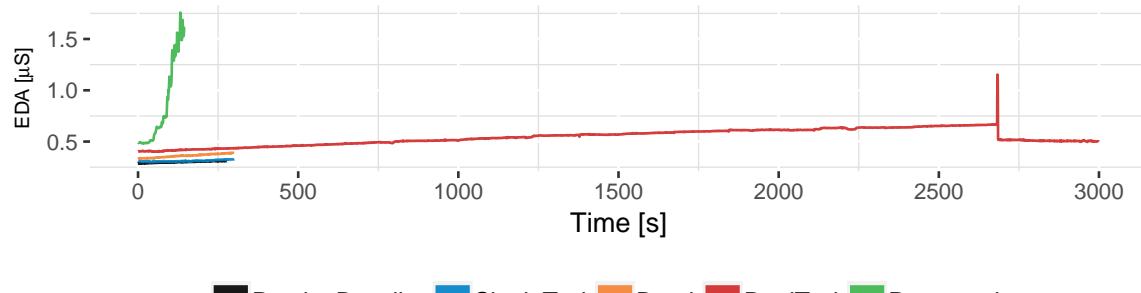
T003 (Intermittent Filler) HR



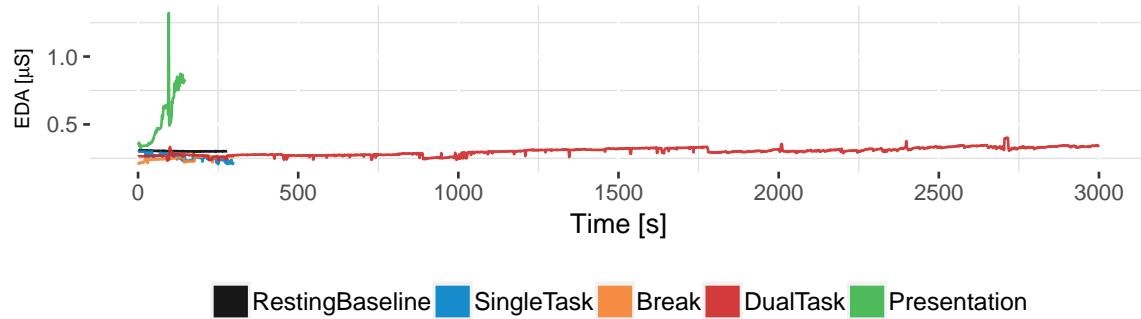
T003 (Intermittent Filler) BR



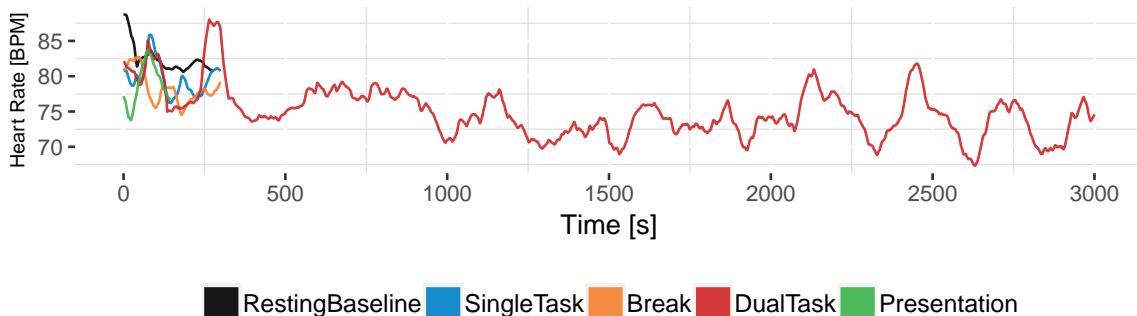
T003 (Intermittent Filler) D.EDA



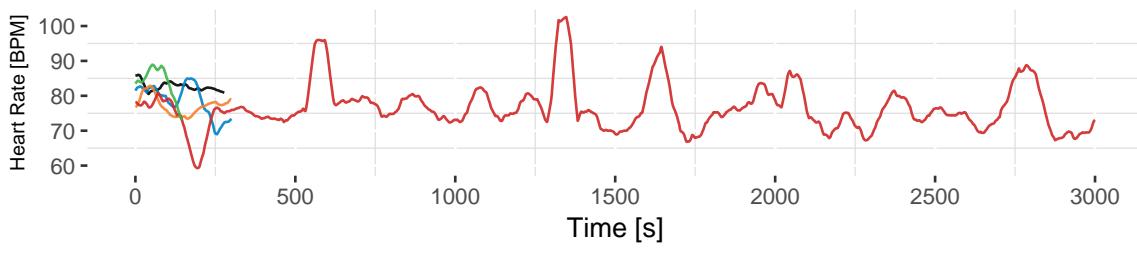
T003 (Intermittent Filler) N.EDA

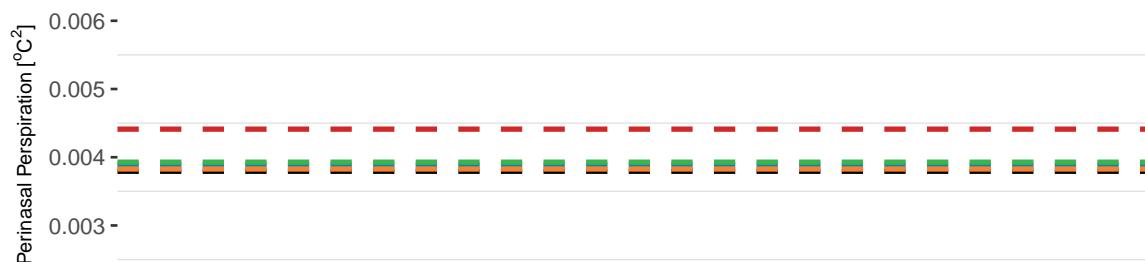
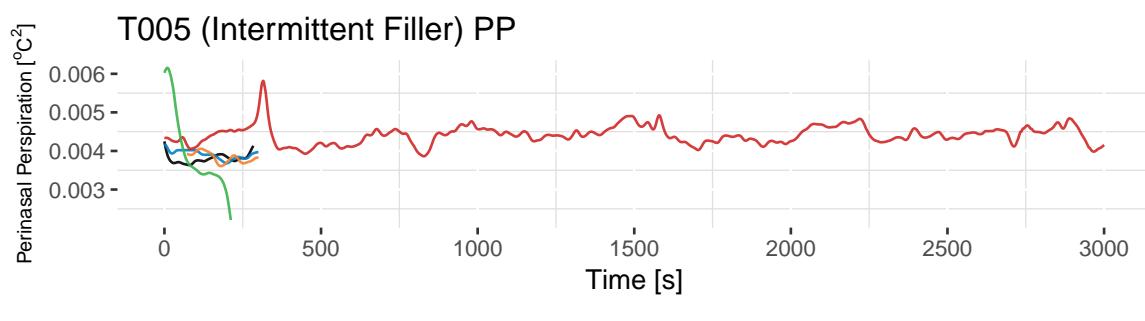


T003 (Intermittent Filler) D.HR



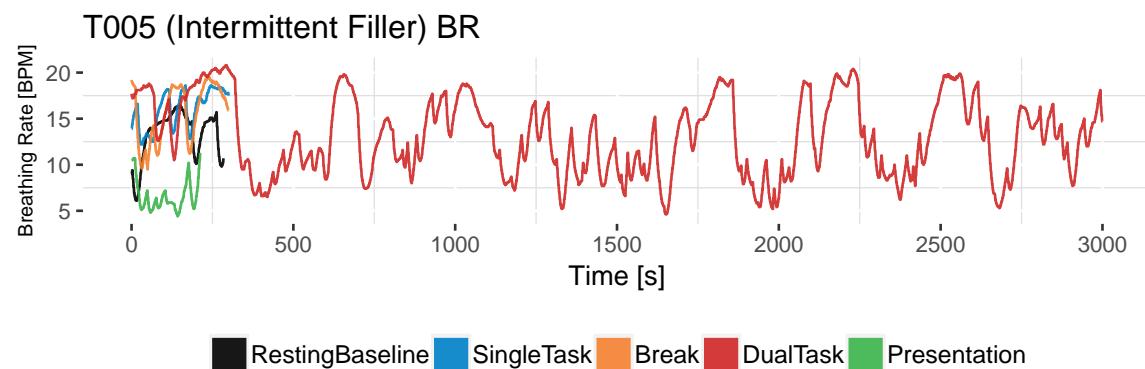
T003 (Intermittent Filler) N.HR



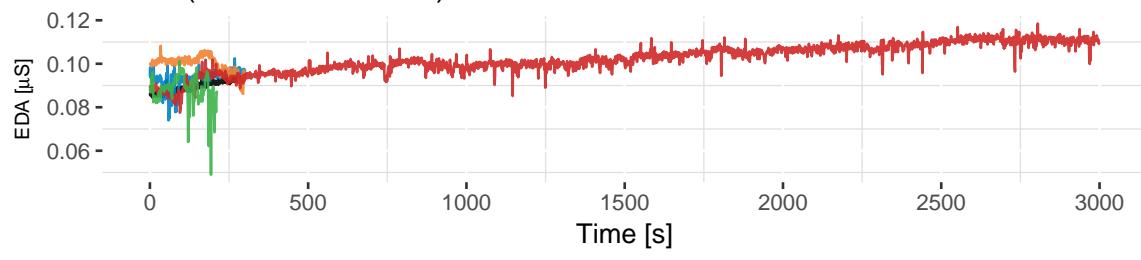


##

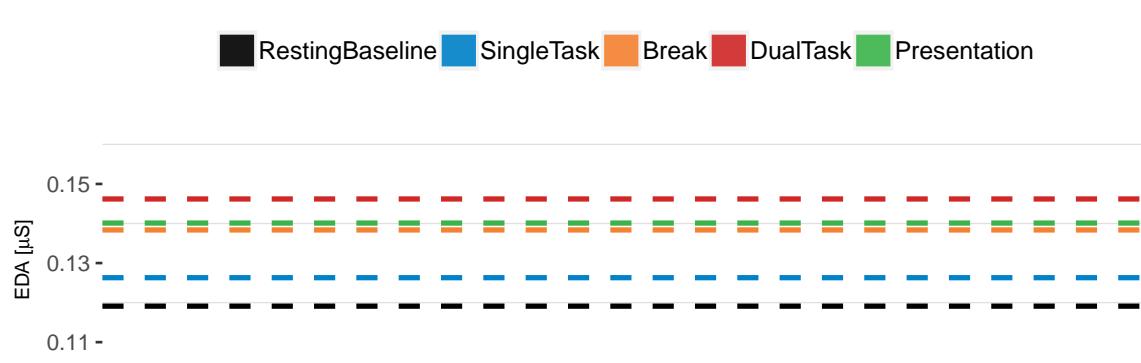
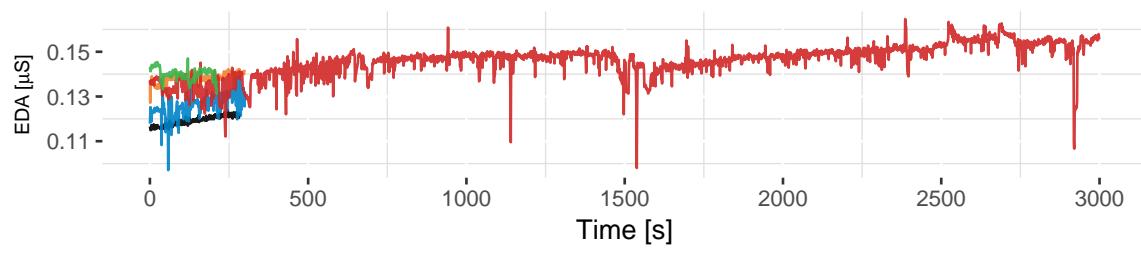
```
##  
## Subject T005 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##
```



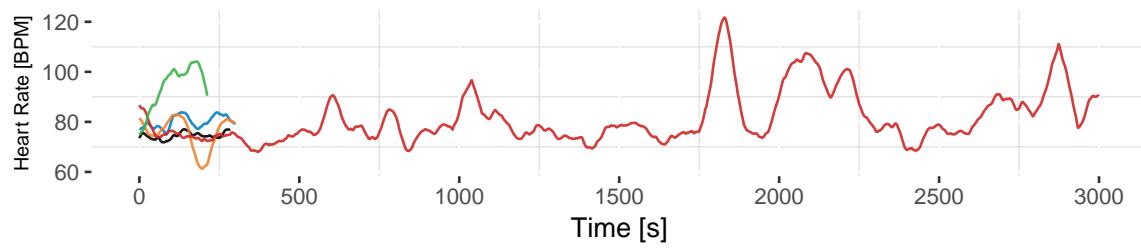
T005 (Intermittent Filler) D.EDA



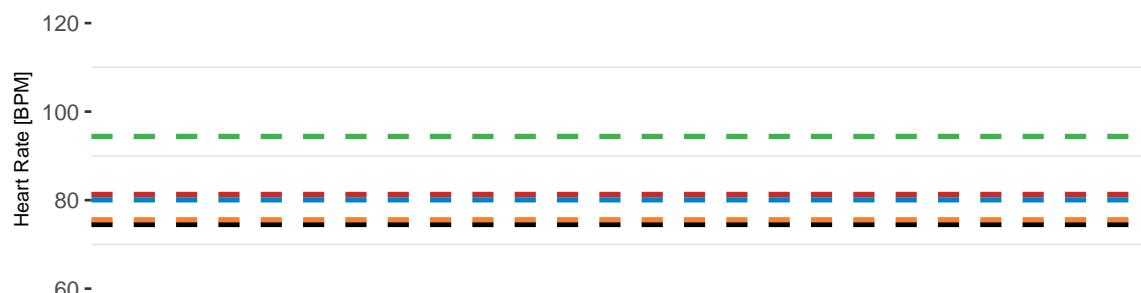
T005 (Intermittent Filler) N.EDA



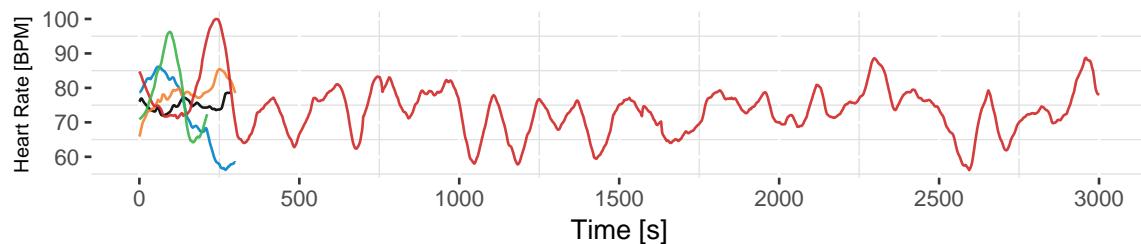
T005 (Intermittent Filler) D.HR



RestingBaseline SingleTask Break DualTask Presentation

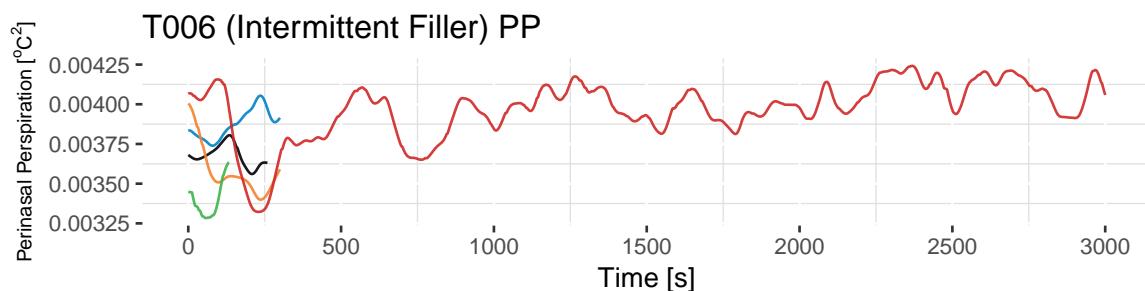


T005 (Intermittent Filler) N.HR

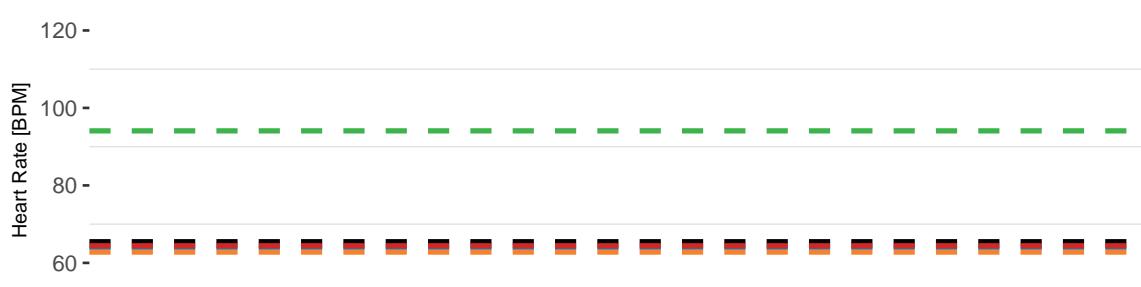
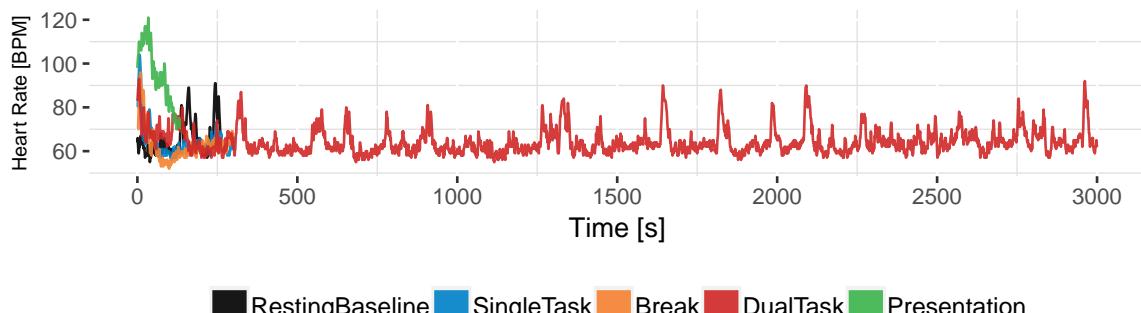


RestingBaseline SingleTask Break DualTask Presentation

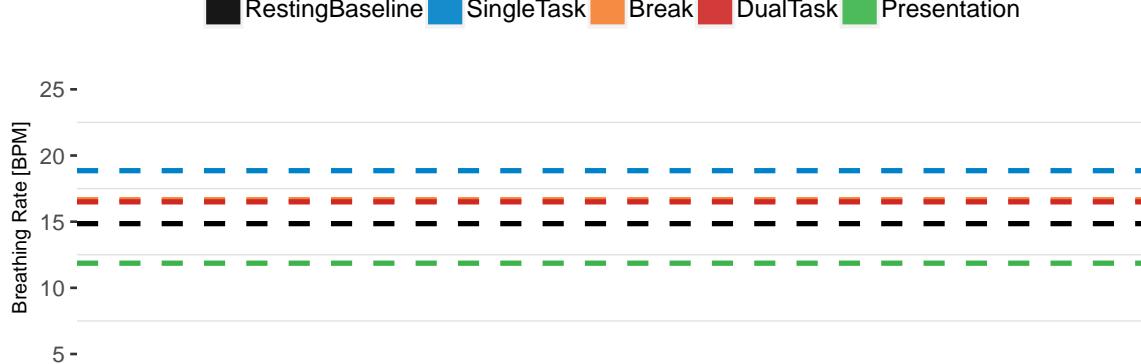
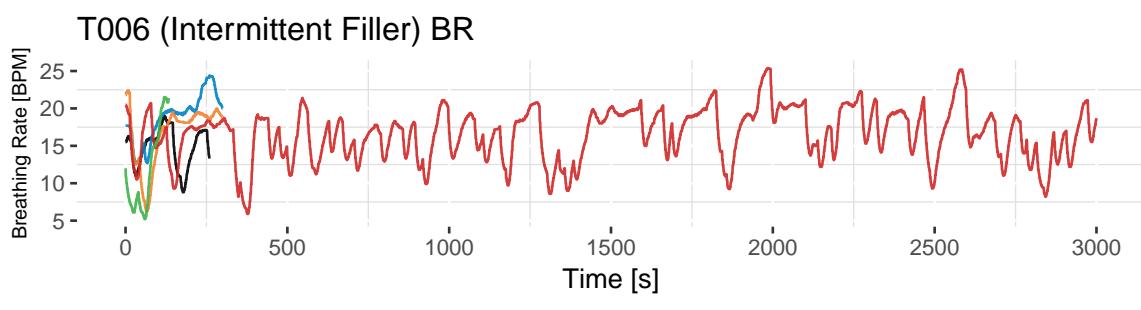




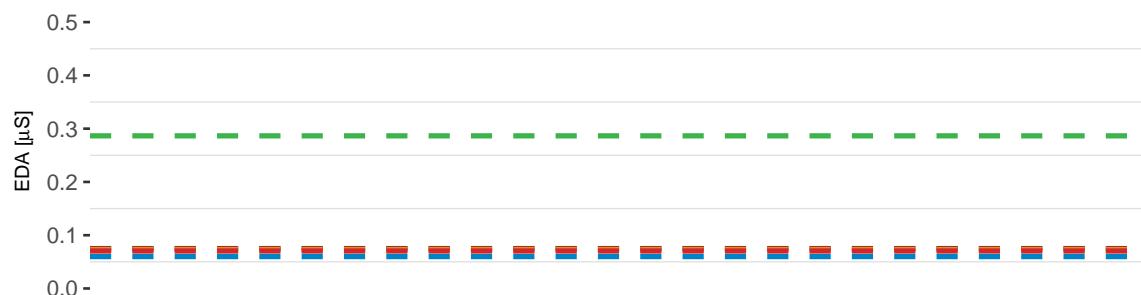
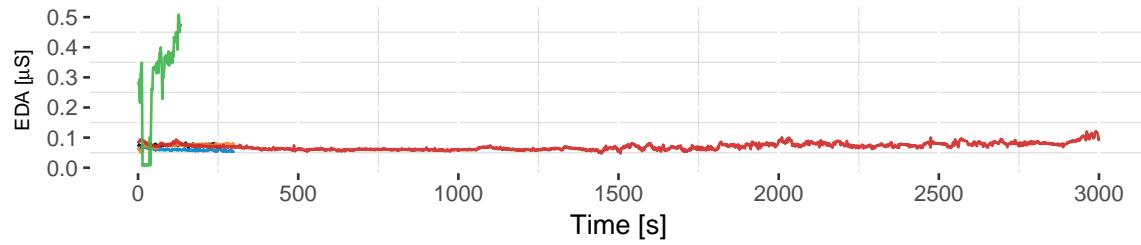
T006 (Intermittent Filler) HR



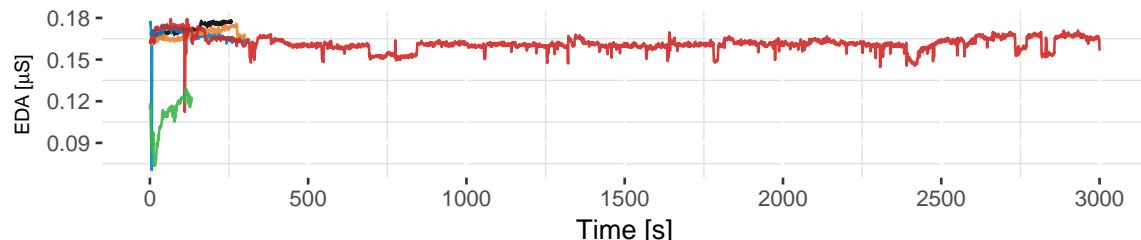
T006 (Intermittent Filler) BR



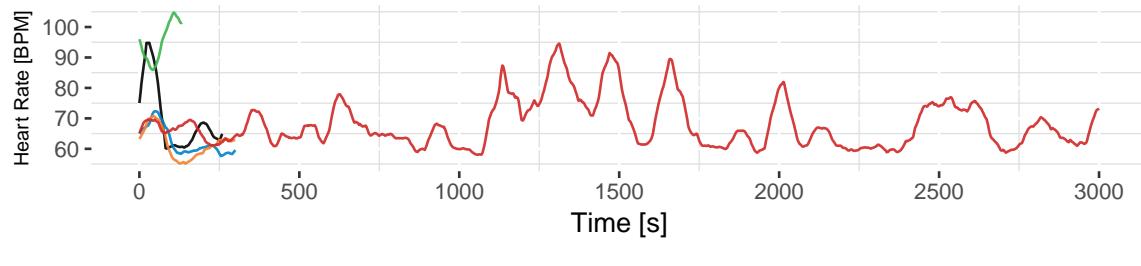
T006 (Intermittent Filler) D.EDA



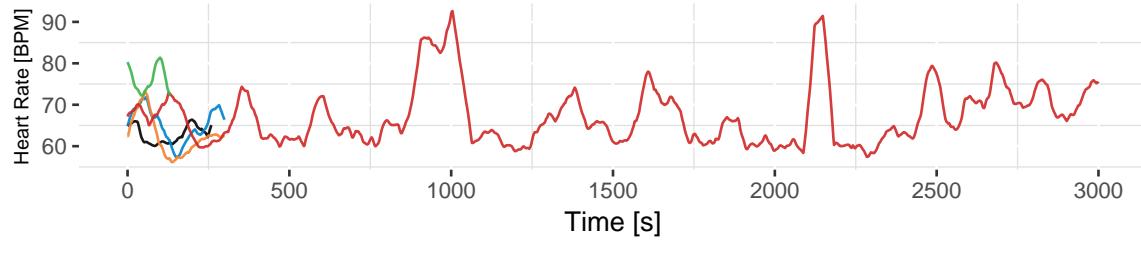
T006 (Intermittent Filler) N.EDA

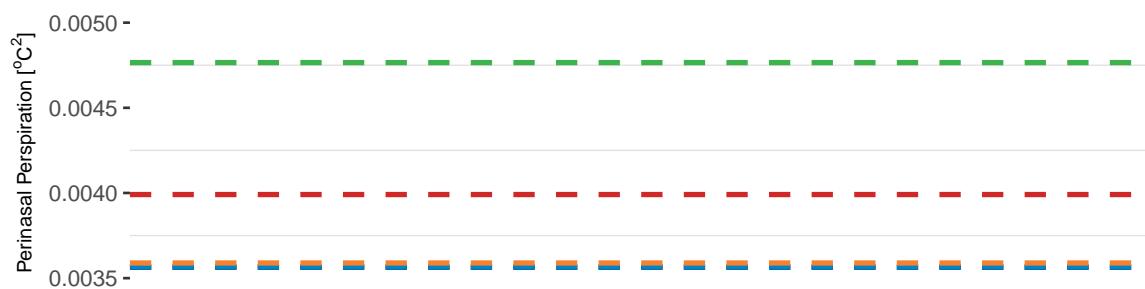
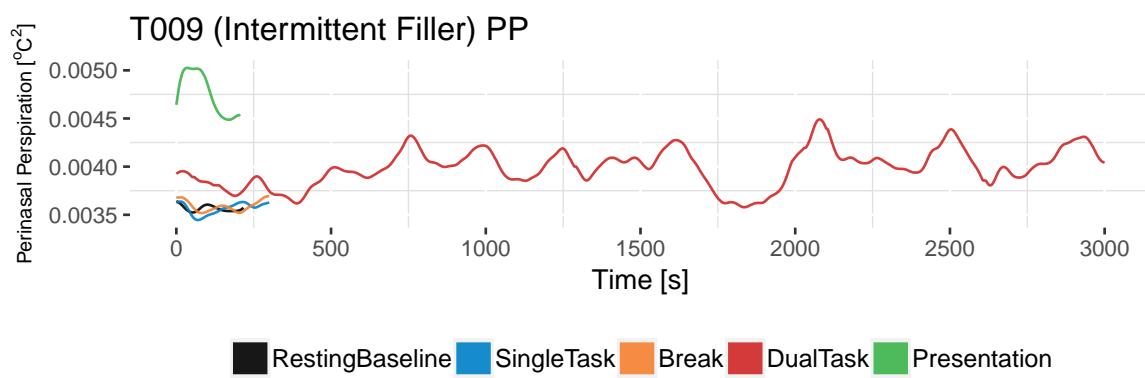


T006 (Intermittent Filler) D.HR



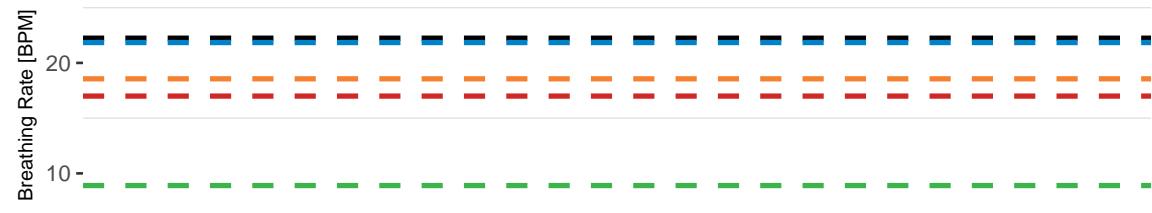
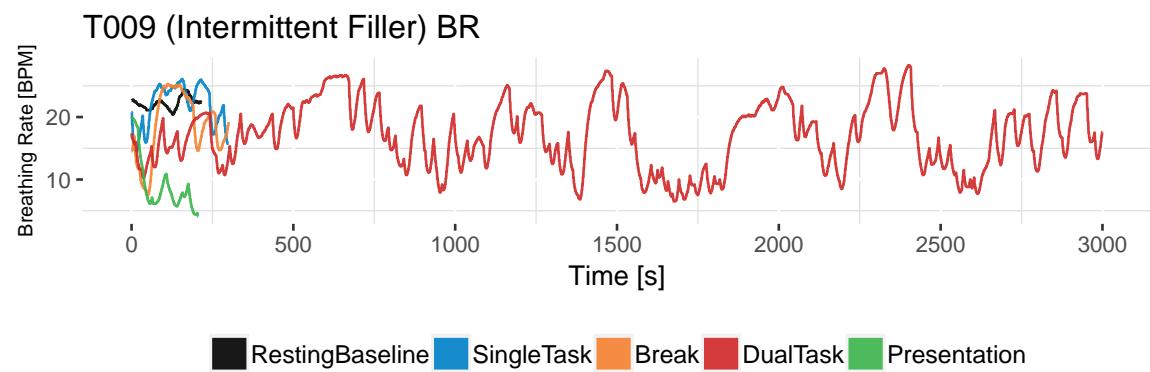
T006 (Intermittent Filler) N.HR



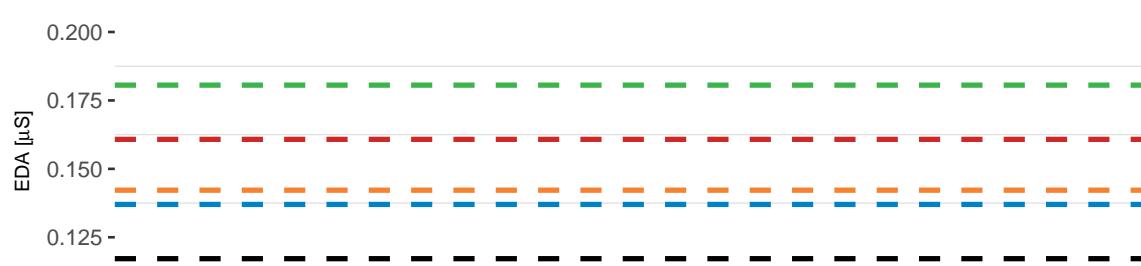
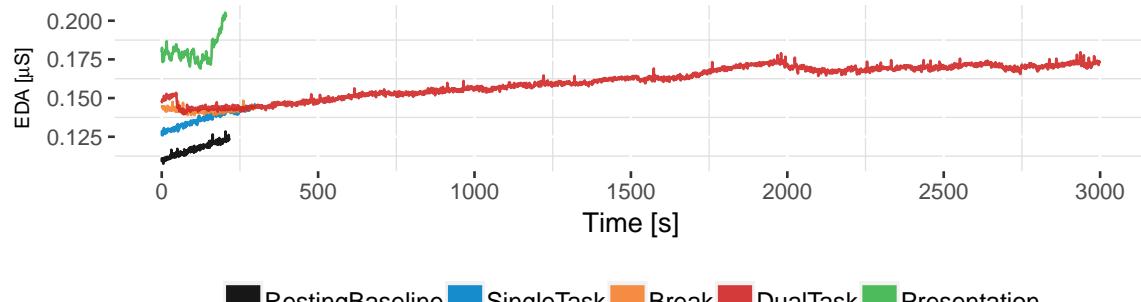


##

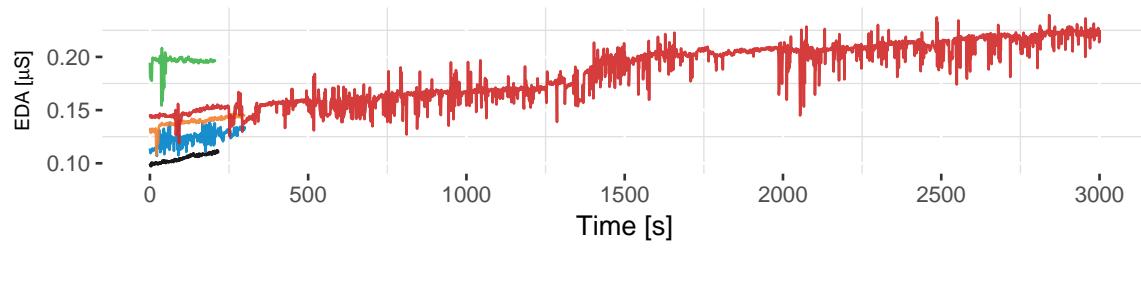
```
##  
## Subject T009 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```



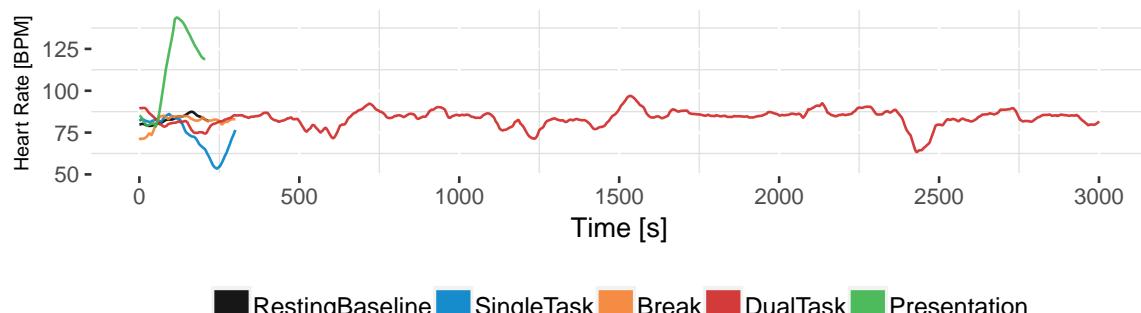
T009 (Intermittent Filler) D.EDA



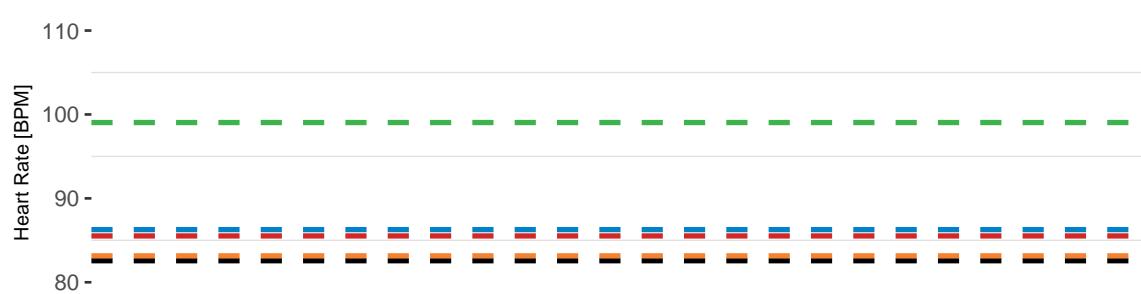
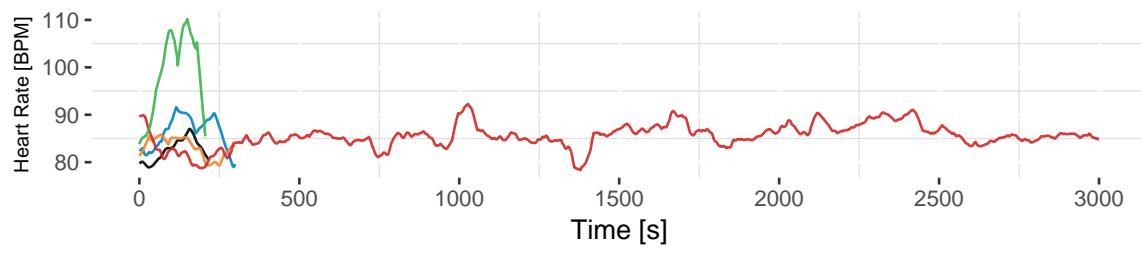
T009 (Intermittent Filler) N.EDA

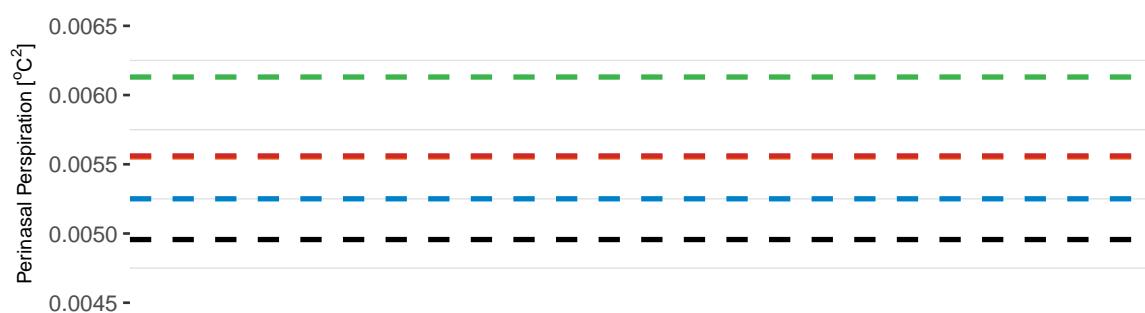


T009 (Intermittent Filler) D.HR

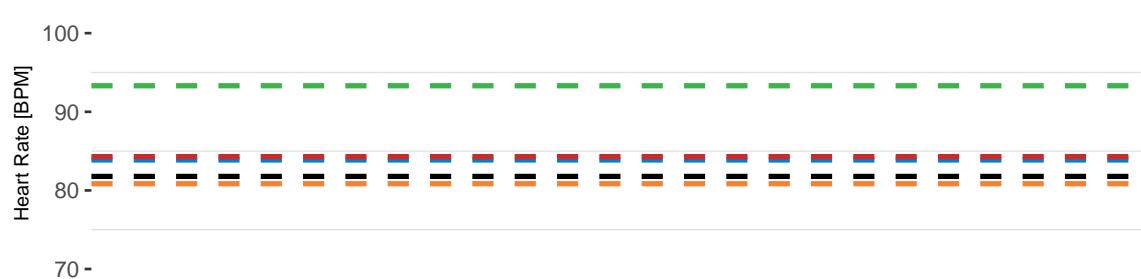
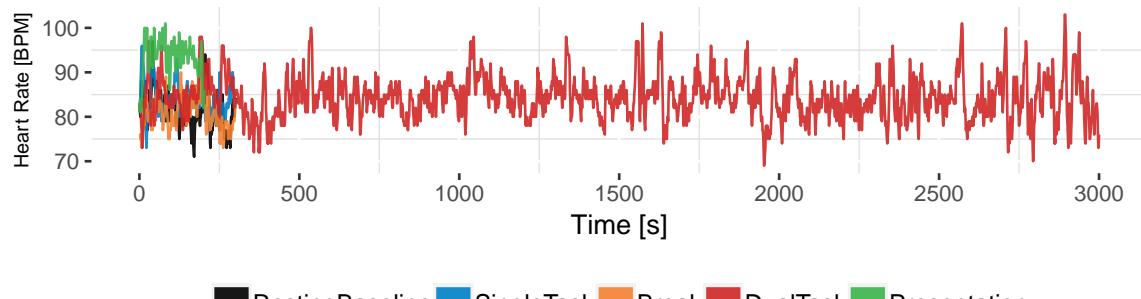


T009 (Intermittent Filler) N.HR

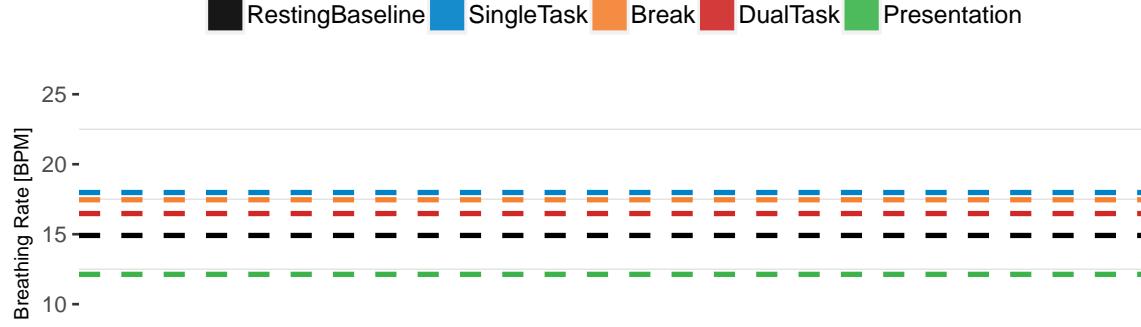
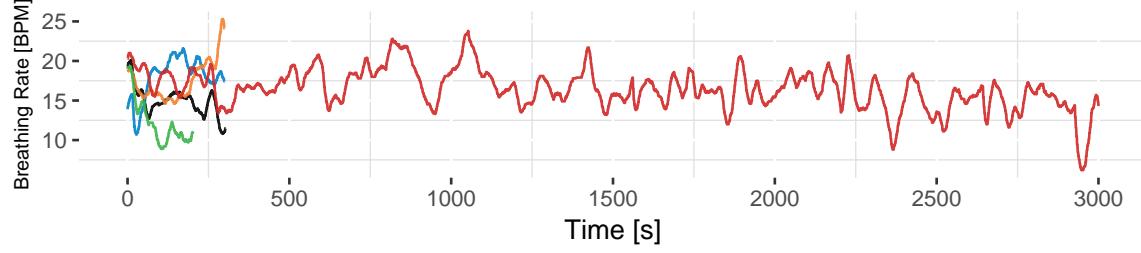




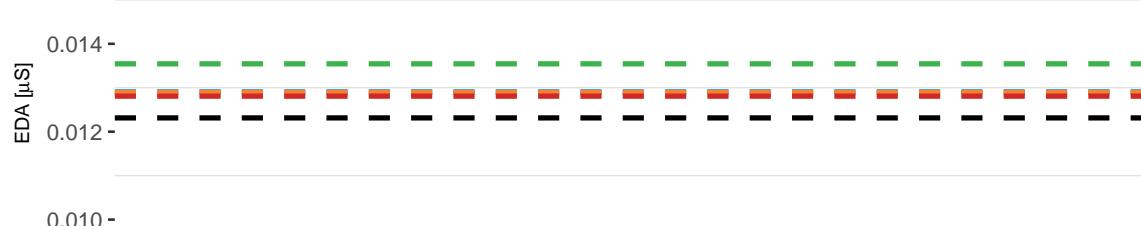
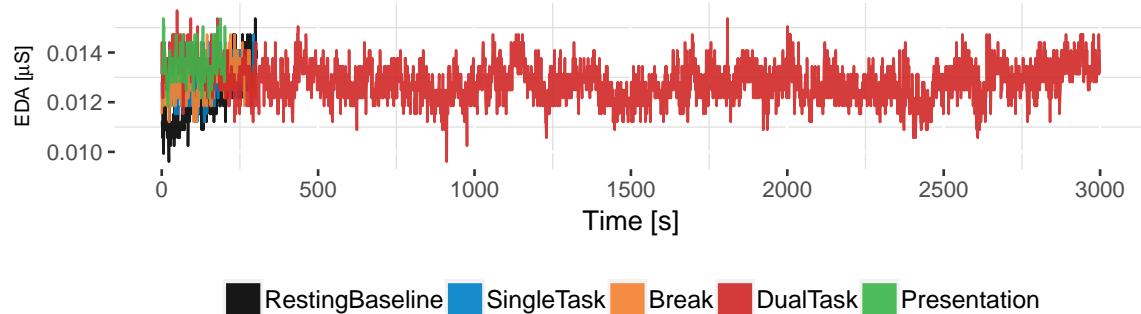
T061 (Intermittent Filler) HR



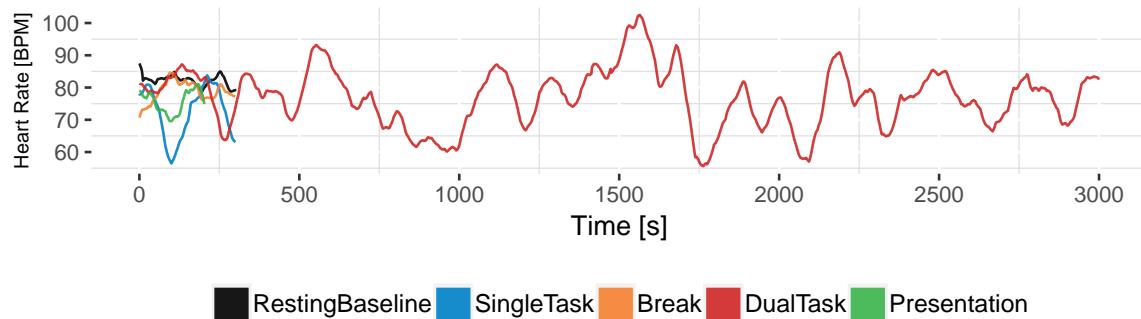
T061 (Intermittent Filler) BR



T061 (Intermittent Filler) N.EDA

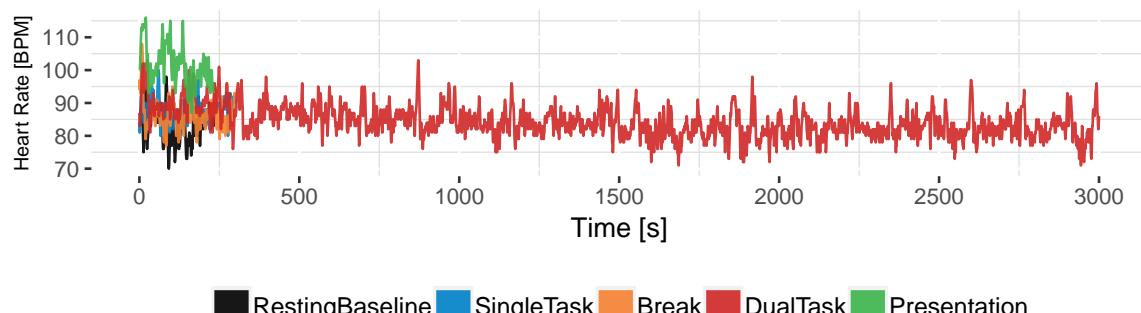


T061 (Intermittent Filler) N.HR

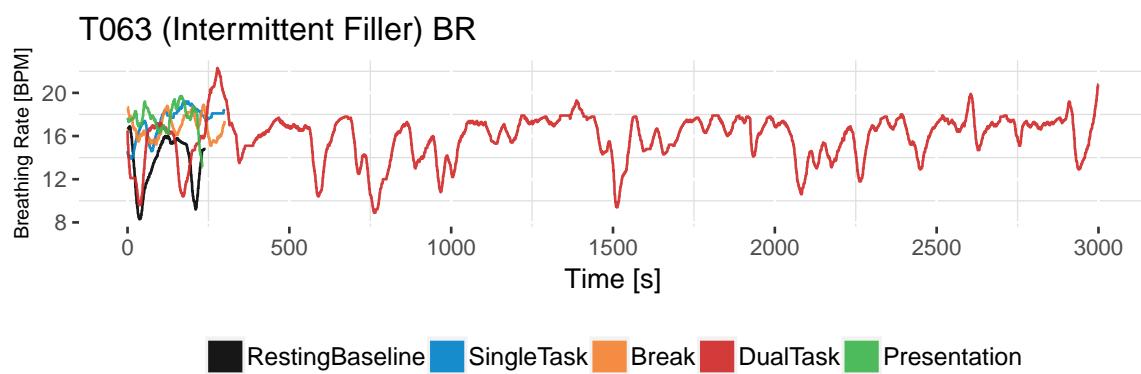




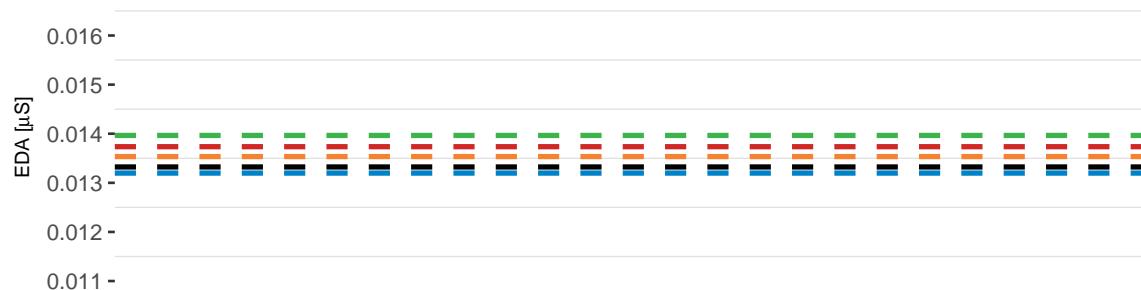
T063 (Intermittent Filler) HR



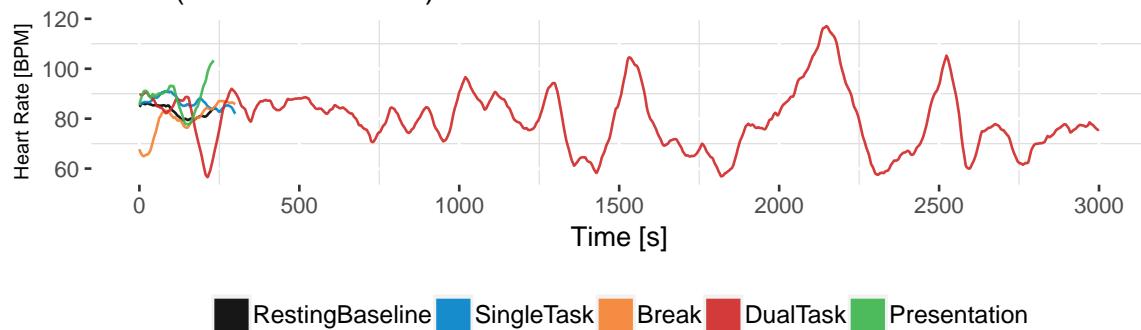
T063 (Intermittent Filler) BR

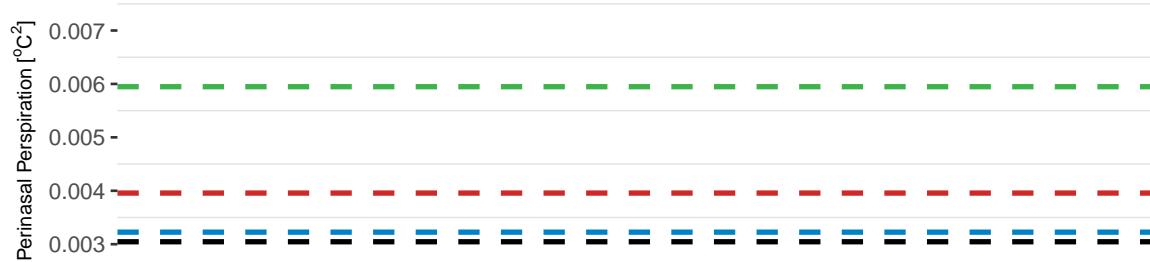
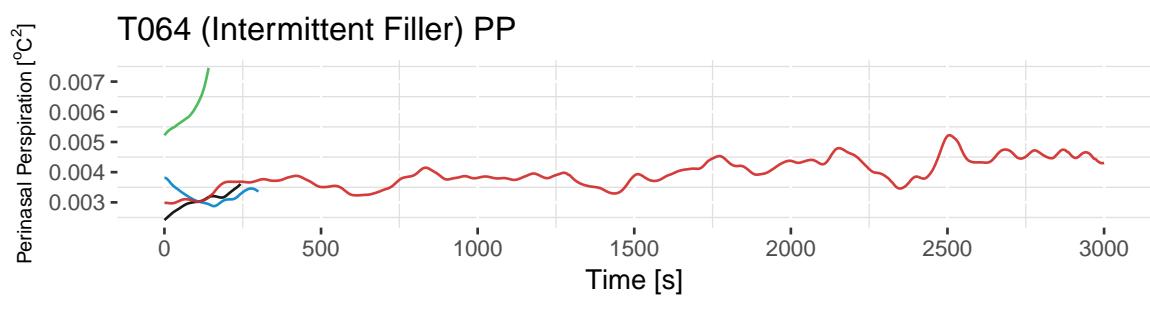


T063 (Intermittent Filler) N.EDA

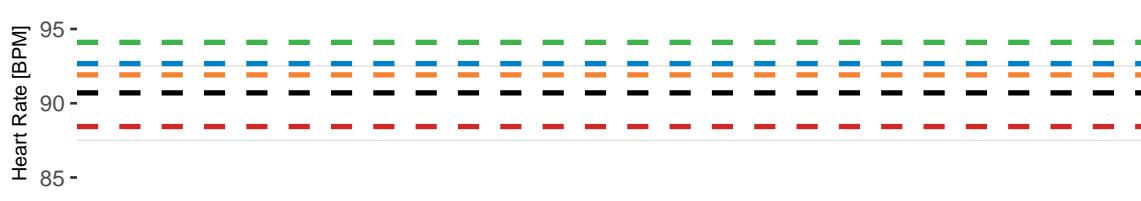
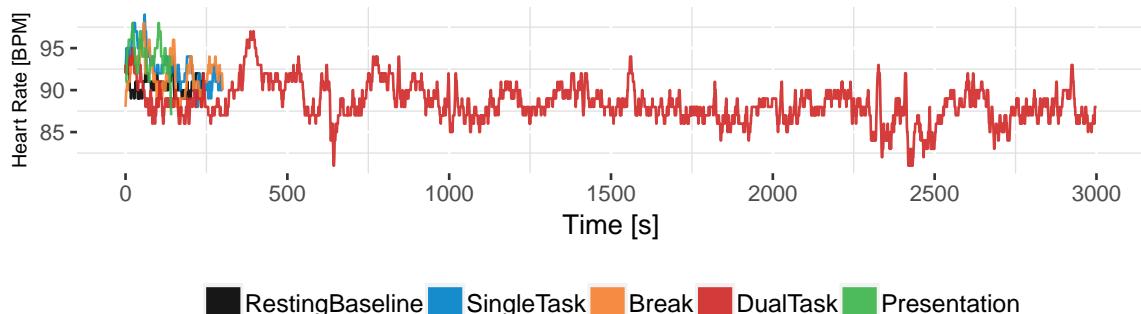


T063 (Intermittent Filler) N.HR

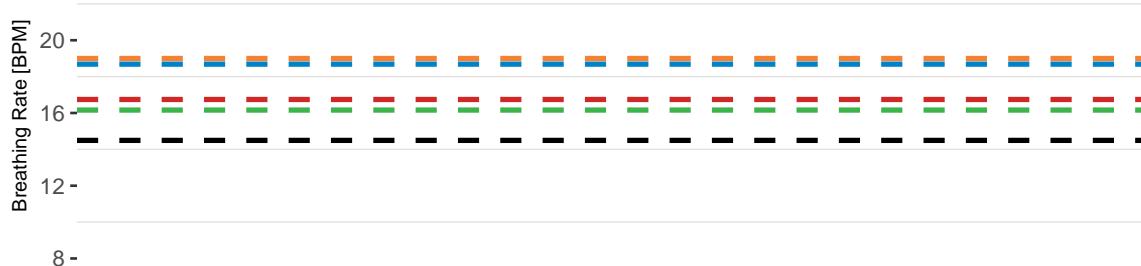
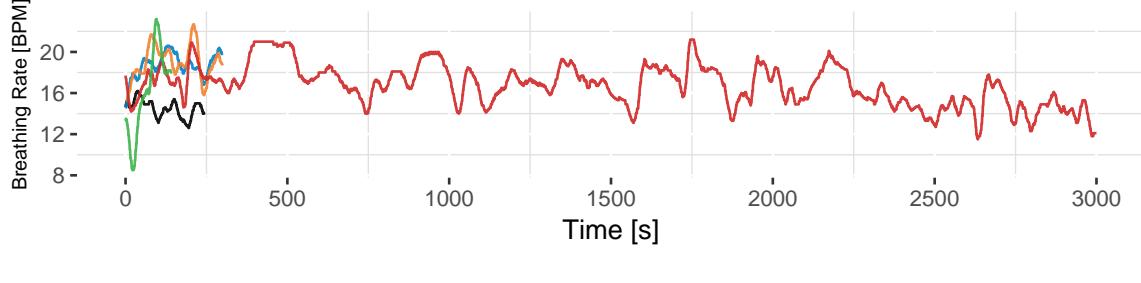




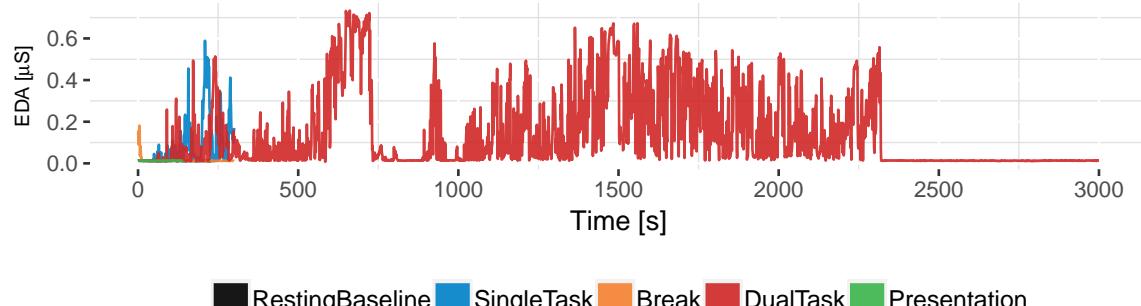
T064 (Intermittent Filler) HR



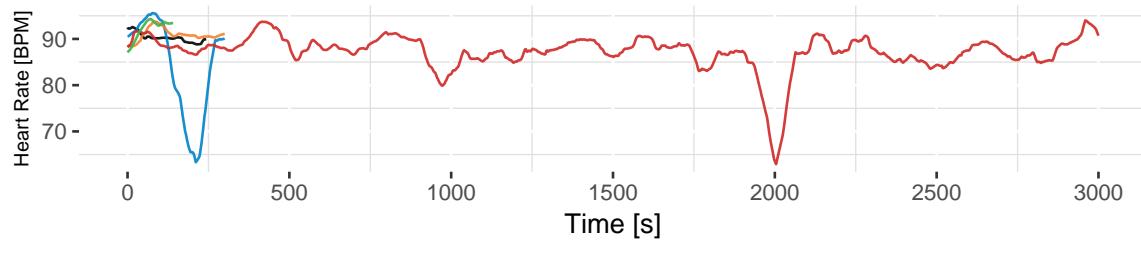
T064 (Intermittent Filler) BR

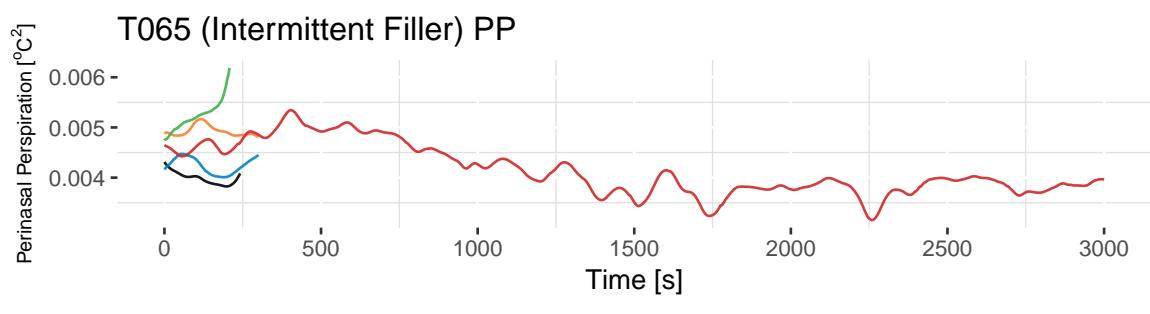


T064 (Intermittent Filler) N.EDA

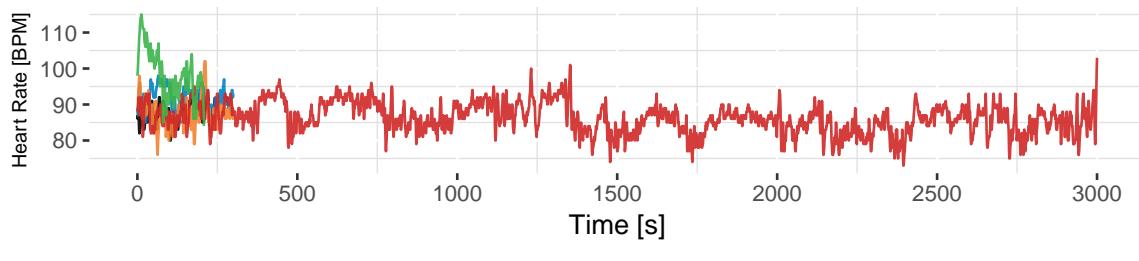


T064 (Intermittent Filler) N.HR





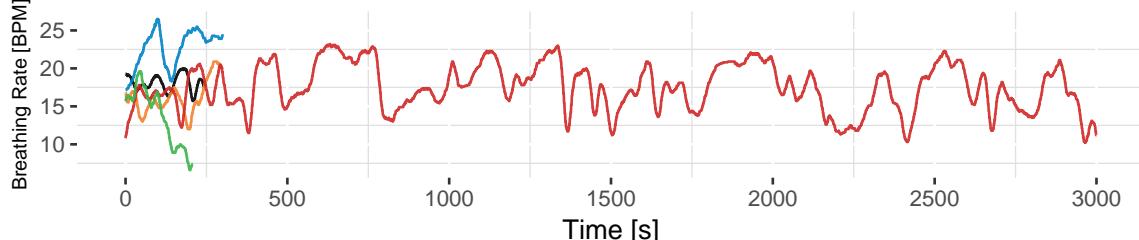
T065 (Intermittent Filler) HR



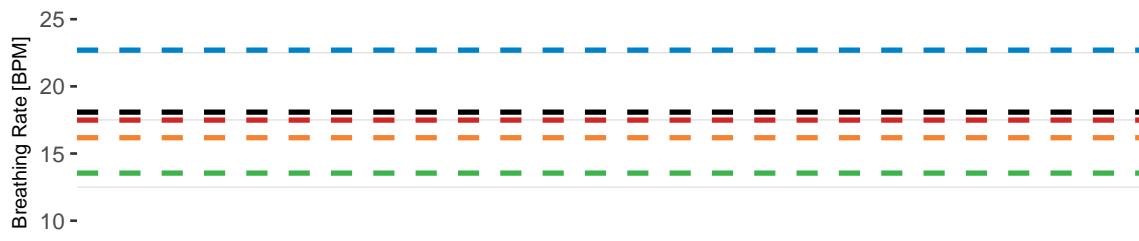
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



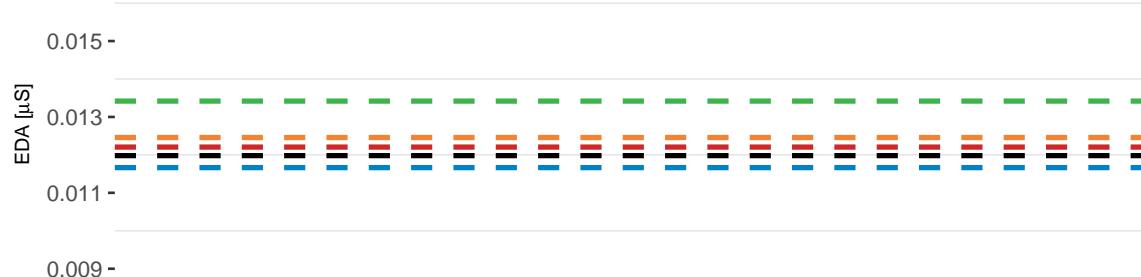
T065 (Intermittent Filler) BR



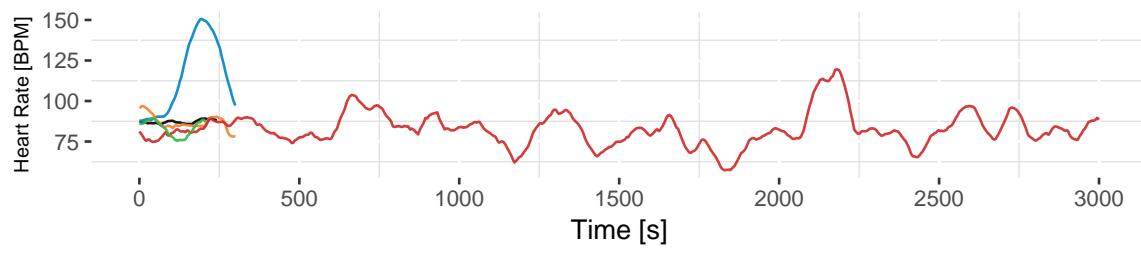
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



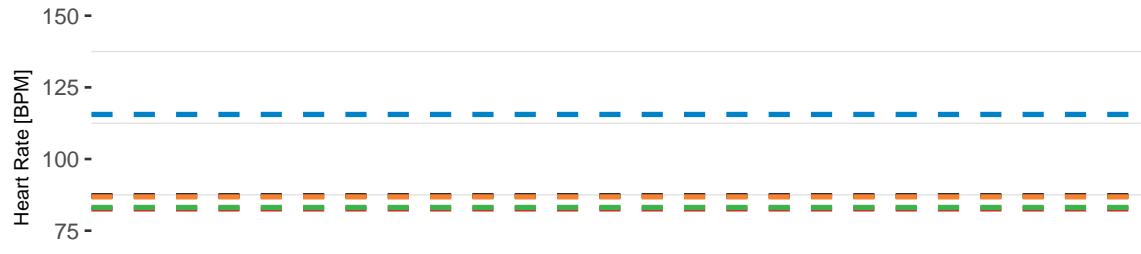
T065 (Intermittent Filler) N.EDA

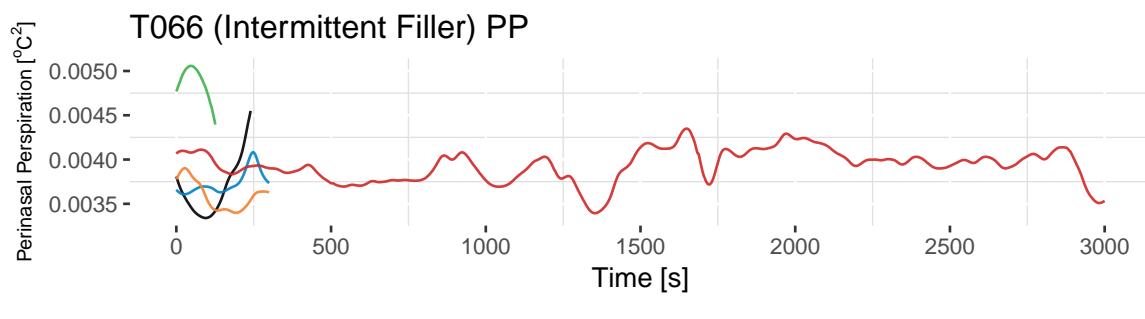


T065 (Intermittent Filler) N.HR



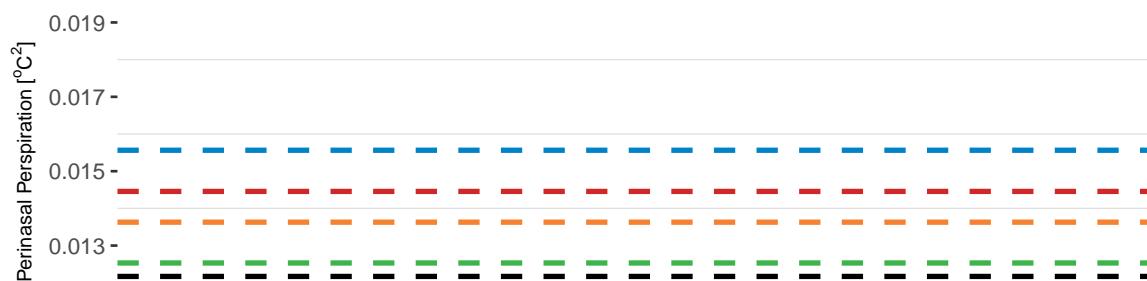
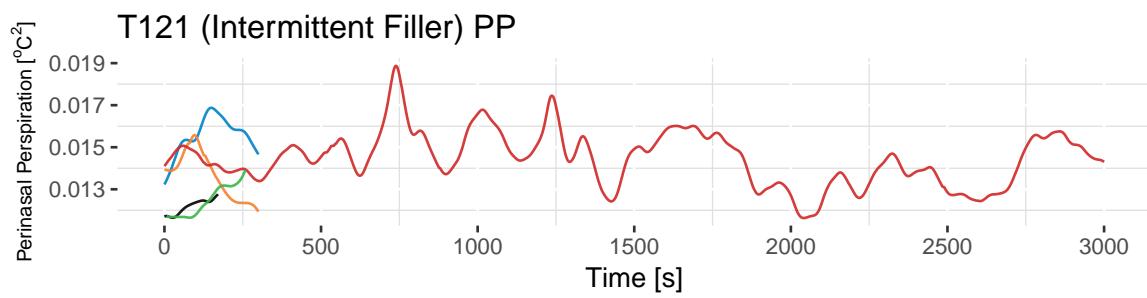
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation





##

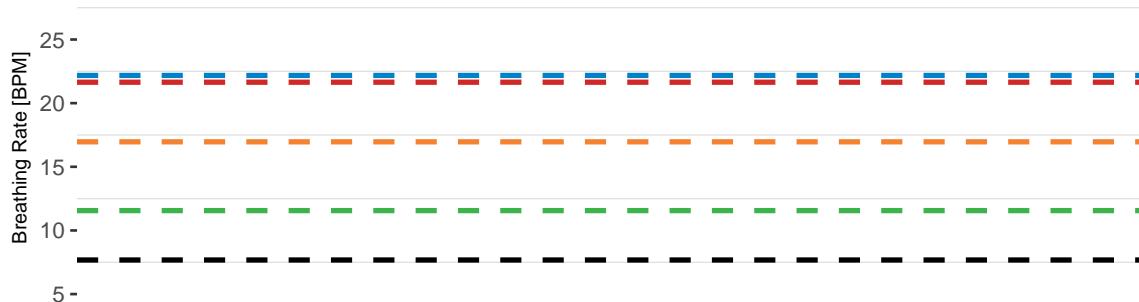
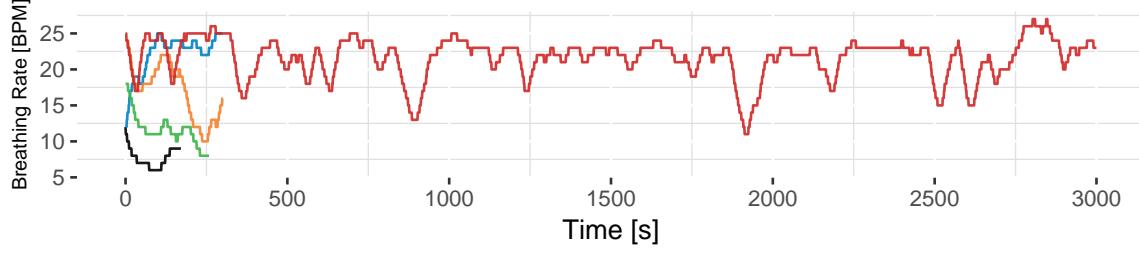
```
##  
## Subject T066 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
## Subject T066 does not have BR signals.  
##  
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```

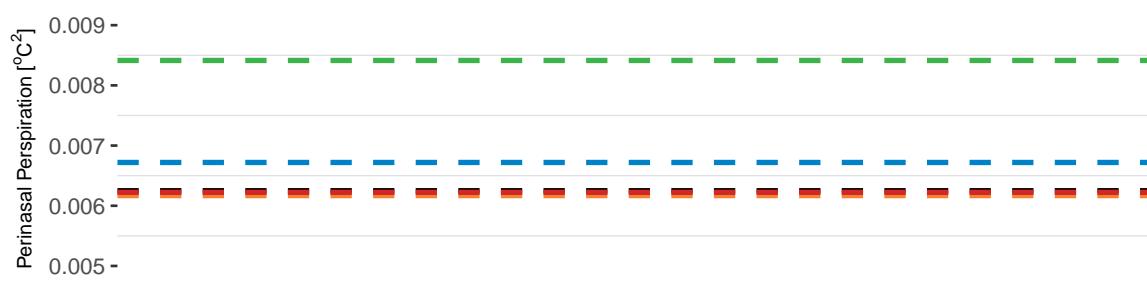
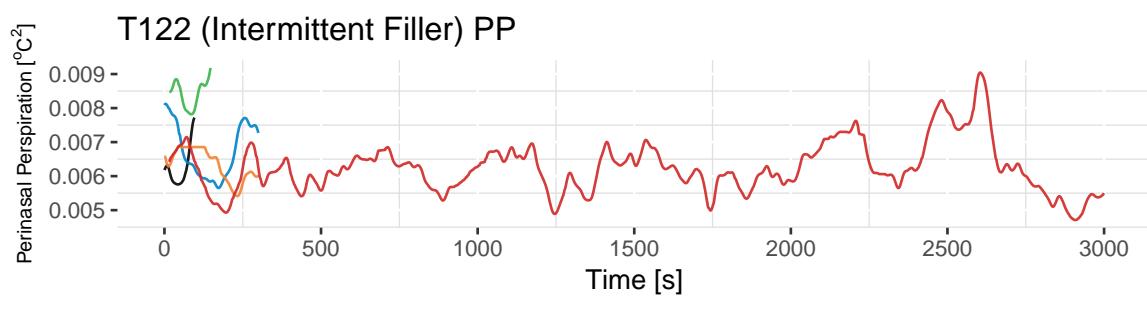


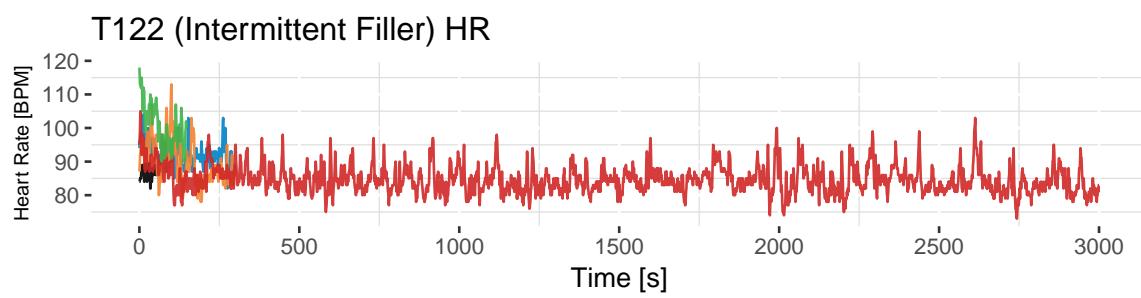
T121 (Intermittent Filler) HR



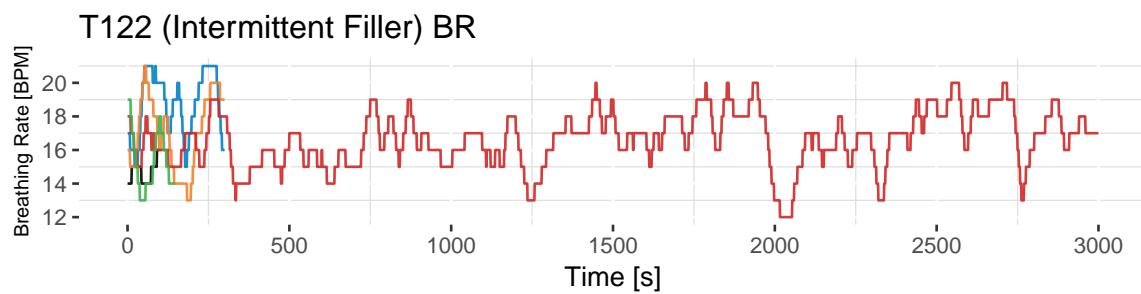
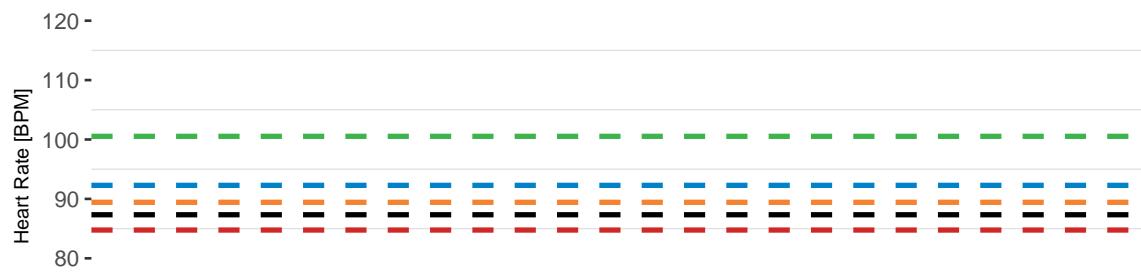
T121 (Intermittent Filler) BR



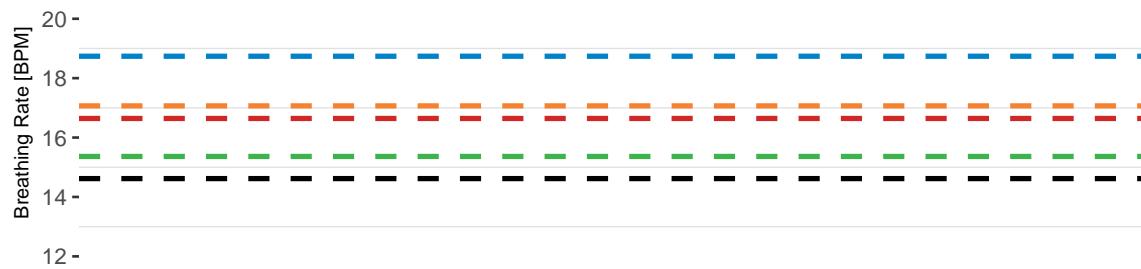


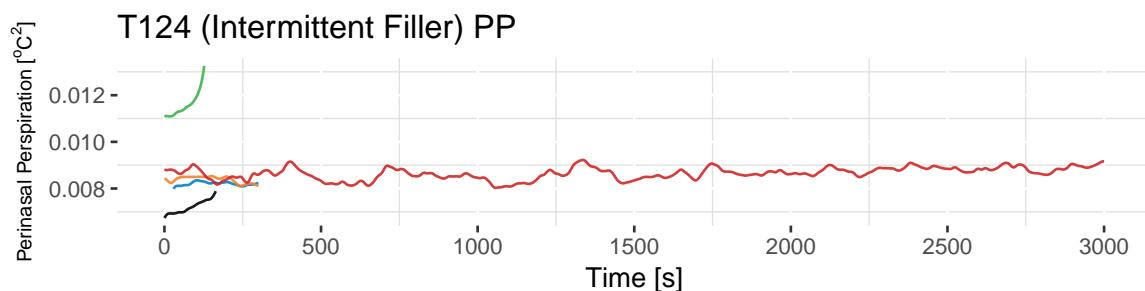


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

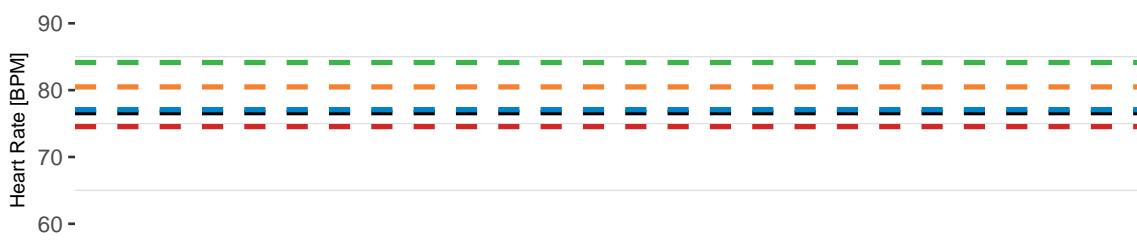
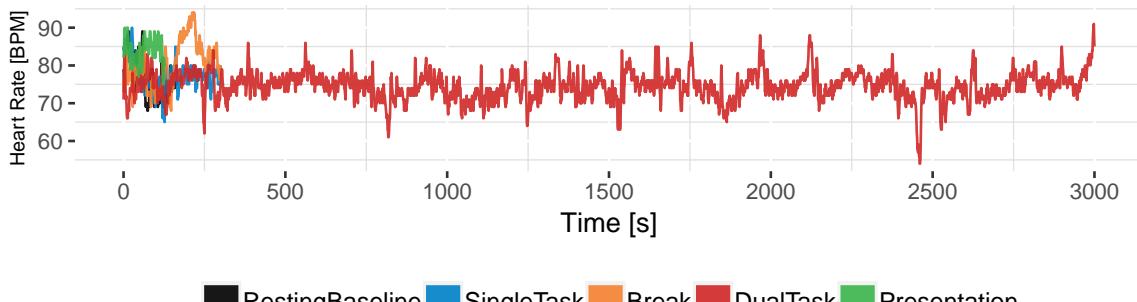


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

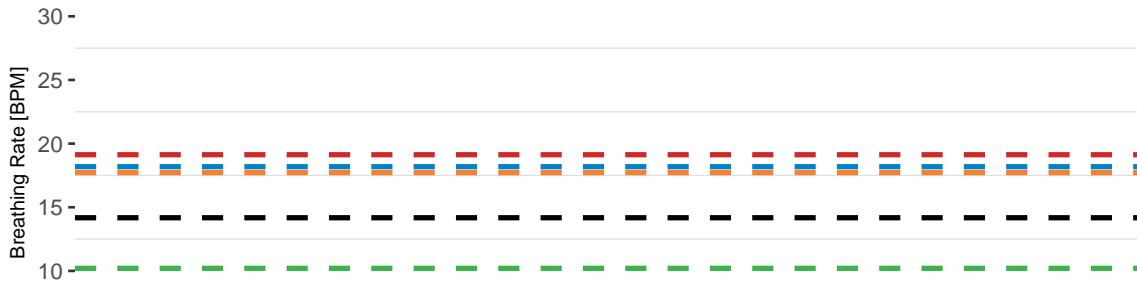
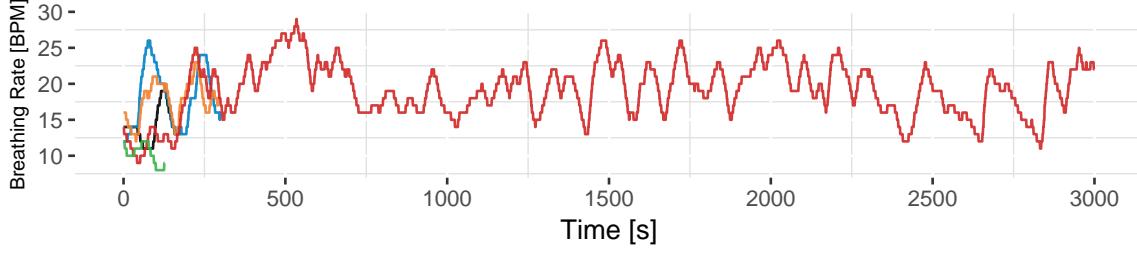




T124 (Intermittent Filler) HR

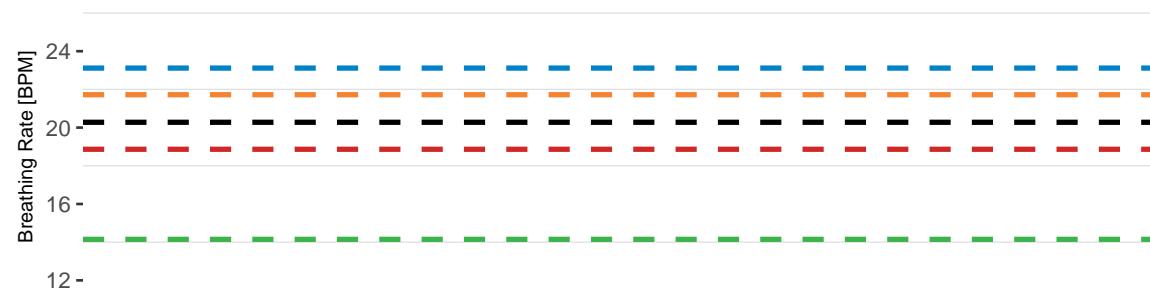
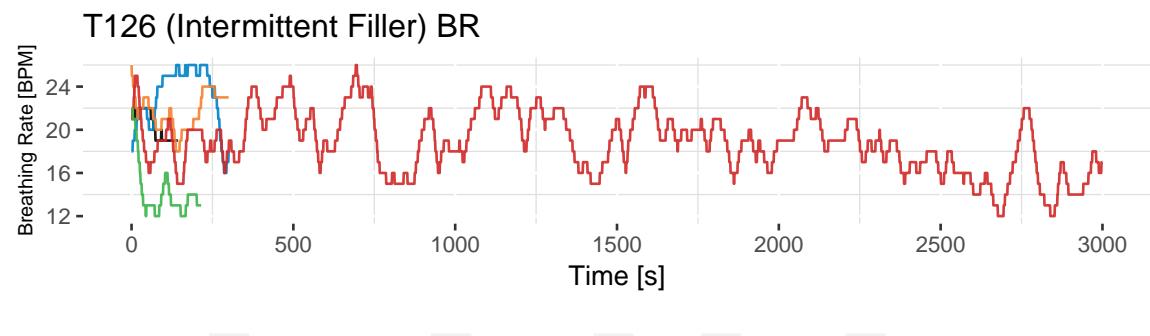


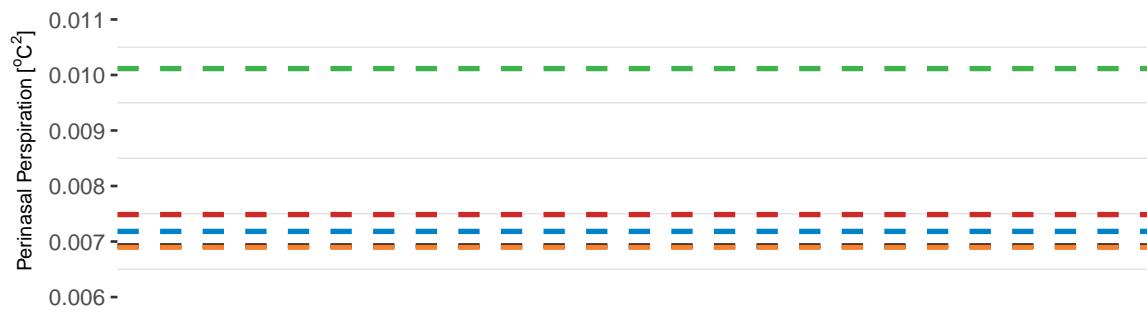
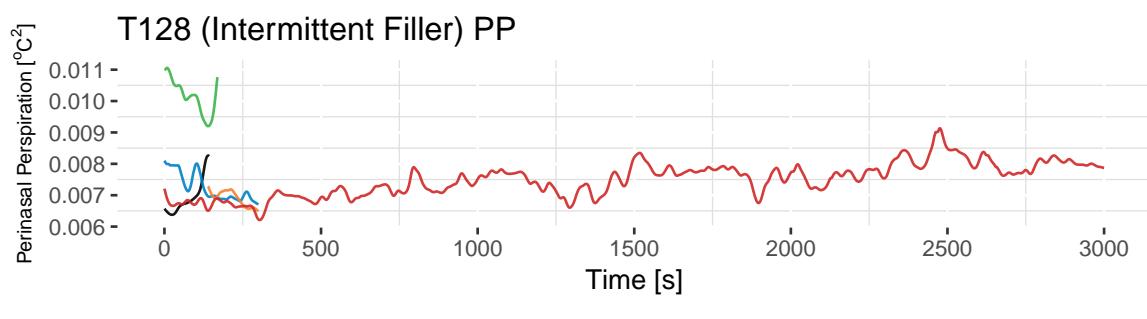
T124 (Intermittent Filler) BR



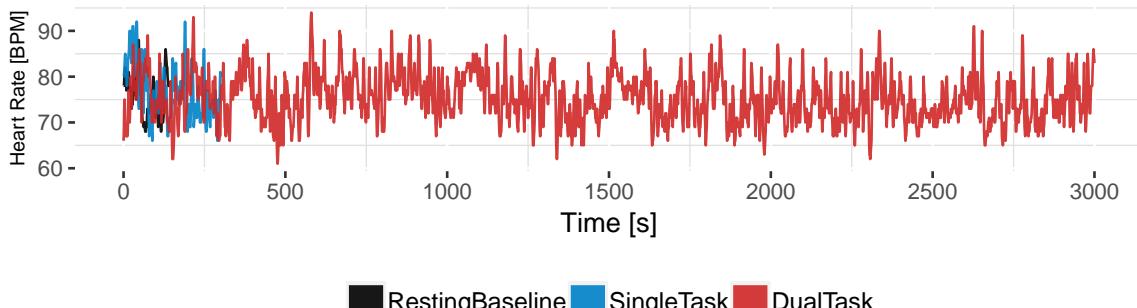
##  
##

```
## Subject T126 does not have PP signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
## Subject T126 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##
```





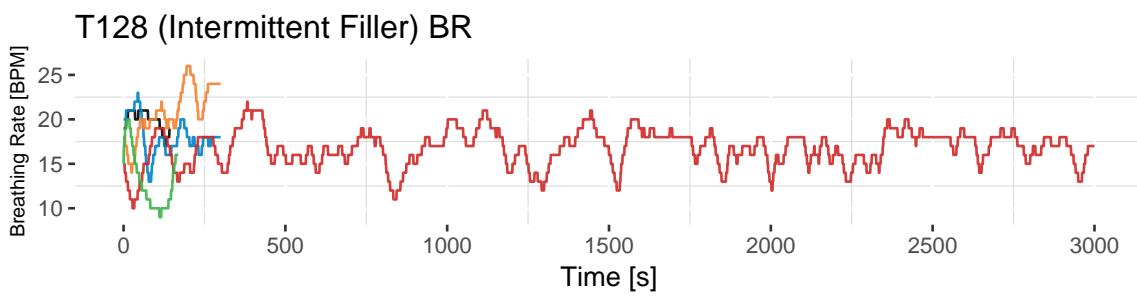
T128 (Intermittent Filler) HR



■ RestingBaseline ■ SingleTask ■ DualTask

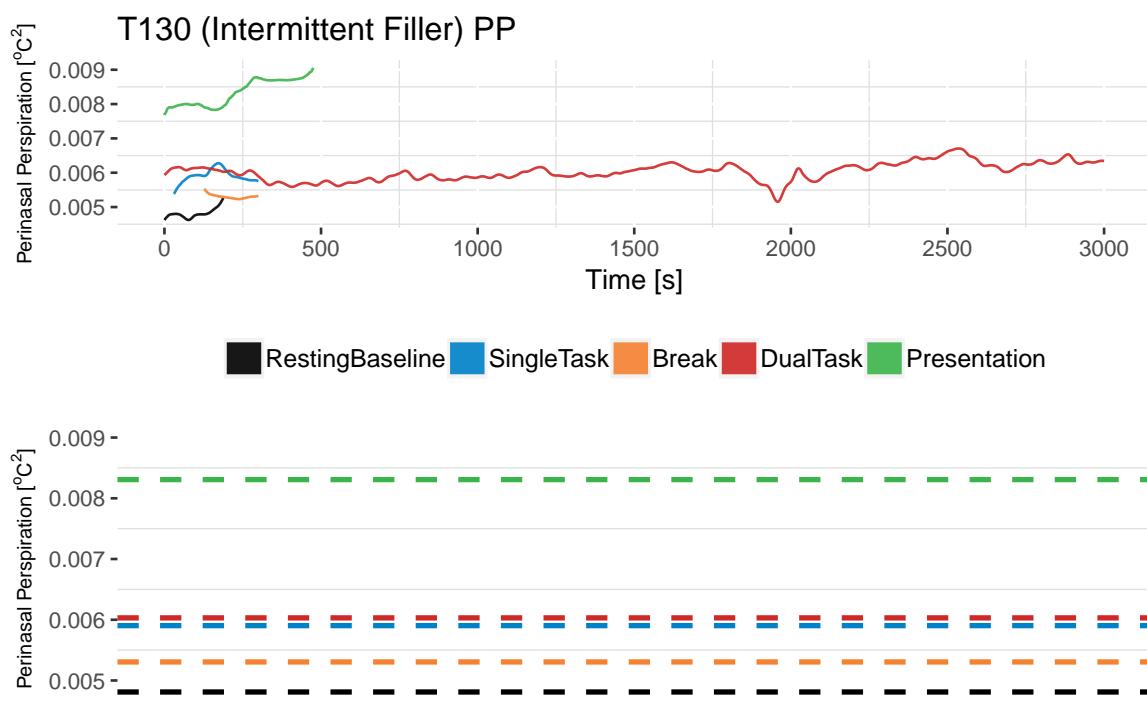


T128 (Intermittent Filler) BR



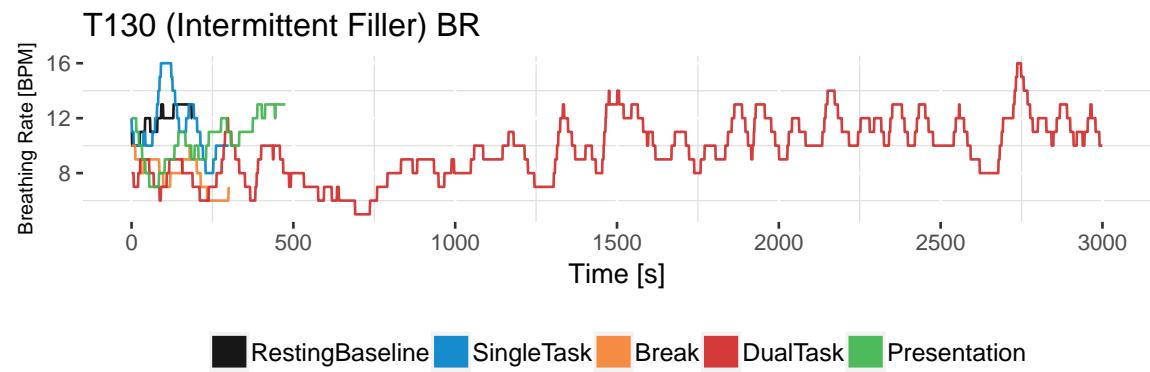
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

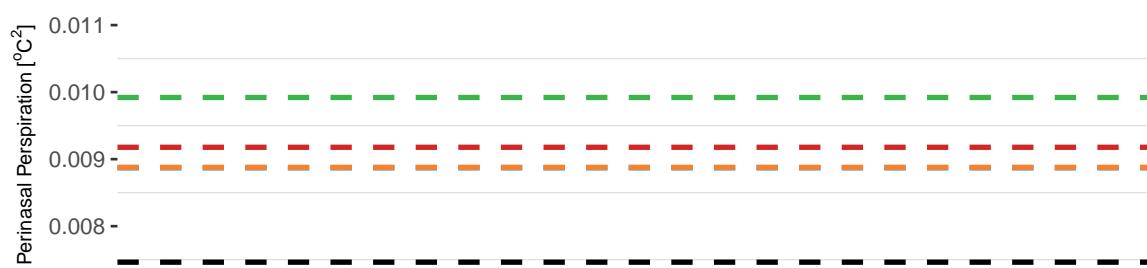
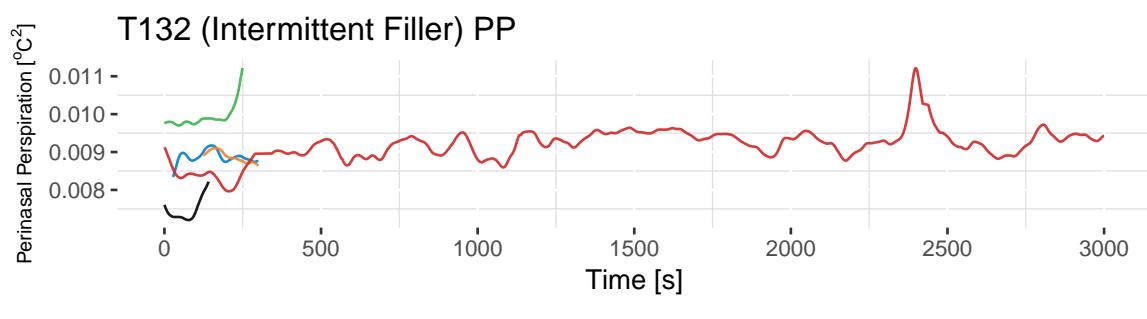


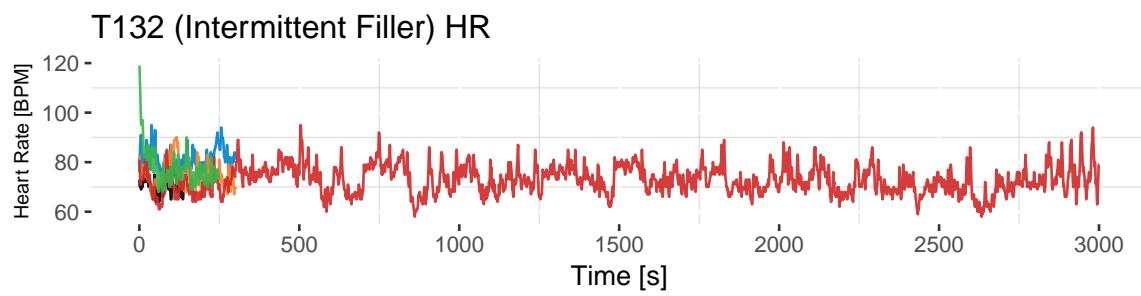


##

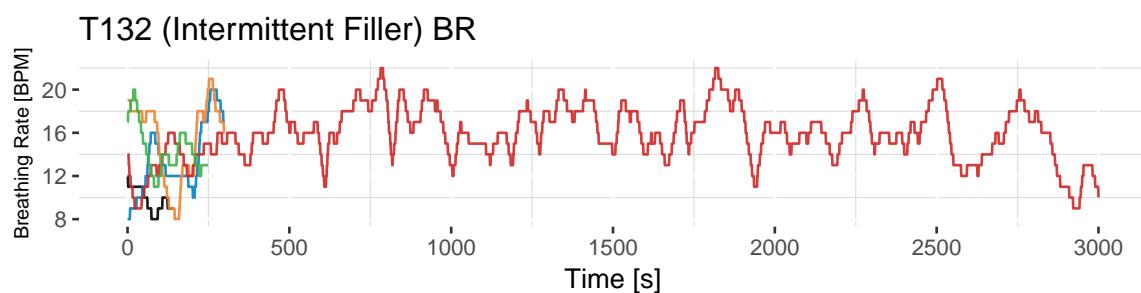
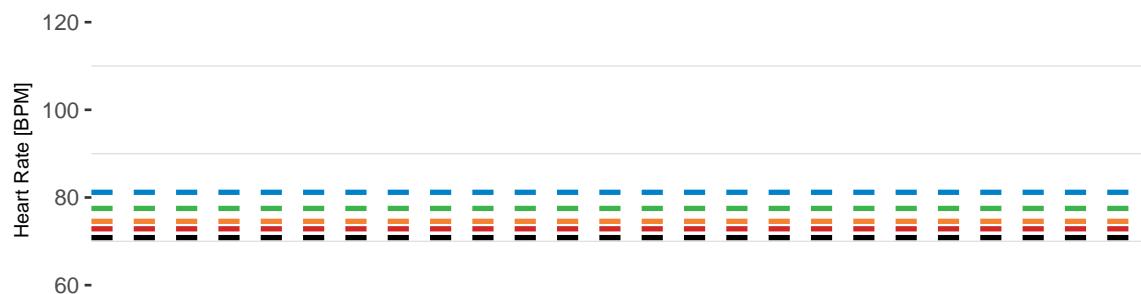
```
##  
## Subject T130 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```







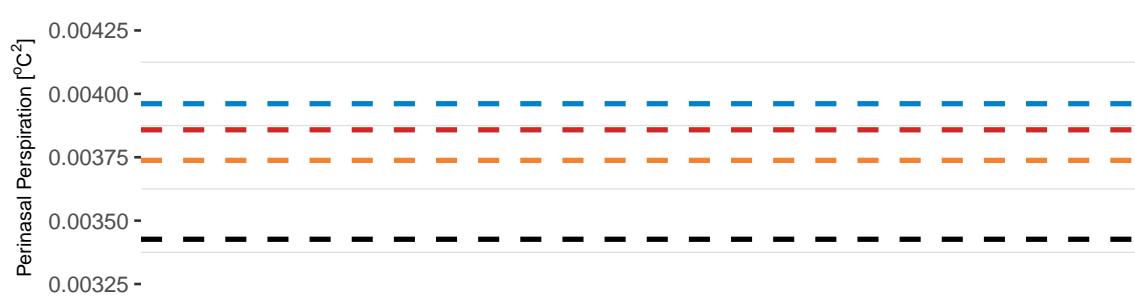
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



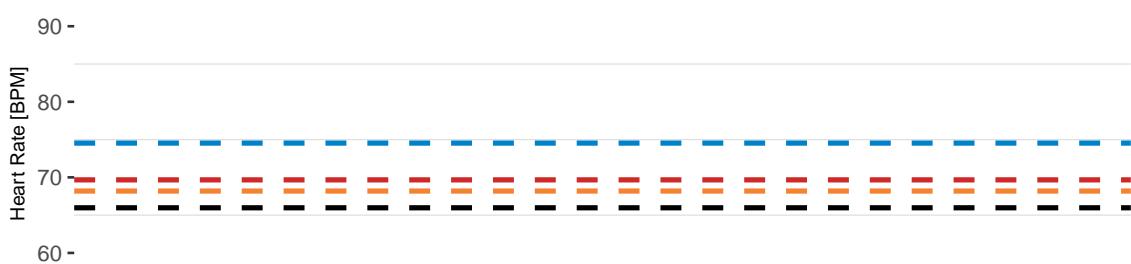
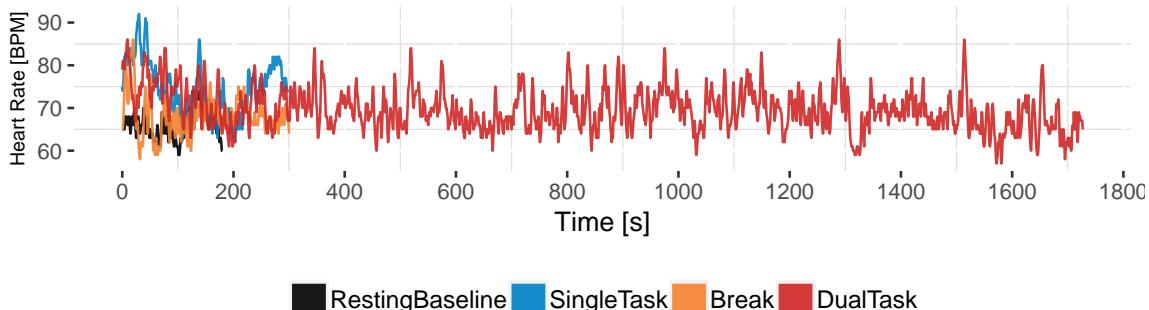
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



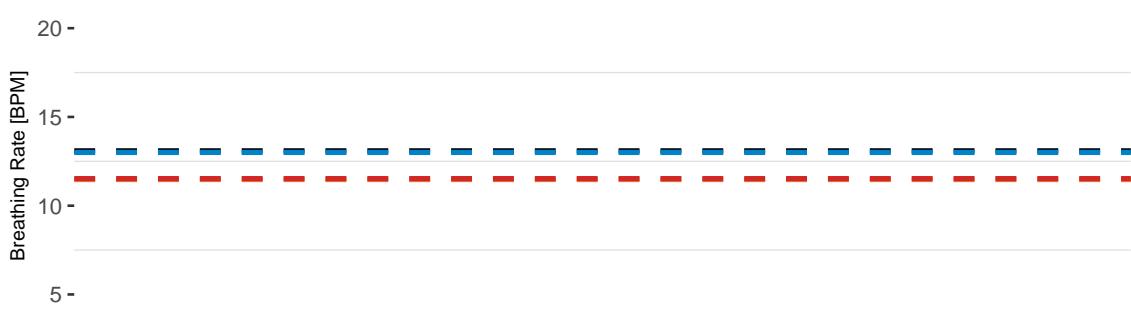
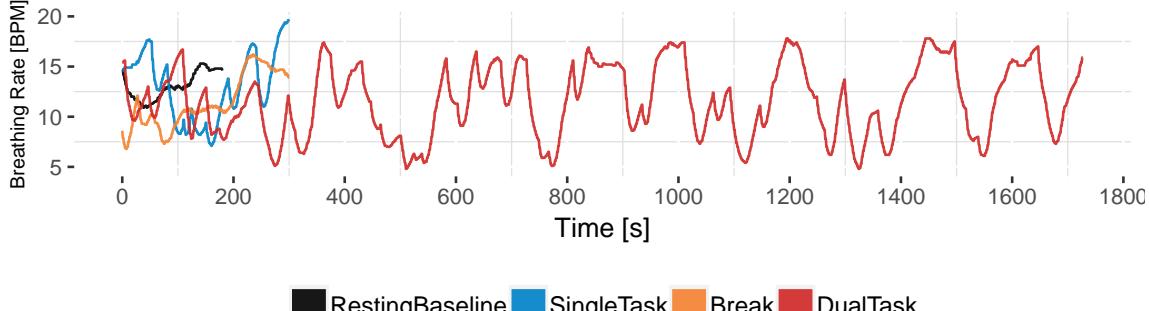
## **Intermittent-Nothing (IN) Subjects**



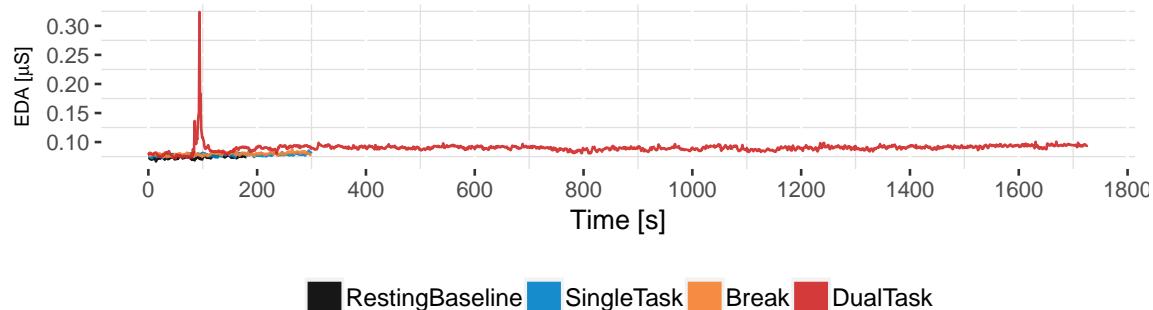
T031 (Intermittent Nothing) HR



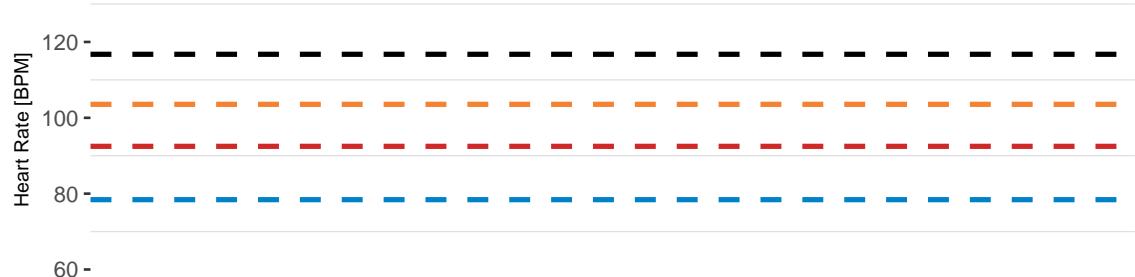
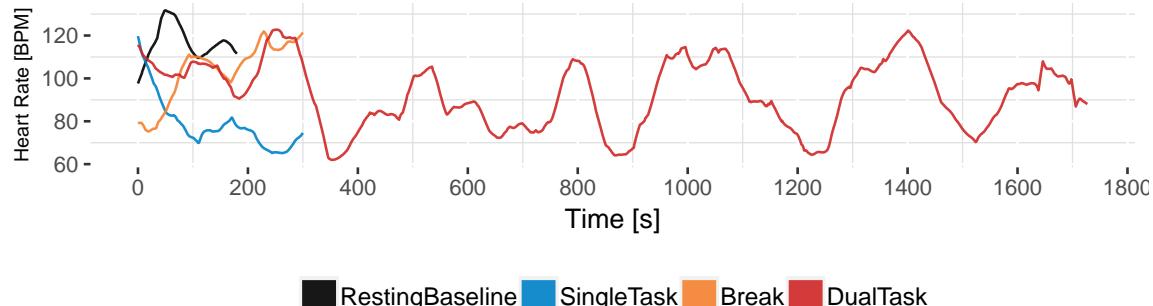
T031 (Intermittent Nothing) BR

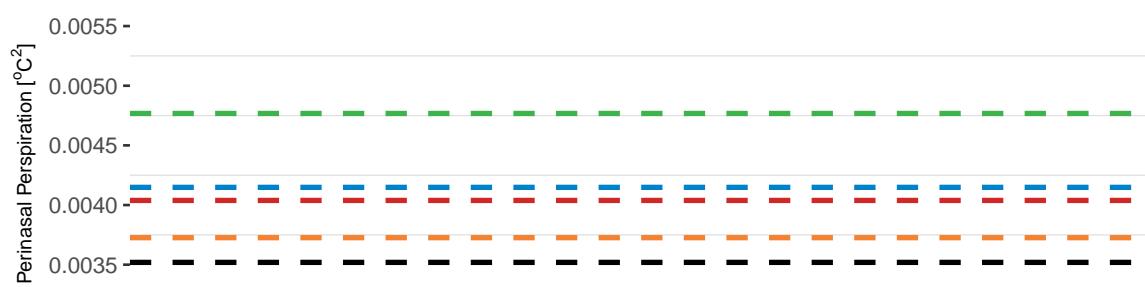


T031 (Intermittent Nothing) N.EDA

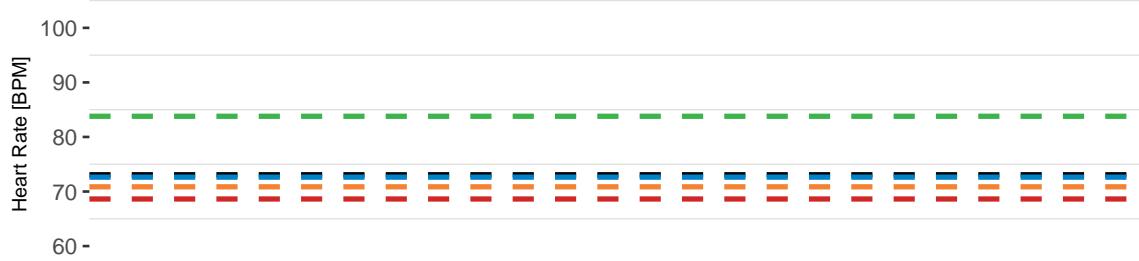
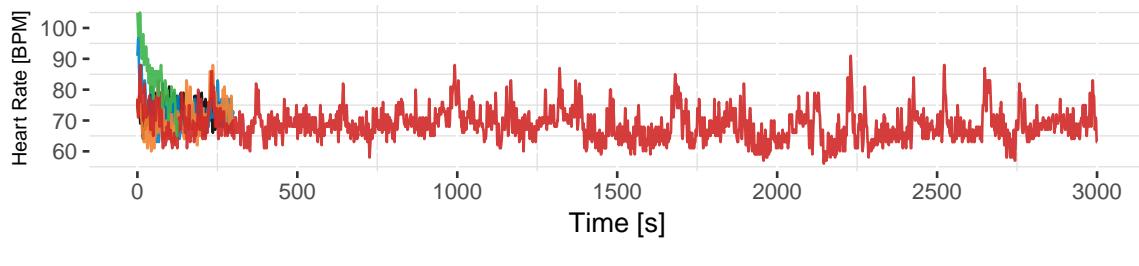


T031 (Intermittent Nothing) N.HR

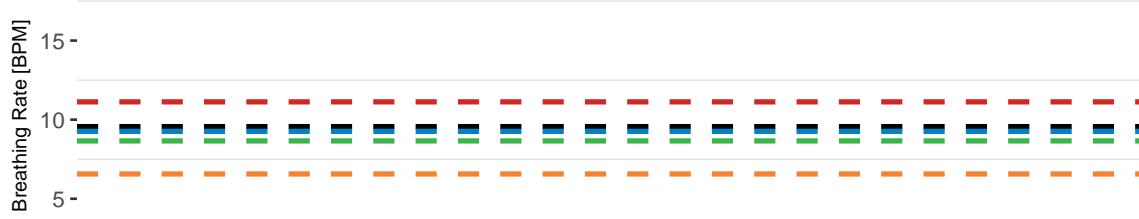
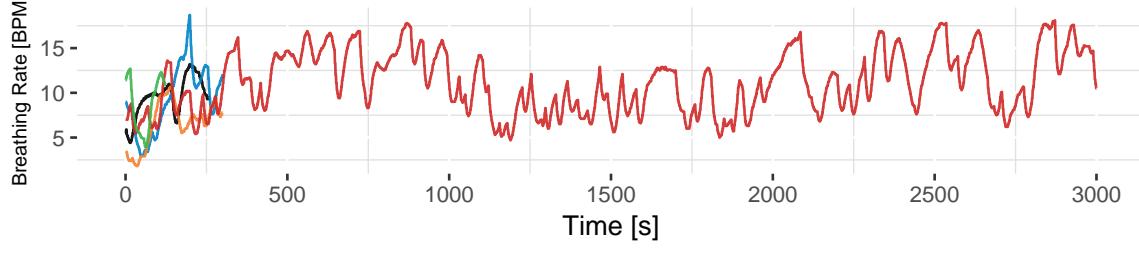




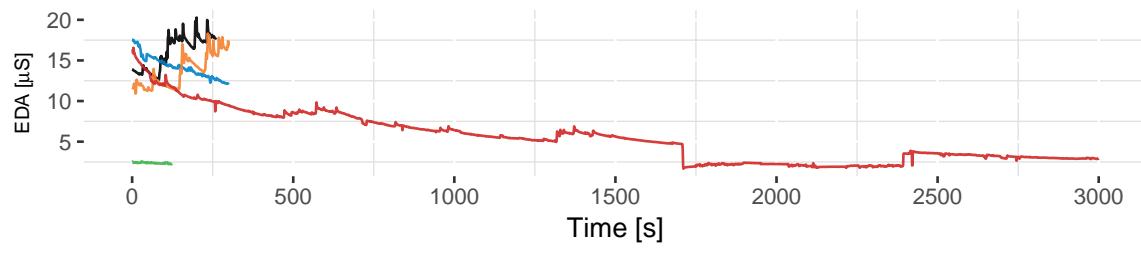
T032 (Intermittent Nothing) HR



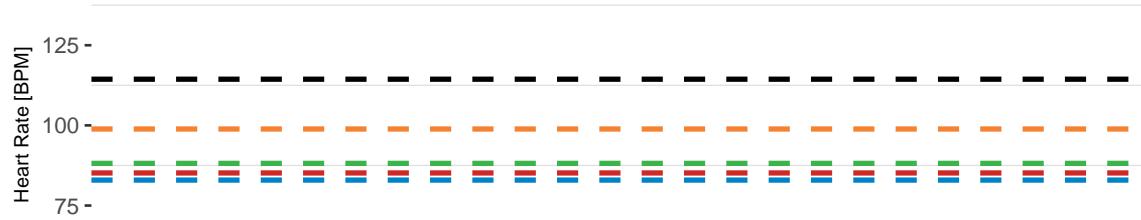
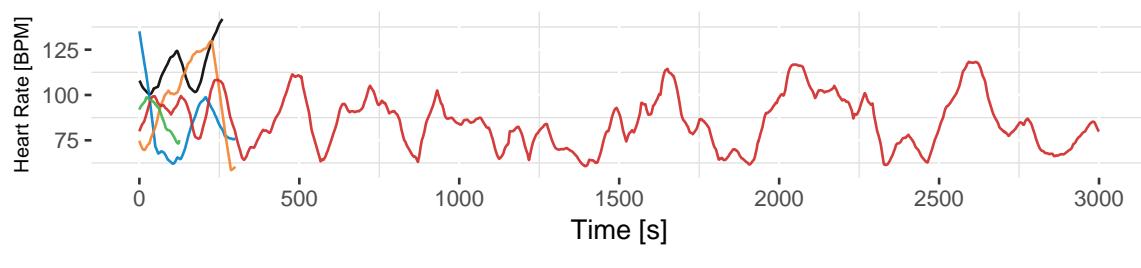
T032 (Intermittent Nothing) BR

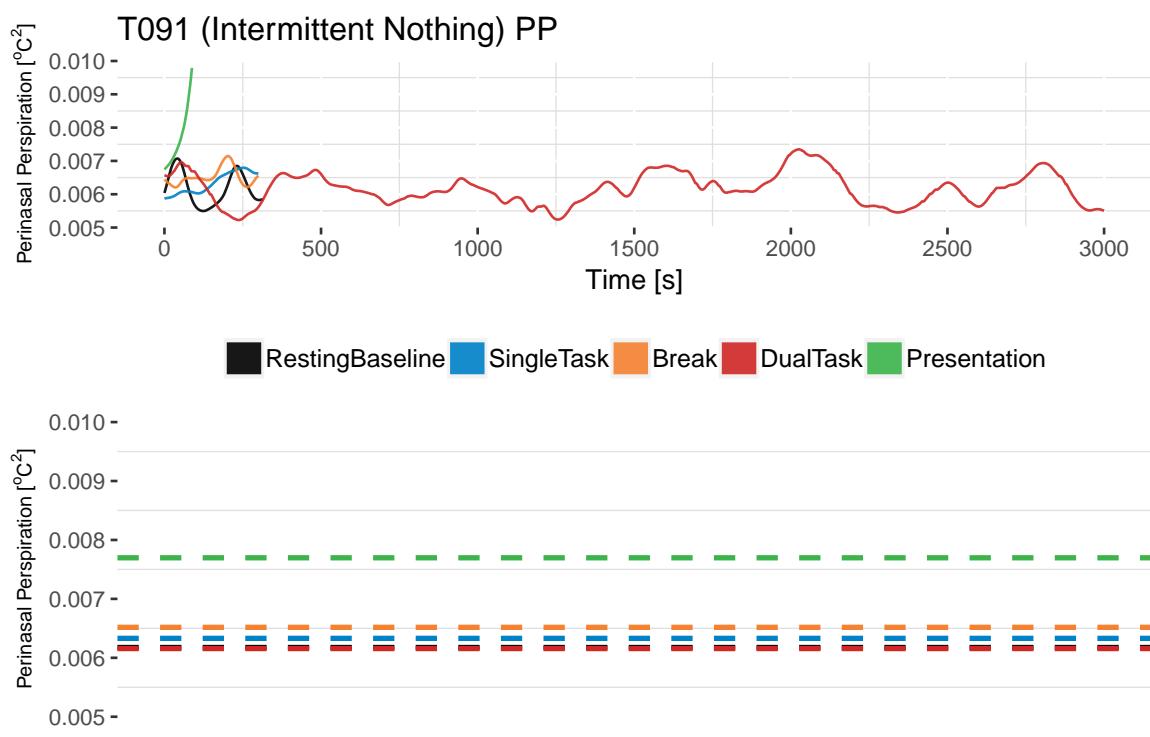


T032 (Intermittent Nothing) N.EDA

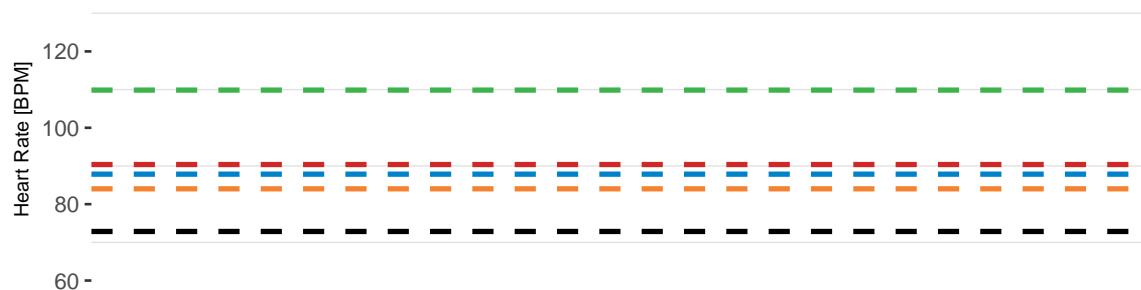
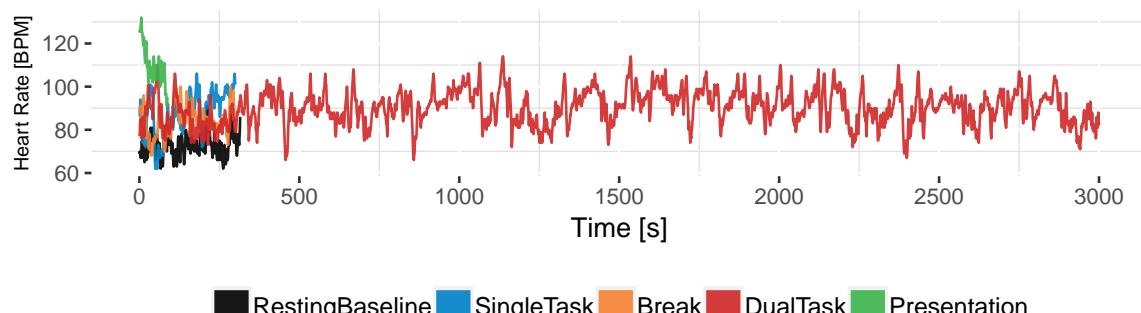


T032 (Intermittent Nothing) N.HR

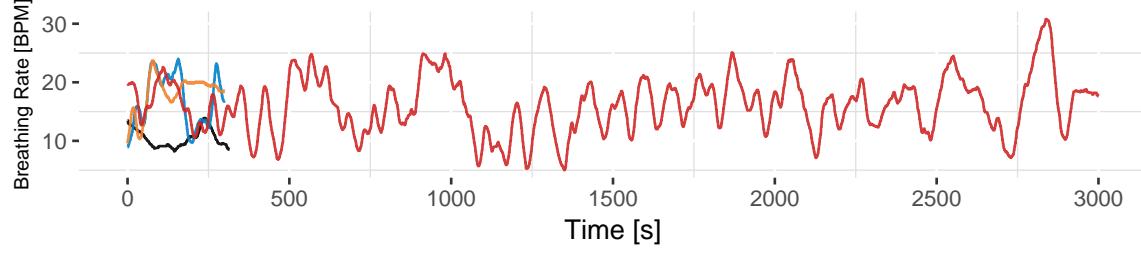




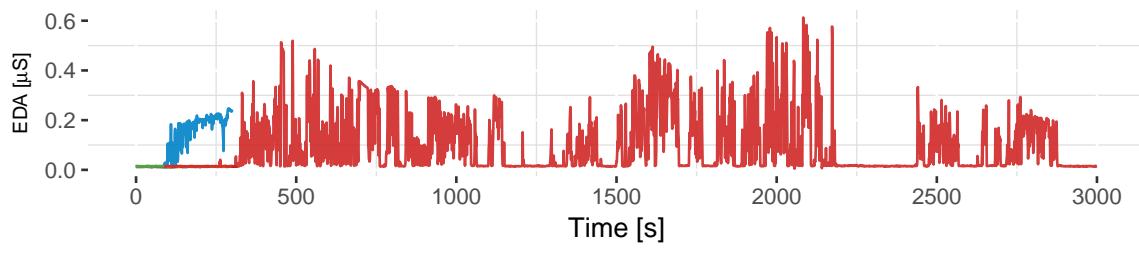
T091 (Intermittent Nothing) HR



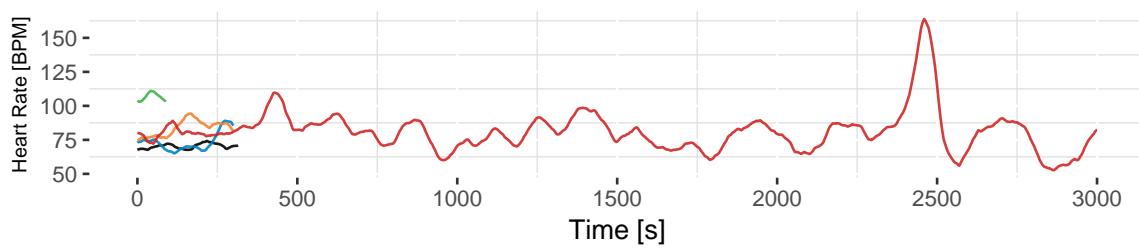
T091 (Intermittent Nothing) BR

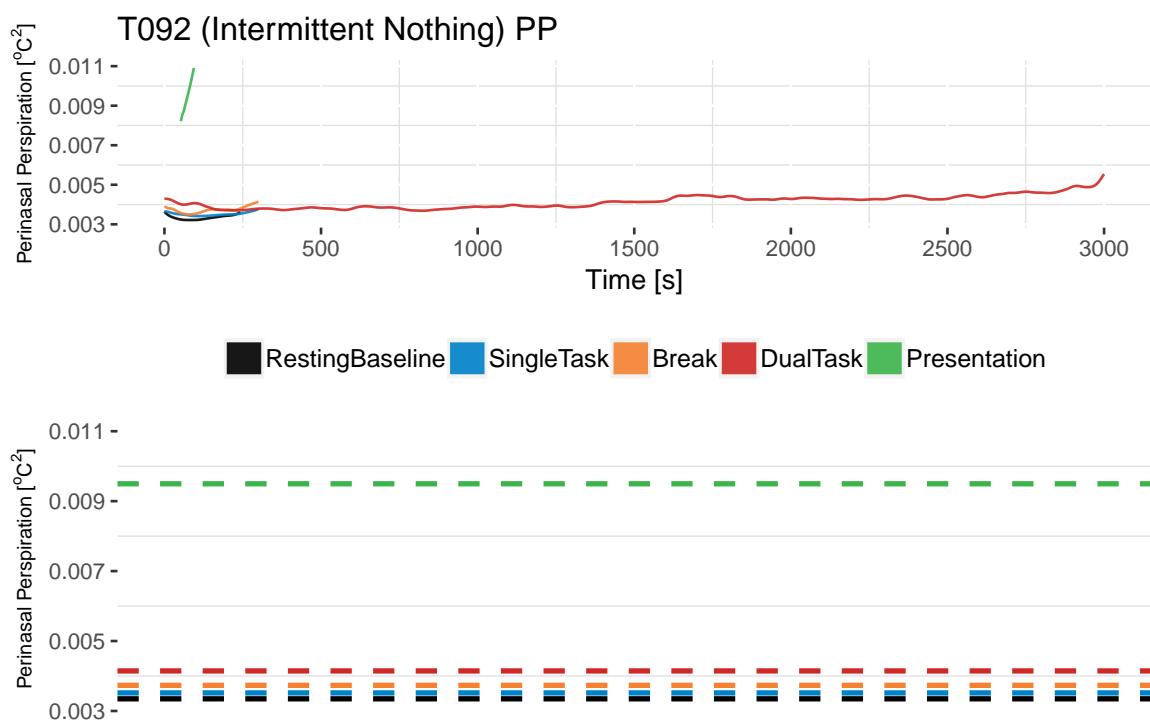


T091 (Intermittent Nothing) N.EDA



T091 (Intermittent Nothing) N.HR





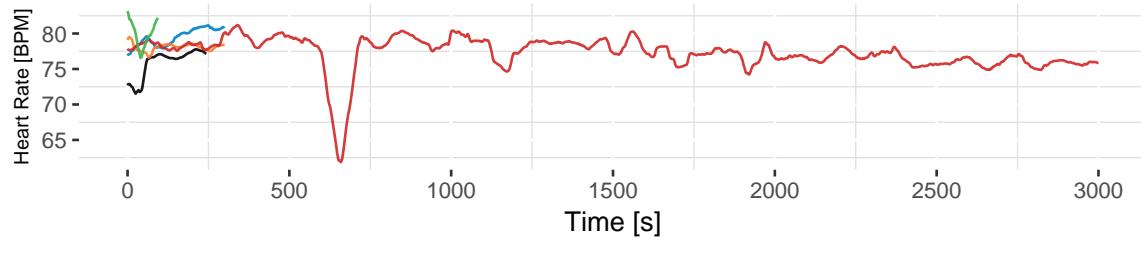
##

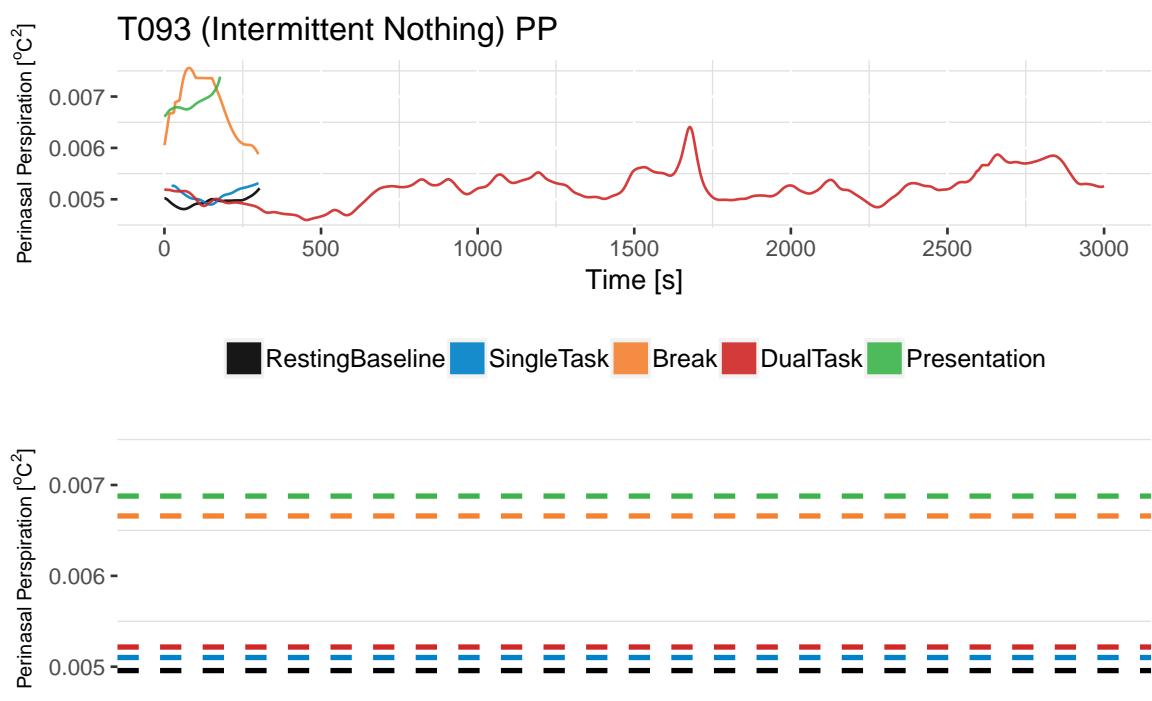


T092 (Intermittent Nothing) N.EDA

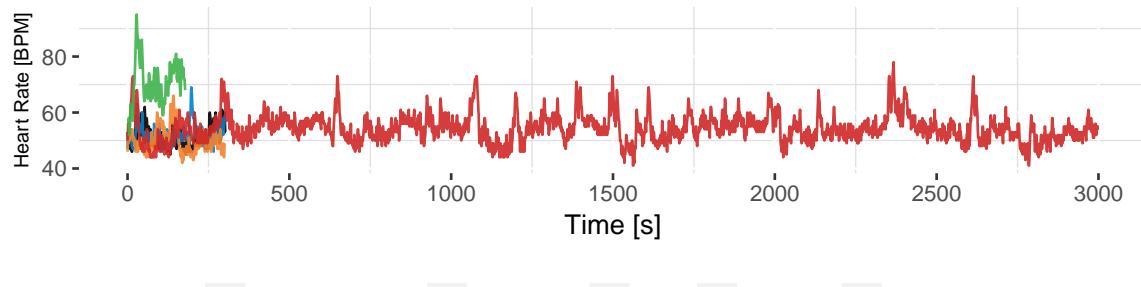


T092 (Intermittent Nothing) N.HR

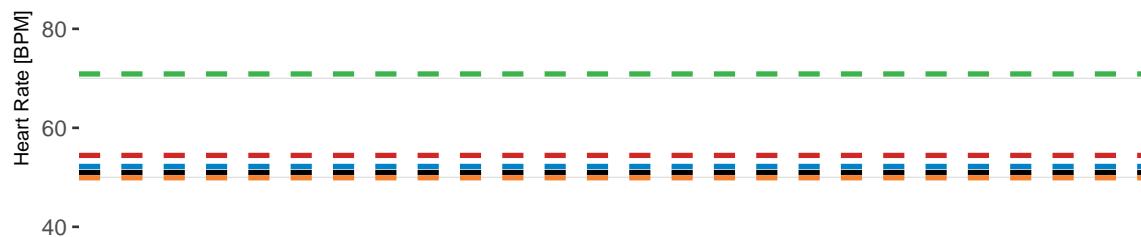




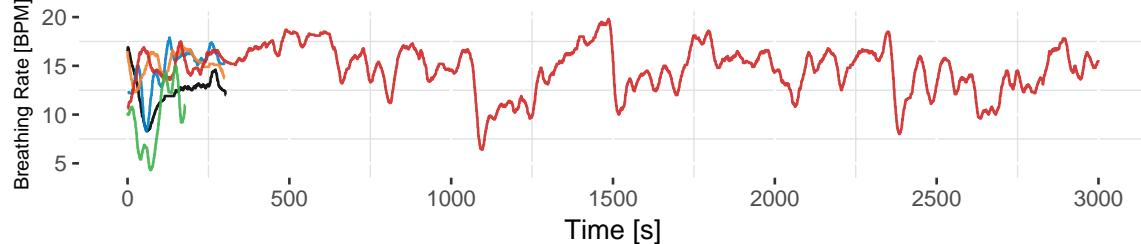
T093 (Intermittent Nothing) HR



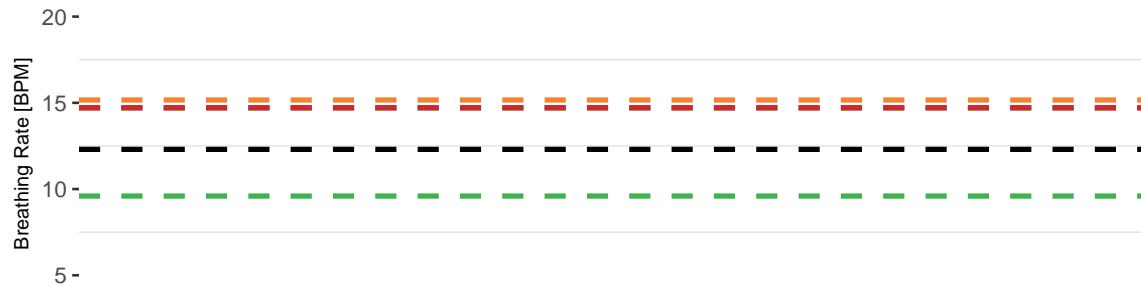
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

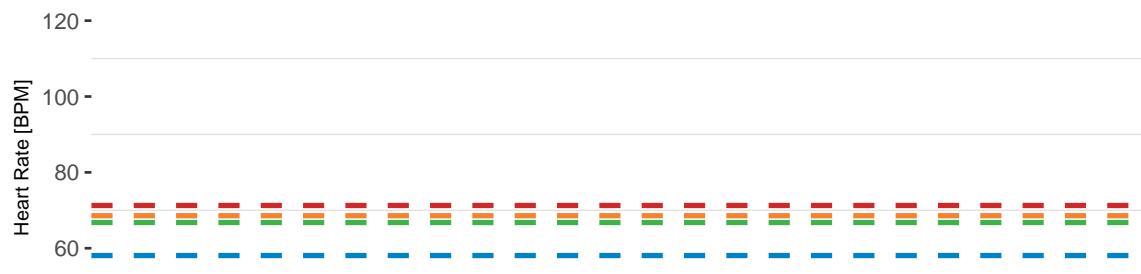
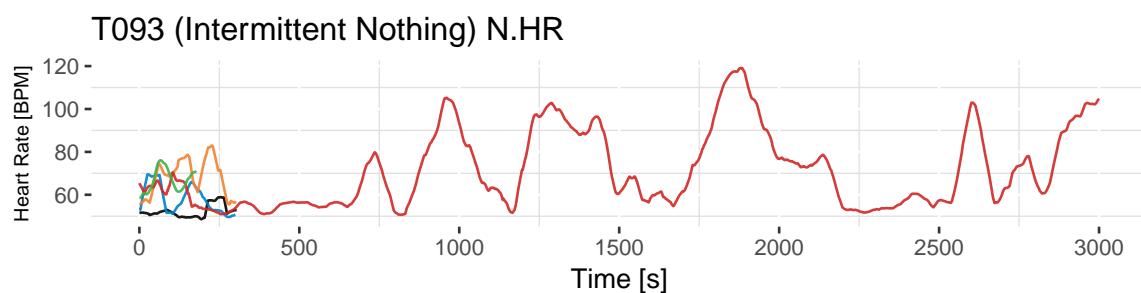
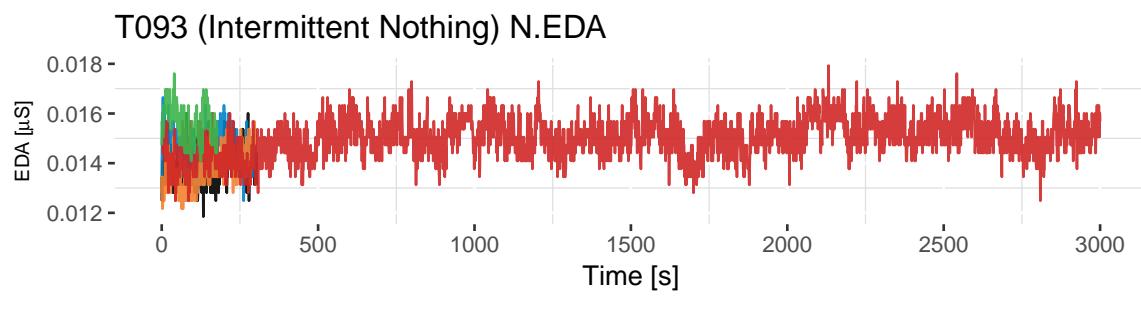


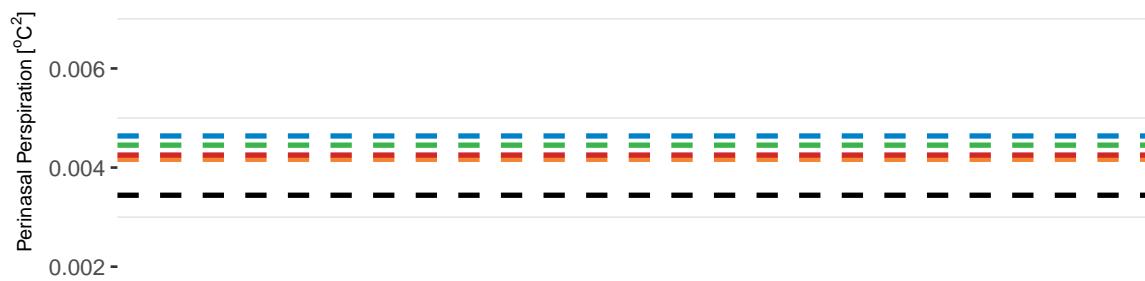
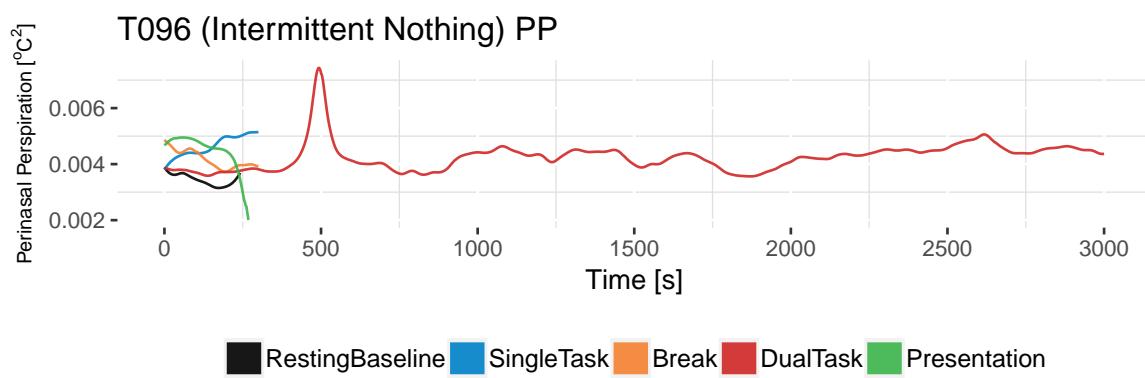
T093 (Intermittent Nothing) BR

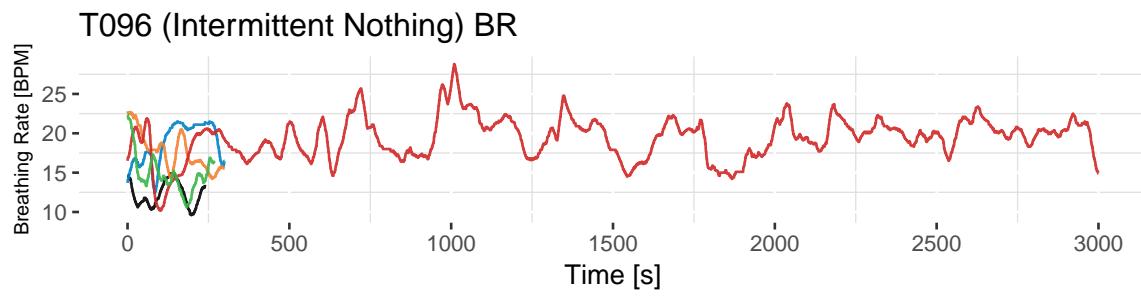
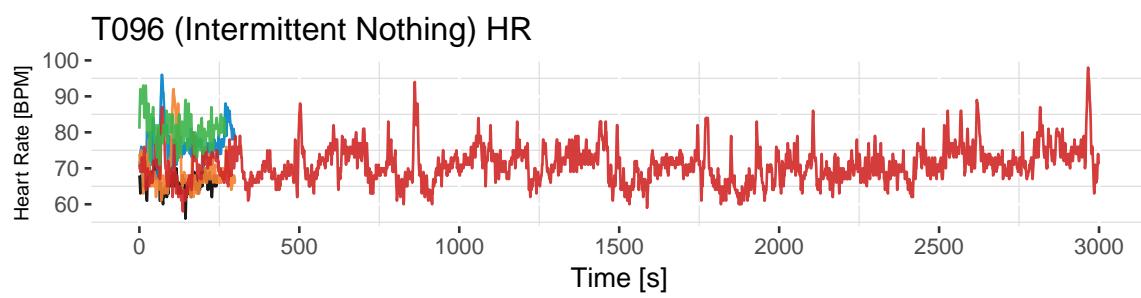


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

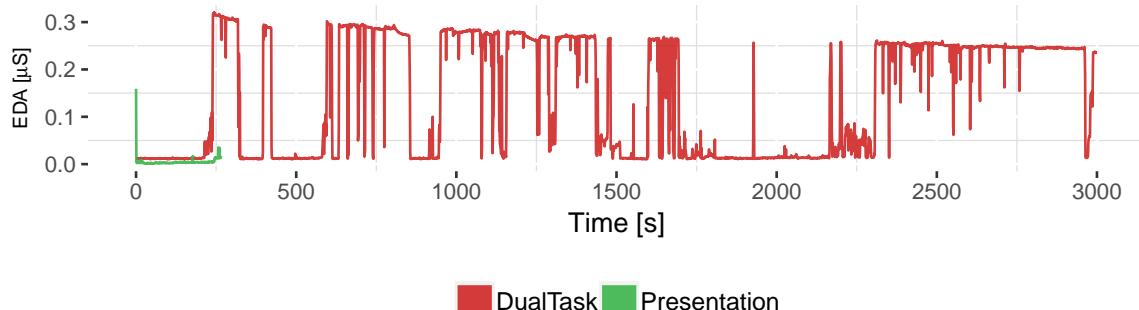




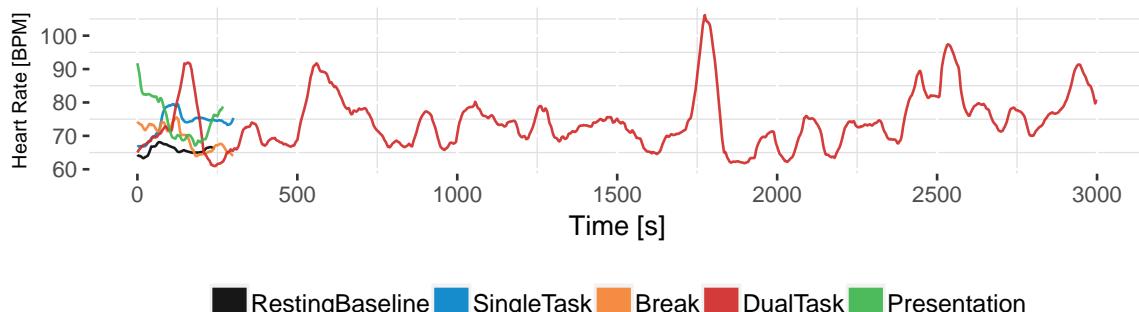




T096 (Intermittent Nothing) N.EDA

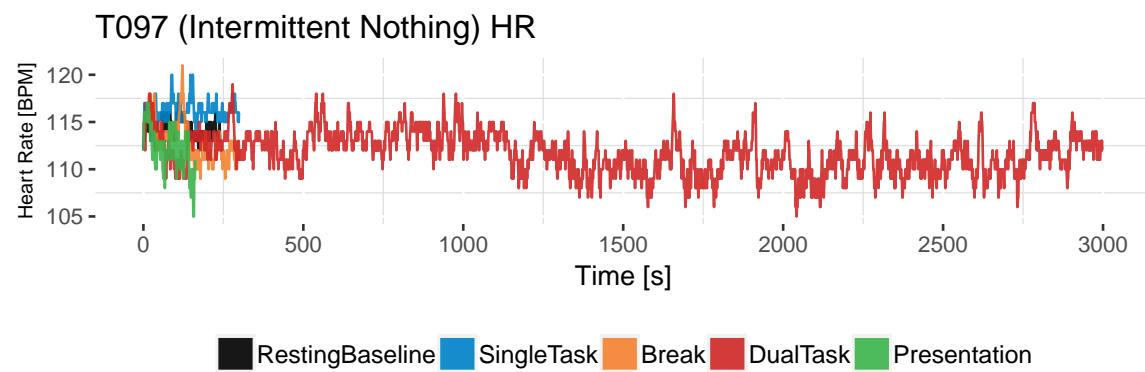


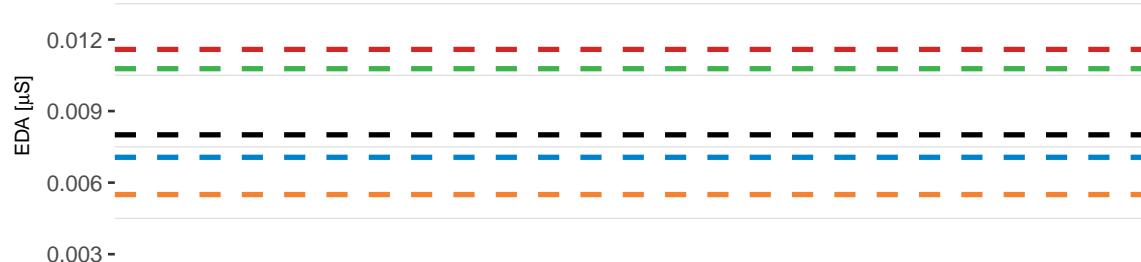
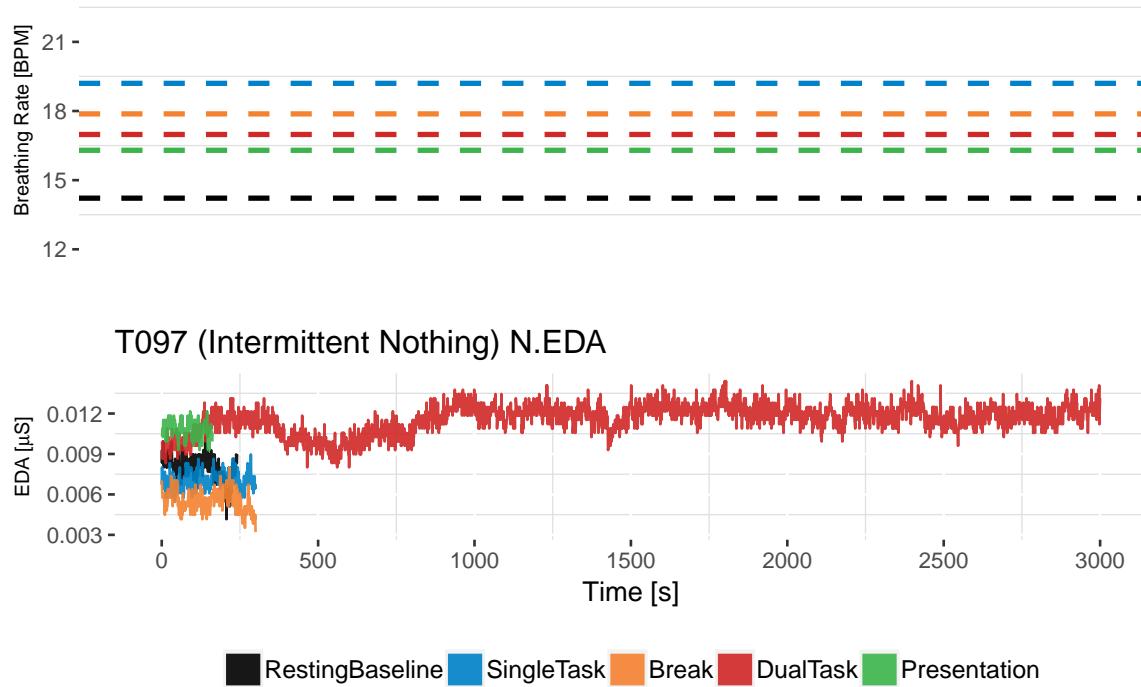
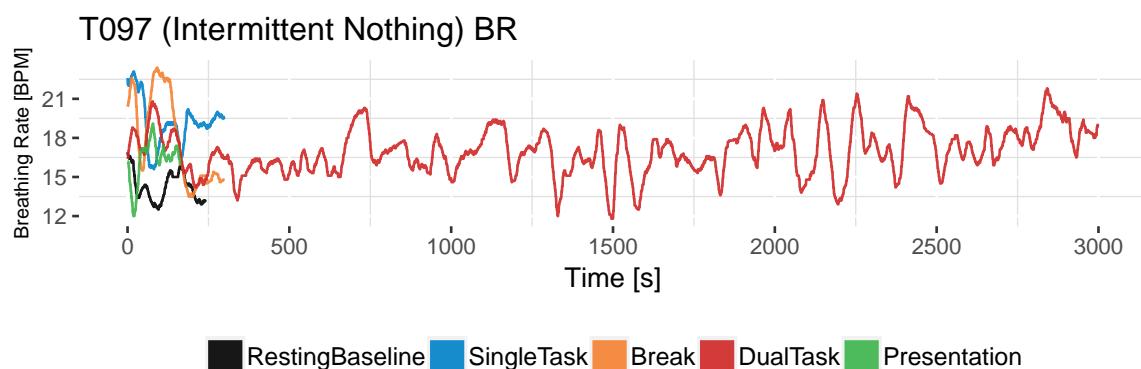
T096 (Intermittent Nothing) N.HR

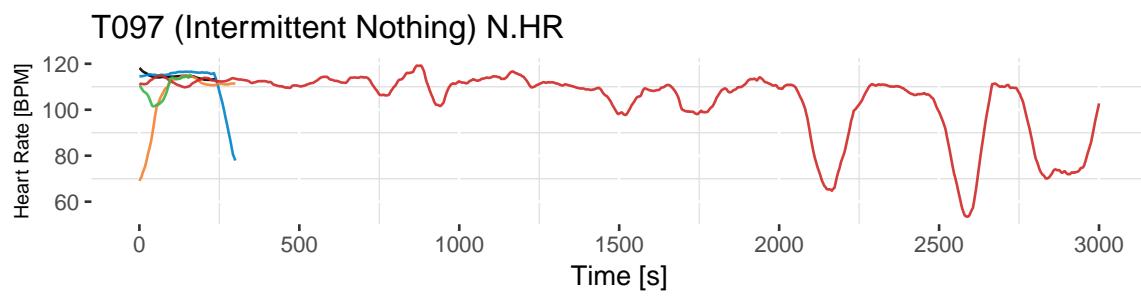


##  
##

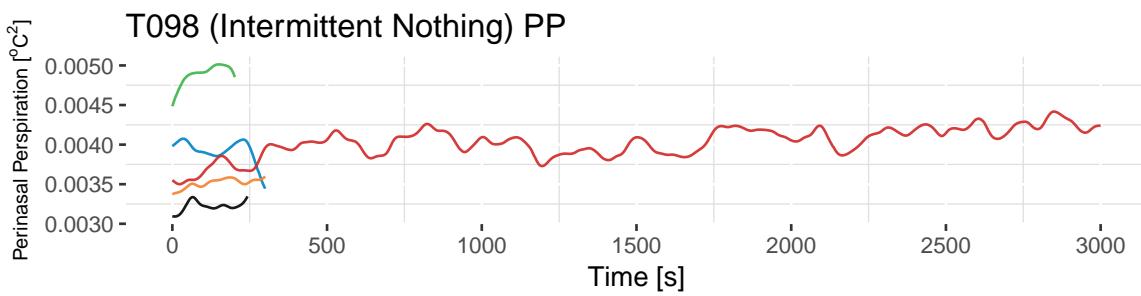
```
## Subject T097 does not have PP signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```







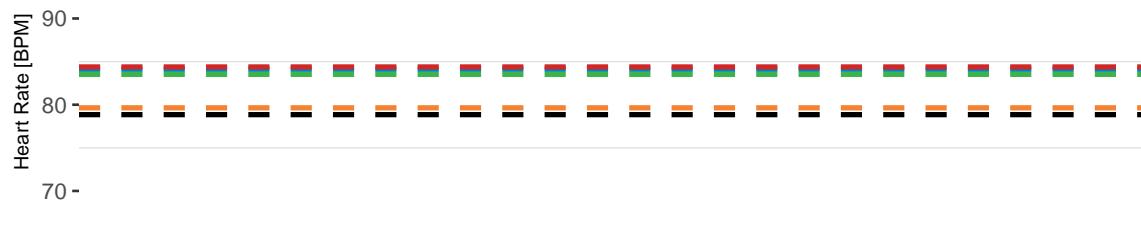
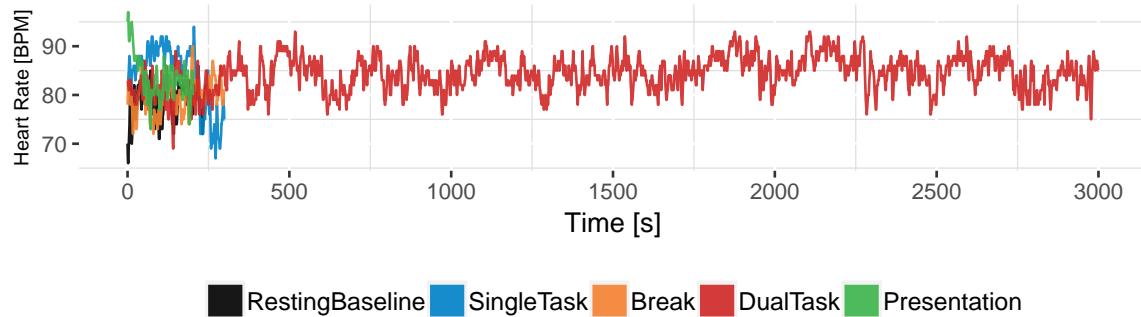
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

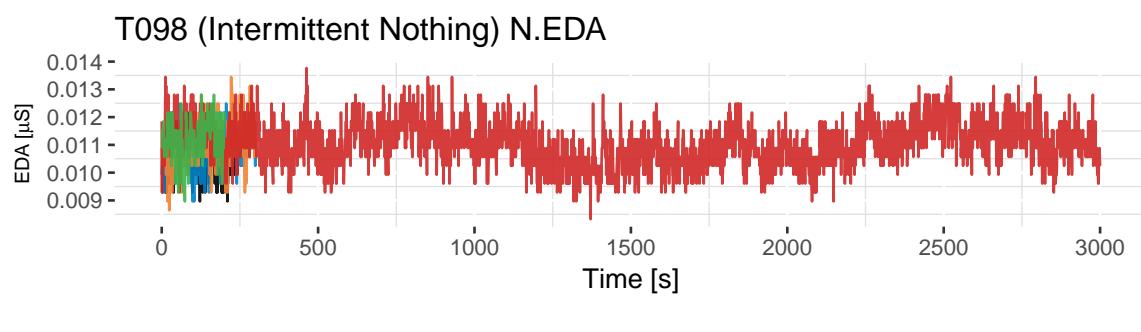
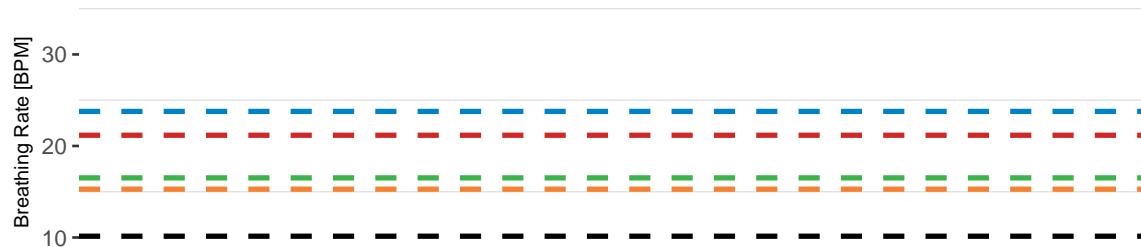
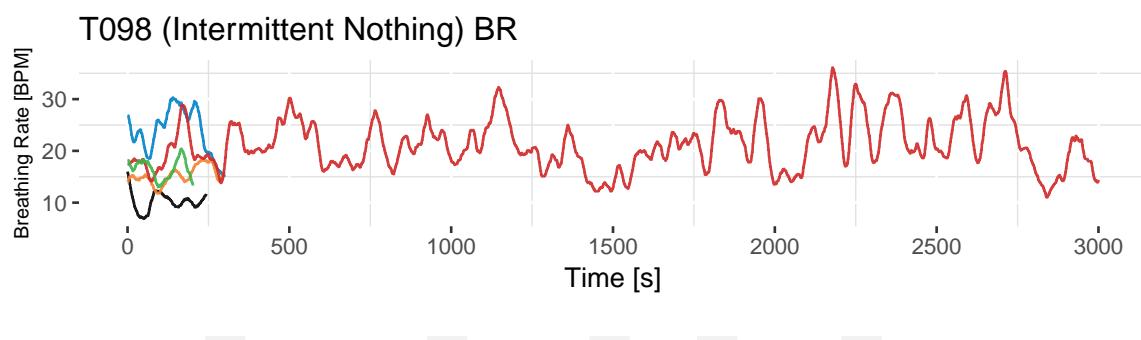


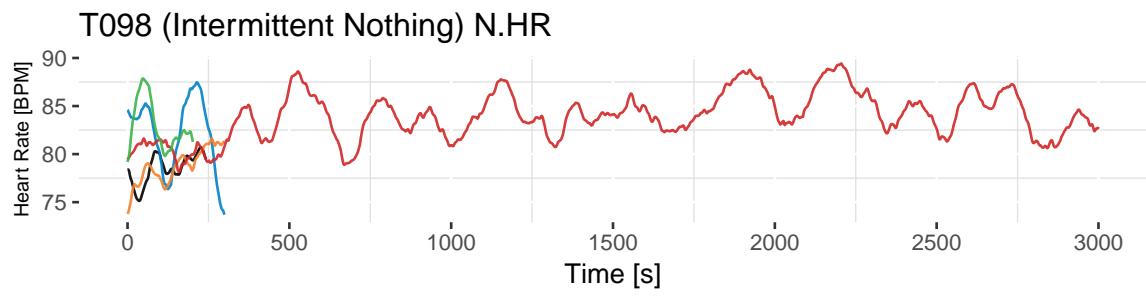
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



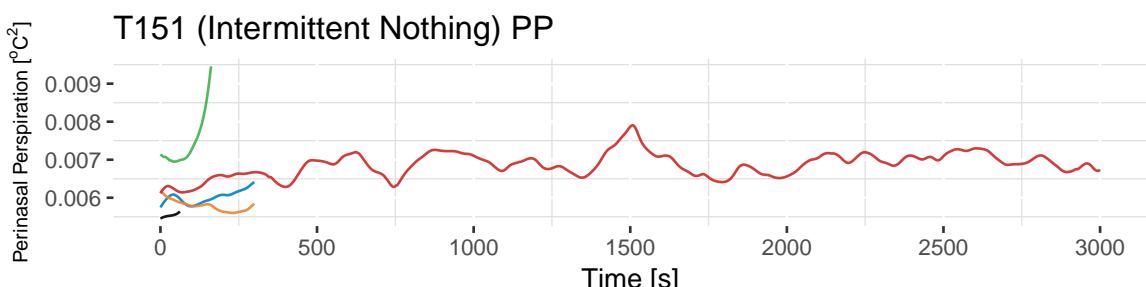
T098 (Intermittent Nothing) HR







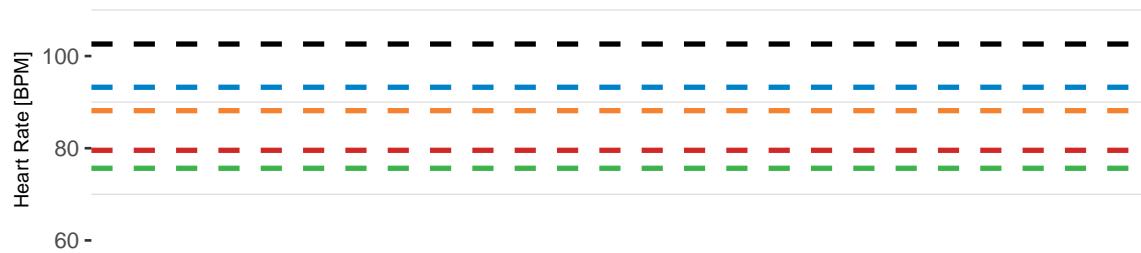
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

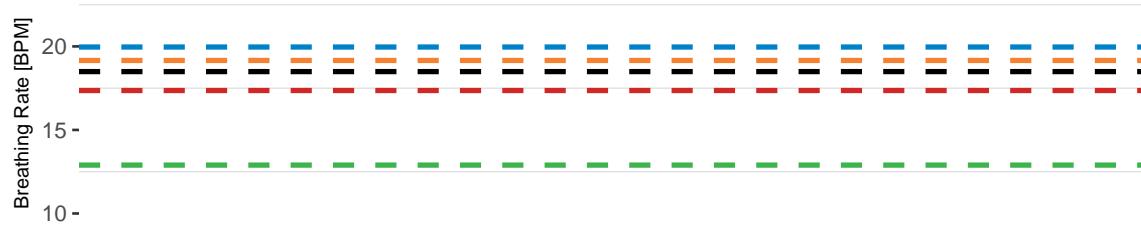


T151 (Intermittent Nothing) HR





■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

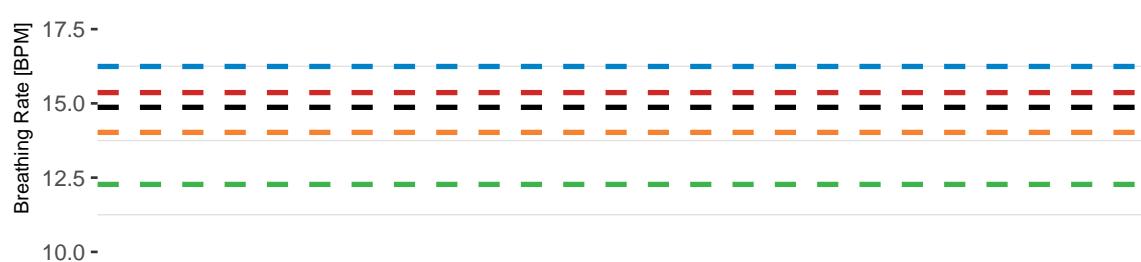
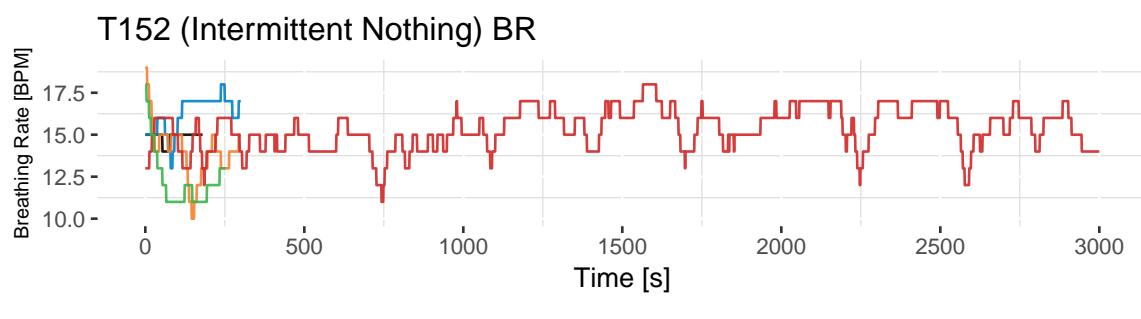


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



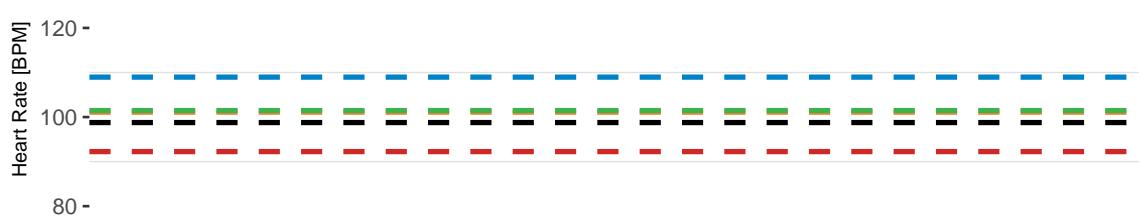
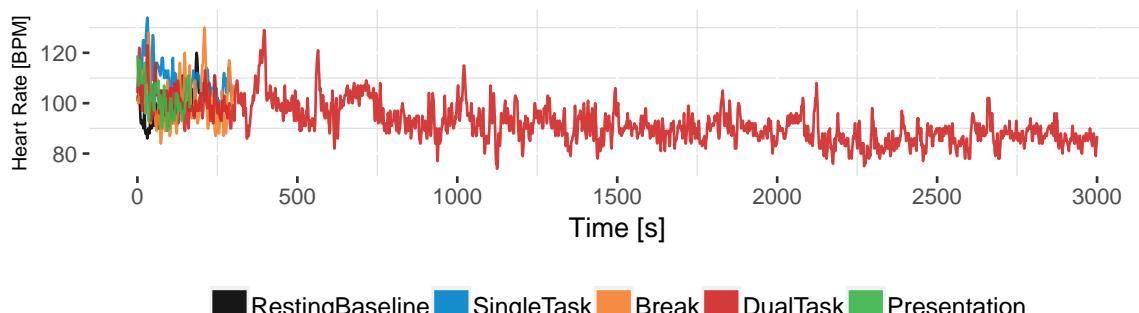
T152 (Intermittent Nothing) HR



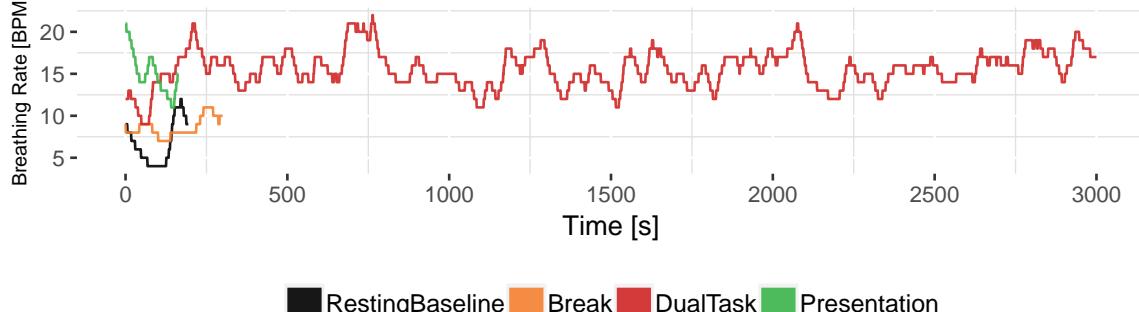


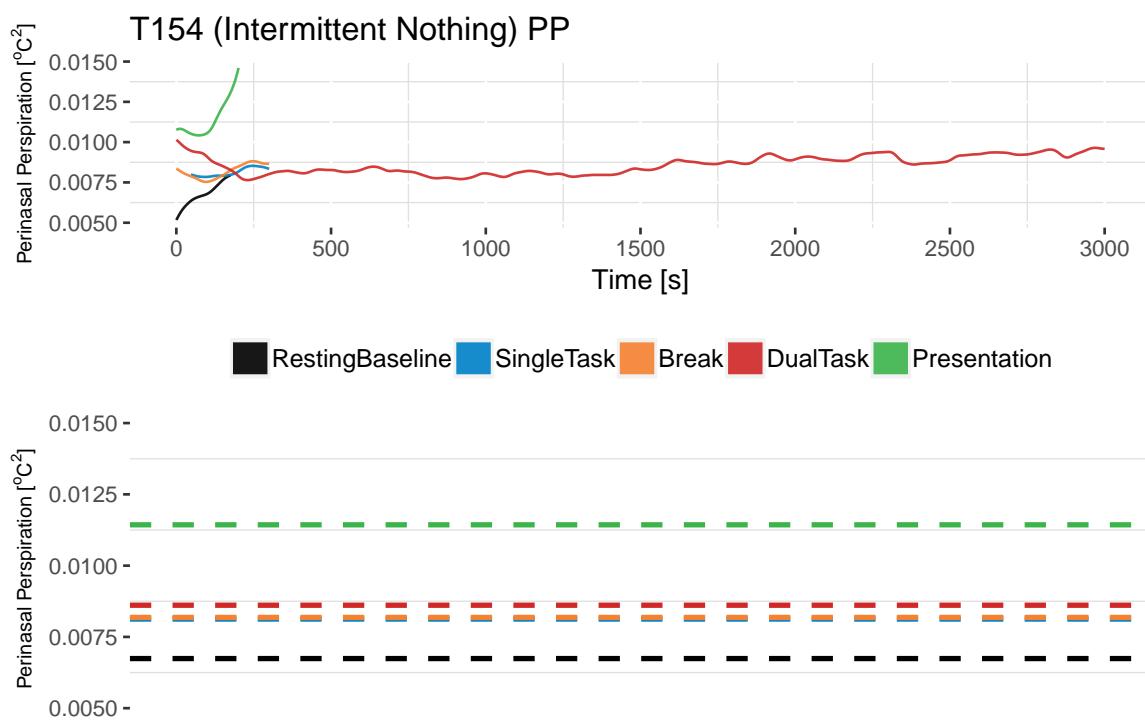
```
##
## Subject T153 does not have PP signals.
##
```

T153 (Intermittent Nothing) HR

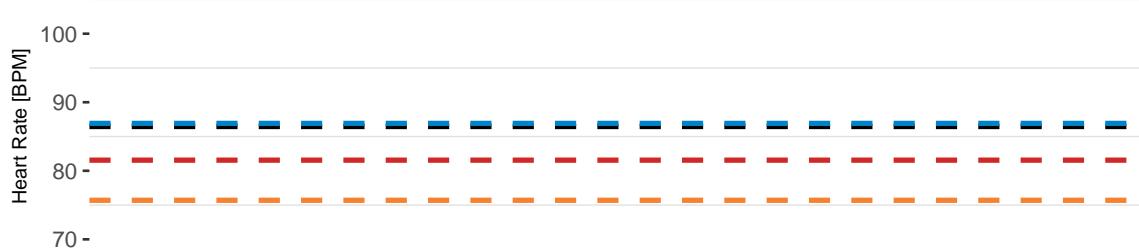


T153 (Intermittent Nothing) BR

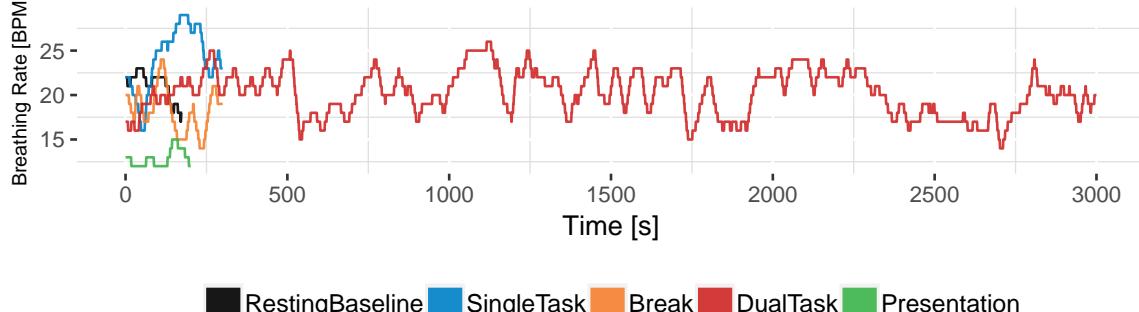


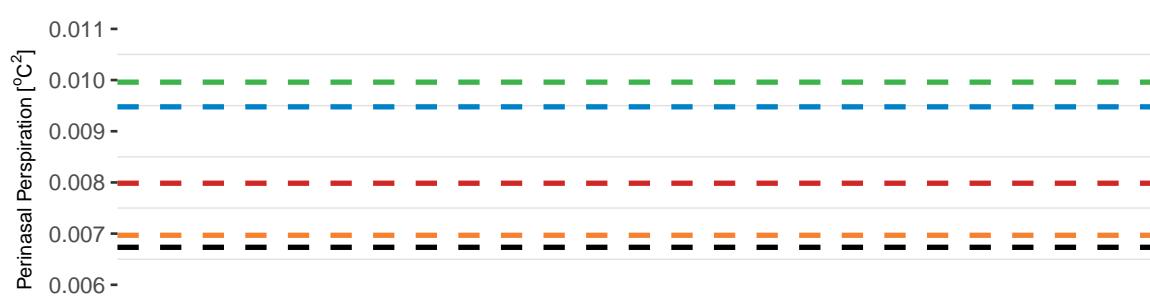
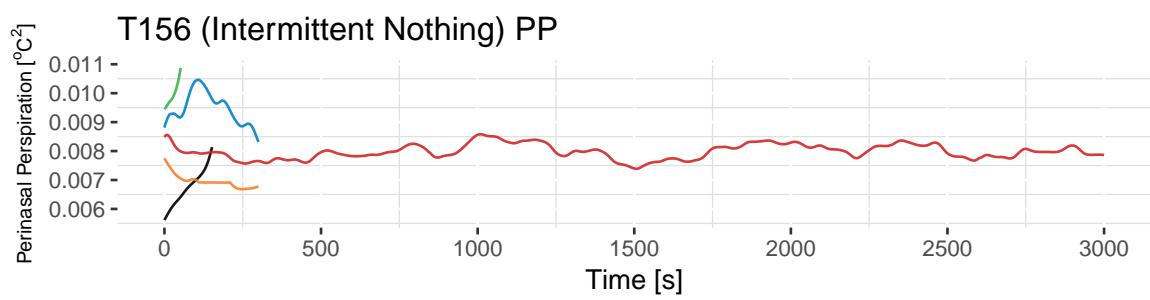


T154 (Intermittent Nothing) HR

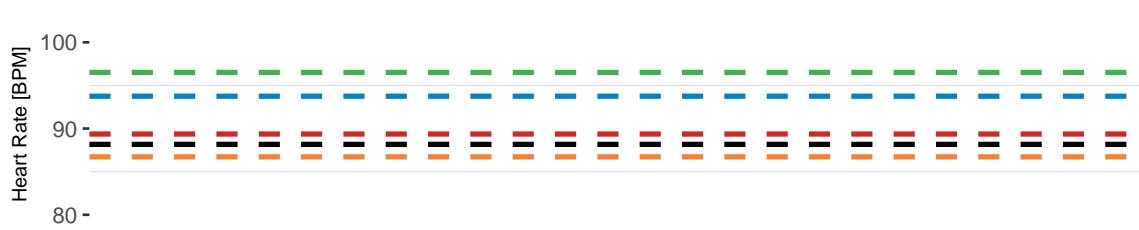
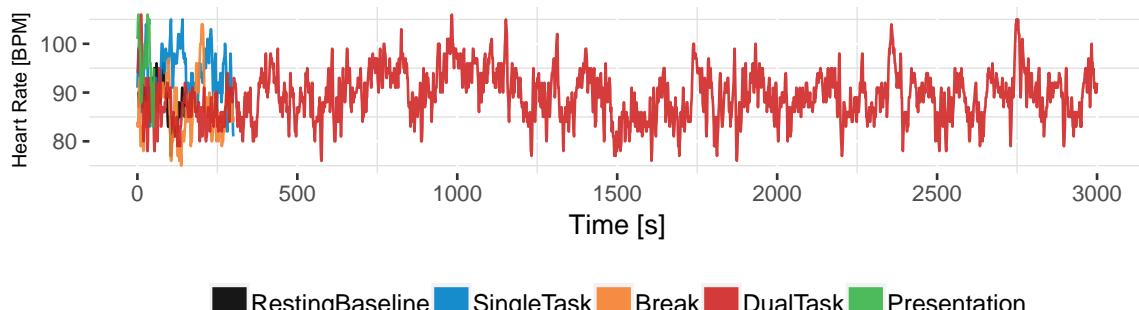


T154 (Intermittent Nothing) BR

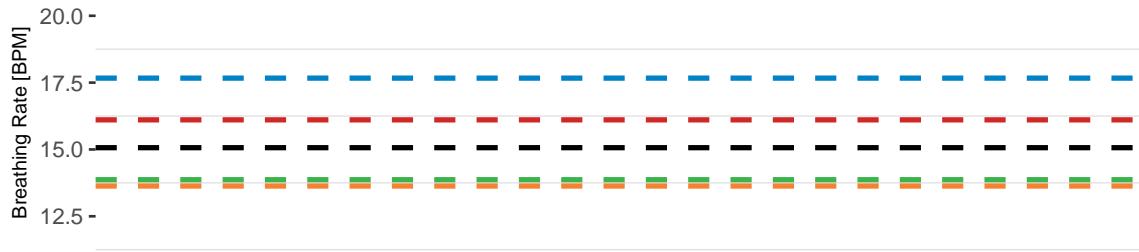
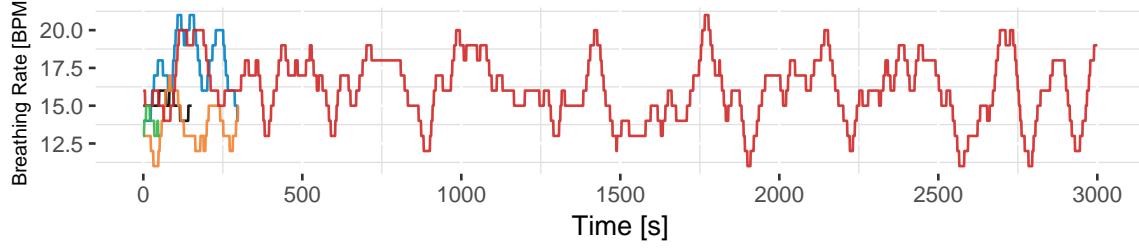


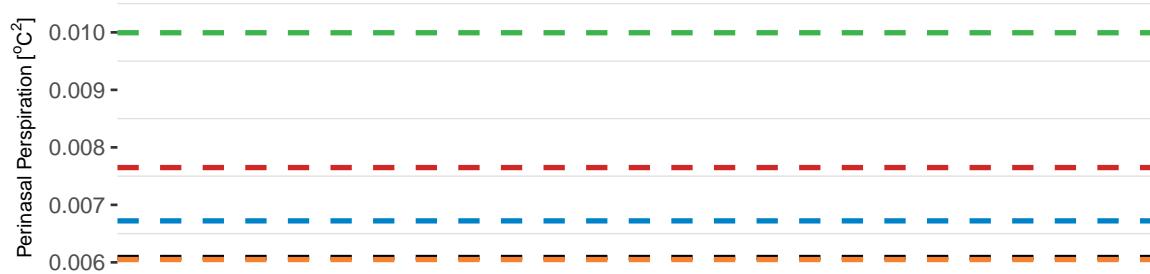
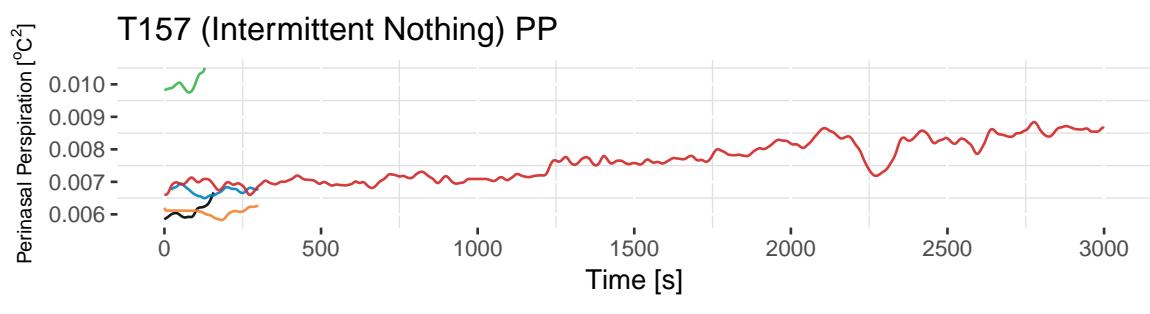


T156 (Intermittent Nothing) HR

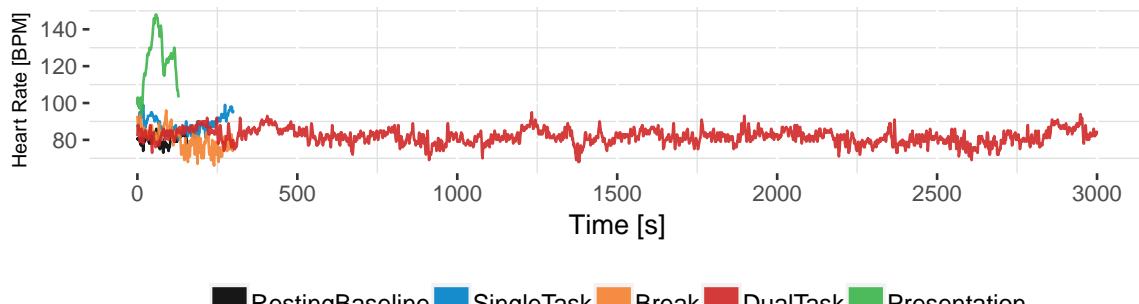


T156 (Intermittent Nothing) BR

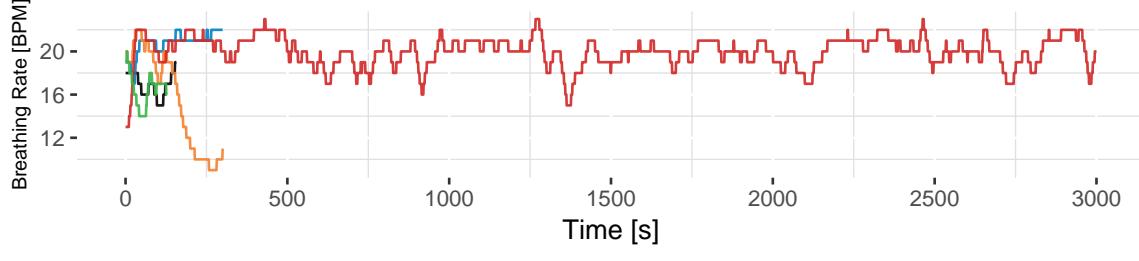




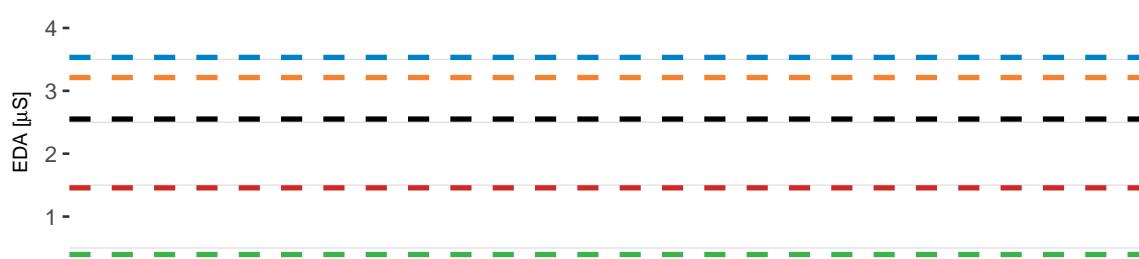
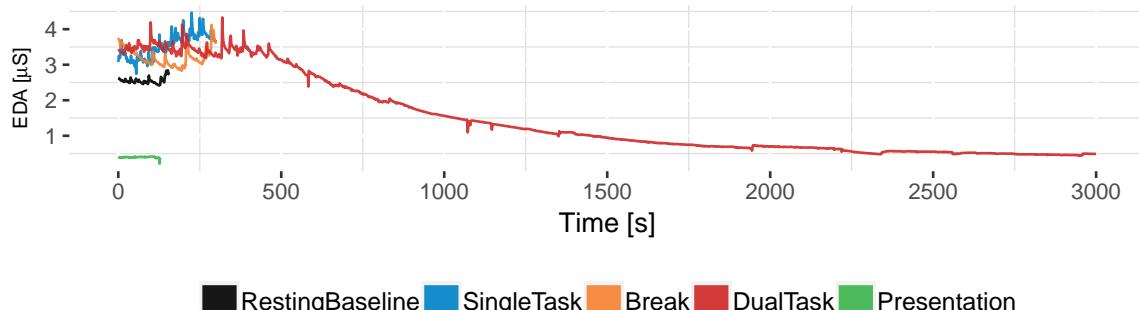
T157 (Intermittent Nothing) HR



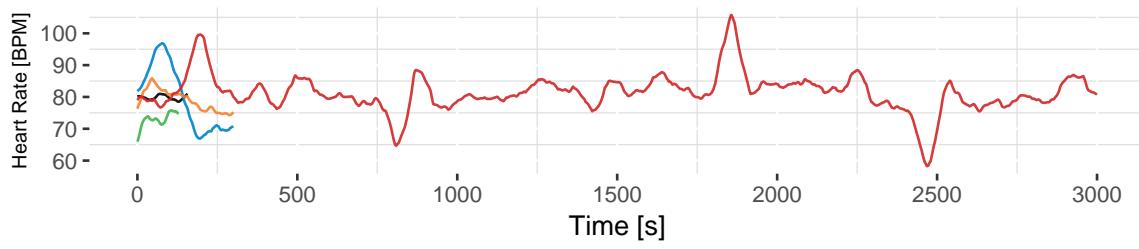
T157 (Intermittent Nothing) BR



T157 (Intermittent Nothing) N.EDA

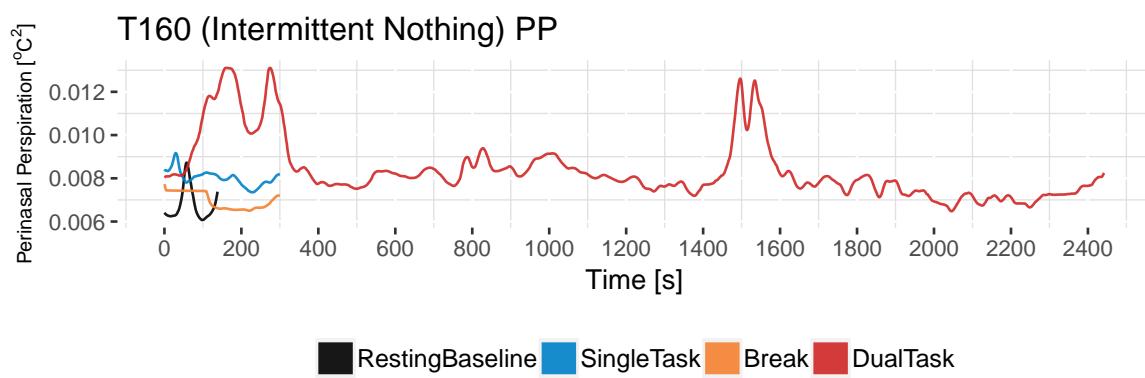


T157 (Intermittent Nothing) N.HR

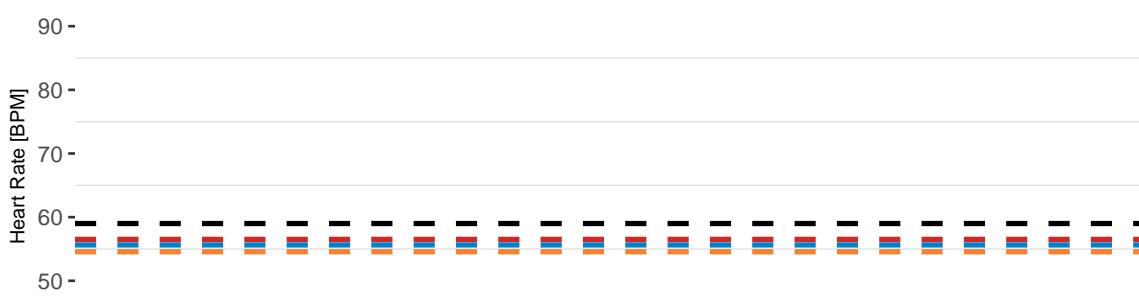
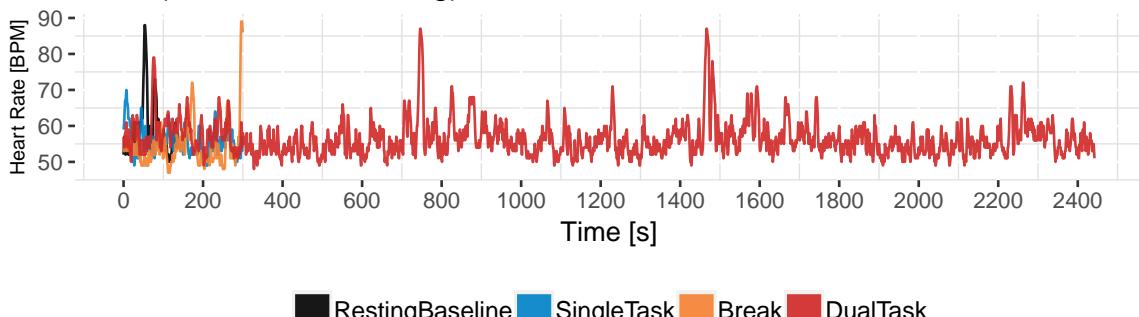


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

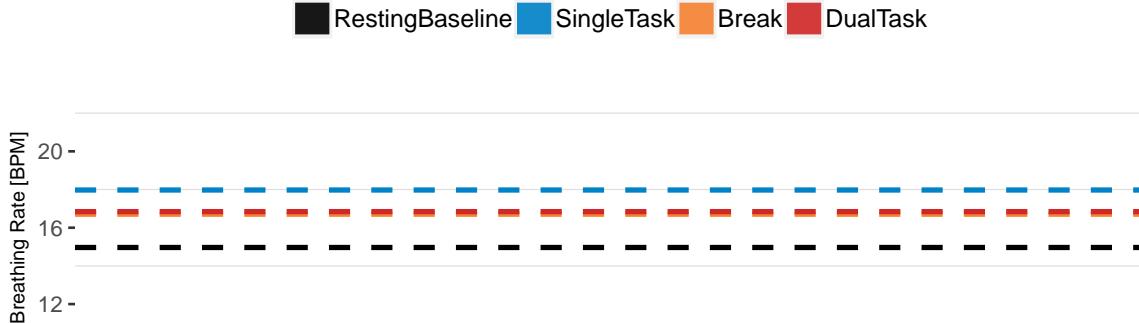
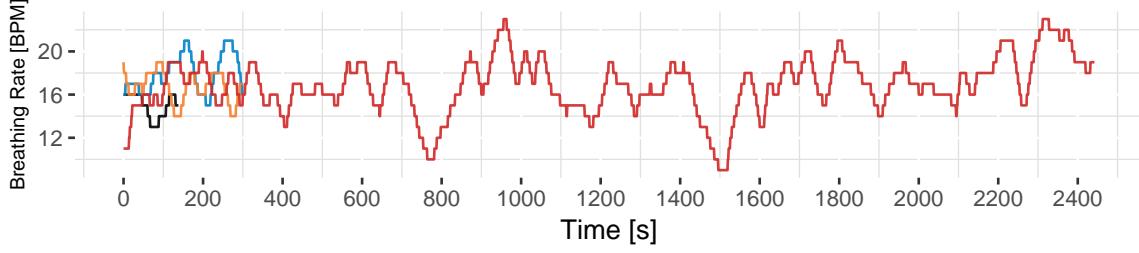




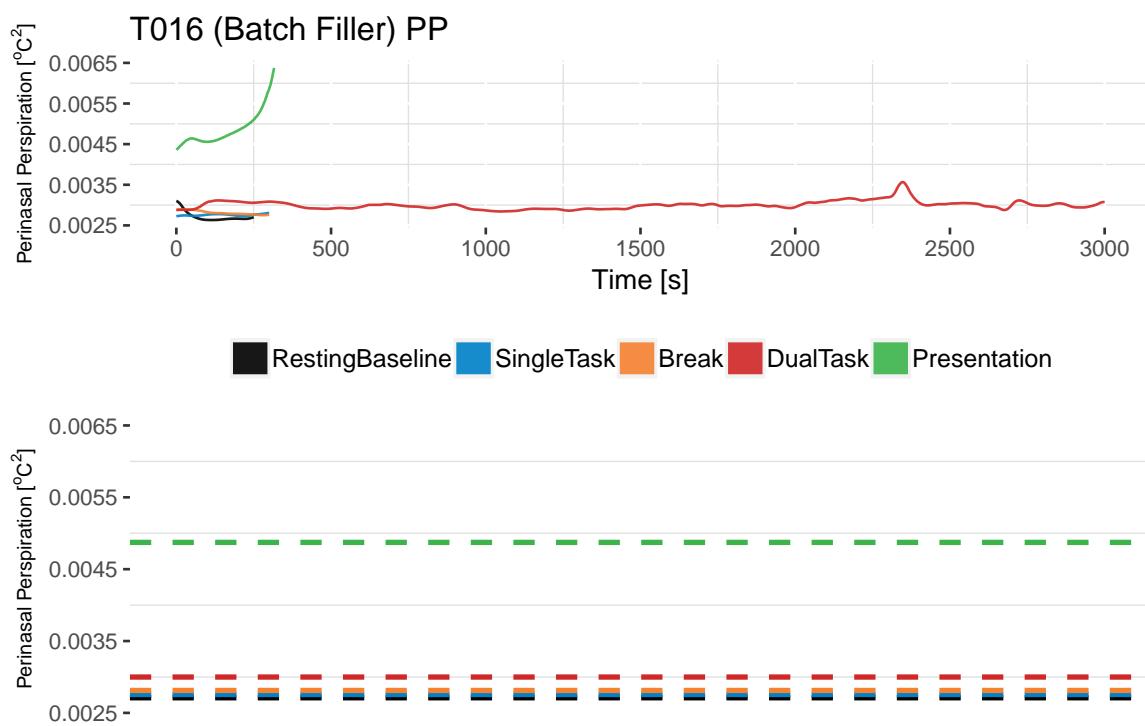
T160 (Intermittent Nothing) HR

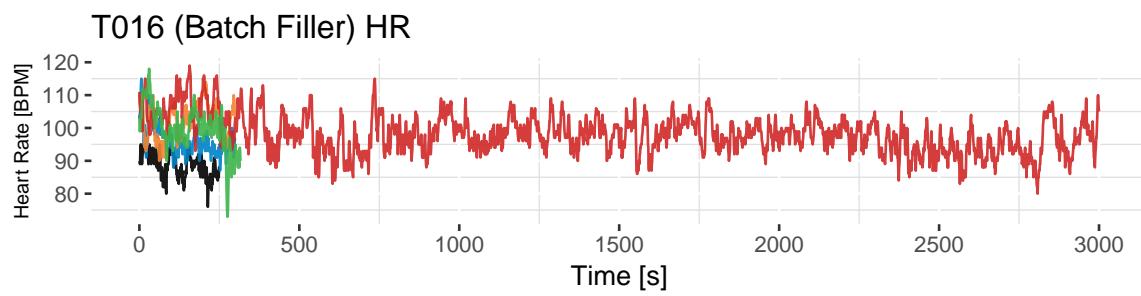


T160 (Intermittent Nothing) BR

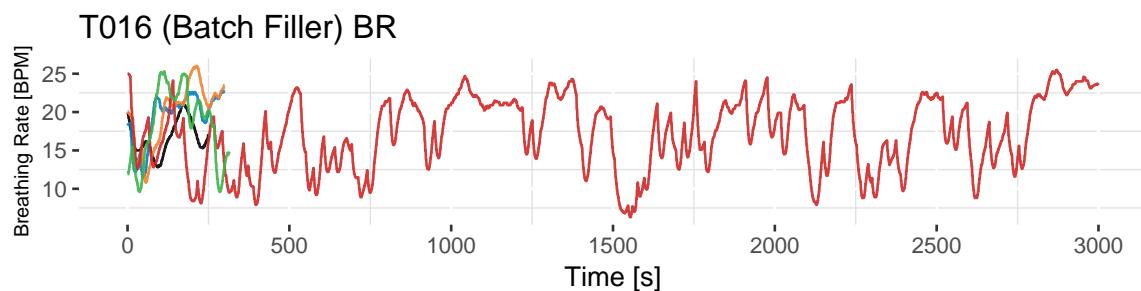


## **Batch-Filler (BF) Subjects**

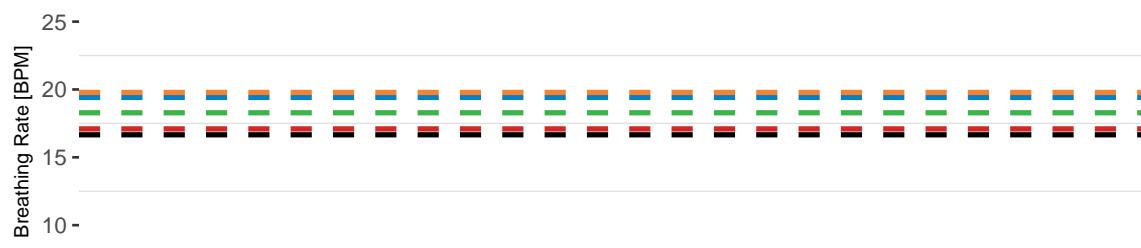




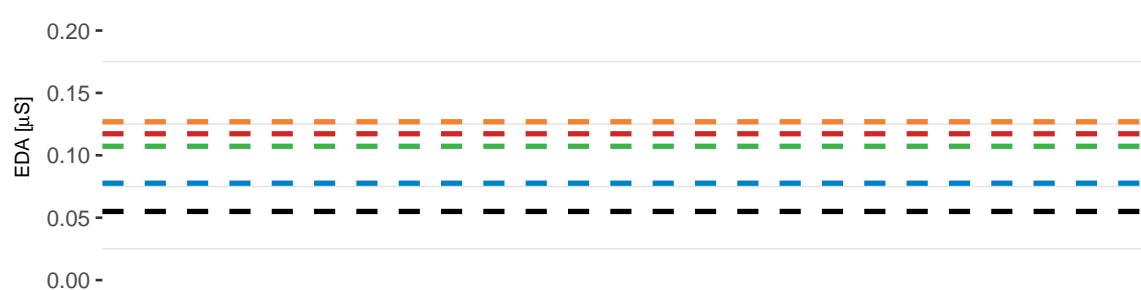
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



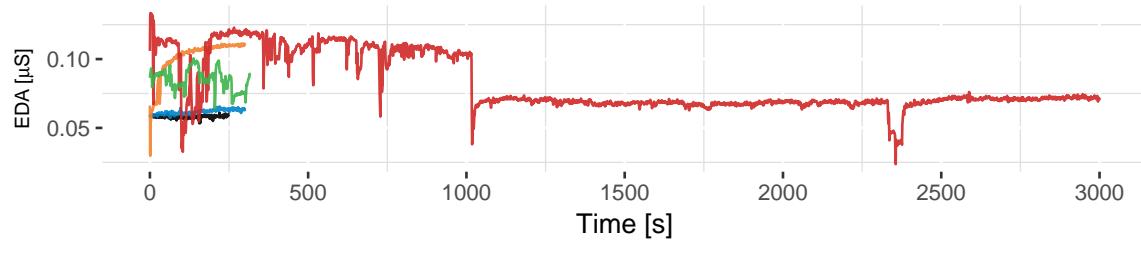
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

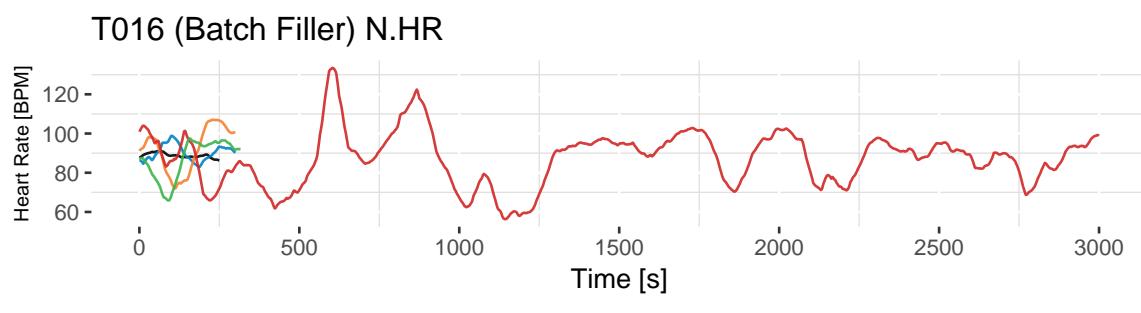
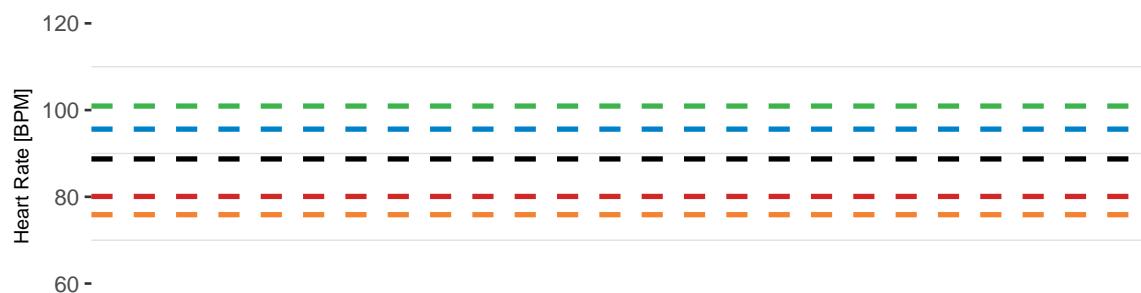
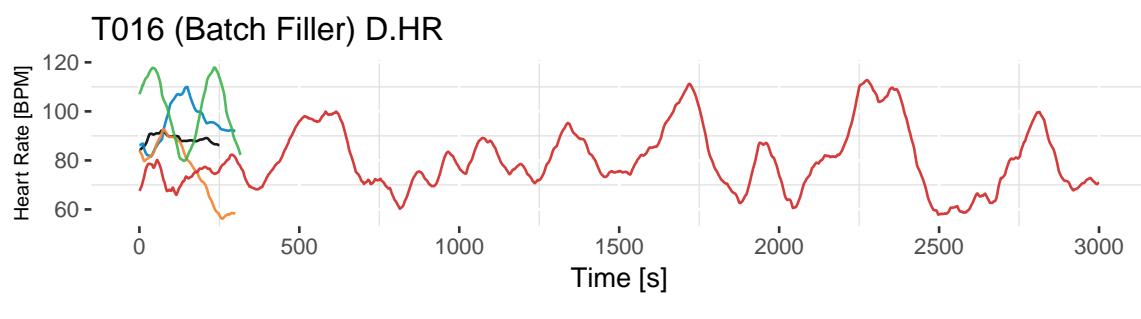


T016 (Batch Filler) D.EDA



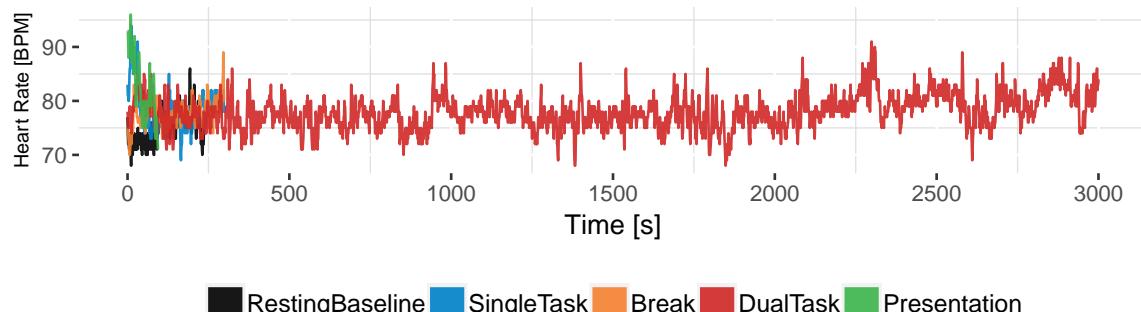
T016 (Batch Filler) N.EDA



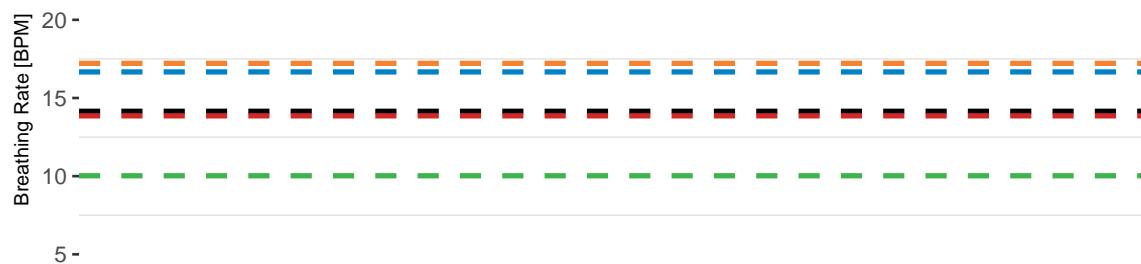
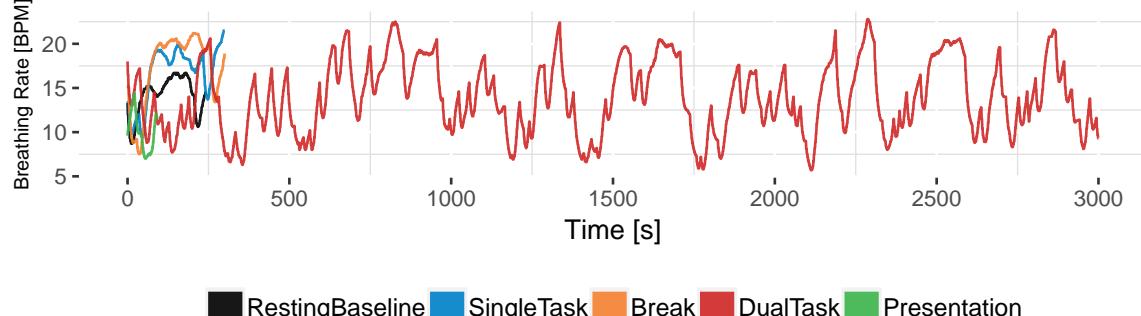




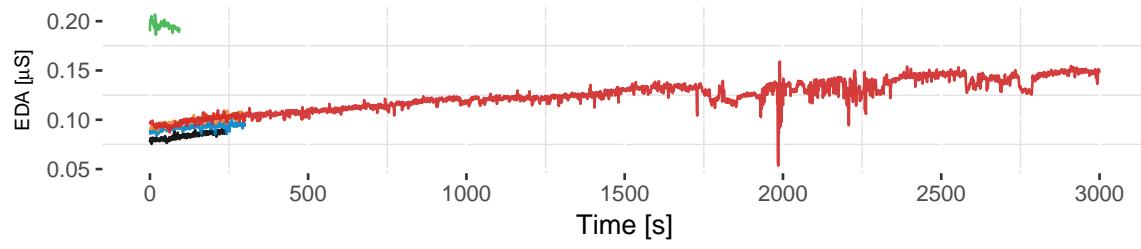
T019 (Batch Filler) HR



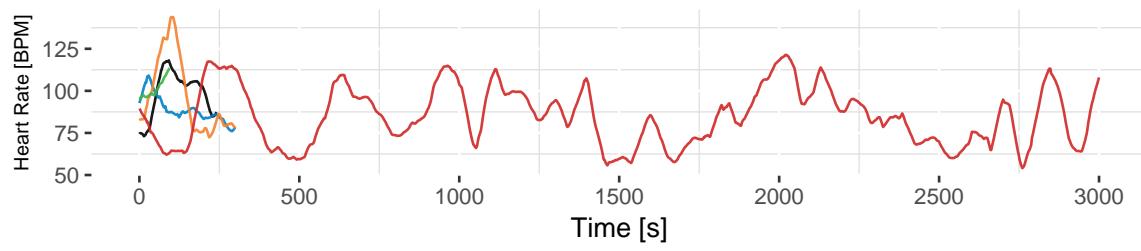
T019 (Batch Filler) BR

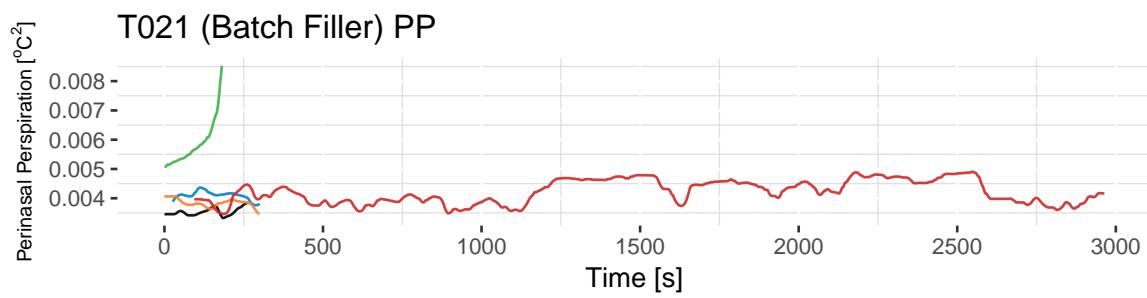


T019 (Batch Filler) N.EDA



T019 (Batch Filler) N.HR





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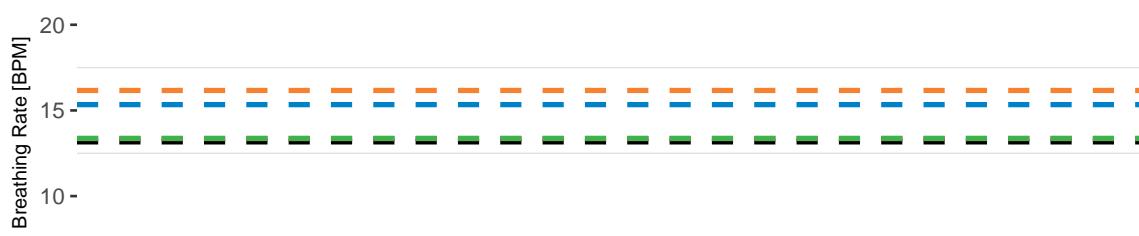
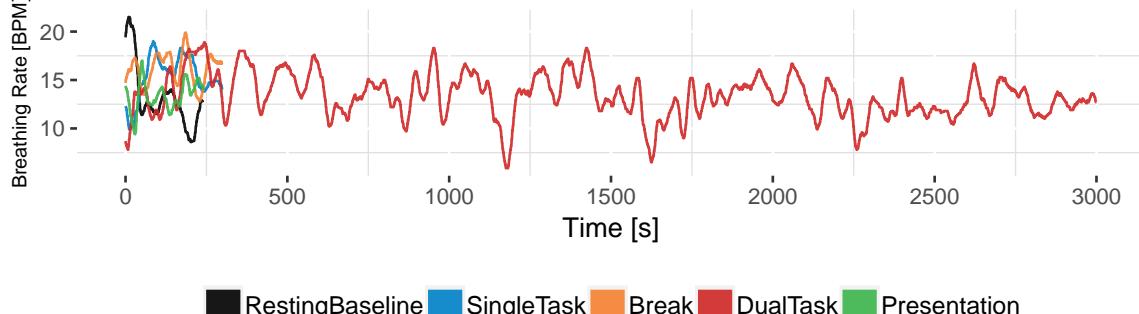
```
##  
## Subject T021 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
##  
## Subject T021 does not have BR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##  
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##  
##  
##  
##
```



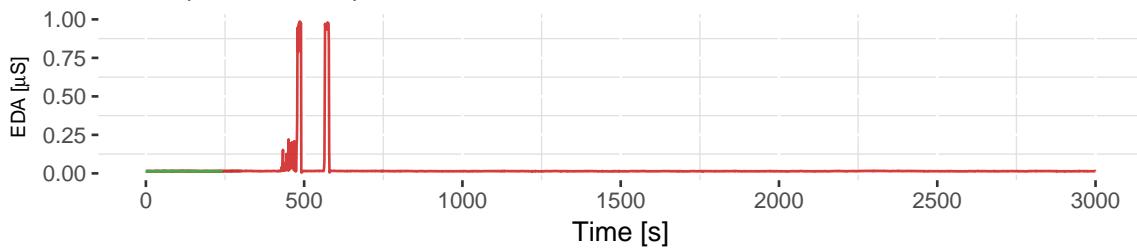
T077 (Batch Filler) HR



T077 (Batch Filler) BR



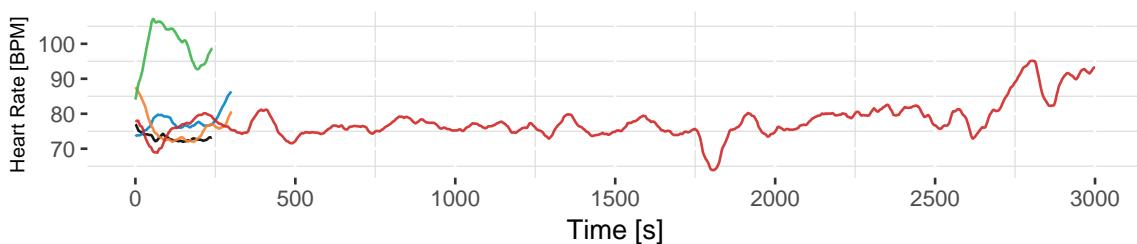
T077 (Batch Filler) N.EDA



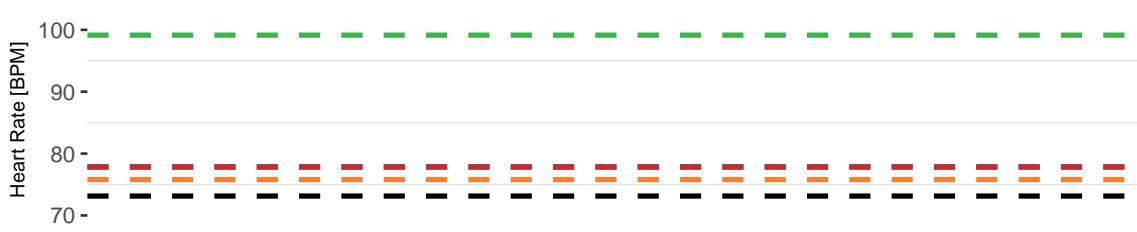
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

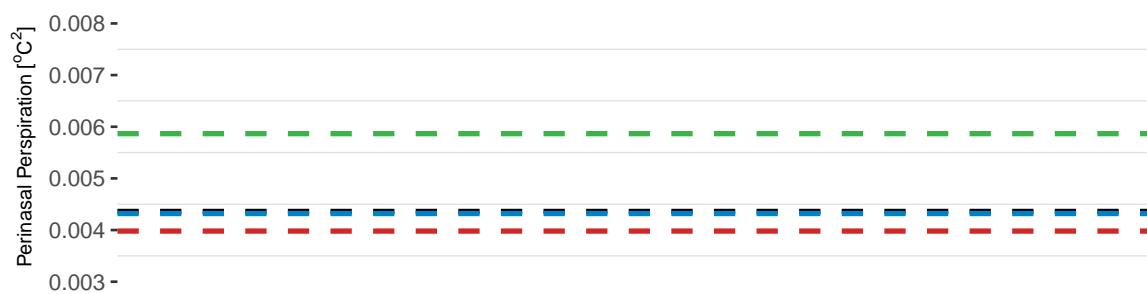
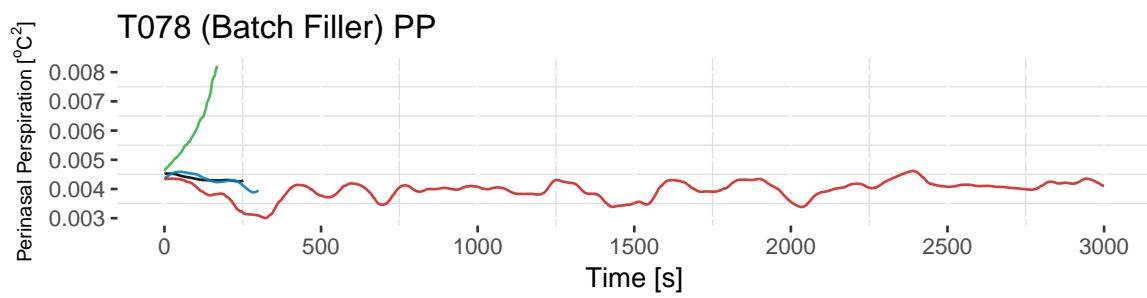


T077 (Batch Filler) N.HR

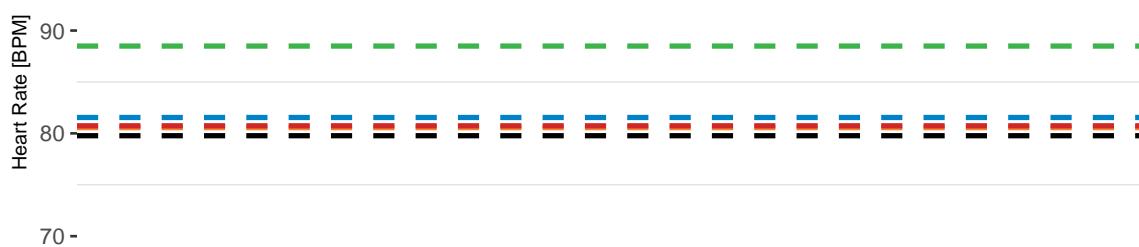
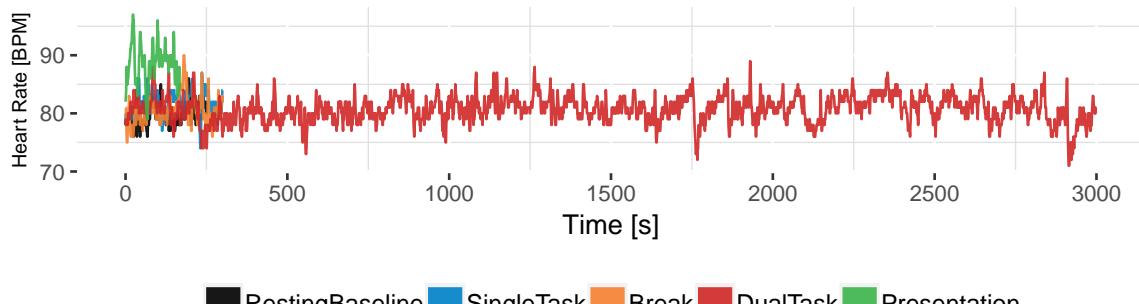


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

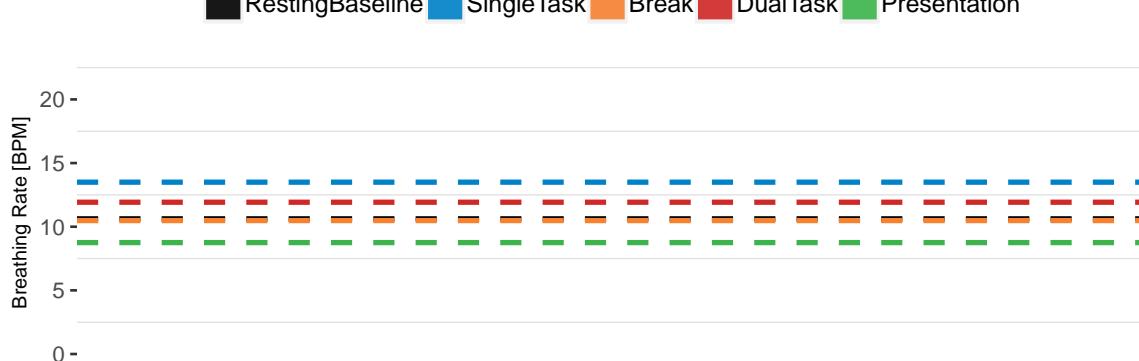
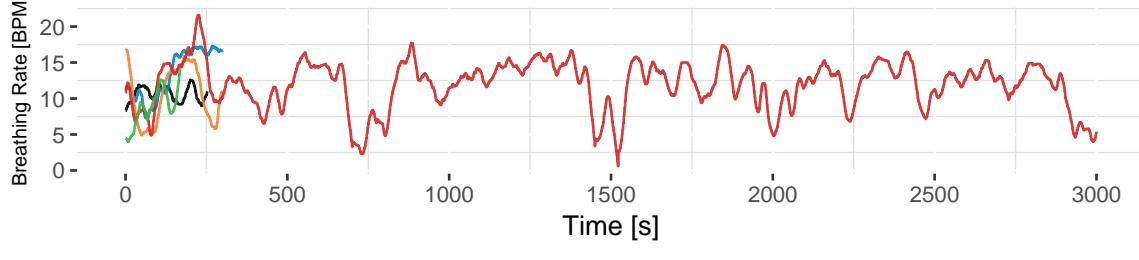




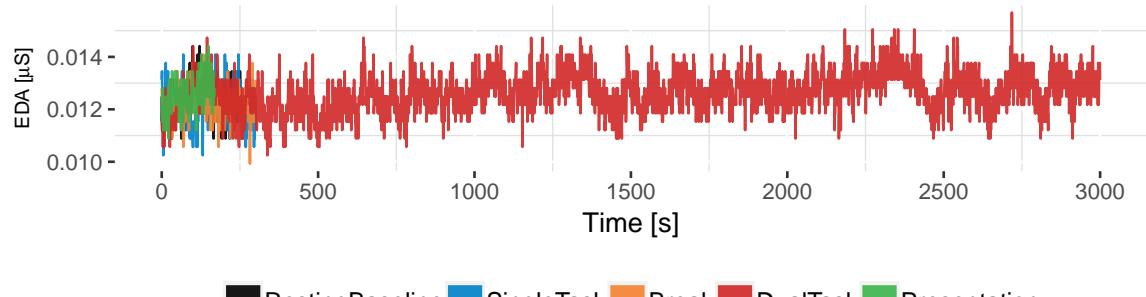
T078 (Batch Filler) HR



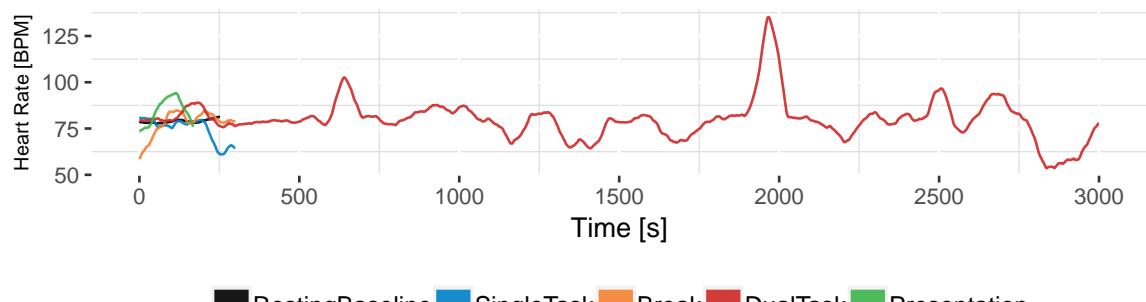
T078 (Batch Filler) BR

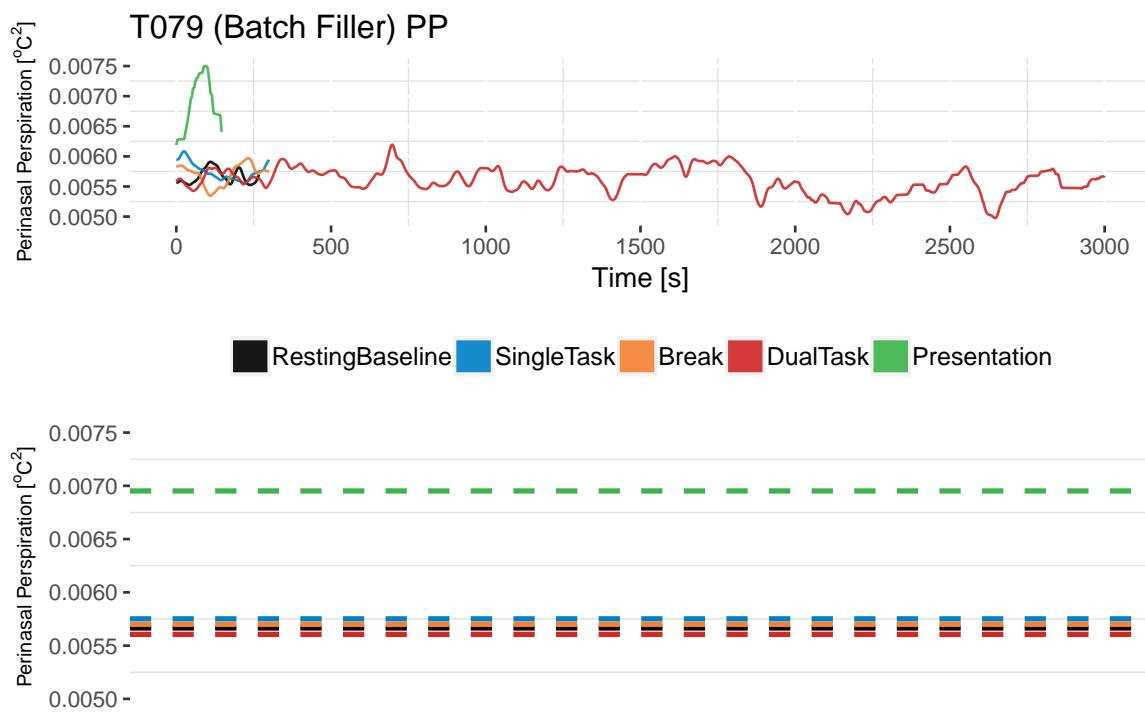


T078 (Batch Filler) N.EDA

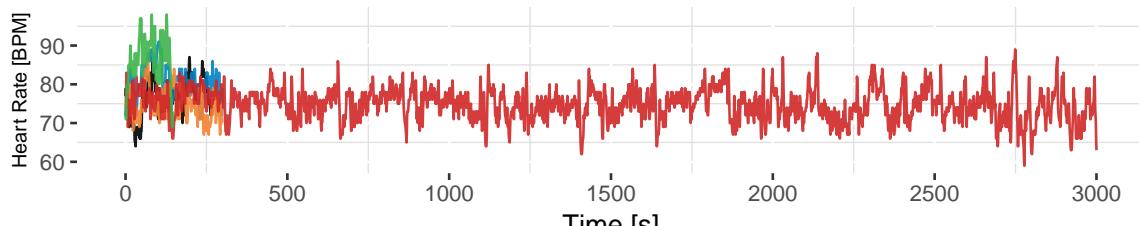


T078 (Batch Filler) N.HR

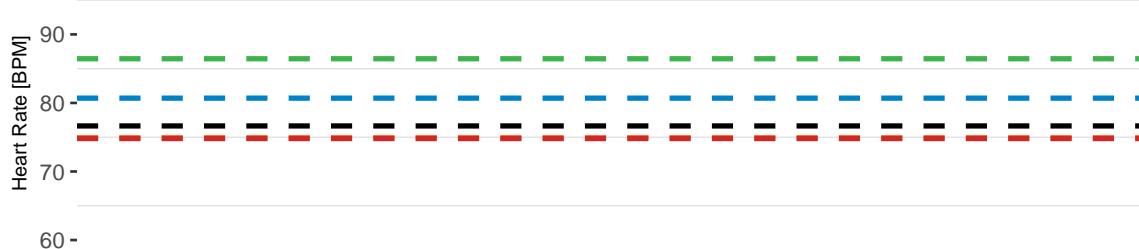




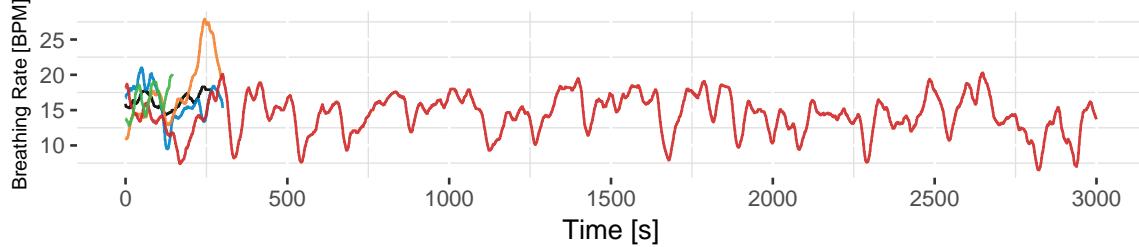
T079 (Batch Filler) HR



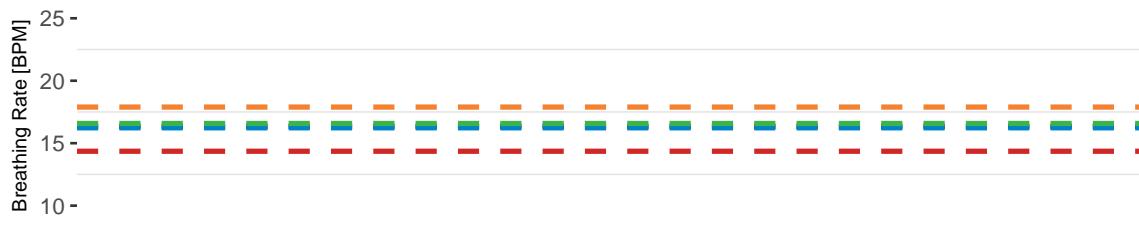
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



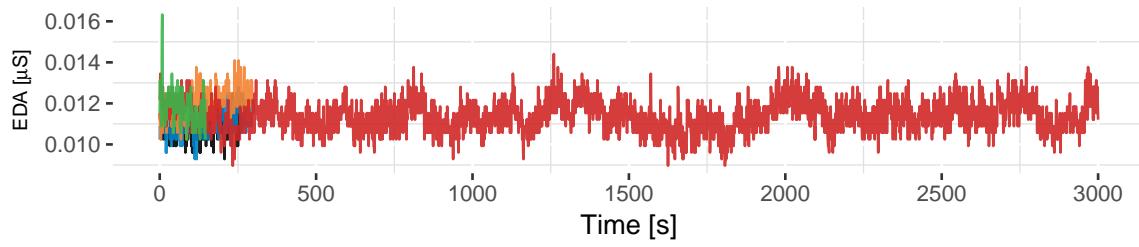
T079 (Batch Filler) BR



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



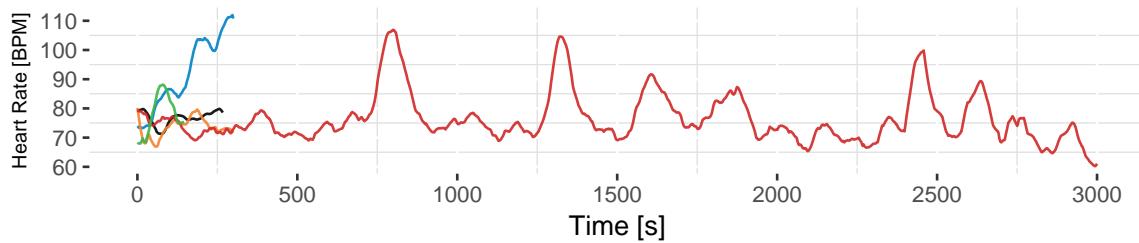
T079 (Batch Filler) N.EDA



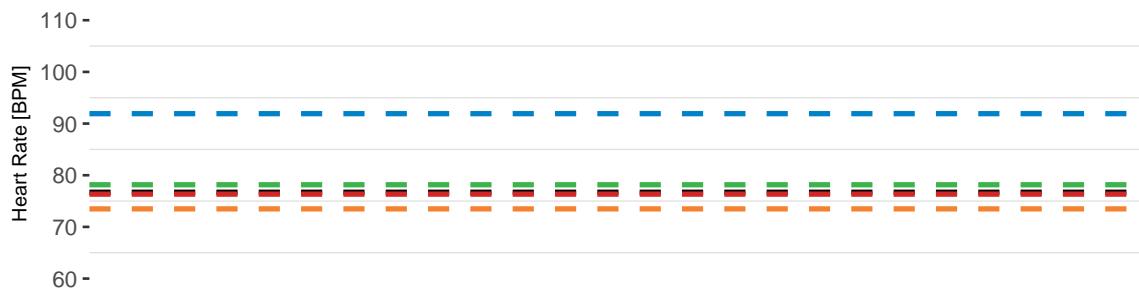
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



T079 (Batch Filler) N.HR

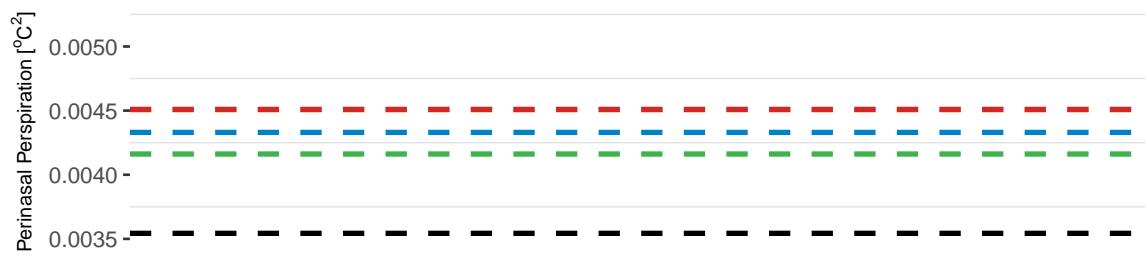


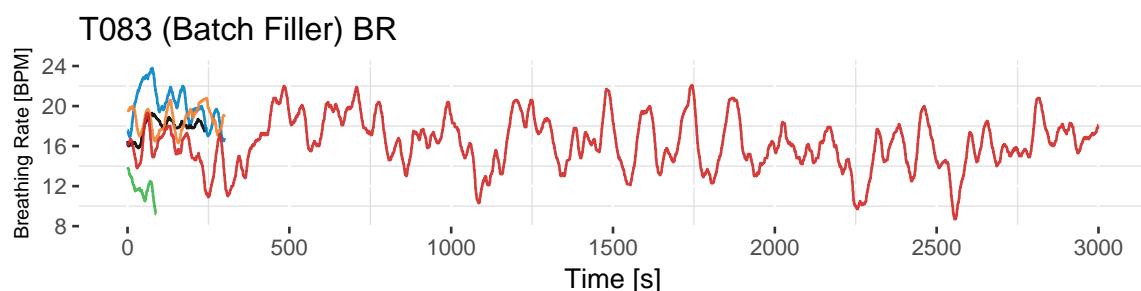
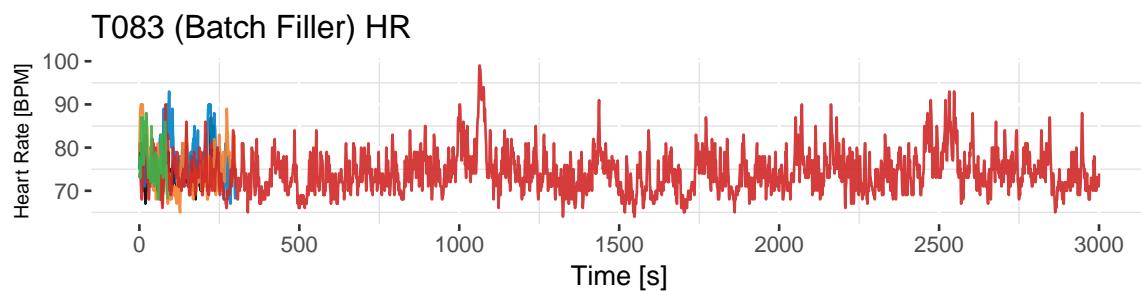
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

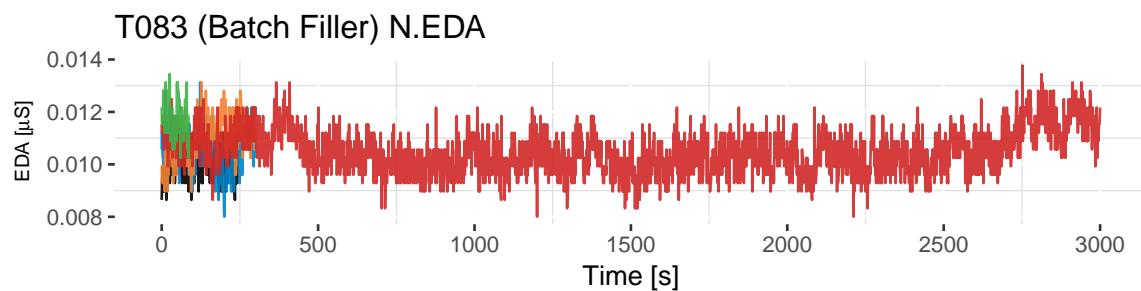


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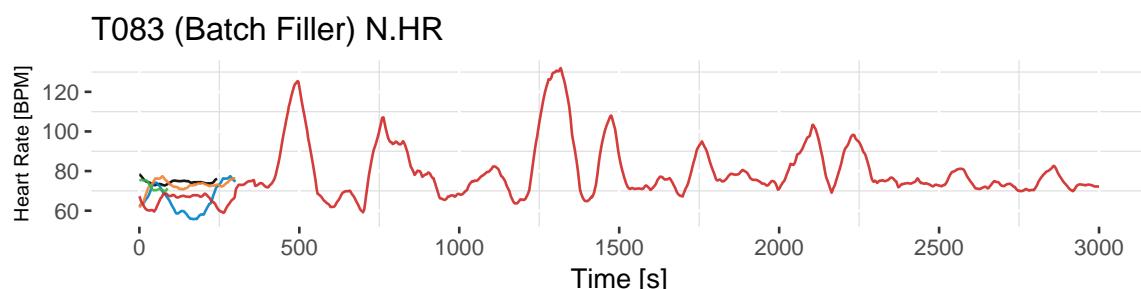




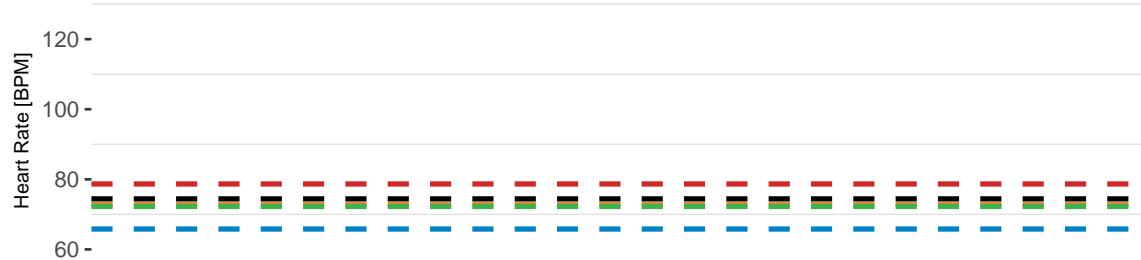


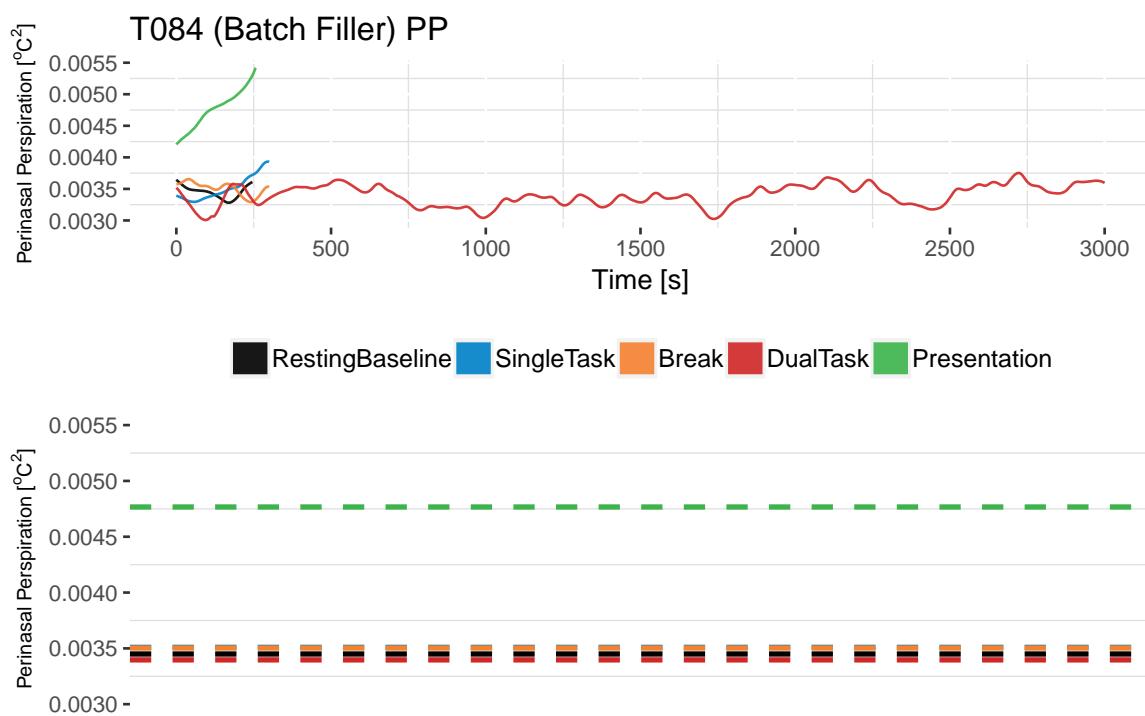


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

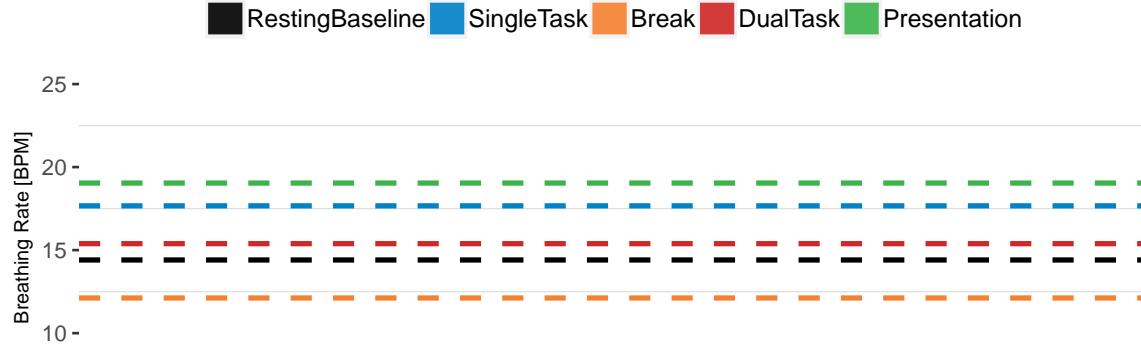
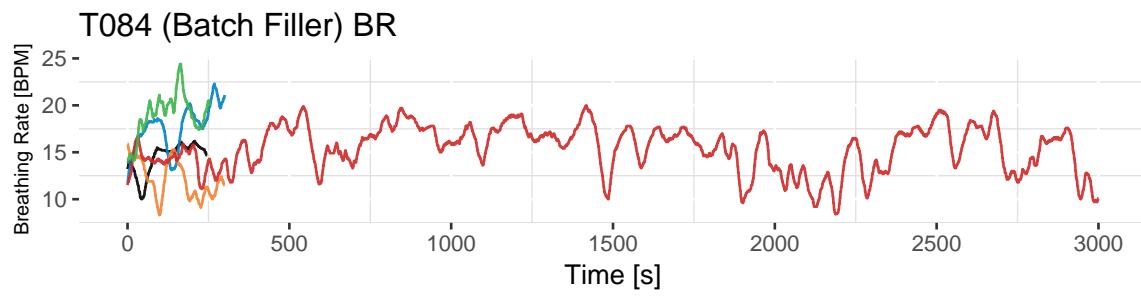


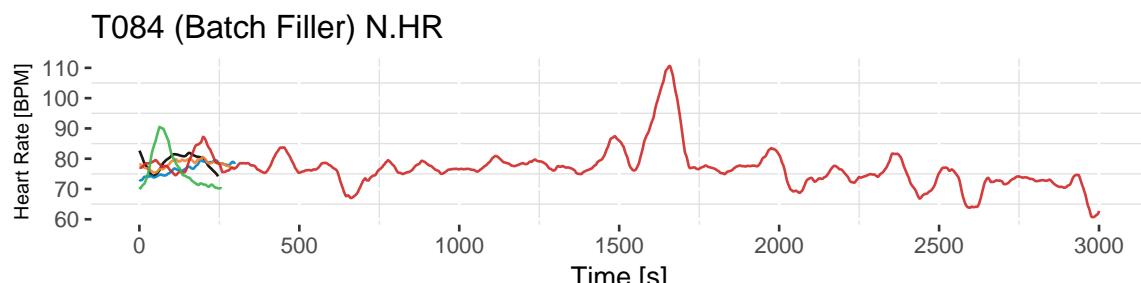
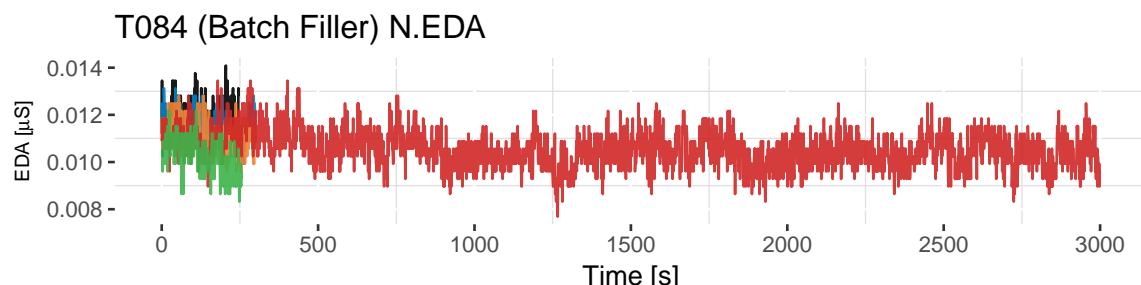


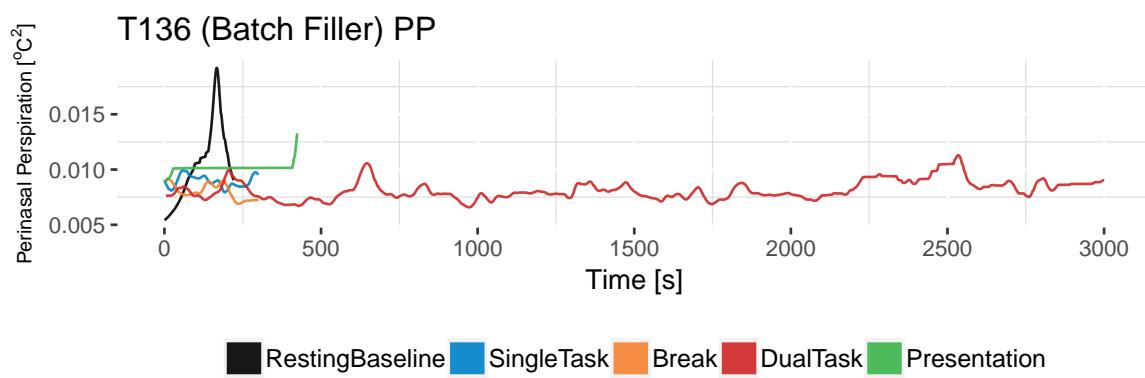
T084 (Batch Filler) HR



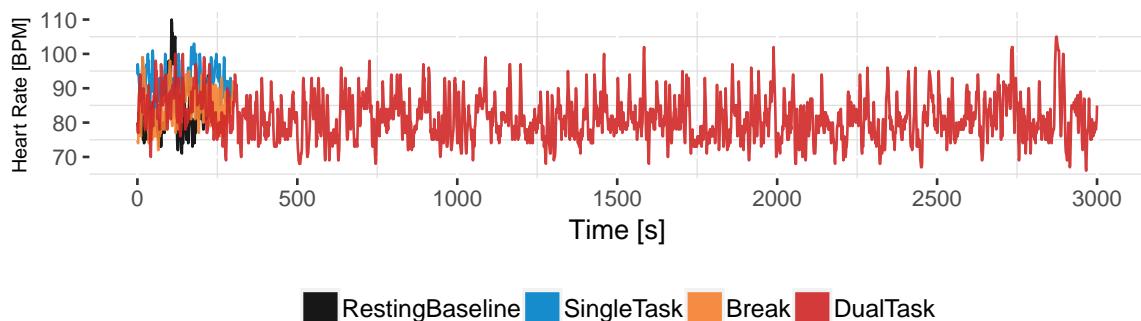
T084 (Batch Filler) BR



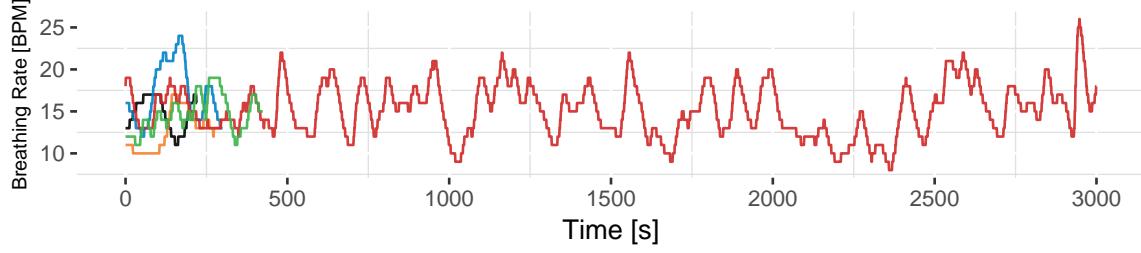




T136 (Batch Filler) HR

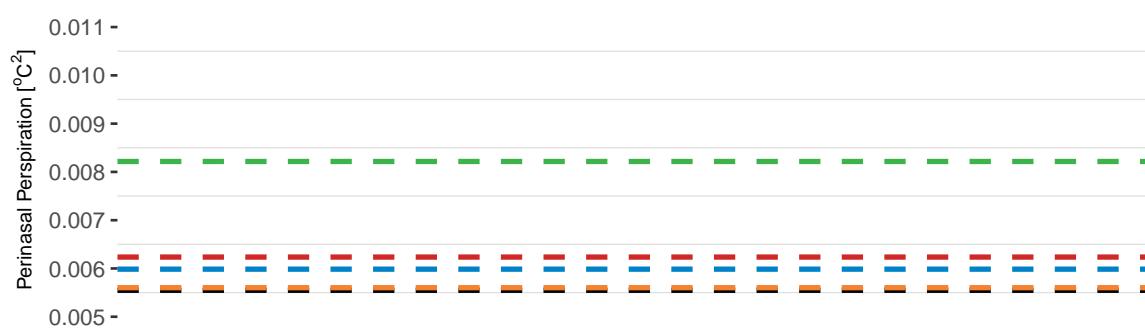
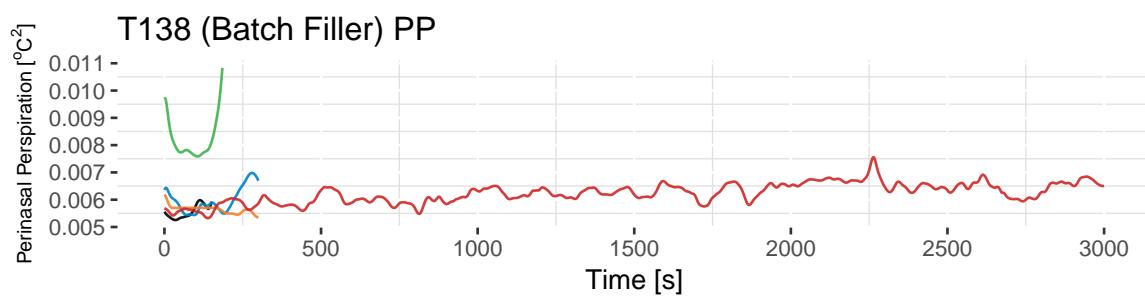


T136 (Batch Filler) BR

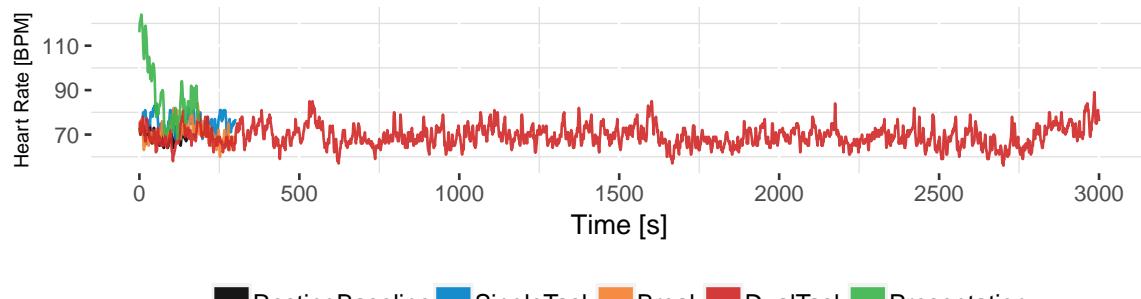


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

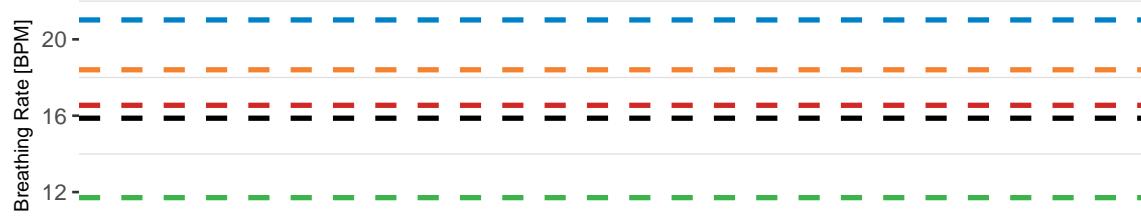
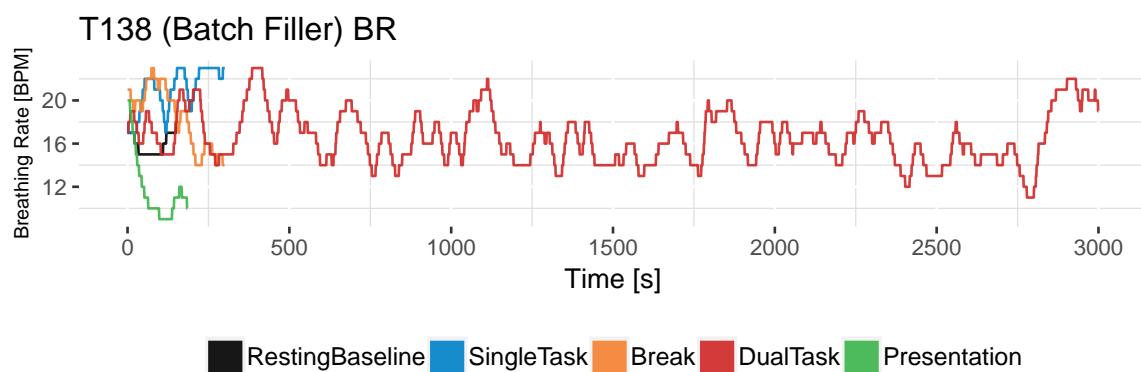


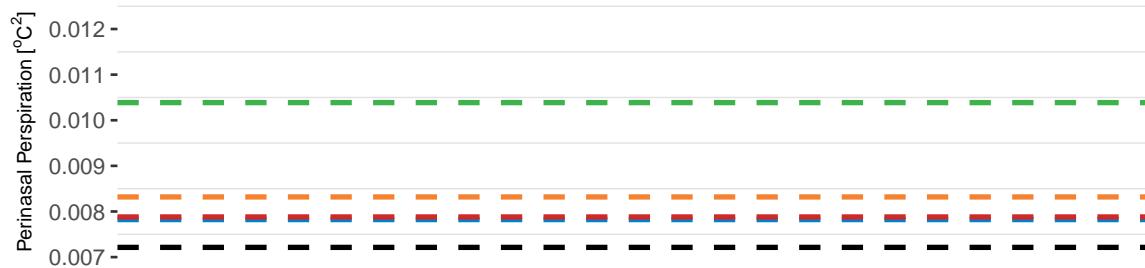
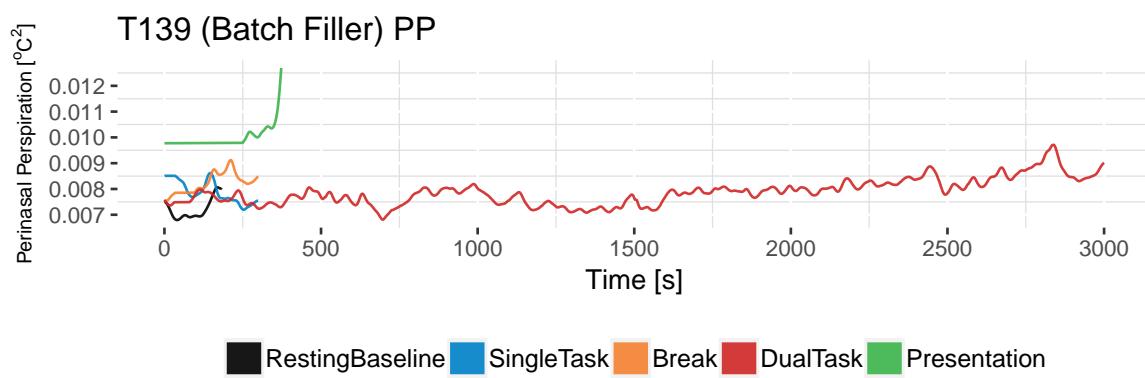


T138 (Batch Filler) HR

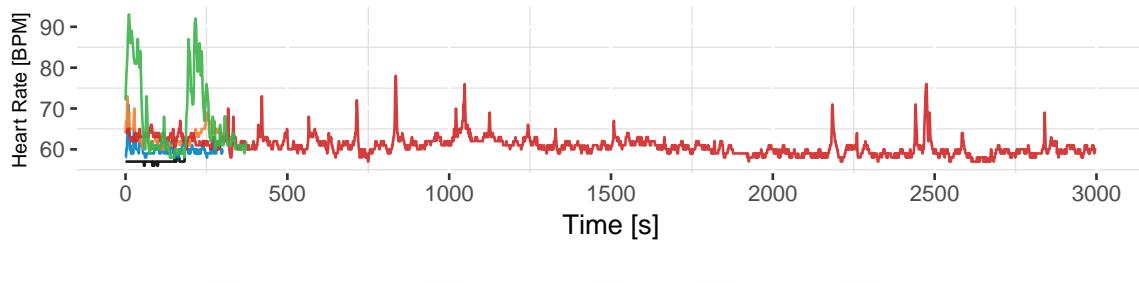


T138 (Batch Filler) BR

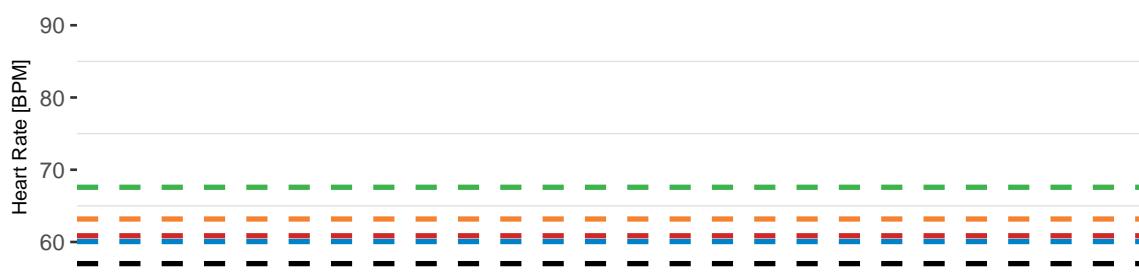




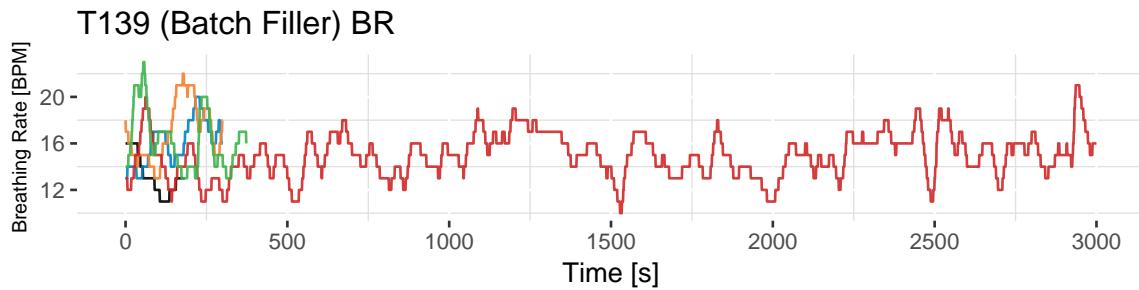
T139 (Batch Filler) HR



■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

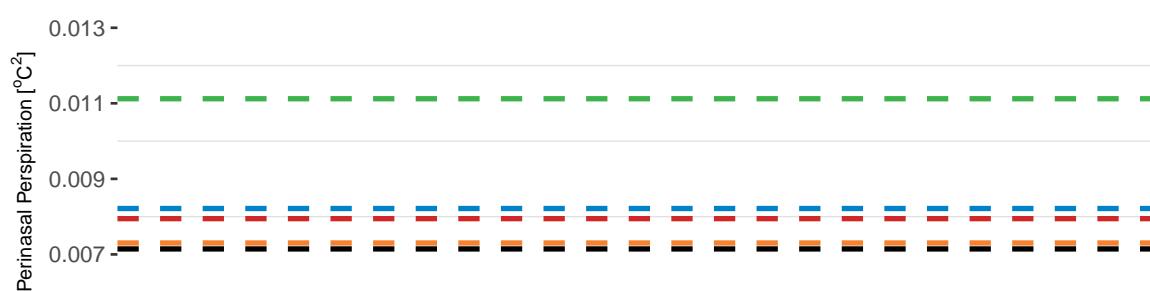
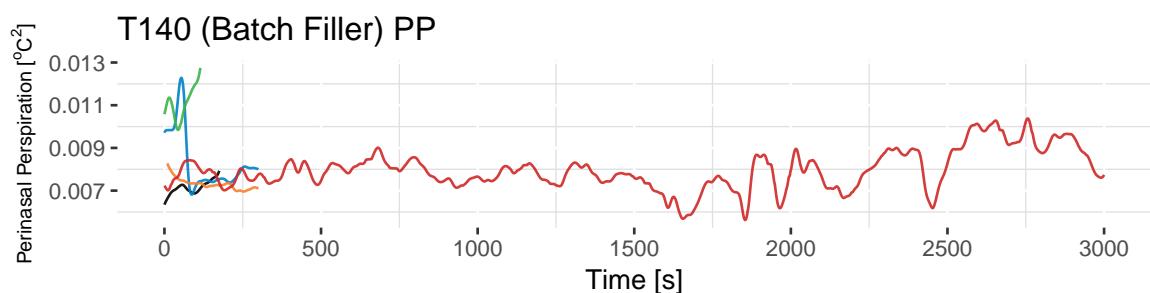


T139 (Batch Filler) BR

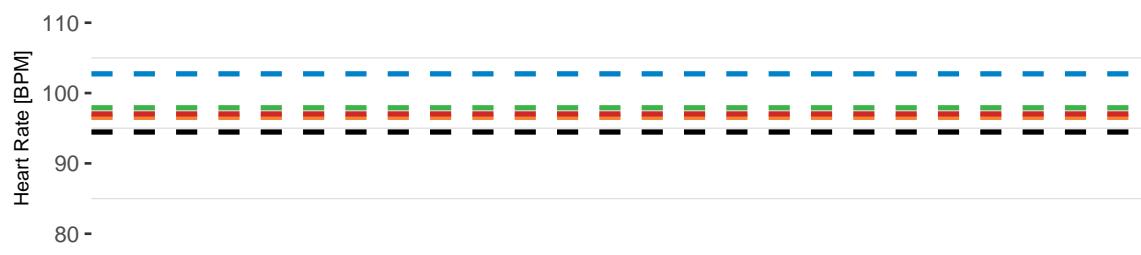


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

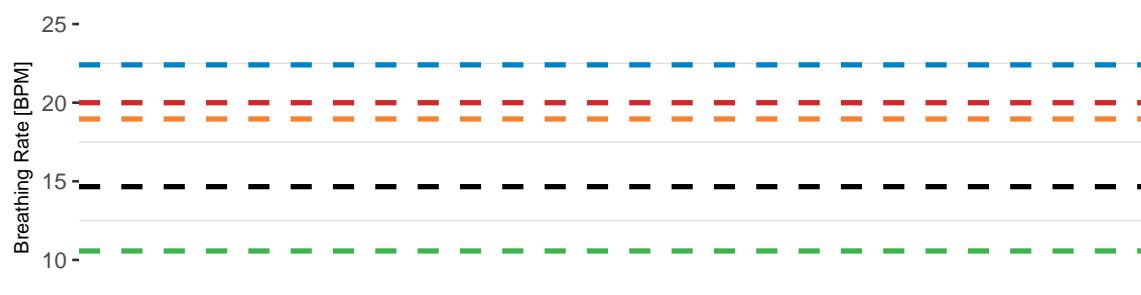
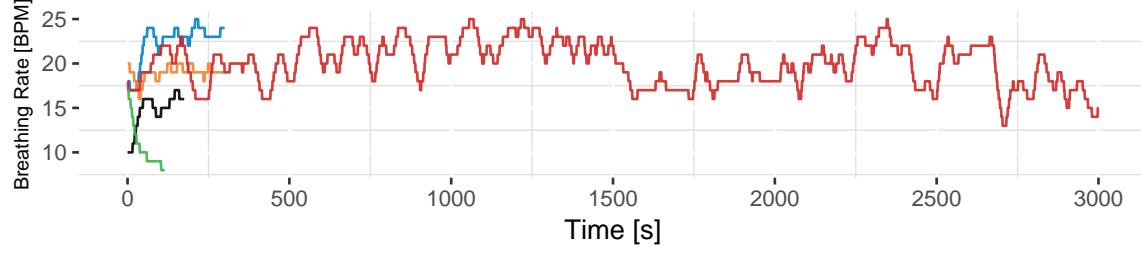


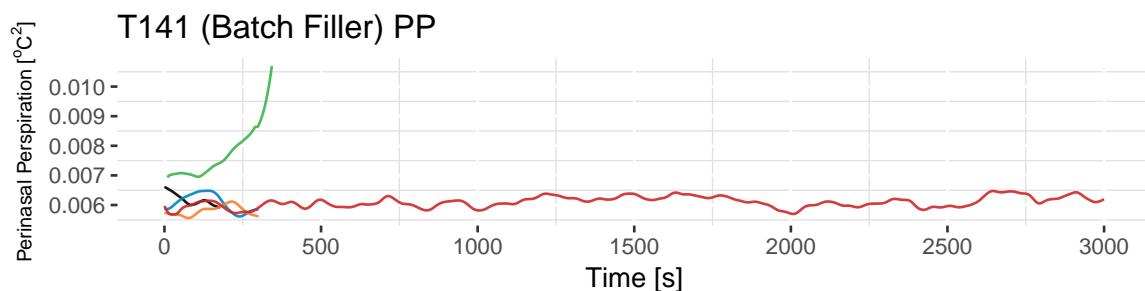


T140 (Batch Filler) HR

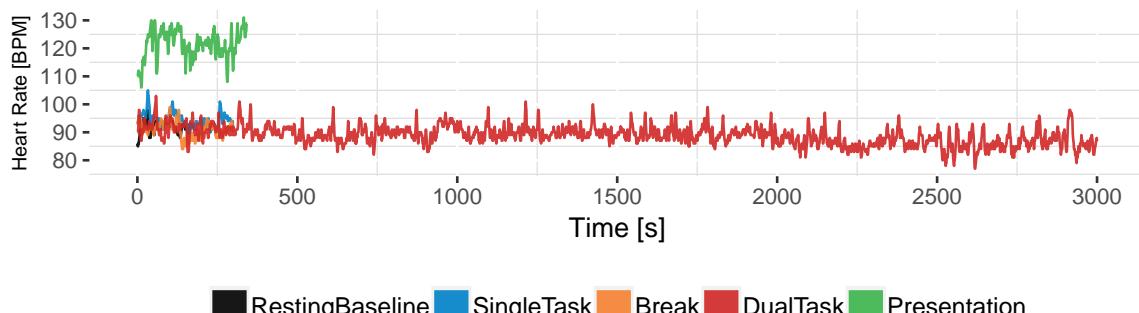


T140 (Batch Filler) BR

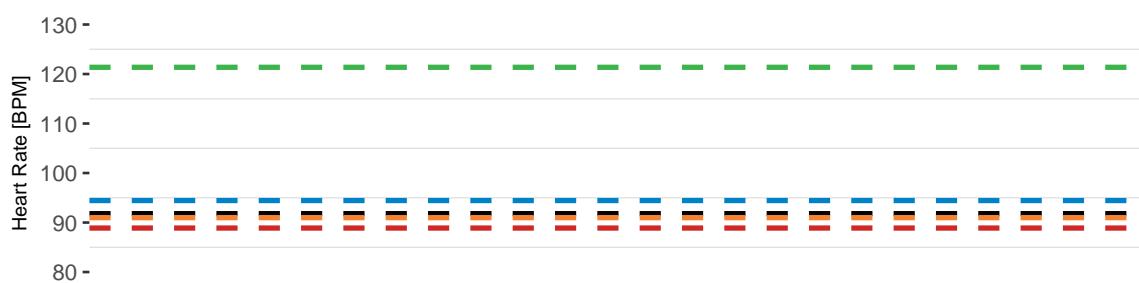




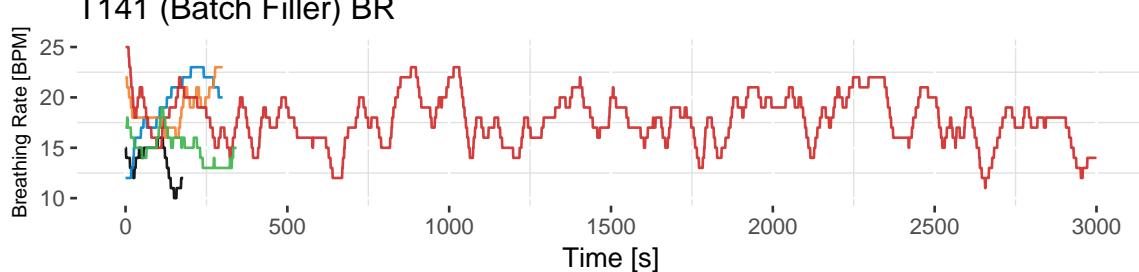
T141 (Batch Filler) HR



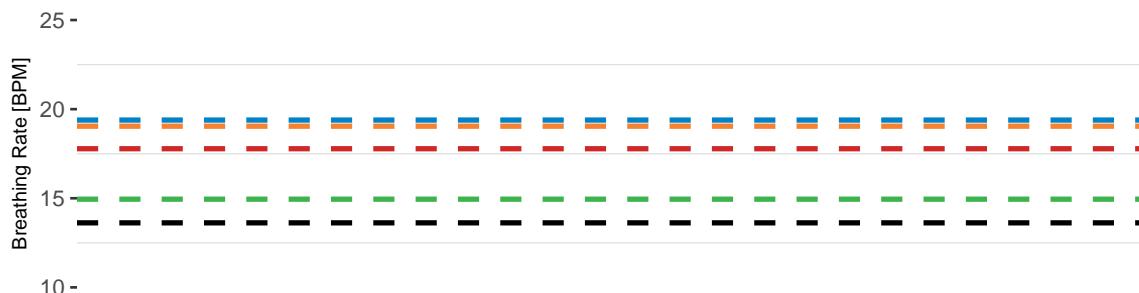
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

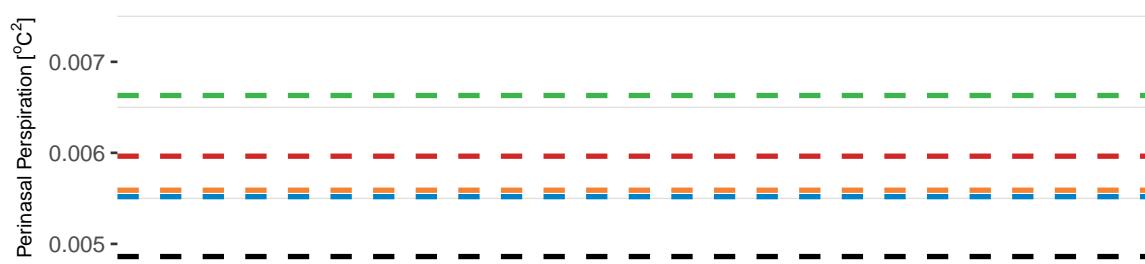
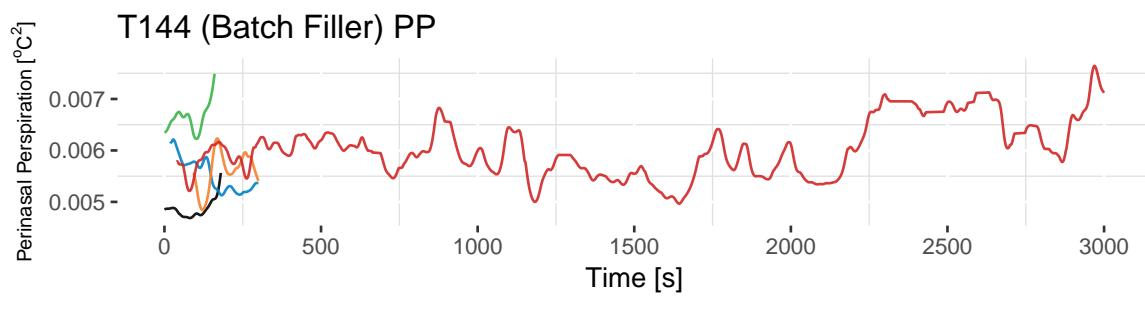


T141 (Batch Filler) BR

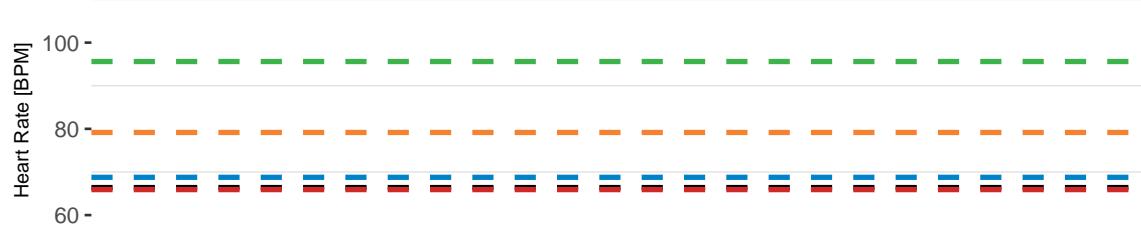
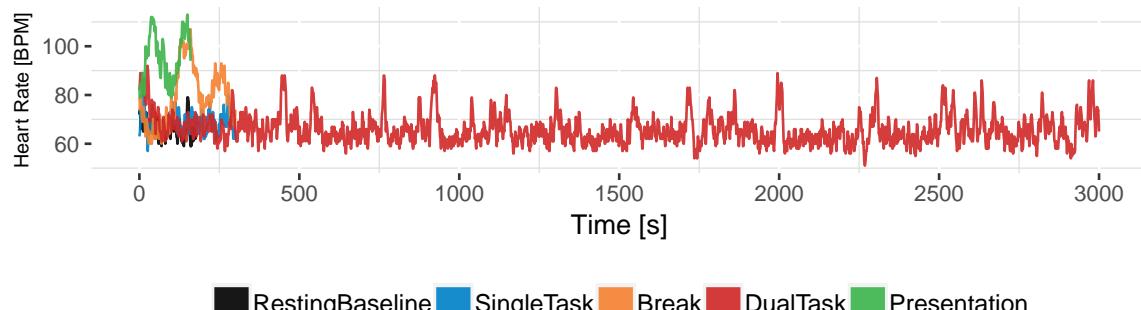


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

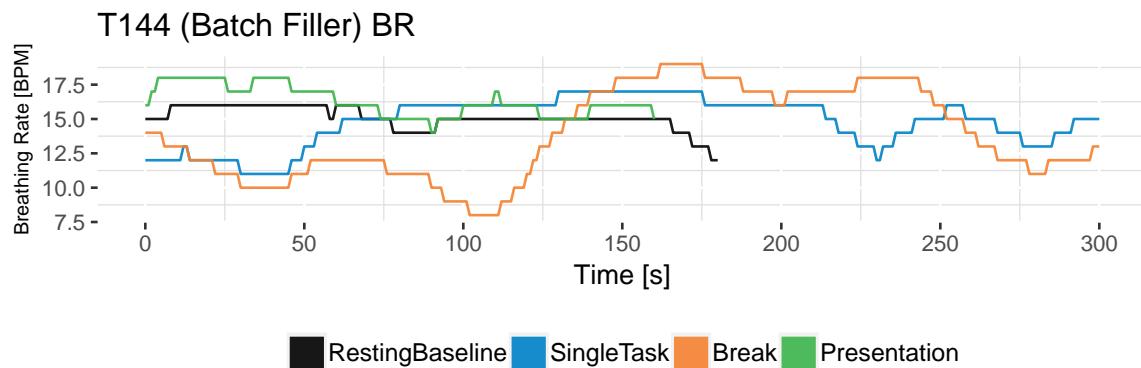


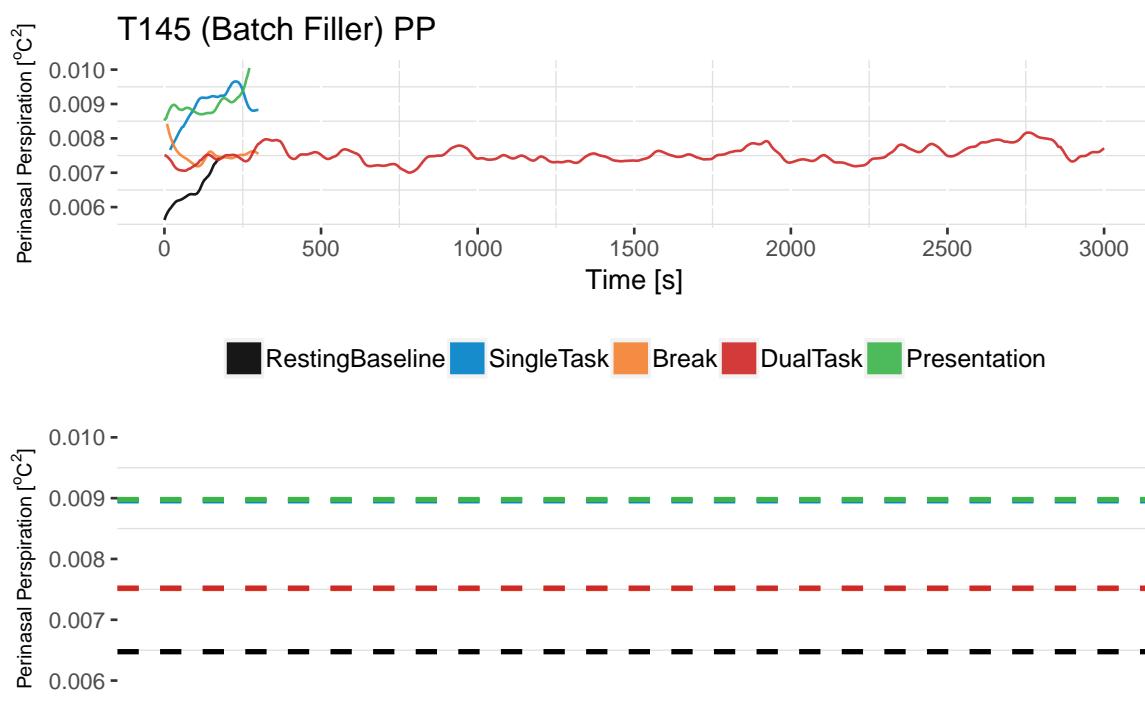


T144 (Batch Filler) HR

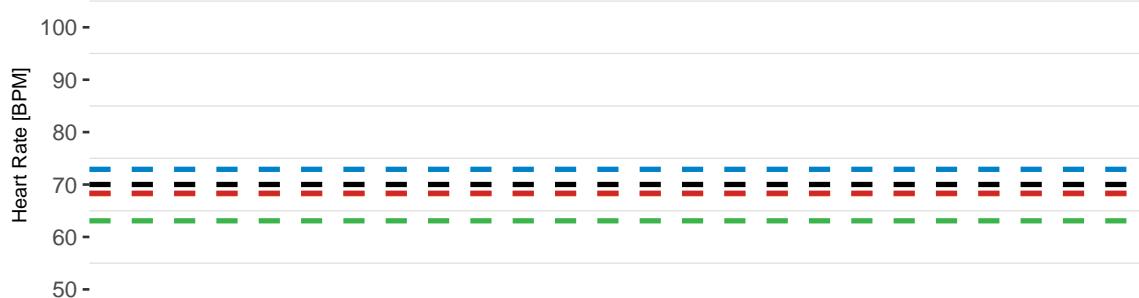
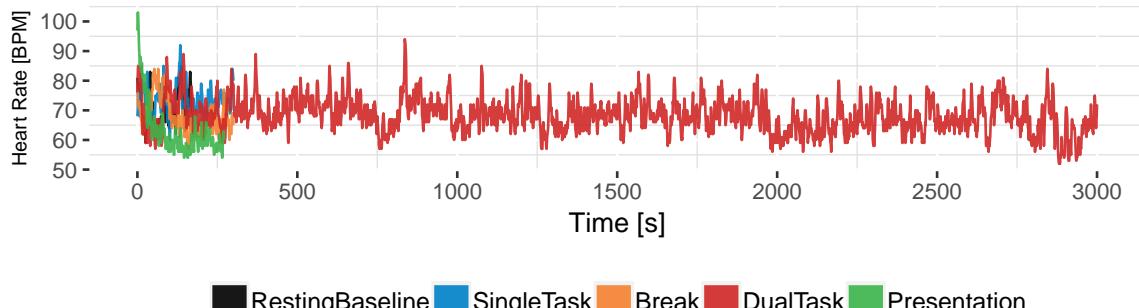


T144 (Batch Filler) BR

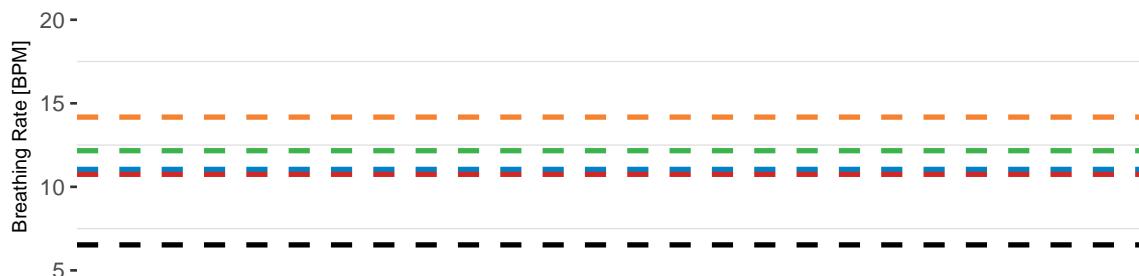
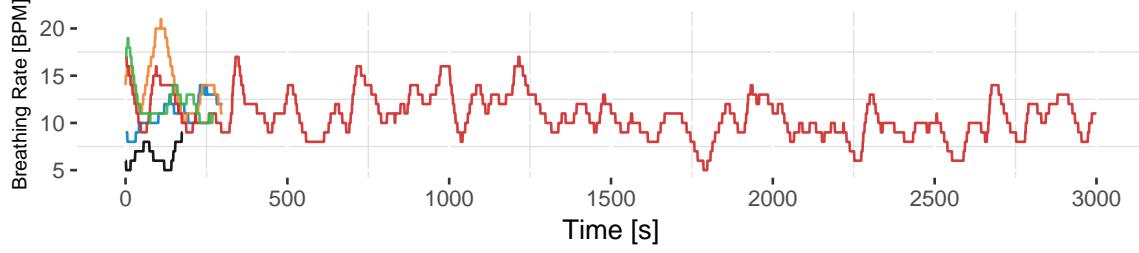




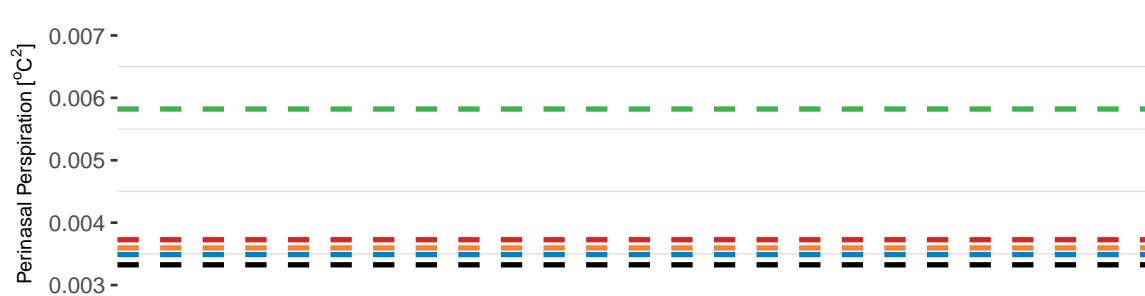
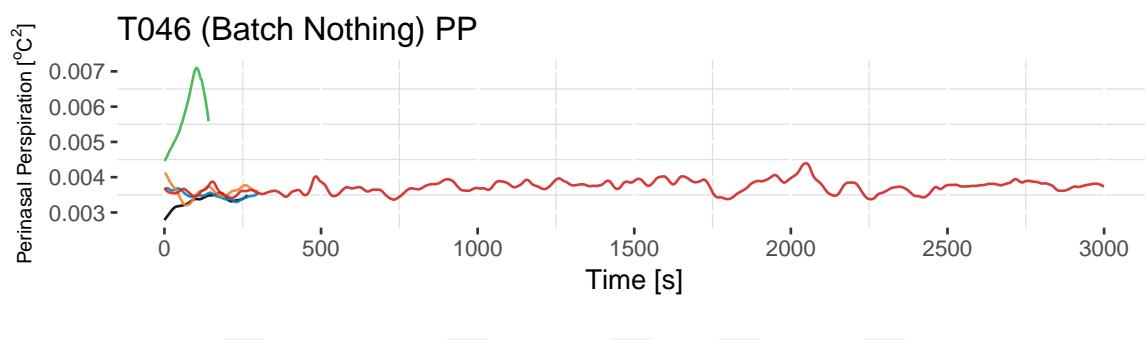
T145 (Batch Filler) HR



T145 (Batch Filler) BR

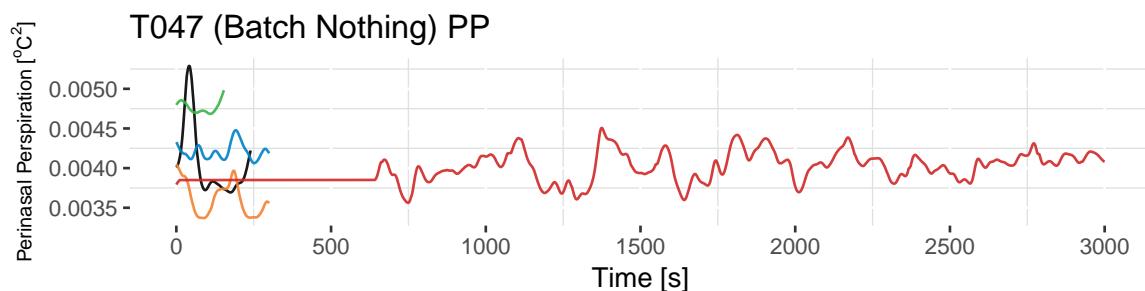


## **Batch-Nothing (BN) Subjects**



##

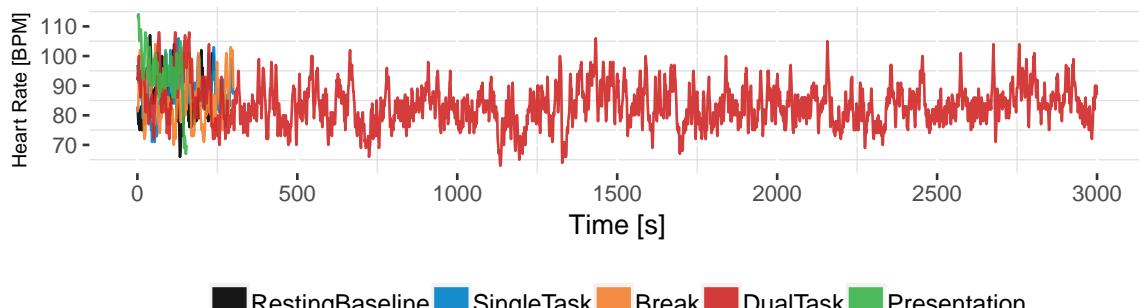




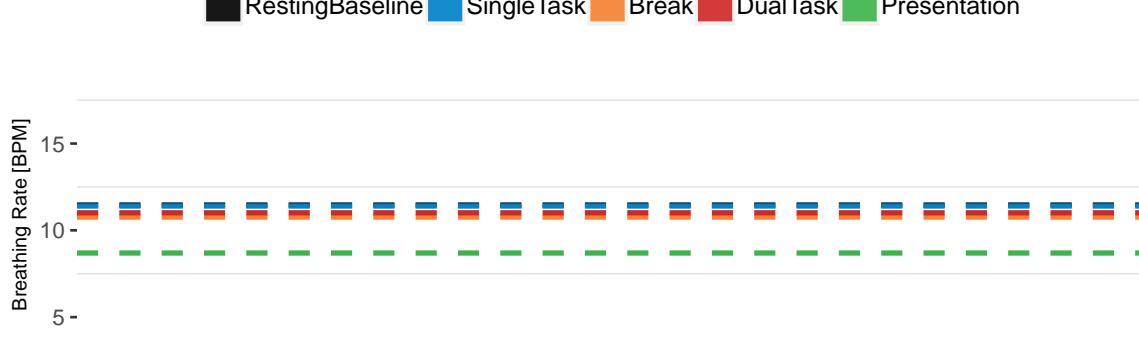
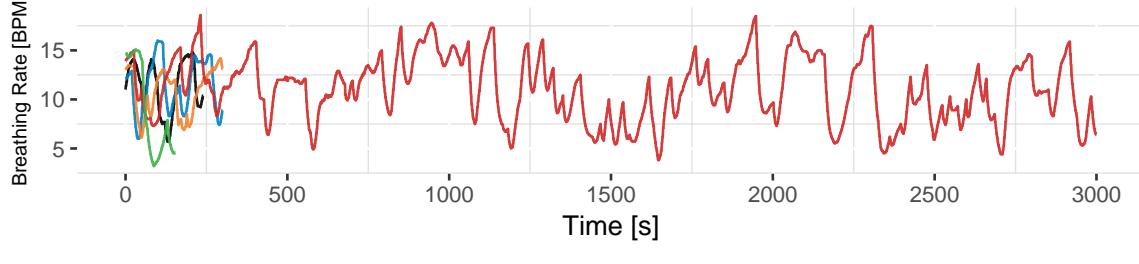
RestingBaseline SingleTask Break DualTask Presentation



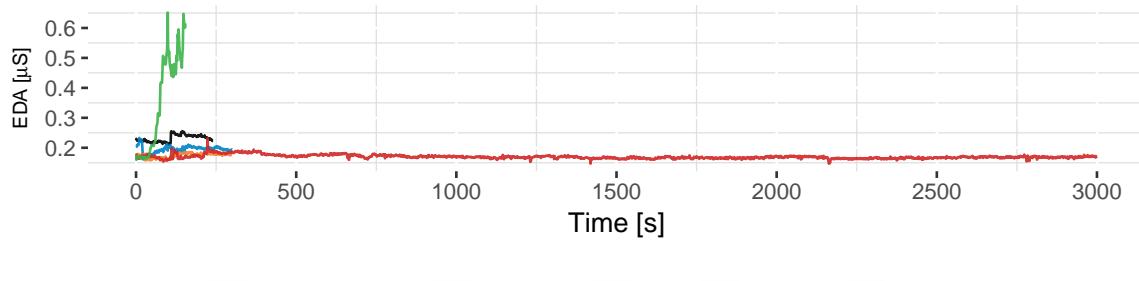
T047 (Batch Nothing) HR



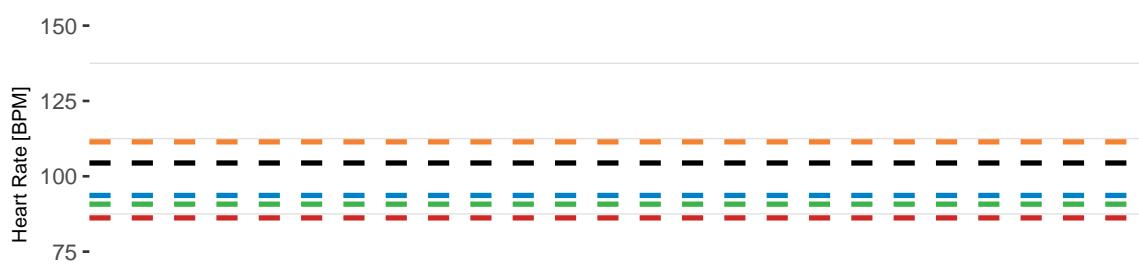
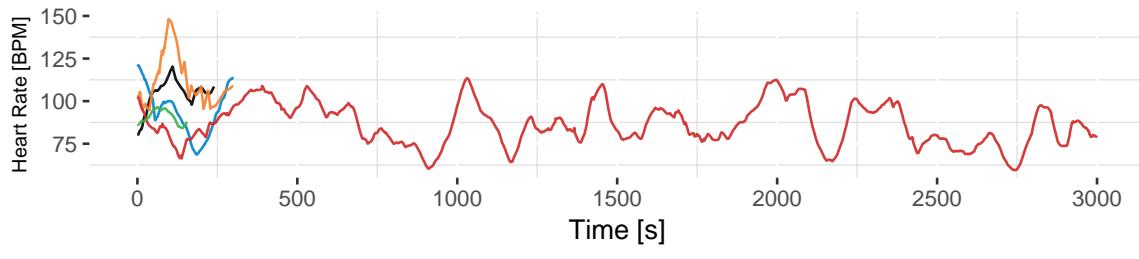
T047 (Batch Nothing) BR

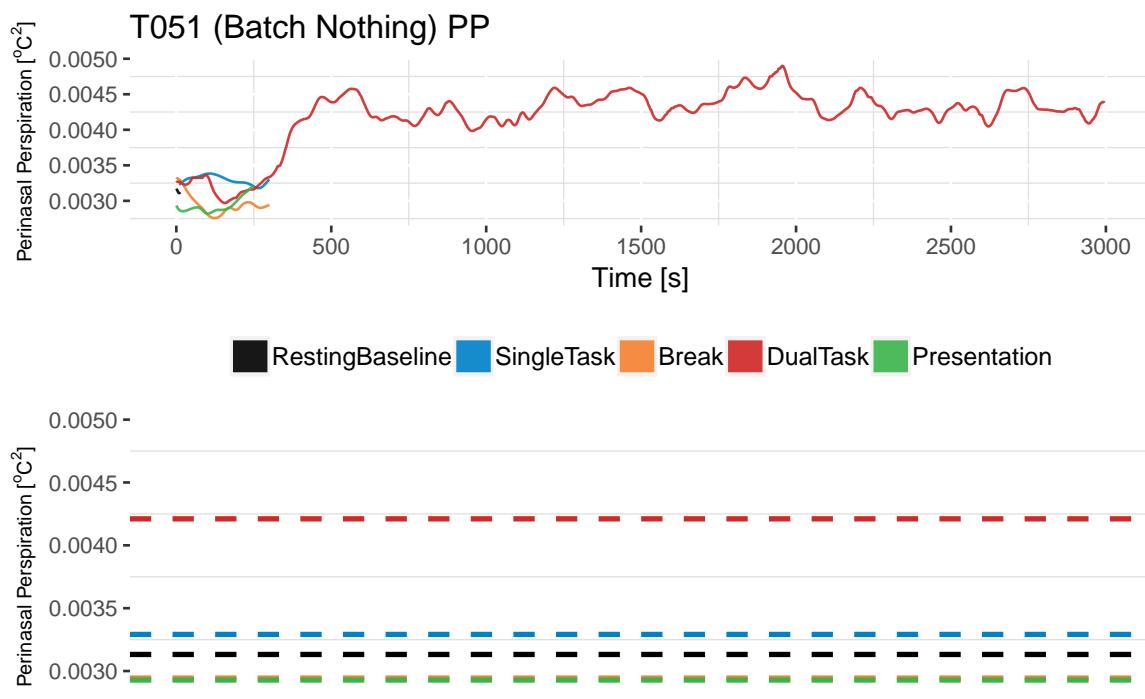


T047 (Batch Nothing) N.EDA



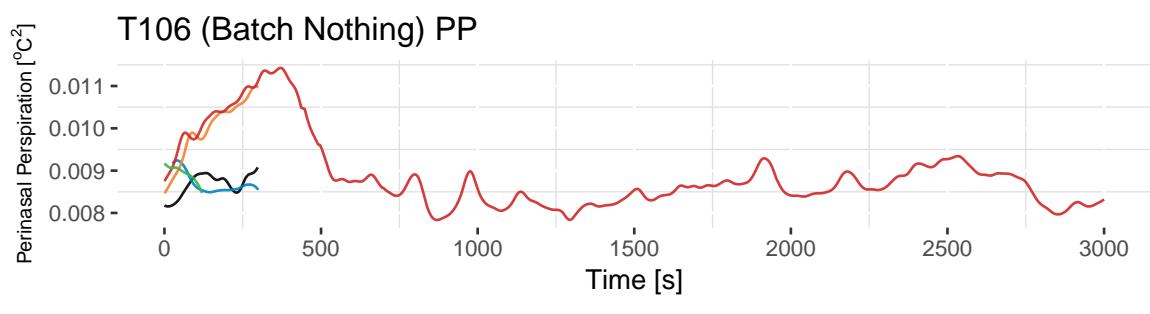
T047 (Batch Nothing) N.HR



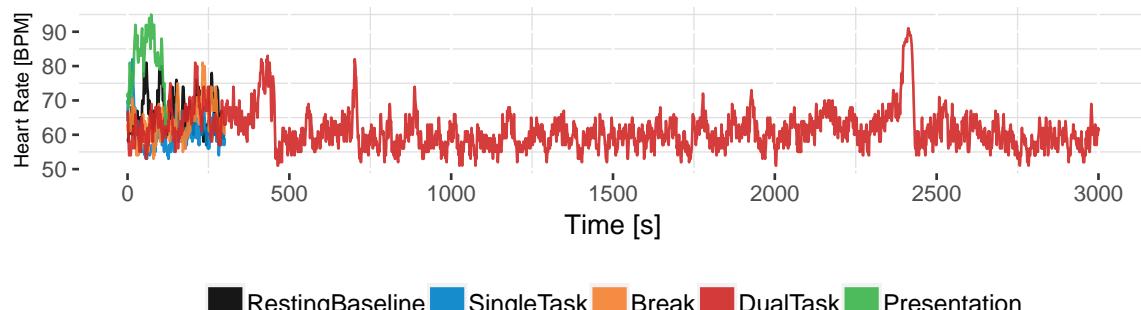


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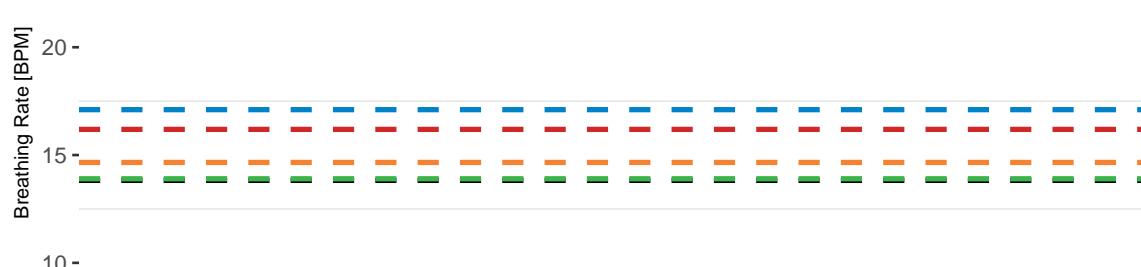
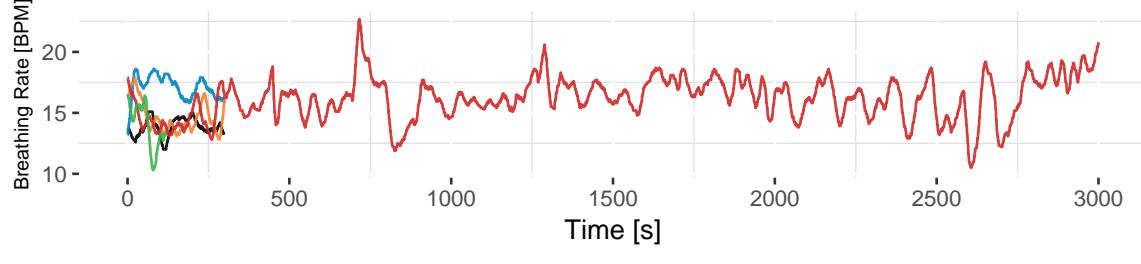




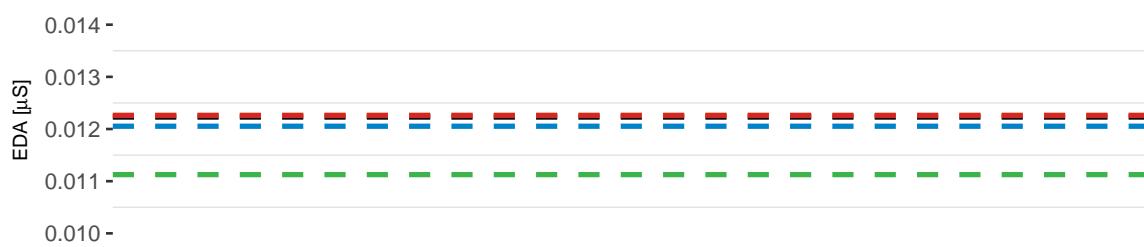
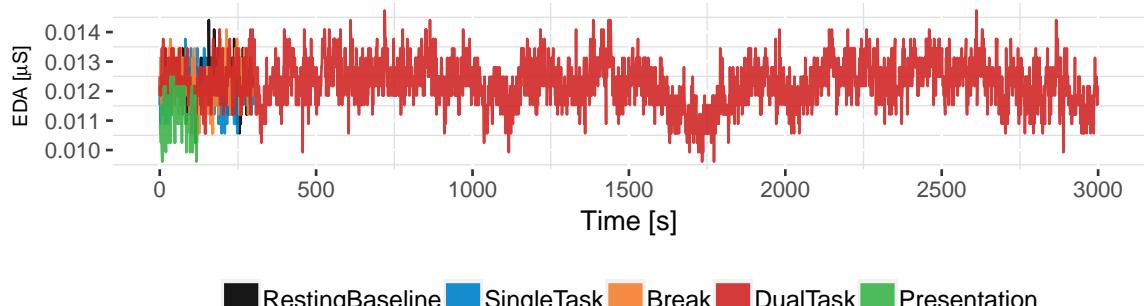
T106 (Batch Nothing) HR



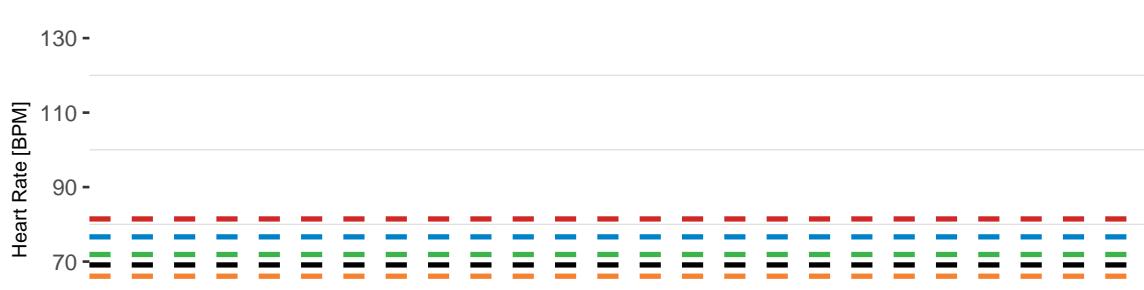
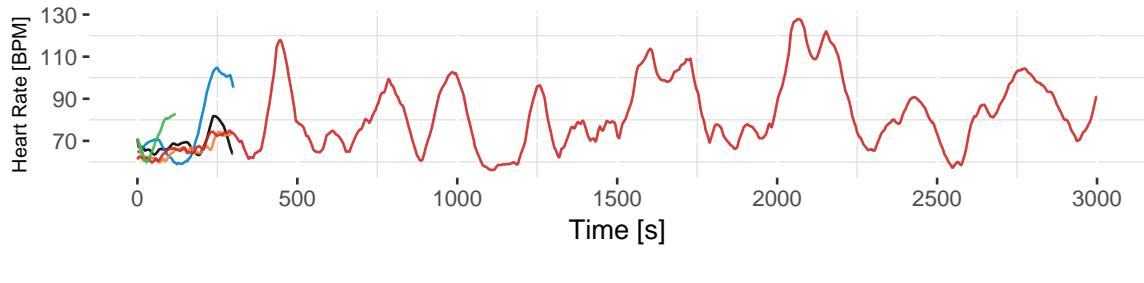
T106 (Batch Nothing) BR

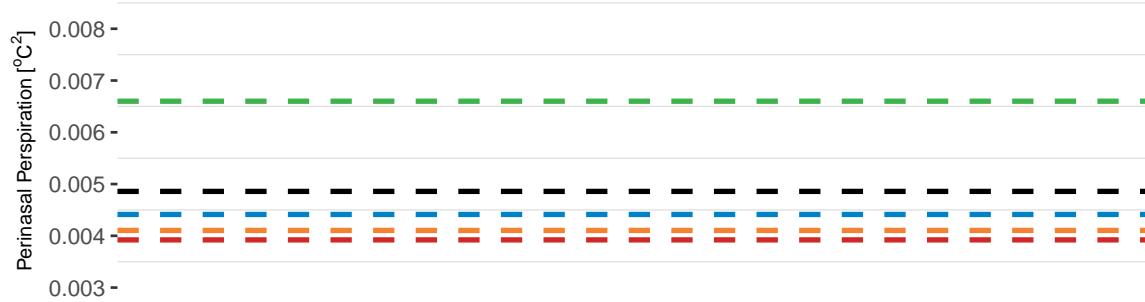
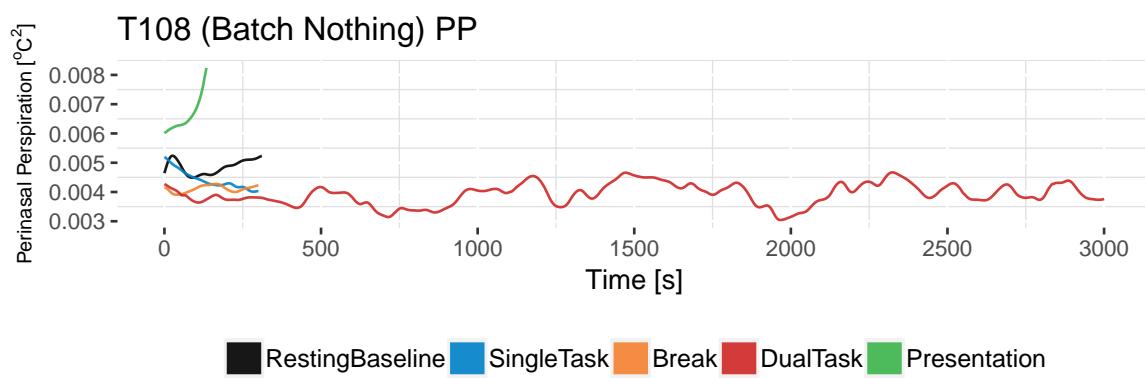


T106 (Batch Nothing) N.EDA



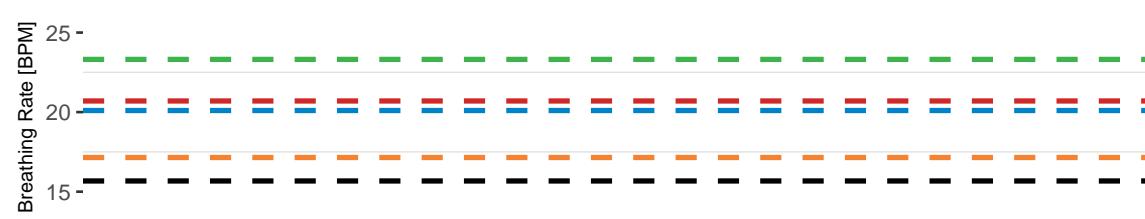
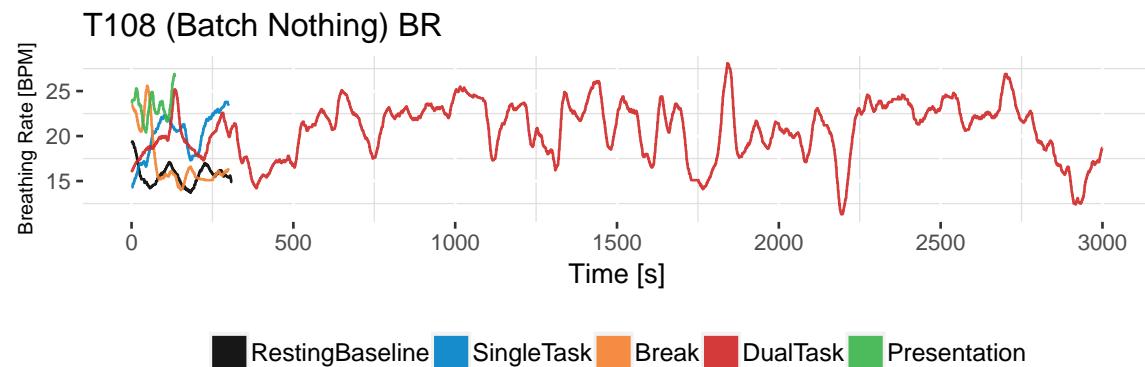
T106 (Batch Nothing) N.HR

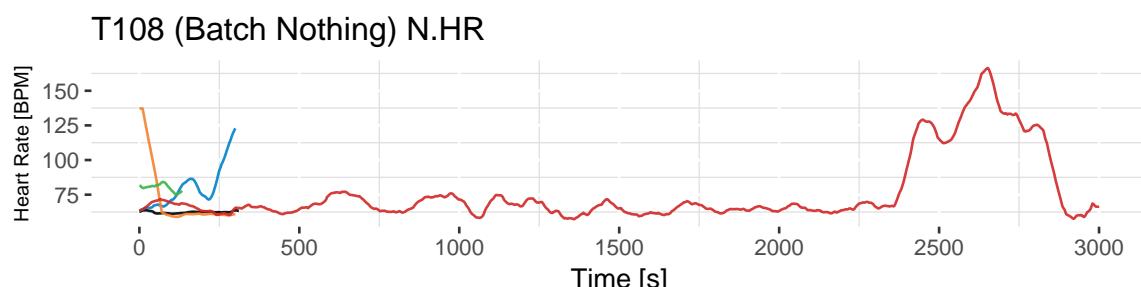
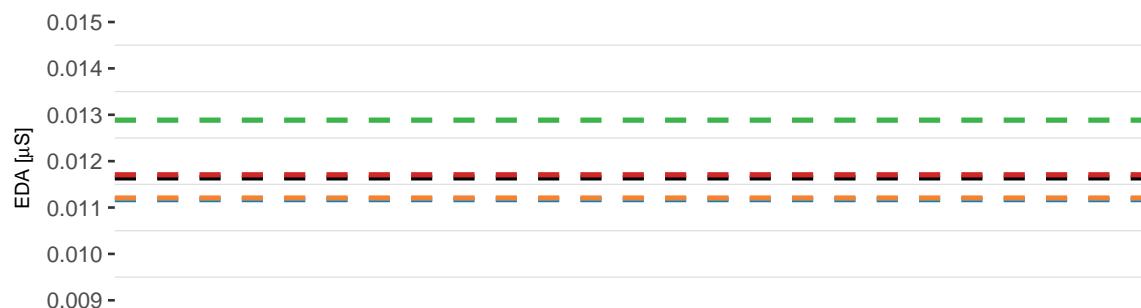
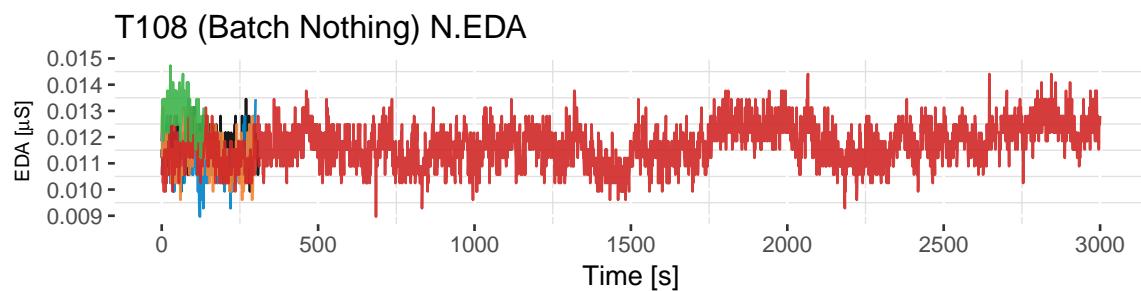


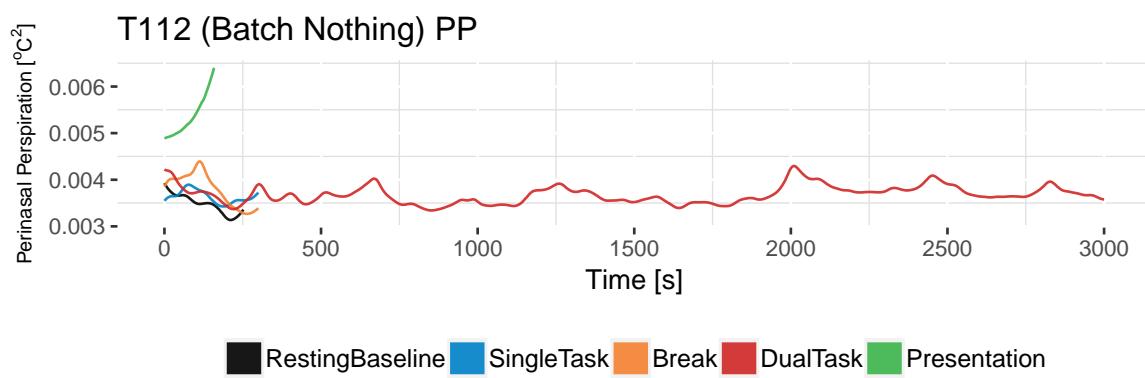


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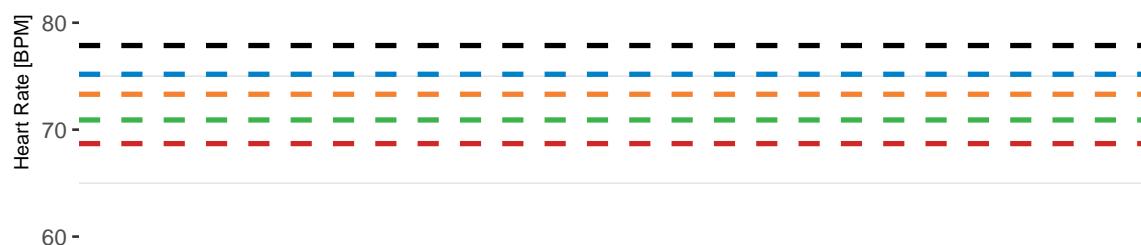
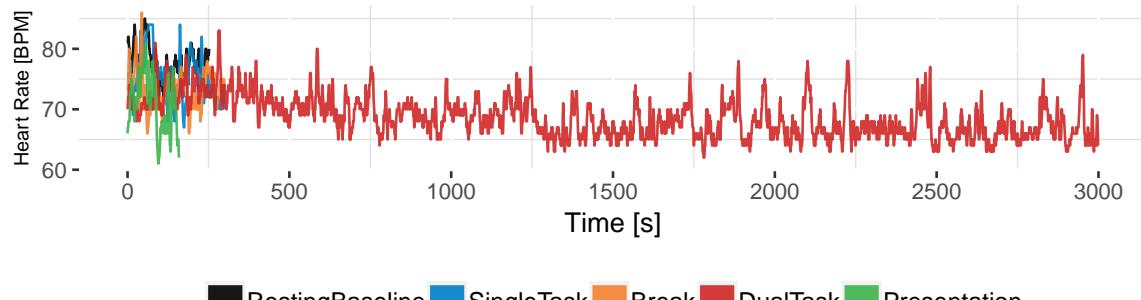
```
##  
## Subject T108 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
```



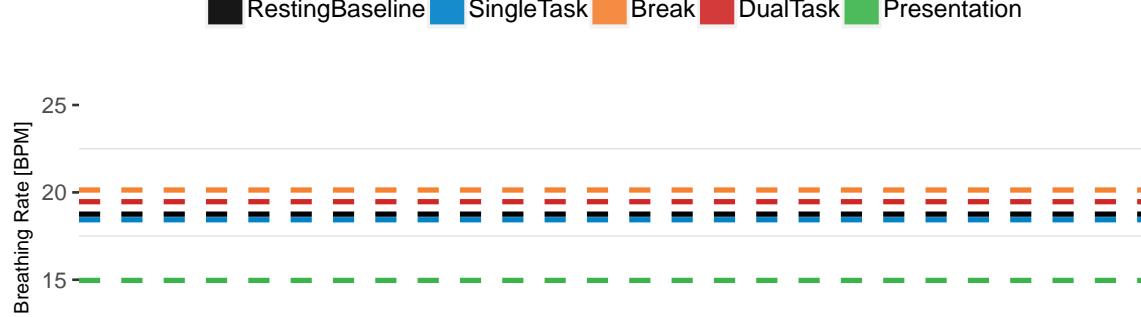
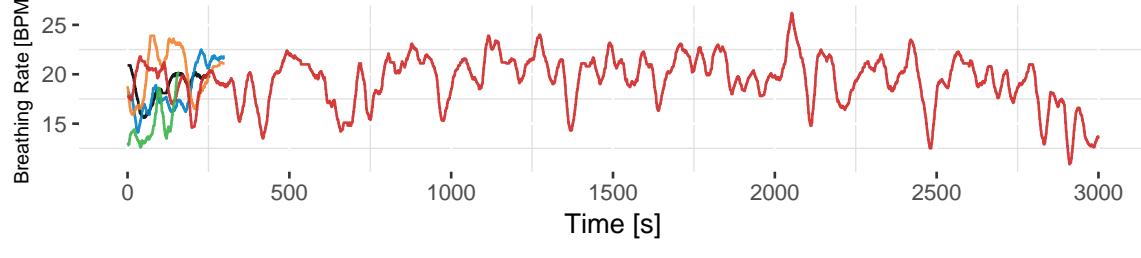




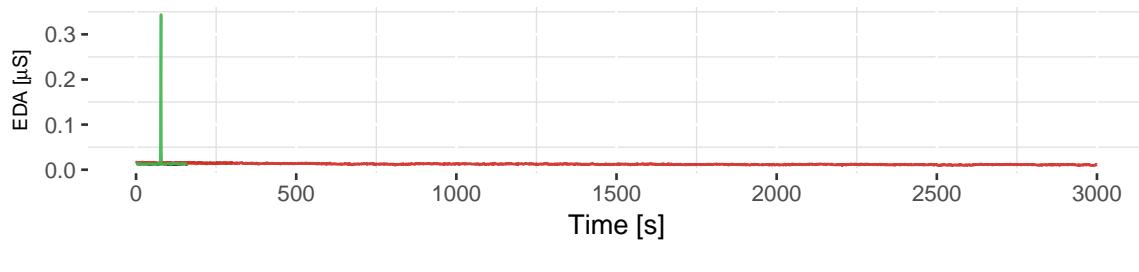
T112 (Batch Nothing) HR



T112 (Batch Nothing) BR



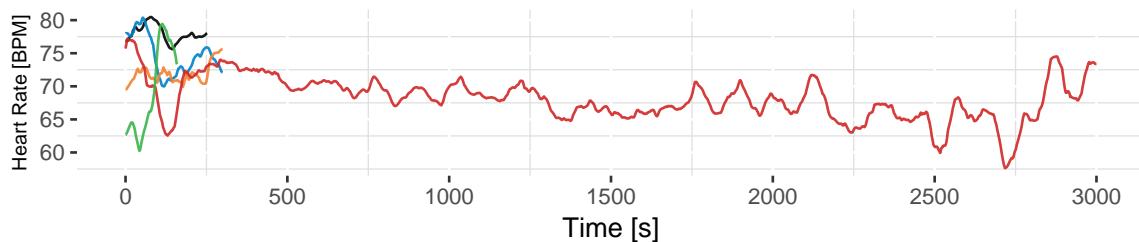
T112 (Batch Nothing) N.EDA



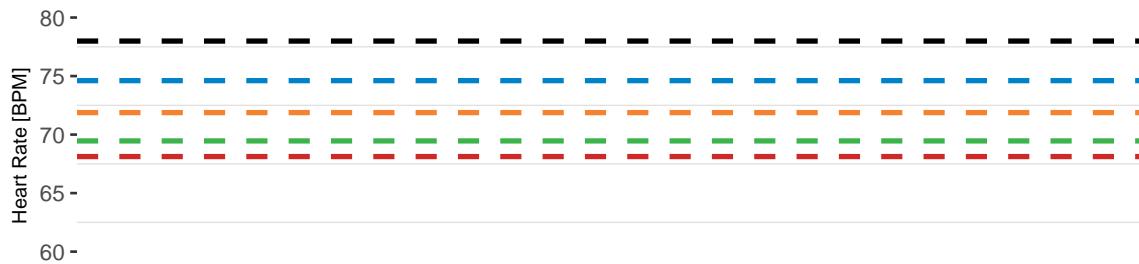
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

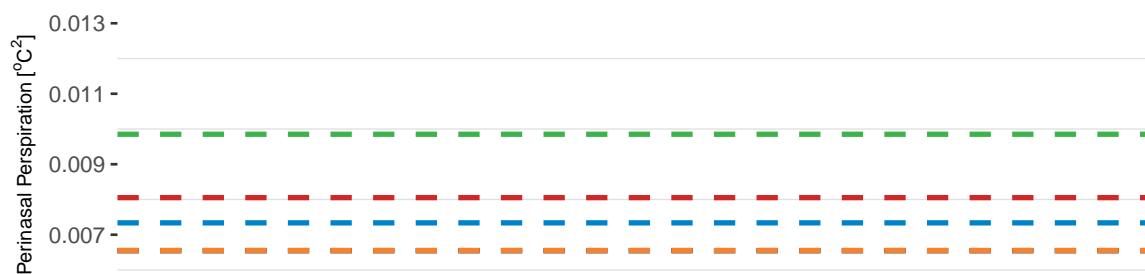
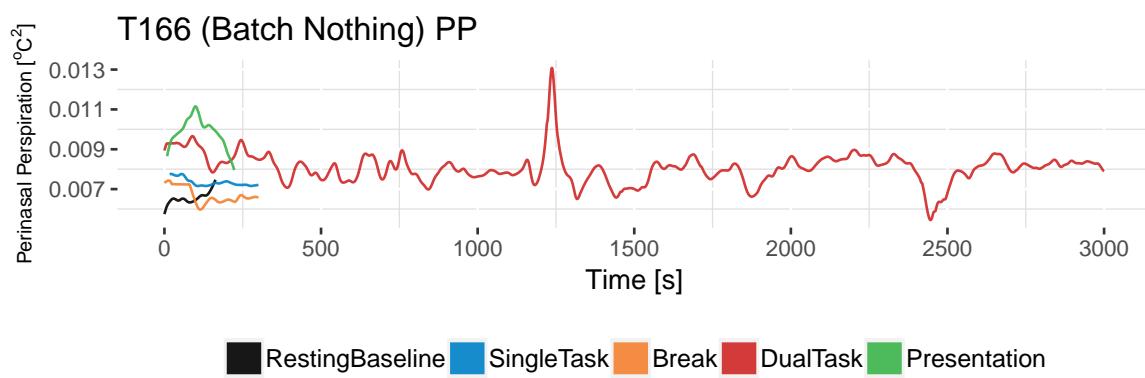


T112 (Batch Nothing) N.HR

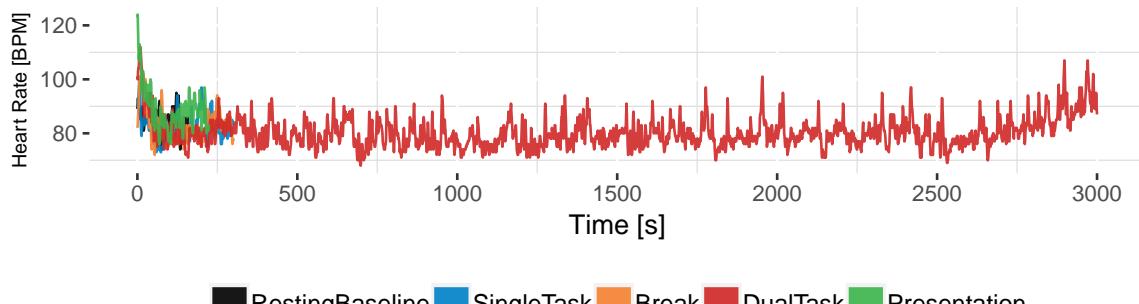


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

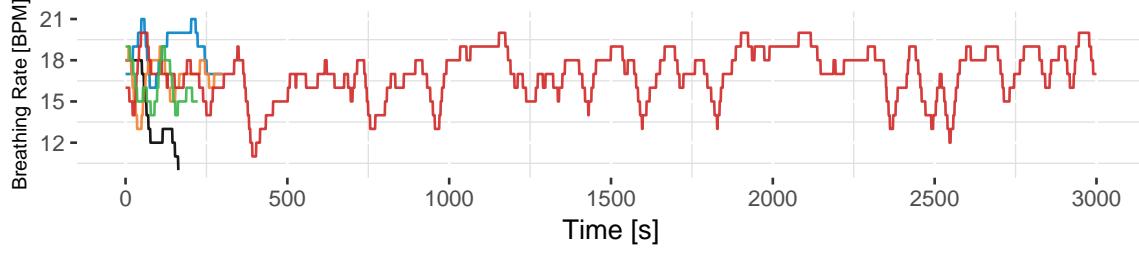




T166 (Batch Nothing) HR

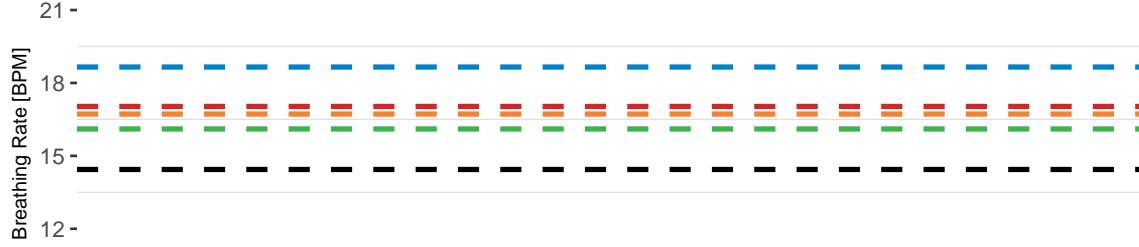


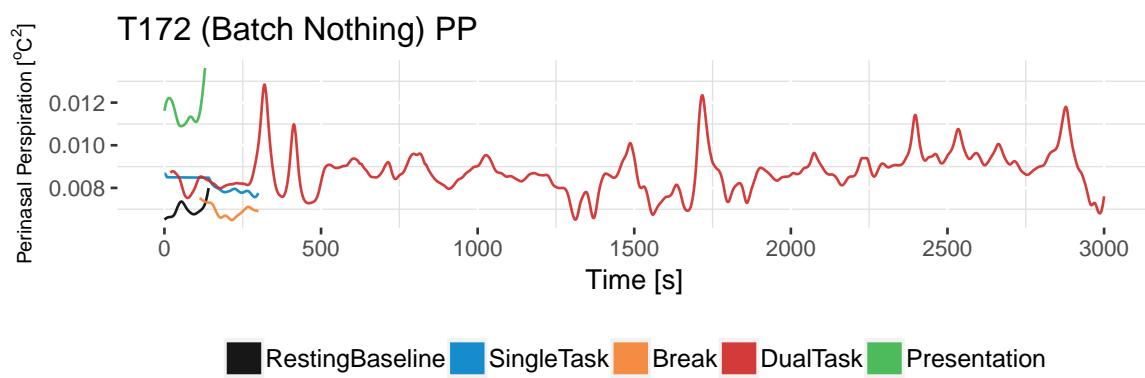
T166 (Batch Nothing) BR



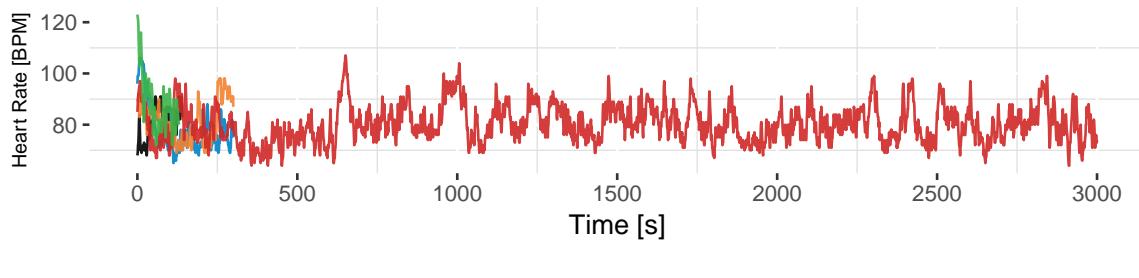
Legend:

- RestingBaseline (black)
- SingleTask (blue)
- Break (orange)
- DualTask (red)
- Presentation (green)

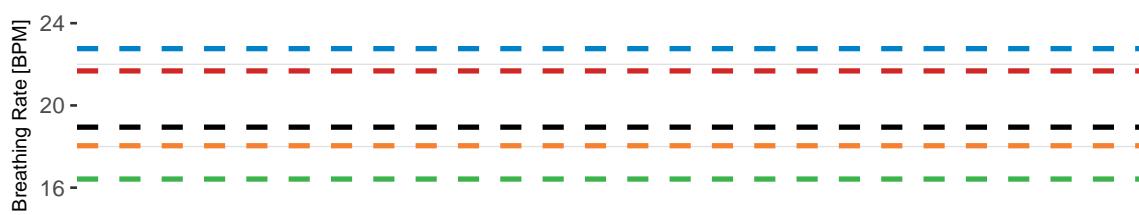
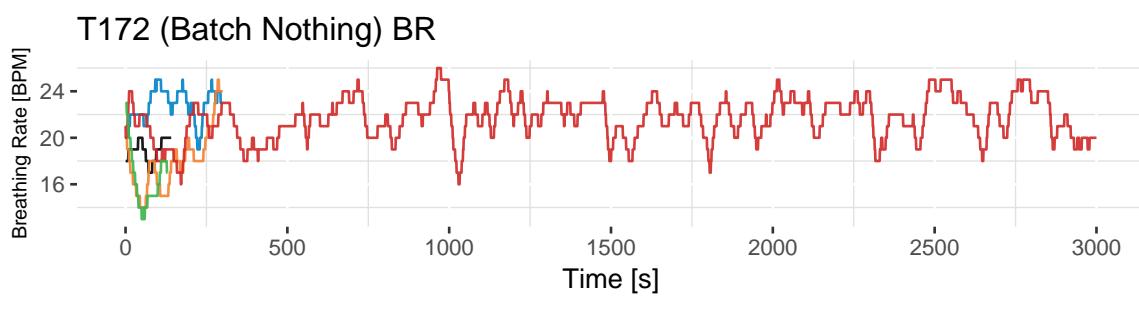


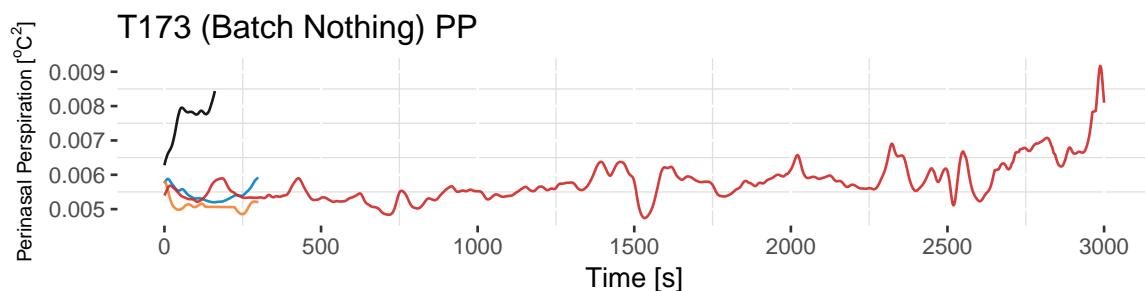


T172 (Batch Nothing) HR

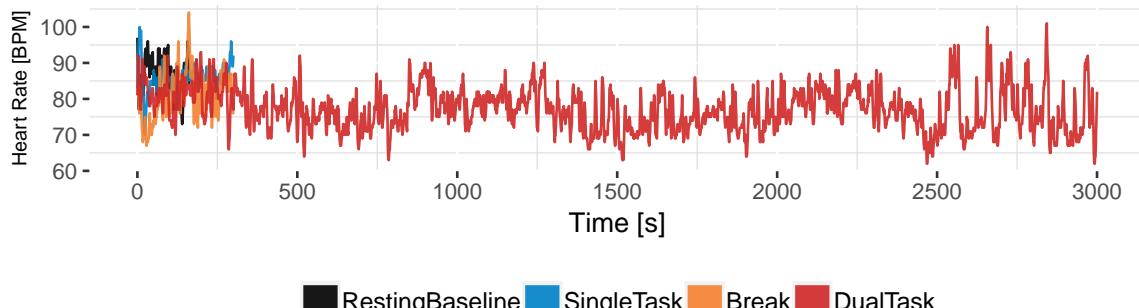


T172 (Batch Nothing) BR

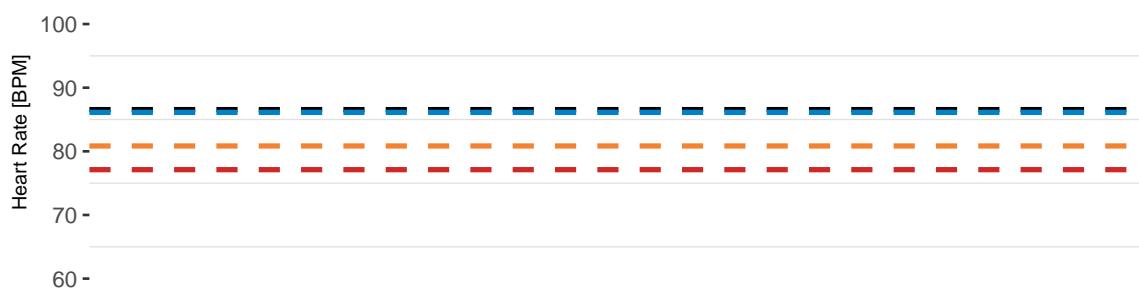




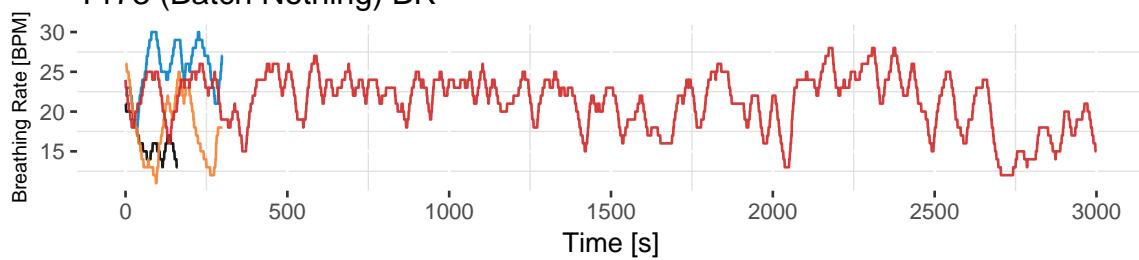
T173 (Batch Nothing) HR



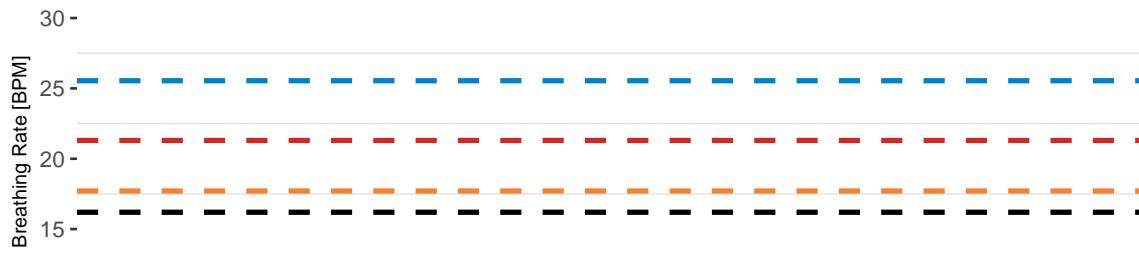
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask

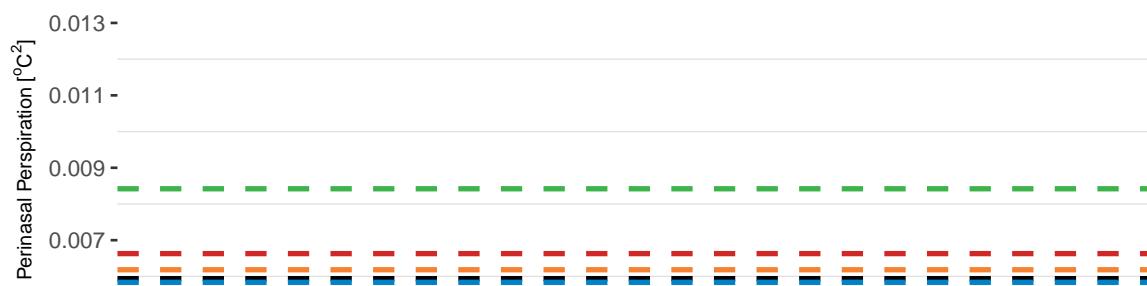
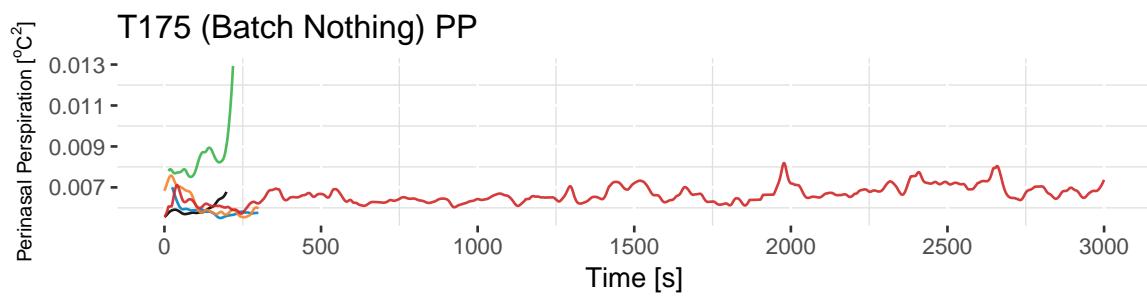


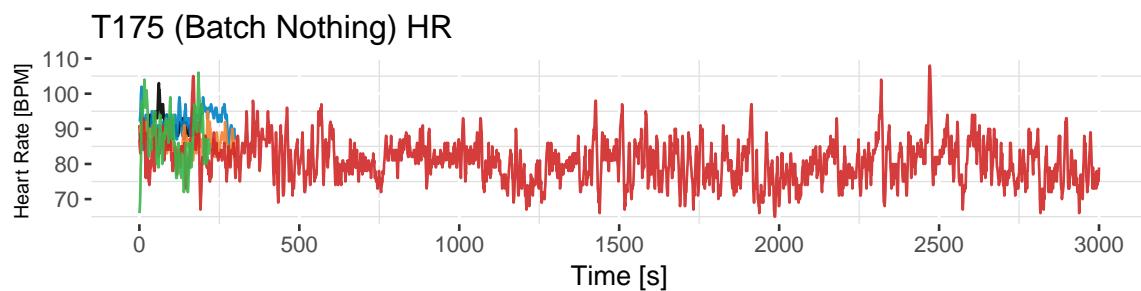
T173 (Batch Nothing) BR



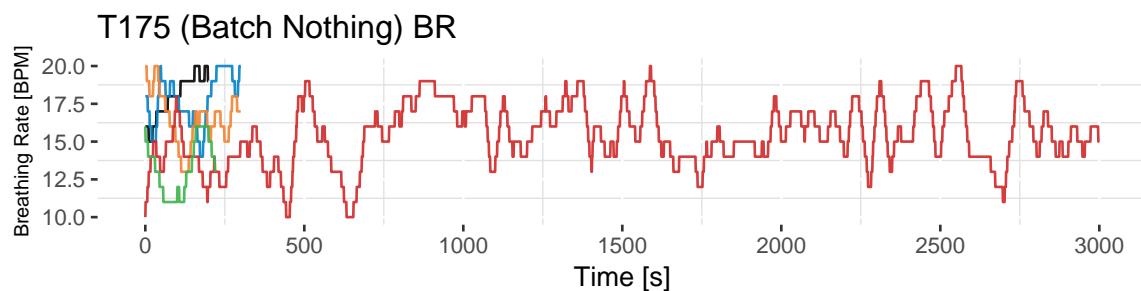
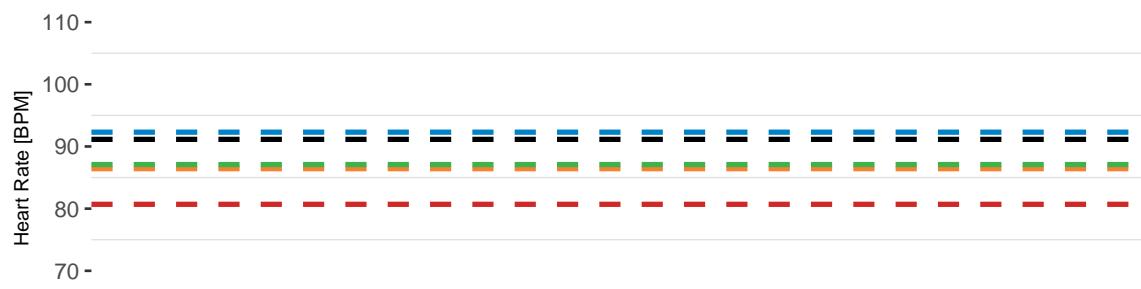
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask





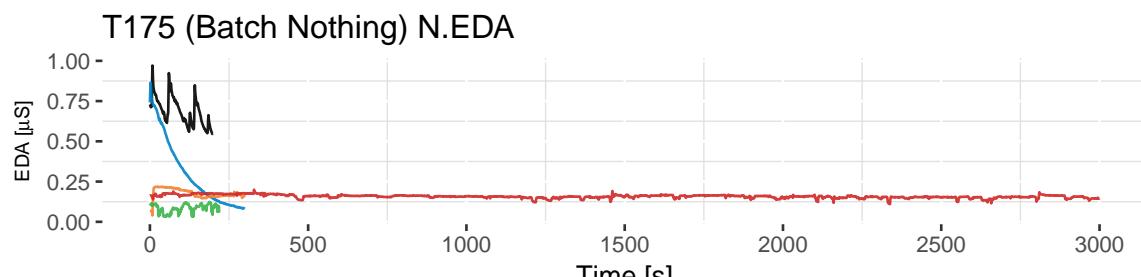


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

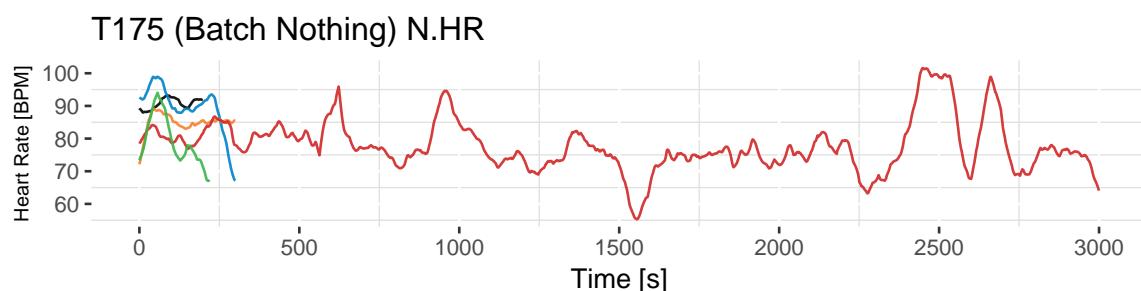
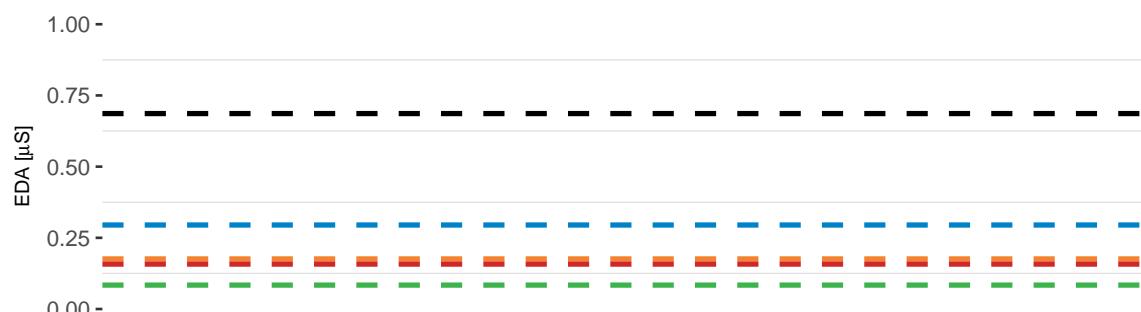


■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation



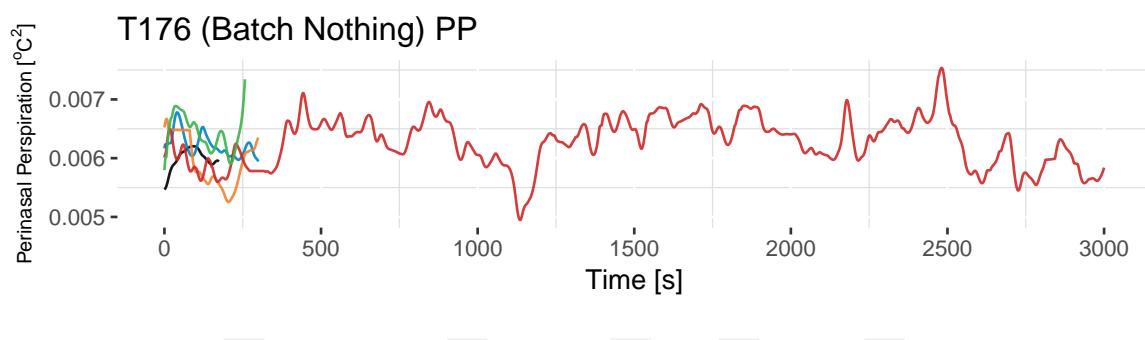


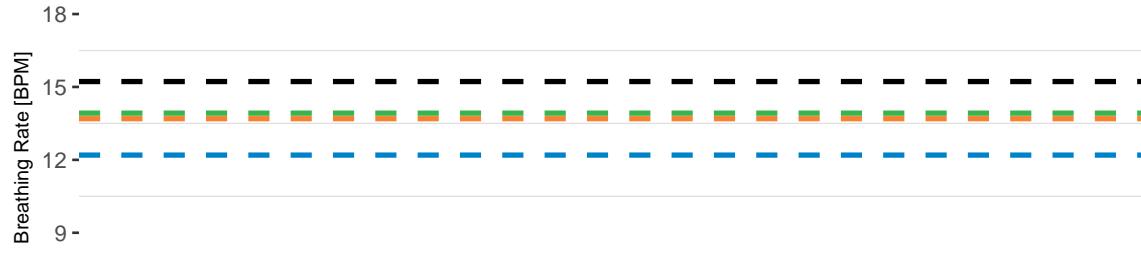
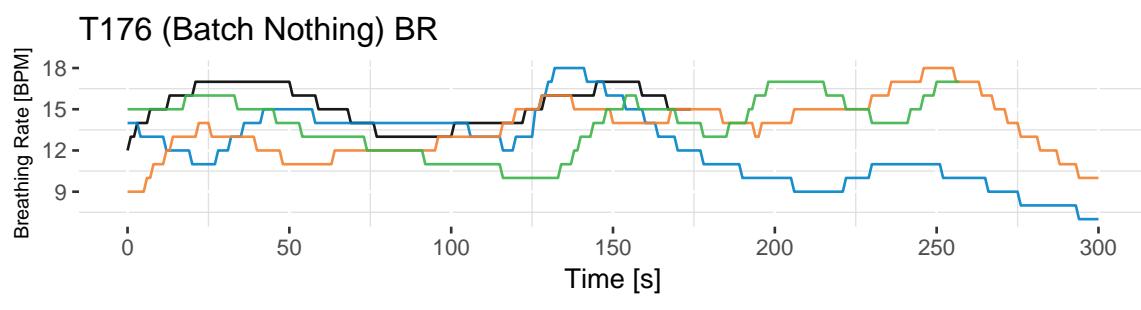
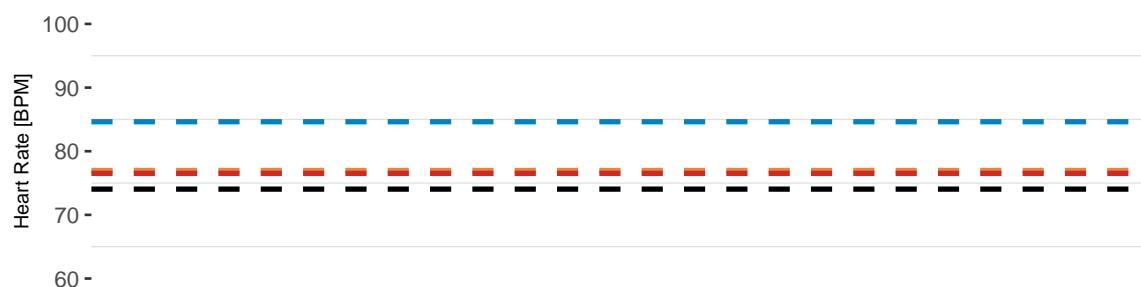
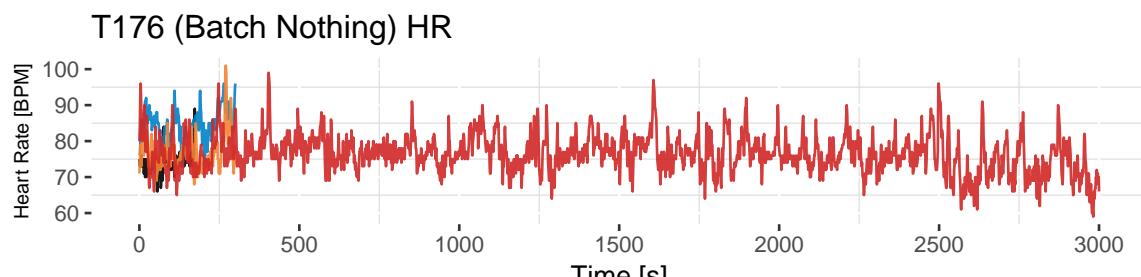
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation

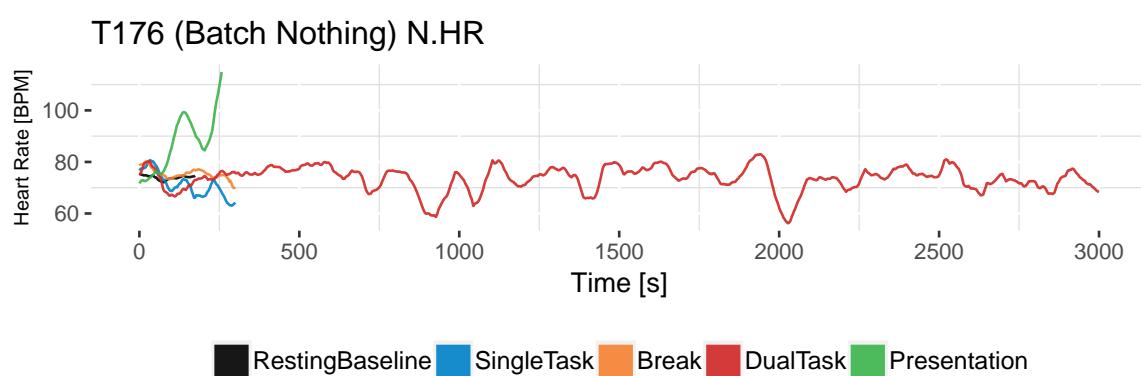
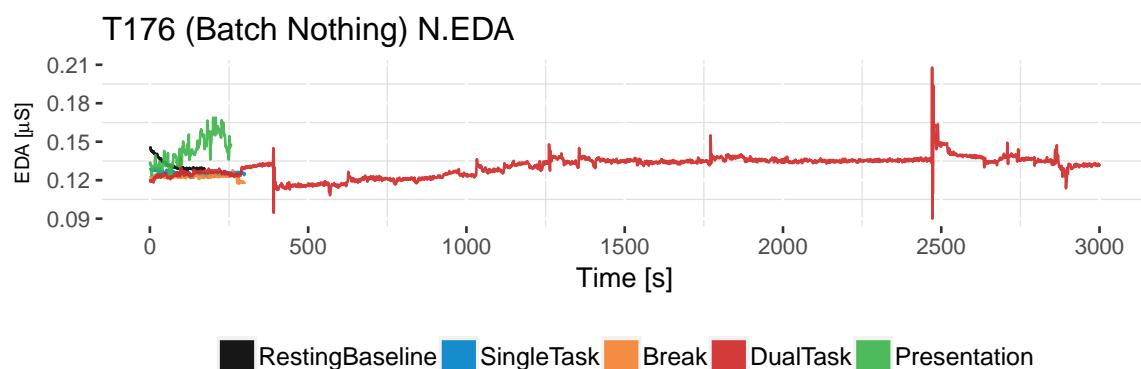


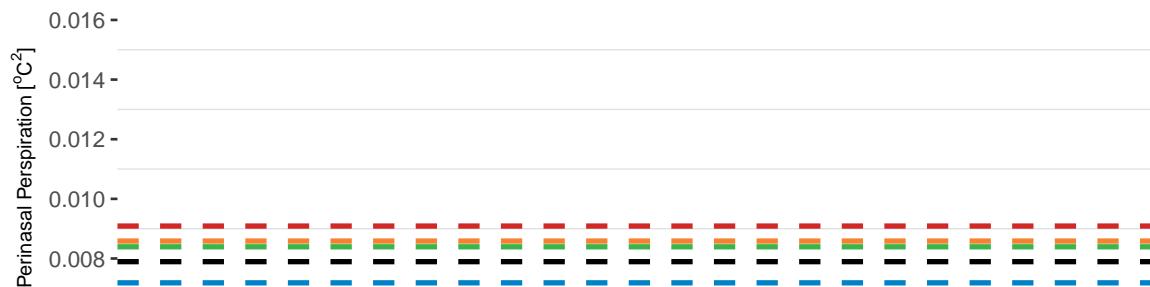
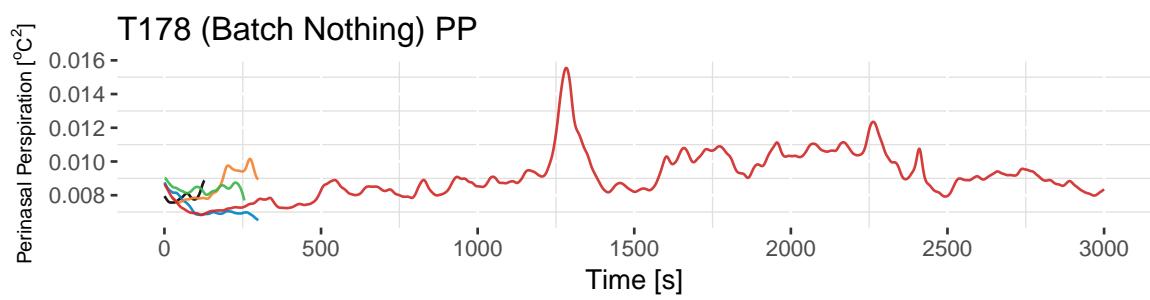
■ RestingBaseline ■ SingleTask ■ Break ■ DualTask ■ Presentation











##

```
##  
## Subject T178 does not have HR signals.  
##  
##  
##  
##  
##  
##  
##  
##  
##
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