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Research Summary

Dr. Nathan Tallent is a distinguished computer scientist within the Future Computing Technologies Group at Pacific Northwest National Laboratory. His research is motivated by emerging challenges in distributed systems, scientific workflows, machine learning, and data management. He leads activities in continuum computing and the Performance Lab for EXtreme Computing and daTa where his contributions have spanned the challenges of performance measurement, modeling, bottleneck diagnosis, and optimization; and includes special attention to bottlenecks in networks, storage, and memory. He has made notable contributions to performance tools, both for performance modeling and for parallel performance analysis. He has more than 70 peer-reviewed publications, serves on several reviewing committees, and received a DOE Early Career award.

Dr. Tallent is leading ASCR projects on performance prediction and diagnosis for extreme-scale scientific workflows. His work partners with ASCR's AMAIS ("Advanced Memory to Support Artificial Intelligence for Science") and ASCR's ENCODE ("End-to-end co-design for performance, energy efficiency, and security in AI-enabled computational science") project in characterizing workloads, generating performance models, analyzing data movement, diagnosing performance bottlenecks, and evaluating new technology. He has led development of several research software prototypes for distributed scientific workflows, distributed AI services, and performance analysis and prediction. In particular, he leads development of DataFlowDrs, a measurement and analysis toolsets for distributed scientific workflows; and the MemGaze the Palm performance analysis and modeling tools. He led development of the SEAK and PERFECT benchmark suites, designed for evaluating performance-power-architecture tradeoffs. He was an original developer of HPCToolkit, a widely used suite of performance tools.

Education

Ph.D., Computer Science, *Rice University*, Houston, TX – May 2010

M.S., Computer Science, *Rice University*, Houston, TX – May 2007

M.Div., *Westminster Theological Seminary*, Philadelphia, PA – May 2002

B.A., Computer Science, *Rice University*, Houston, TX – May 1998

Selected Work

- ◇ US DOE Early Career (2021)
- ◇ *IPDPS* 25, 25, 24, 23, 17, 16, 16; *SC* 23, 21, 17, 15, 10, 09; *CLUSTER* 24, 24, 18, 22; *ICS* 25, 14, 11; *AAAI* 26, *ICDM* 25; *BigData* 20, 19; *IISWC* 20, 18; *ISPASS* 20; *PPoPP* 15, 10, 09; *PLDI* 09; *JPDC* 23; *TPDS* 21, 20; C&C 10; *IEEE Computer* 09
- ◇ Best paper nominees: SSDBM '25, ICS '25, IISWC '18, SC '15, PLDI '09.
- ◇ ACM/IEEE-CS George Michael Memorial HPC Fellowship (2009)

Professional Experience

- ◇ Chief Computer Scientist, Pacific Northwest National Laboratory, Jan. 2022–present.
- ◇ Senior Computer Scientist, Pacific Northwest National Laboratory, Oct. 2011–2021.
- ◇ Research Scientist, Dept. of Computer Science, Rice University, Apr. 2010–Oct. 2011.
- ◇ Performance Tools Consultant (Samara Technology Group, SiCortex), Jan. 2007–Mar. 2011.

Professional Leadership

- ◇ PI, DOE ASCR (Early Career) “Orchestration for Distributed and Data-Intensive Scientific Exploration,” 2021-2026.
- ◇ Co-PI, AT SCALE (LDRD) “Data-Intensive Scientific Exploration”, 2024-25.
- ◇ Chief Scientist, PNNL Agile investment “Cloud, HPC, and Edge for Science and Security” (CHESS), 2022-24.
- ◇ Co-PI, DMC (LDRD) “Fixing Amdahl’s Law within the Limits of Accelerated Systems” (Fallacy), 2019-22.
- ◇ PI, DOE ASCR “Integrated End-to-end Performance Prediction and Diagnosis for Extreme Scientific Workflows” (IPPD/2), 2017-2020.
- ◇ Co-PI, DOE ASCR “Performance Insight for Programmers and Exascale Runtimes” (PIPER), 2012-16.
- ◇ Chair of Performance track, ICPP 2024 (53rd International Conference on Parallel Processing)
- ◇ Chair of Performance Modeling, Evaluation, and Analysis Track, ISC 2022 (Intl. Supercomputing Conf.) 2022.
- ◇ Co-Organizer, Workshop on Principles of Memory Hierarchy Optimization, in conjunction with PPOPP (Principles and Practice of Parallel Programming) 2022.
- ◇ Workshop/Tutorial Chair, IEEE International Symposium on Workload Characterization 2022.
- ◇ Co-chair, Performance Track, SC (Intl. Conf. for High Performance Computing, Networking, Storage and Analysis), 2019.
- ◇ Co-Organizer, Workshop on Suite of Embedded Applications and Kernels, in conjunction with DAC (Design Automation Conf.) 2015.
- ◇ Co-Organizer, The Second Intl. Workshop on High-performance Infrastructure for Scalable Tools, in conjunction with ICS (Intl. Conf. on Supercomputing) 2012.
- ◇ Co-Organizer, The First Intl. Workshop on High-performance Infrastructure for Scalable Tools, in conjunction with ICS (Intl. Conf. on Supercomputing) 2011.

Professional Service

- ◇ Western Washington University, Dept. of Computer Science Advisory Board
- ◇ Program Committee, ACM 35rd International Symposium on High-Performance Parallel and Distributed Computing (HPDC 2026)
- ◇ 1st Workshop On Data Reduction And Energy-Aware Data Movement (Dream) SCA 2026
- ◇ Program Committee, 40th IEEE International Parallel and Distributed Processing Symposium (IPDPS’26)
- ◇ Program Committee, 53rd International Conference on Parallel Processing (ICPP 2025)
- ◇ Program Committee, IEEE/ACM Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2025 (SC25)
- ◇ Program Committee, ACM 34rd International Symposium on High-Performance Parallel and Distributed Computing (HPDC 2025)
- ◇ Program Committee, 30th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPOPP 2025)
- ◇ ACM SIGHPC Doctoral Dissertation Award Committee
- ◇ Program Committee, ACM 33rd International Symposium on High-Performance Parallel and Distributed Computing (HPDC 2024)
- ◇ Program Committee, IEEE/ACM Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2024 (SC24)
- ◇ Program Committee, 53rd International Conference on Parallel Processing (ICPP 2024)
- ◇ Program Committee, 30th International European Conference on Parallel and Distributed Computing (Euro-PAR 2024)

- ◇ Program Committee, 38th IEEE International Parallel and Distributed Processing Symposium (IPDPS'24)
- ◇ Program Committee, 29th ACM SIGPLAN Symposium on Principles and Practice of Parallel Programming (PPoPP 2024)
- ◇ Program Committee, 10th IEEE/ACM International Conference on Big Data Computing, Applications and Technologies (BDCAT2023)
- ◇ Program Committee, 2023 Workshop on Programming and Performance Visualization Tools (ProTools)
- ◇ Program Committee, IEEE/ACM Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2023 (SC23)
- ◇ Program Committee, IEEE 37th Intl. Parallel and Distributed Processing Symp., 2023
- ◇ Program Committee, Intl. Supercomputing Conf., 2023
- ◇ Program Committee, 37th ACM International Conference on Supercomputing, 2023 (ICS).
- ◇ Program Committee, IEEE 37th Intl. Parallel and Distributed Processing Symp., 2023 (IPDPS).
- ◇ Program Committee, IEEE/ACM Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2022.
- ◇ Program Committee, 2022 Workshop on Programming and Performance Visualization Tools (ProTools)
- ◇ Program Committee, 51th International Conference on Parallel Processing, 2022.
- ◇ Program Committee, IEEE/ACM International Symposium on Cluster, Cloud and Internet Computing, 2022
- ◇ Program Committee, IEEE 36th Intl. Parallel and Distributed Processing Symp., 2022
- ◇ Program Committee, Supercomputing Frontiers Asia 2022
- ◇ 2021 Program Committee, Workshop on Programming and Performance Visualization Tools (ProTools)
- ◇ Program Committee, IEEE/ACM Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2021.
- ◇ Program Committee, 50th International Conference on Parallel Processing, 2021
- ◇ Program Committee, IEEE 35th Intl. Parallel and Distributed Processing Symp., 2021
- ◇ 2020 Program Committee, Workshop on Programming and Performance Visualization Tools (ProTools)
- ◇ Program Committee, Intl. Conf. on Massive Storage Systems and Technology, 2020.
- ◇ Program Committee, Supercomputing Asia, 2020.
- ◇ Program Committee, IEEE/ACM Intl. Symp. on Cluster, Cloud and Grid Computing, 2020.
- ◇ Program Committee, IEEE Cluster Conf., 2019.
- ◇ Program Committee, Intl. Conf. on Parallel Processing, 2019.
- ◇ Extended Program Committee, ACM/SIGARCH Intl. Conf. on Supercomputing, 2019.
- ◇ Program committee, IEEE/ACM Intl. Symp. on Cluster, Cloud and Grid Computing, 2019.
- ◇ Program Committee, IEEE/ACM Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2018.
- ◇ Program Committee, ACM/SIGARCH Intl. Conf. on Supercomputing, 2018.
- ◇ Program Committee, IEEE/ACM Intl. Symp. on Cluster, Cloud and Grid Computing, 2018.
- ◇ Program Committee, IEEE Cluster Conf., 2018.
- ◇ Program Committee, Intl. Conf. on Parallel Processing, 2018.
- ◇ Program committee, Supercomputing Asia, 2018.
- ◇ Program Committee, IEEE Cluster Conf., 2017.

- ◇ Program Committee, IEEE/ACM Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, Nov. 2016.
- ◇ Program Committee, IEEE/ACM Intl. Symp. on Cluster, Cloud and Grid Computing, 2016.
- ◇ Program Committee, IEEE 30th Intl. Parallel and Distributed Processing Symp., 2016.
- ◇ Program Committee, ACM/SIGARCH 29th Intl. Conf. on Supercomputing, 2015.
- ◇ Program Committee, 15th IEEE/ACM Intl. Symp. on Cluster, Cloud and Grid Computing, 2015.
- ◇ Program Committee: Fifth Intl. Workshop on Parallel Software Tools and Tool Infrastructures (part of ICPP 2014), 2014.
- ◇ Program Committee, PROPER 2014: 7th Workshop on Productivity and Performance (part of EuroPar 2014), 2014.
- ◇ Extended Review Committee, ICS 2014: The ACM/SIGARCH 28th Intl. Conf. on Supercomputing, 2014.
- ◇ Program Committee, DAC Workshop on Suite of Embedded Applications and Kernels (part of 51st Design Automation Conf.), 2014.
- ◇ Program Committee, HIPS 2014: 19th Intl. Workshop on High-Level Parallel Programming Models and Supportive Environments (part of IPDPS 2014), 2014.
- ◇ Birds of a Feather Committee Member, SC 2013: The IEEE/Computer and ACM/SIGARCH 2013 Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2013.
- ◇ Program Committee, PSTI 2013: Fourth Intl. Workshop on Parallel Software Tools and Tool Infrastructures (part of ICPP 2013), Lyon, France, October, 2013.
- ◇ Program Committee, PROPER 2013: 6th Workshop on Productivity and Performance (part of EuroPar 2013), 2013.
- ◇ Program Committee, ASPLOS 2013: The ACM 18th Intl. Conf. on Architectural Support for Programming Languages and Operating Systems, 2013.
- ◇ Program Committee, PPOPP 2013: The ACM/SIGPLAN 18th Symp. on Principles and Practice of Parallel Programming, 2013.
- ◇ Program Committee, PSTI 2012: Third Intl. Workshop on Parallel Software Tools and Tool Infrastructures (part of ICPP 2012), 2012.
- ◇ External Review Committee, PPOPP 2012: The ACM/SIGPLAN 17th Symp. on Principles and Practice of Parallel Programming, 2012.
- ◇ Program Committee, SC 2011: The IEEE/Computer and ACM/SIGARCH 2011 Intl. Conf. for High Performance Computing, Networking, Storage and Analysis, 2011.
- ◇ Program Committee, PSTI 2011: Second Intl. Workshop on Parallel Software Tools and Tool Infrastructures (part of ICPP 2011), 2011.
- ◇ Program Committee, ICS 2011: The ACM/SIGARCH 25th Intl. Conf. on Supercomputing, 2011.
- ◇ Program Committee, CGO 2011: 2011 Intl. Symp. on Code Generation and Optimization, 2011.
- ◇ Reviewer for *ACM Transactions on Architecture and Code Optimization*, *ACM Transactions on Parallel Computing, Concurrency and Computation: Practice and Experience*, *IEEE Computer*, *IEEE Transactions on Computers*, *IEEE Transactions on Parallel and Distributed Systems*, *Journal of Systems and Software*, *Journal of Parallel and Distributed Computing*, *Parallel Computing: Systems & Applications*, *Software: Practice and Experience*.

Publications (conference, journal)

- ◇ Gong, Chengyu, Shen, Gefei, Guo, Luanzheng, **Tallent, Nathan R.**, and Zhao, Dongfang. **Jan. 2026**. “Order-Preserving Dimension Reduction for Multimodal Semantic Embedding”. In: *Proceedings of the AAAI Conference on Artificial Intelligence* 40.
- ◇ Mehboob, Talha, Guo, Luanzheng, **Tallent, Nathan R.**, Zink, Michael, and Irwin, David. **Nov. 2025**. “PowerTrip:

- Exploiting Federated Heterogeneous Datacenter Power for Distributed ML Training”. In: *Proc. of the 16th ACM Symposium on Cloud Computing*.
- ◇ Fu, Jiuzhou, Guo, Luanzheng, **Tallent, Nathan R.**, and Zhao, Dongfang. **Nov. 2025**. “ProHD: Projection-Based Hausdorff Distance Approximation”. In: *Proc. of the 25th IEEE Intl. Conf. on Data Mining*.
 - ◇ Zhang, Boyuan, Fang, Bo, Ye, Fanjiang, Guo, Luanzheng, Song, Fengguang, **Tallent, Nathan R.**, and Tao, Dingwen. **June 2025**. “BMQSim: Overcoming Memory Constraints in Quantum Circuit Simulation with a High-Fidelity Compression Framework”. In: *Proc. of the 39th ACM Intl. Conf. on Supercomputing*. **Best Paper Runner Up**. New York, NY, USA: Association for Computing Machinery, pp. 689–704. doi: [10.1145/3721145.3725747](https://doi.org/10.1145/3721145.3725747).
 - ◇ Firoz, Jesun, Lee, Hyungro, Guo, Luanzheng, Tang, Meng, **Tallent, Nathan R.**, and Peng, Zhen. **June 2025**. “FastFlow: Rapid Workflow Response by Prioritizing Critical Data Flows and their Interactions”. In: *Proc. of the 37th Intl. Conf. on Scalable Scientific Data Management*. **Best Paper Nominee**. ACM, pp. 1–12. doi: [10.1145/3733723.3733735](https://doi.org/10.1145/3733723.3733735).
 - ◇ Lee, Hyungro, Firoz, Jesun, **Tallent, Nathan R.**, Guo, Luanzheng, and Halappanavar, Mahantesh. **June 2025**. “FlowForecaster: Automatically Inferring Detailed & Interpretable Workflow Scaling Models for Forecasts”. In: *Proc. of the 39th IEEE Intl. Parallel and Distributed Processing Symp.* IEEE Computer Society, pp. 420–432. doi: [10.1109/IPDPS64566.2025.00045](https://doi.org/10.1109/IPDPS64566.2025.00045).
 - ◇ Newaz, Nahid, Ghosh, Sayan, **Tallent, Nathan R.**, and Qu, Guangzhi. **June 2025**. “Locality Aware Process Remapping for Distributed-Memory Graph Workloads”. In: *Proc. of the 39th IEEE Intl. Parallel and Distributed Processing Symp.* IEEE Computer Society, pp. 447–459. doi: [10.1109/IPDPS64566.2025.00047](https://doi.org/10.1109/IPDPS64566.2025.00047).
 - ◇ Abebe, Waqwoya, Strube, Jan, Guo, Luanzheng, **Tallent, Nathan R.**, Bel, Oceane, Spurgeon, Steven, Doty, Christina, and Jannesari, Ali. **Jan. 2025**. “SAM-I-Am: Semantic boosting for zero-shot atomic-scale electron micrograph segmentation”. In: *Computational Materials Science* 246, p. 113400. doi: [10.1016/j.commatsci.2024.113400](https://doi.org/10.1016/j.commatsci.2024.113400).
 - ◇ Suetterlein, Joshua, Young, Stephen J., Firoz, Jesun, Manzano, Joseph, Frieze, Ryan, **Tallent, Nathan R.**, Barker, Kevin, and Stavenger, Timothy. **Sept. 2024**. “HPC Network Simulation Tuning via Automatic Extraction of Hardware Parameters”. In: *Proc. of the 2024 IEEE High Performance Extreme Computing Conference*, pp. 1–10. doi: [10.1109/HPEC62836.2024.10938439](https://doi.org/10.1109/HPEC62836.2024.10938439).
 - ◇ Hou, Shiyue, **Tallent, Nathan R.**, Wang, Li, and Mi, Ningfang. **Oct. 2024**. “Performance Analysis of Data Processing in Distributed File Systems with Near Data Processing”. In: *11th Intl. Symp. on Networks, Computers and Communications*. IEEE. doi: [10.1109/ISNCC62547.2024.10758994](https://doi.org/10.1109/ISNCC62547.2024.10758994).
 - ◇ Suriyakumar, Yasodha, **Tallent, Nathan R.**, Marquez, Andrés, and Karavanic, Karen. **Sept. 2024**. “MemFriend: Understanding Memory Performance with Spatial-Temporal Affinity”. In: *Proc. of the International Symposium on Memory Systems (MemSys 2024)*. New York, NY, USA: ACM. doi: [10.1145/3695794.3695820](https://doi.org/10.1145/3695794.3695820).
 - ◇ Sarkar, Aishwarya, Ghosh, Sayan, **Tallent, Nathan R.**, and Jannesari, Ali. **Sept. 2024**. “MassiveGNN: Efficient Training via Prefetching for Massively Connected Distributed Graphs”. In: *Proc. of the 2024 IEEE Conf. on Cluster Computing*. IEEE, pp. 62–73. doi: [10.1109/CLUSTER59578.2024.00013](https://doi.org/10.1109/CLUSTER59578.2024.00013).
 - ◇ Tang, Meng, Cernuda, Jaime, Ye, Jie, Guo, Luanzheng, **Tallent, Nathan R.**, Kougkas, Anthony, and Sun, Xian-He. **Sept. 2024**. “DaYu: Optimizing Distributed Scientific Workflows by Decoding Dataflow Semantics and Dynamics”. In: *Proc. of the 2024 IEEE Conf. on Cluster Computing*. IEEE, pp. 357–369. doi: [10.1109/CLUSTER59578.2024.00038](https://doi.org/10.1109/CLUSTER59578.2024.00038).
 - ◇ Newaz, Nahid, Ghosh, Sayan, **Tallent, Nathan R.**, Suetterlein, Joshua, Mollah, Atiqul, and Ming, Hua. **May 2024**. “Graph Analytics on Jellyfish Topology”. In: *Proc. of the 38th IEEE Intl. Parallel and Distributed Processing Symp.* doi: [10.1109/IPDPS57955.2024.00079](https://doi.org/10.1109/IPDPS57955.2024.00079).
 - ◇ Lee, Hyungro, Guo, Luanzheng, Tang, Meng, Firoz, Jesun, **Tallent, Nathan**, Kougkas, Anthony, and Sun, Xian-He. **Nov. 2023**. “Data Flow Lifecycles for Optimizing Workflow Coordination”. In: *Proc. of the Intl. Conf. for High Performance Computing, Networking, Storage and Analysis (SuperComputing)*. SC ’23. New York, NY, USA: Association for Computing Machinery. doi: [10.1145/3581784.3607104](https://doi.org/10.1145/3581784.3607104).

- ◇ Chen, Jou-An, Sung, Hsin-Hsuan, Shen, Xipeng, **Tallent, Nathan**, Barker, Kevin, and Li, Ang. **2023e**. “Accelerating matrix-centric graph processing on GPUs through bit-level optimizations”. In: *Journal of Parallel and Distributed Computing* 177, pp. 53–67. doi: <https://doi.org/10.1016/j.jpdc.2023.02.013>.
- ◇ Kilic, Ozgur O., **Tallent, Nathan R.**, Suriyakumar, Yasodhadevi, Xie, Chenhao, Marquez, Andrés, and Eranian, Stephane. **Sept. 2022**. “MemGaze: Rapid and Effective Load-Level Memory and Data Analysis”. In: *Proc. of the 2022 IEEE Conf. on Cluster Computing*. IEEE. doi: [10.1109/CLUSTER51413.2022.00058](https://doi.org/10.1109/CLUSTER51413.2022.00058).
- ◇ **Sathanur:2022:learning**.
- ◇ Chen, Jou-An, Sung, Hsin-Hsuan, **Tallent, Nathan R.**, Barker, Kevin, Shen, Xipeng, and Li, Ang. **May 2022**. “Bit-GraphBLAS: Bit-Level Optimizations of Matrix-Centric Graph Processing on GPU”. in: *Proc. of the 36th IEEE Intl. Parallel and Distributed Processing Symp.* IEEE. doi: [10.1109/IPDPS53621.2022.00056](https://doi.org/10.1109/IPDPS53621.2022.00056).
- ◇ Ghosh, Sayan, **Tallent, Nathan R.**, Minutoli, Marco, Halappanavar, Mahantesh, Peri, Ramesh, and Kalyanaraman, Ananth. **Nov. 2021**. “Single-node Partitioned-Memory for Huge Graph Analytics: Cost and Performance Tradeoffs”. In: *Proc. of the Intl. Conf. for High Performance Computing, Networking, Storage and Analysis (SuperComputing)*. New York, NY, USA: ACM, pp. 1–14. doi: [10.1145/3458817.3476156](https://doi.org/10.1145/3458817.3476156).
- ◇ Acer, Seher, **Tallent, Nathan R.** et al. **Sept. 2021**. “EXAGRAPH: Graph and Combinatorial Methods for Enabling Exascale Applications”. In: *International Journal of High Performance Computing Applications*, p. 10943420211029299. doi: [10.1177/10943420211029299](https://doi.org/10.1177/10943420211029299).
- ◇ Ghosh, Sayan, **Tallent, Nathan R.**, and Halappanavar, Mahantesh. **Aug. 2021**. “Characterizing Performance of Graph Neighborhood Communication Patterns”. In: *IEEE Transactions on Parallel and Distributed Systems*. doi: [10.1109/TPDS.2021.3101425](https://doi.org/10.1109/TPDS.2021.3101425).
- ◇ Bel, Oceane, Pata, Joosep, Vlimant, Jean-Roch, **Tallent, Nathan**, Balcas, Justas, and Spiropulu, Maria. **May 2021**. “Diolkos: Improving ethernet throughput through dynamic port selection”. In: *18th ACM International Conference on Computing Frontiers*. New York, NY, USA: ACM. doi: [10.1145/3457388.3458659](https://doi.org/10.1145/3457388.3458659).
- ◇ Friese, Ryan D., Mutlu, Burcu O., **Tallent, Nathan R.**, Suetterlein, Joshua, and Strube, Jan. **Dec. 2020**. “Effectively Using Remote I/O For Work Composition in Distributed Workflows”. In: *Proc. of the 2020 IEEE Intl. Conf. on Big Data*. IEEE Computer Society, pp. 426–433. doi: [10.1109/BigData50022.2020.9378352](https://doi.org/10.1109/BigData50022.2020.9378352).
- ◇ Barik, Reet, Minutoli, Marco, Halappanavar, Mahantesh, **Tallent, Nathan R.**, and Kalyanaraman, Ananth. **Oct. 2020**. “Vertex Reordering for Real-world Graphs and Applications: An Empirical Evaluation”. In: *Proc. of the 2020 IEEE Intl. Symp. on Workload Characterization*. doi: [10.1109/IISWC50251.2020.00031](https://doi.org/10.1109/IISWC50251.2020.00031).
- ◇ Bel, Oceane, Chang, Kenneth, **Tallent, Nathan R.**, Duellmann, Dirk, Miller, Ethan L., Nawab, Faisal, and Long, Darrell D. E. **Oct. 2020**. “Geomancy: Automated Performance Enhancement through Data Layout Optimization”. In: *36th Intl. Conf. on Massive Storage Systems and Technology*. doi: <https://storageconference.us/2020/Papers/10.GeoMancy.pdf>.
- ◇ Kilic, Ozgur O., **Tallent, Nathan R.**, and Friese, Ryan D. **Oct. 2020**. “Rapid Memory Footprint Access Diagnostics”. In: *Proc. of the 2020 IEEE Intl. Symp. on Performance Analysis of Systems and Software*. IEEE Computer Society, pp. 273–284. doi: [10.1109/ISPASS48437.2020.00047](https://doi.org/10.1109/ISPASS48437.2020.00047).
- ◇ Li, Ang, Song, S., Chen, J., Li, J., Liu, X., **Tallent, Nathan**, and Barker, Kevin J. **Jan. 2020**. “Evaluating Modern GPU Interconnect: PCIe, NVLink, NV-SLI, NVSwitch and GPUDirect”. In: *IEEE Transactions on Parallel and Distributed Systems* 31.1, pp. 94–110. doi: [10.1109/TPDS.2019.2928289](https://doi.org/10.1109/TPDS.2019.2928289).
- ◇ Suetterlein, Joshua, Friese, Ryan D., **Tallent, Nathan R.**, and Schram, Malachi. **Dec. 2019**. “TAZeR: Hiding the Cost of Remote I/O in Distributed Scientific Workflows”. In: *Proc. of the 2019 IEEE Intl. Conf. on Big Data*. IEEE Computer Society, pp. 383–394. doi: [10.1109/BigData47090.2019.9006418](https://doi.org/10.1109/BigData47090.2019.9006418).
- ◇ Bhuiyan, Tanveer Hossain, Halappanavar, Mahantesh, Friese, Ryan D., Medal, Hugh, de la Torre, Luis, Sathanur, Arun, and **Tallent, Nathan R.** **2019e**. “Stochastic Programming Approach for Resource Selection Under Demand Uncertainty”. In: *Job Scheduling Strategies for Parallel Processing*. Ed. by Dalibor Klusáček, Walfredo Cirne, and Narayan Desai. Cham: Springer International Publishing, pp. 107–126. doi: [10.1007/978-3-030-10632-4_6](https://doi.org/10.1007/978-3-030-10632-4_6).
- ◇ Li, Ang, Song, Shuaiwen Leon, Liu, Xu, **Tallent, Nathan**, and Barker, Kevin. **Sept. 2018**. “Tartan: Evaluating Modern GPU Interconnect via a Multi-GPU Benchmark Suite”. In: *Proc. of the 2018 IEEE Intl. Symp. on Workload*

Characterization. Best Paper Finalist, pp. 191–202. DOI: [10.1109/IISWC.2018.8573483](https://doi.org/10.1109/IISWC.2018.8573483).

- ◇ Friese, Ryan D., **Tallent, Nathan R.**, Schram, Malachi, Halappanavar, Mahantesh, and Barker, Kevin J. **Sept. 2018**. “Optimizing Distributed Data-Intensive Workflows”. In: *Proc. of the 2018 IEEE Conf. on Cluster Computing*. IEEE, pp. 279–289. DOI: [10.1109/CLUSTER.2018.00045](https://doi.org/10.1109/CLUSTER.2018.00045).
- ◇ Gawande, Nitin A., Daily, Jeff A., Siegel, Charles, **Tallent, Nathan R.**, and Vishnu, Abhinav. **May 2018**. “Scaling Deep Learning Workloads: NVIDIA DGX-1/Pascal and Intel Knights Landing”. In: *Future Generation Computer Systems*. DOI: <https://doi.org/10.1016/j.future.2018.04.073>.
- ◇ **Tallent, Nathan R.**, Kerbyson, Darren J., and Hoisie, Adolfo. **Nov. 2017**. “Representative Paths Analysis”. In: *Proc. of the Intl. Conf. for High Performance Computing, Networking, Storage and Analysis (SuperComputing)*. New York, NY, USA: ACM, 34:1–34:12. DOI: [10.1145/3126908.3126962](https://doi.org/10.1145/3126908.3126962).
- ◇ **Tallent, Nathan R.**, Gawande, Nitin A., Siegel, Charles, Vishnu, Abhinav, and Hoisie, Adolfo. **Dec. 2017**. “Evaluating On-Node GPU Interconnects for Deep Learning Workloads”. In: *High Performance Computing Systems. Performance Modeling, Benchmarking, and Simulation*. Ed. by Stephen Jarvis, Steven Wright, and Simon Hammond. Springer International Publishing, pp. 3–21. DOI: [10.1007/978-3-319-72971-8_1](https://doi.org/10.1007/978-3-319-72971-8_1).
- ◇ Friese, Ryan D., **Tallent, Nathan R.**, Vishnu, Abhinav, Kerbyson, Darren J., and Hoisie, Adolfo. **May 2017**. “Generating Performance Models for Irregular Applications”. In: *Proc. of the 31st IEEE Intl. Parallel and Distributed Processing Symp.* Los Alamitos, CA, USA: IEEE Computer Society, pp. 317–326. DOI: [10.1109/IPDPS.2017.61](https://doi.org/10.1109/IPDPS.2017.61).
- ◇ **Tallent, Nathan R.**, Barker, Kevin J., Chavarria-Miranda, Daniel, Tumeo, Antonino, Halappanavar, Mahantesh, Márquez, Andrés, Kerbyson, Darren J., and Hoisie, Adolfo. **Aug. 2016**. “Modeling the Impact of Silicon Photonics on Graph Analytics”. In: *Proc. of the 11th IEEE Intl. Conf. on Networking, Architecture, and Storage*. IEEE Computer Society, pp. 1–11. DOI: [10.1109/NAS.2016.7549410](https://doi.org/10.1109/NAS.2016.7549410).
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- ◇ **Tallent, Nathan R. Sept. 2016.** *Using Predictive Modeling to Evaluate Analytics Workloads*. Scalable Approaches to High Performance and High Productivity Computing.
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- ◇ Froyd, Nathan, Mellor-Crummey, John, and **Tallent, Nathan R. Oct. 2003.** *A Sample-Driven Call Stack Profiler*. 4th Symp. of the Los Alamos Computer Science Institute (LACSI 2003).

Mentoring, Advising

- ◇ Mentoring for more than 30 Post Doctoral Researchers and Interns
- ◇ Ph.D. Committee, Hasanur Rashid, University of Delaware
- ◇ Ph.D. Committee, Yasodha Suriyakumar, Portland State University
- ◇ Ph.D. Committee, Oceane Bel, University of California, Santa Cruz