

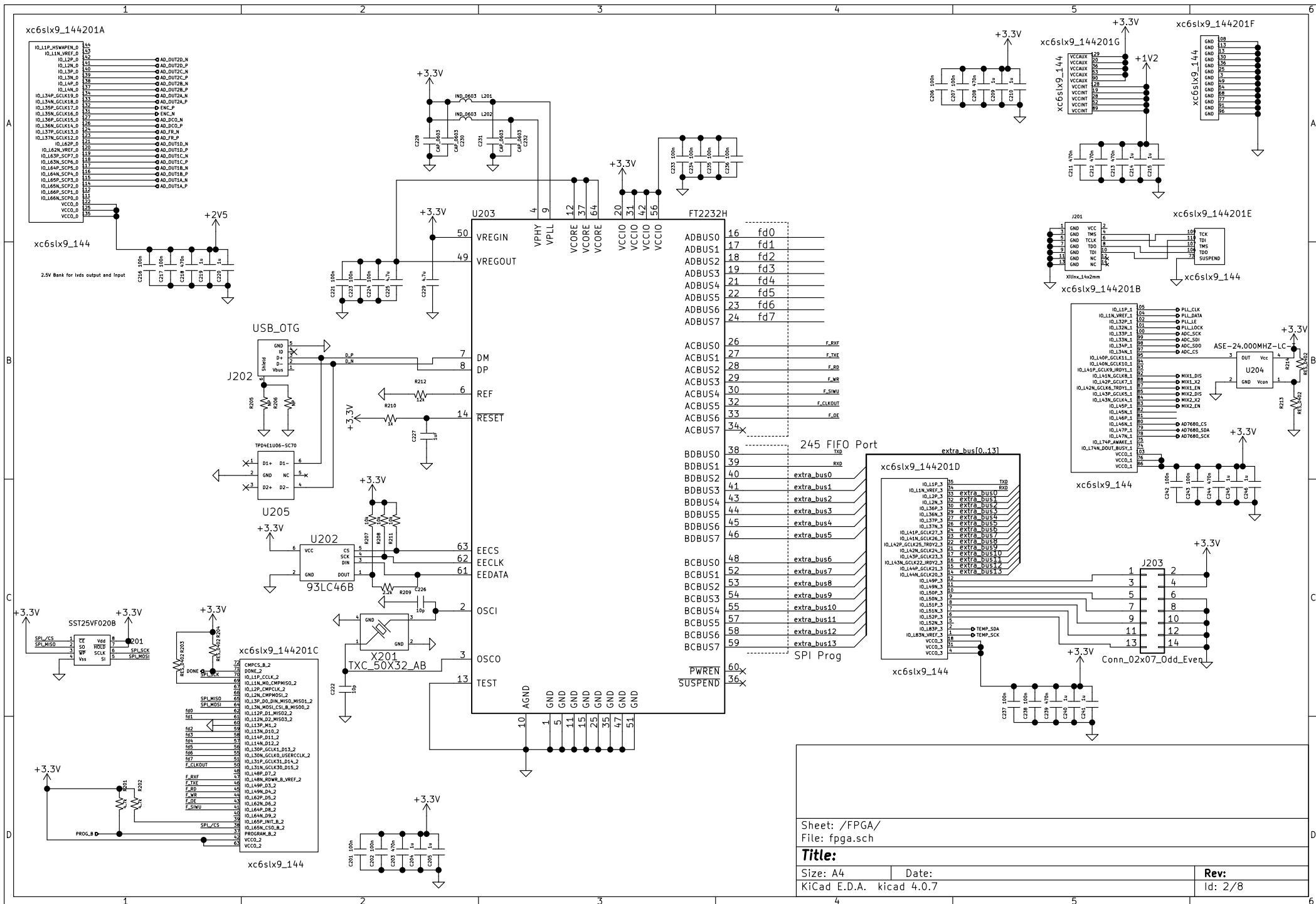
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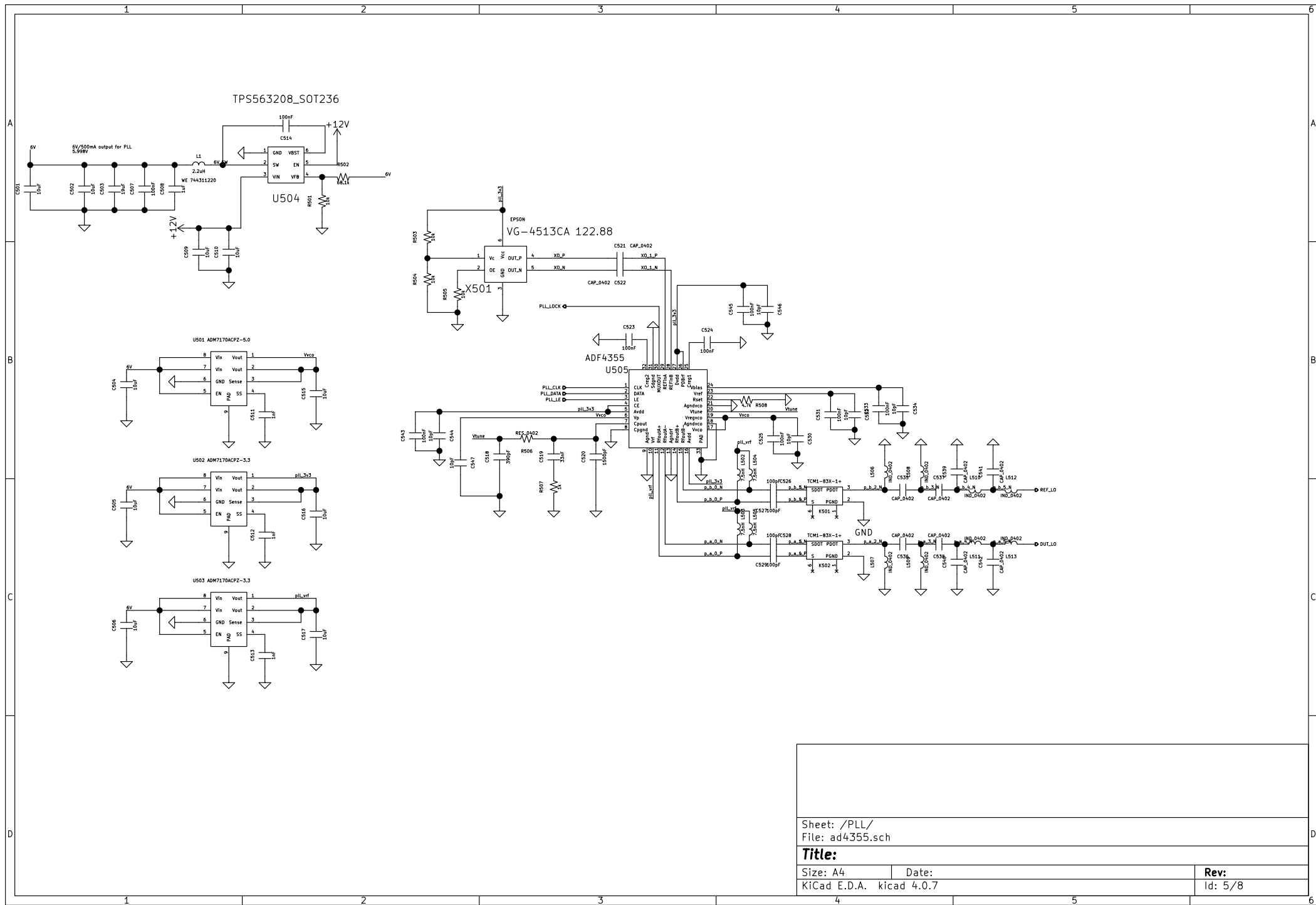
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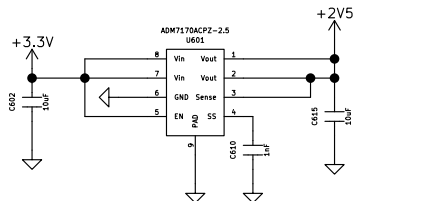
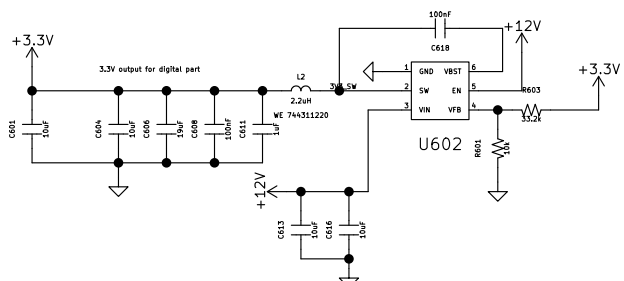
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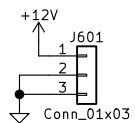
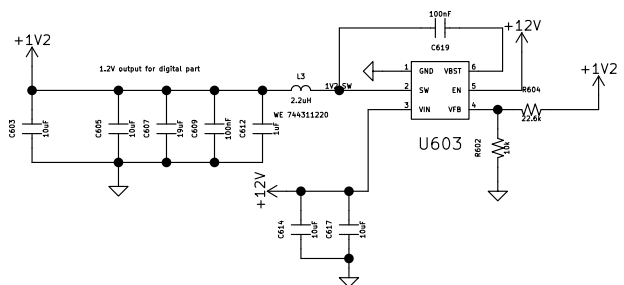


FPGA/FT232H Power rails

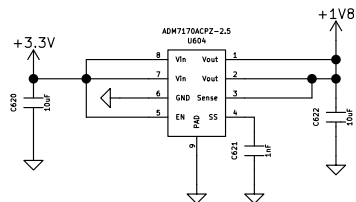
TPS563208_SOT236



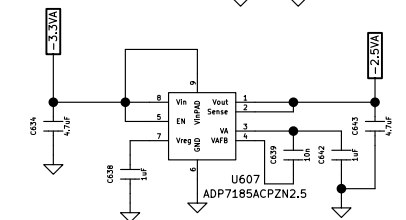
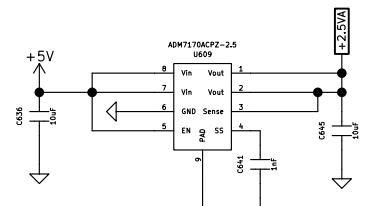
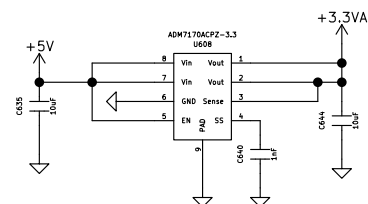
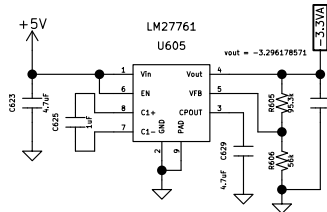
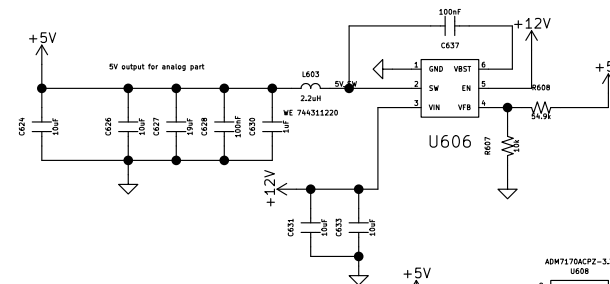
TPS563208_SOT236



ADC LDO Filtered near ADC



TPS563208_SOT236



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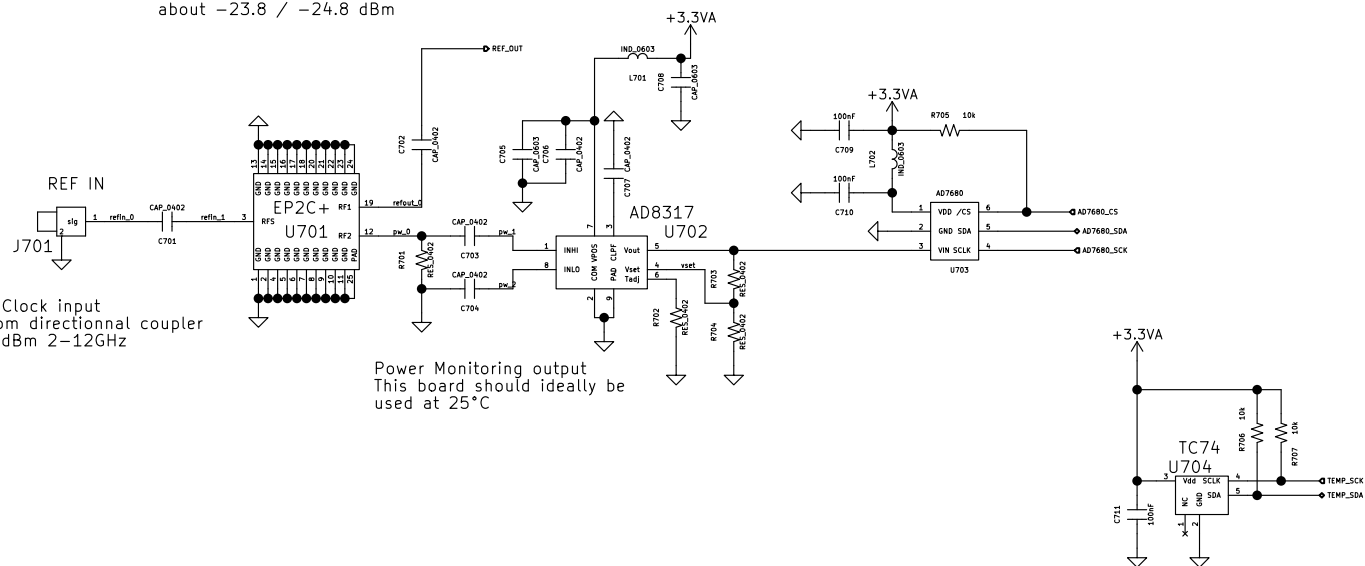
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Reference Clock Output
Feeds the first mixer and used as a reference
about -23.8 / -24.8 dBm

Reference Clock input
Coming from directionnal coupler
about -20dBm 2-12GHz

Power Monitoring output
This board should ideally be
used at 25°C



Sheet: /Power Detection & Reference Input/
File: power_det.sch

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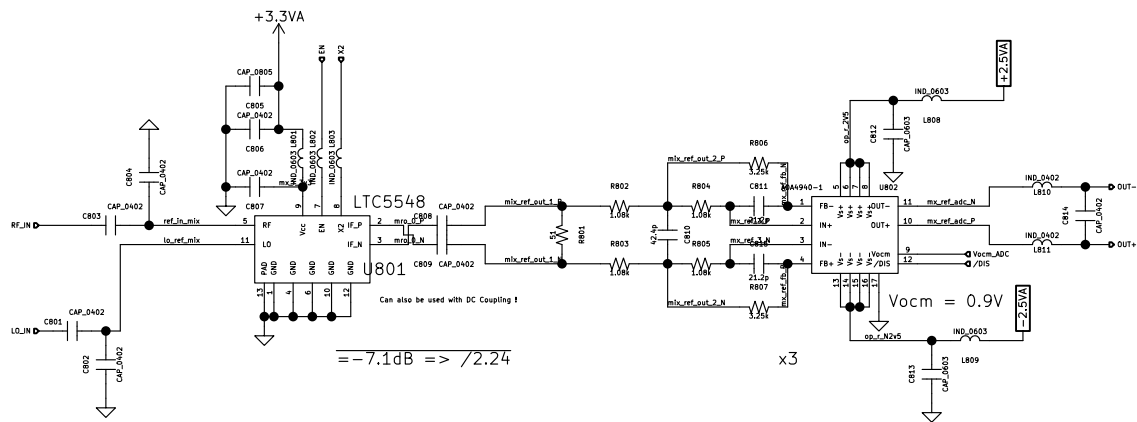
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Sheet: /Mixer REF/
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Id: 8/8