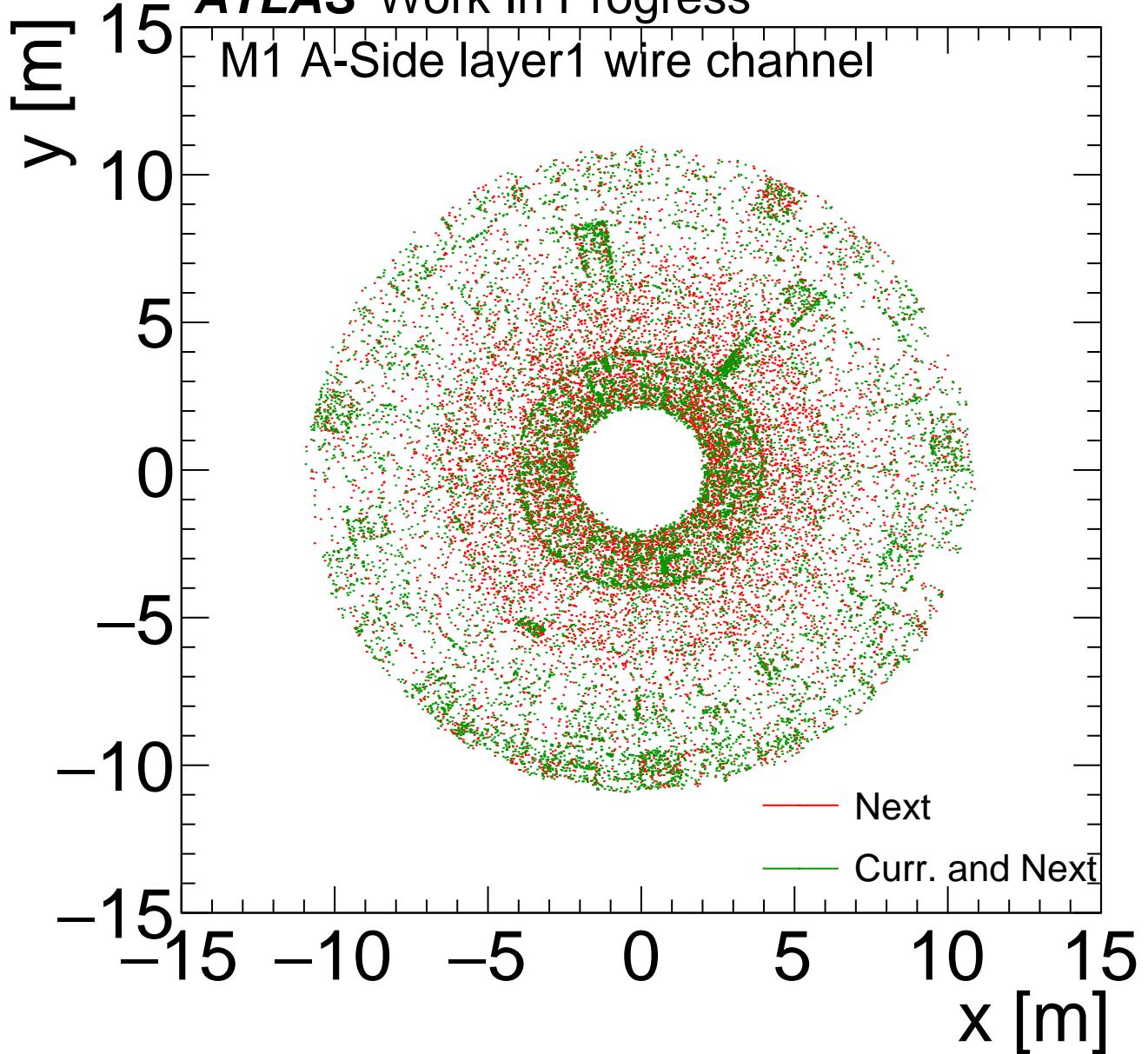


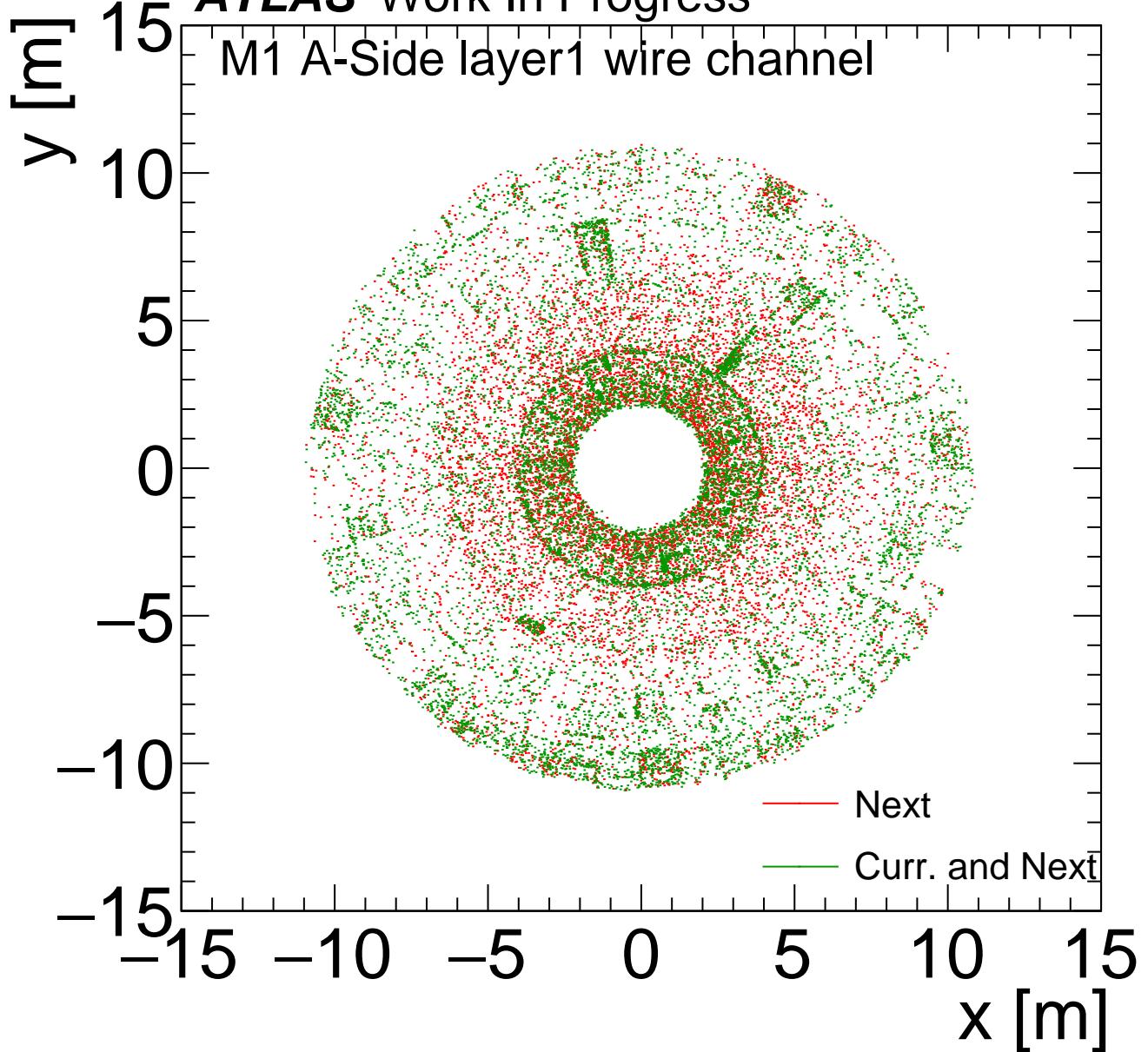
**ATLAS** Work In Progress

M1 A-Side layer1 wire channel



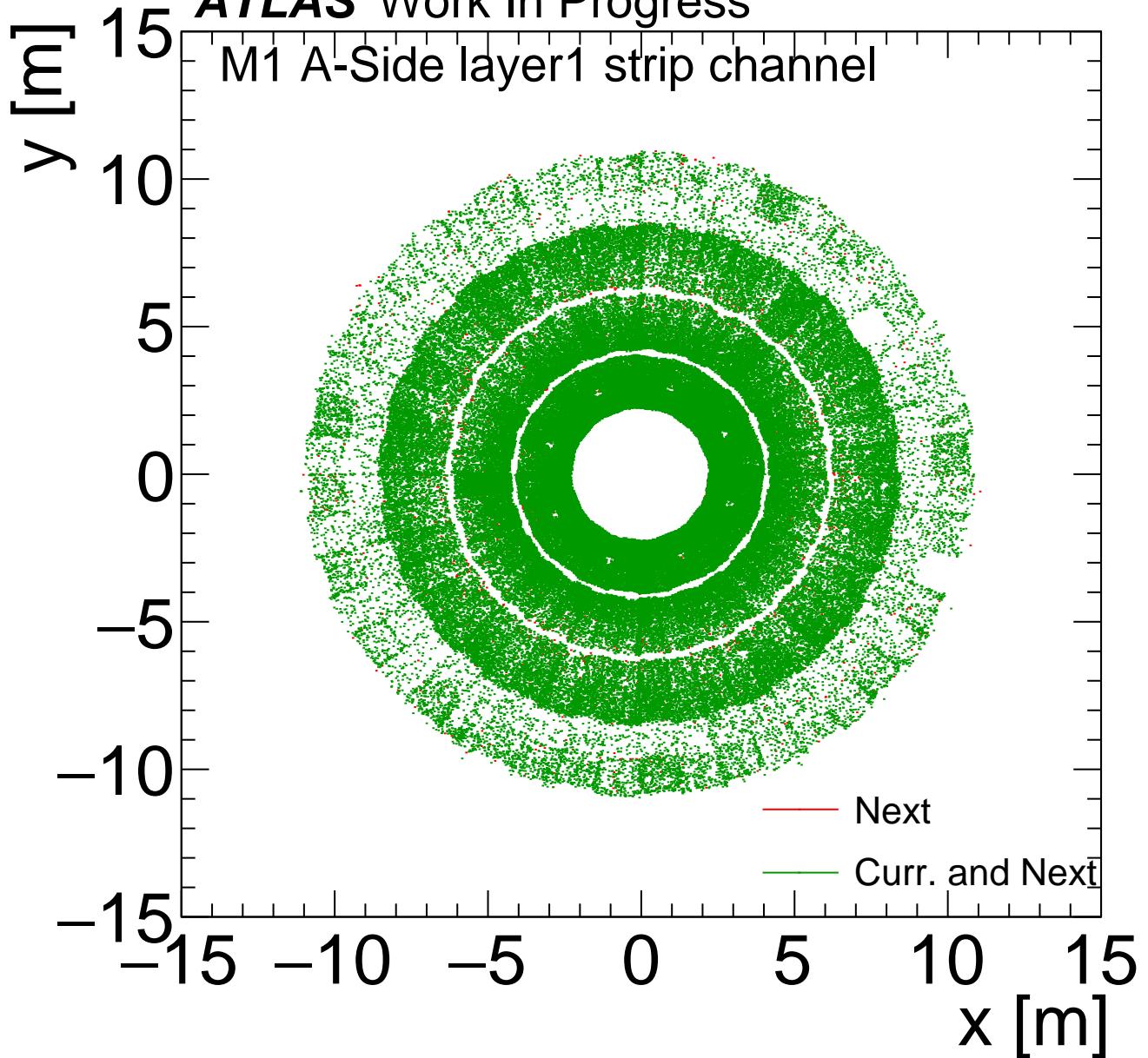
**ATLAS** Work In Progress

M1 A-Side layer1 wire channel



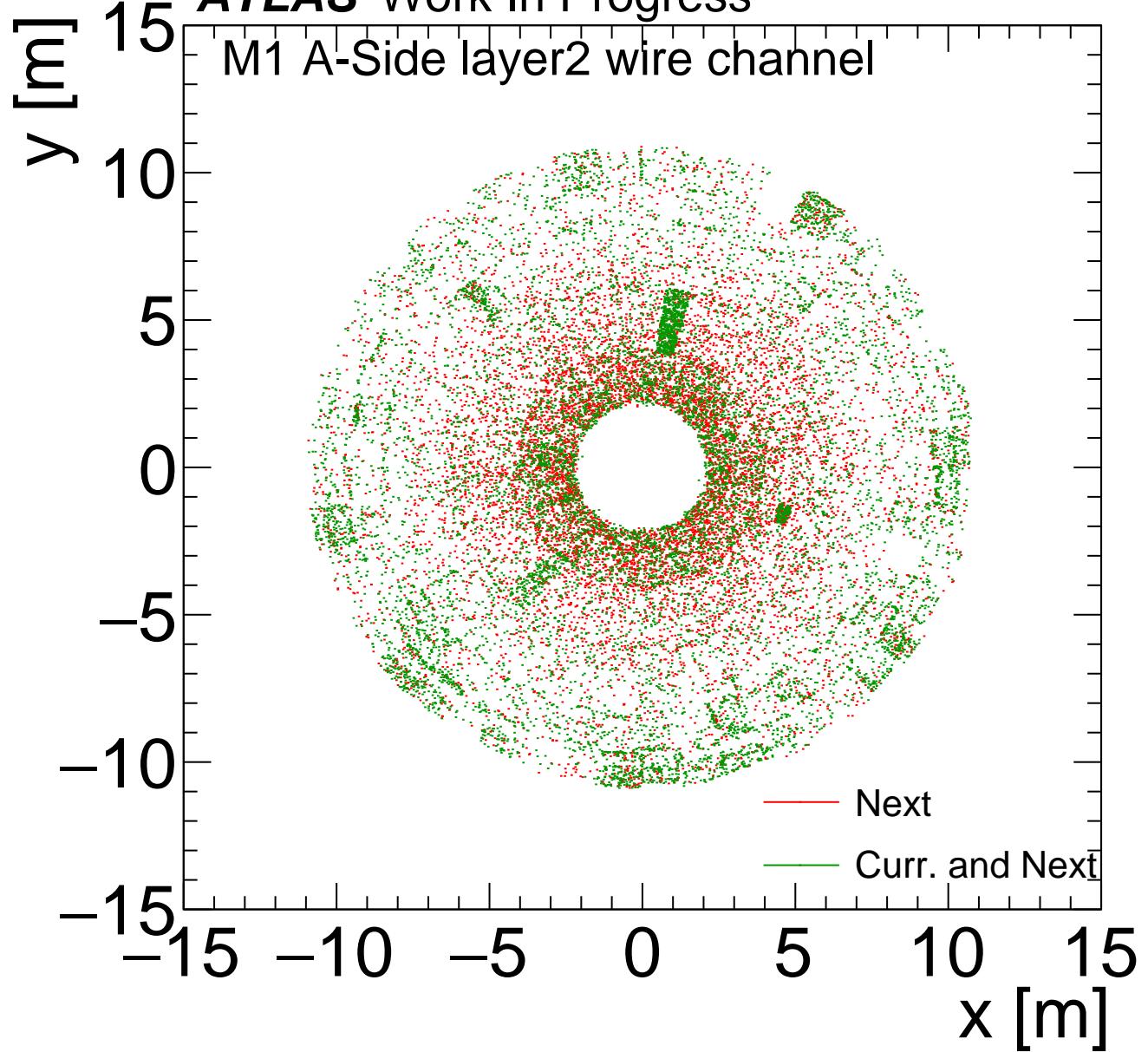
**ATLAS** Work In Progress

M1 A-Side layer1 strip channel



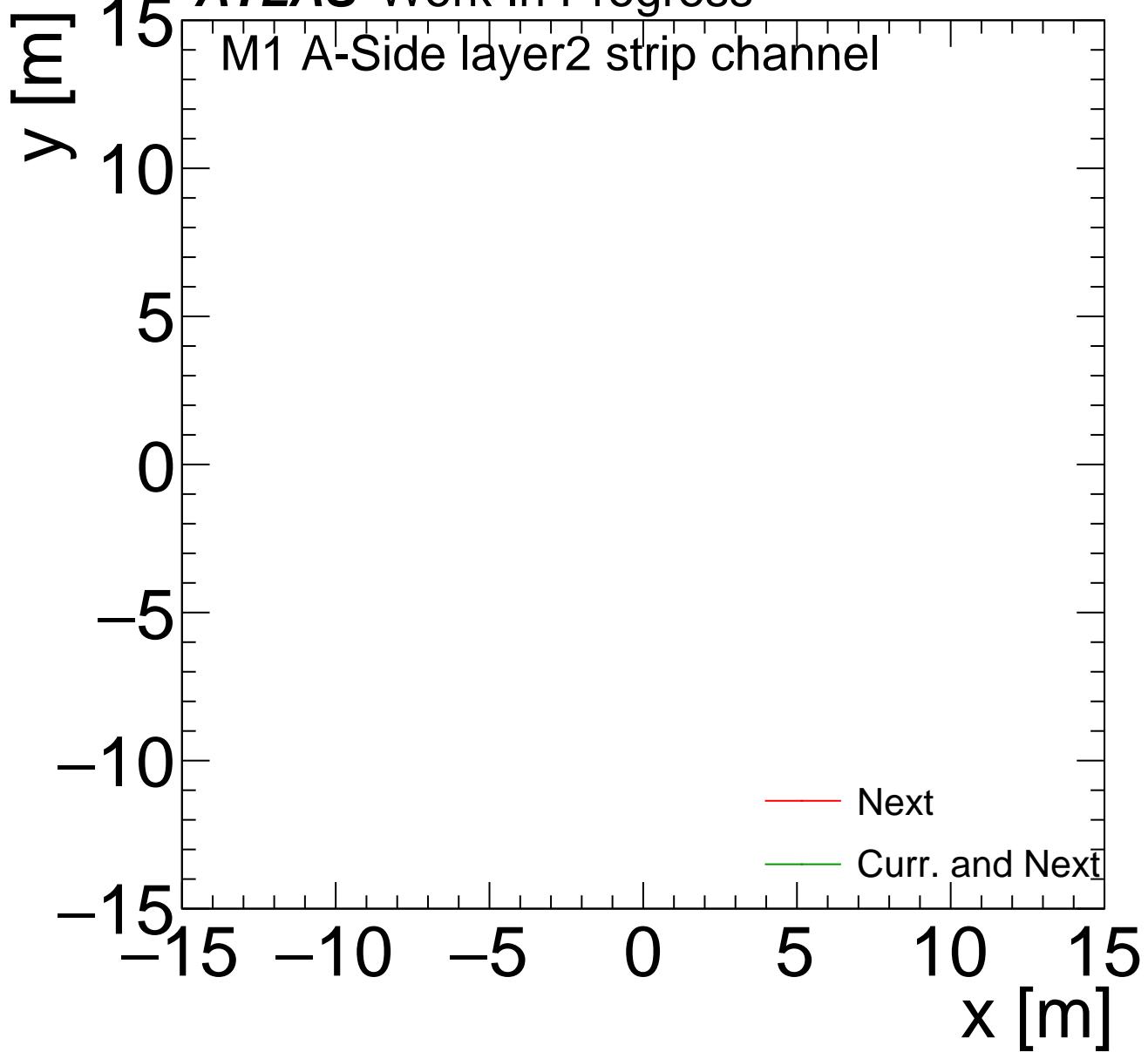
**ATLAS** Work In Progress

M1 A-Side layer2 wire channel



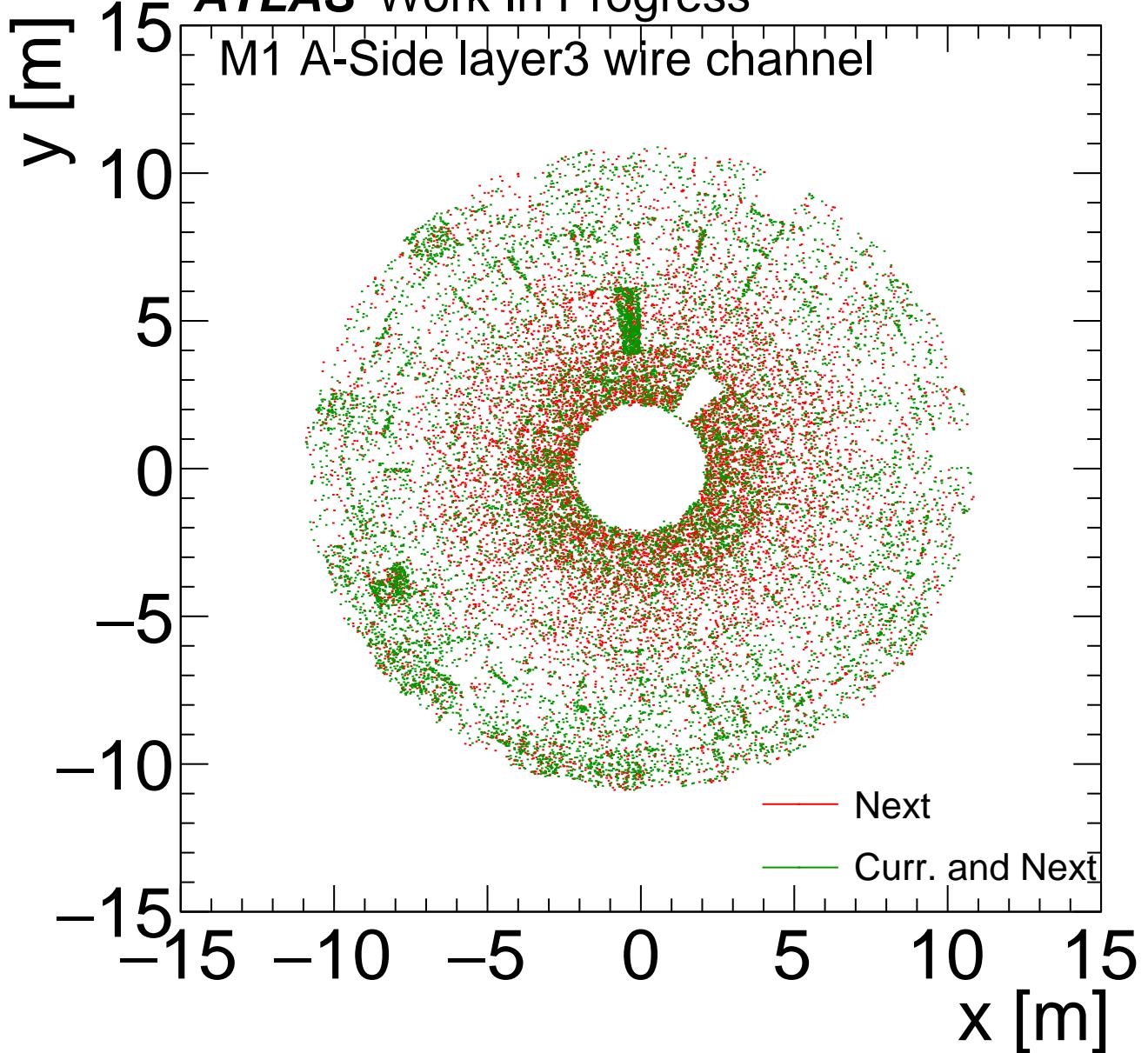
**ATLAS** Work In Progress

M1 A-Side layer2 strip channel



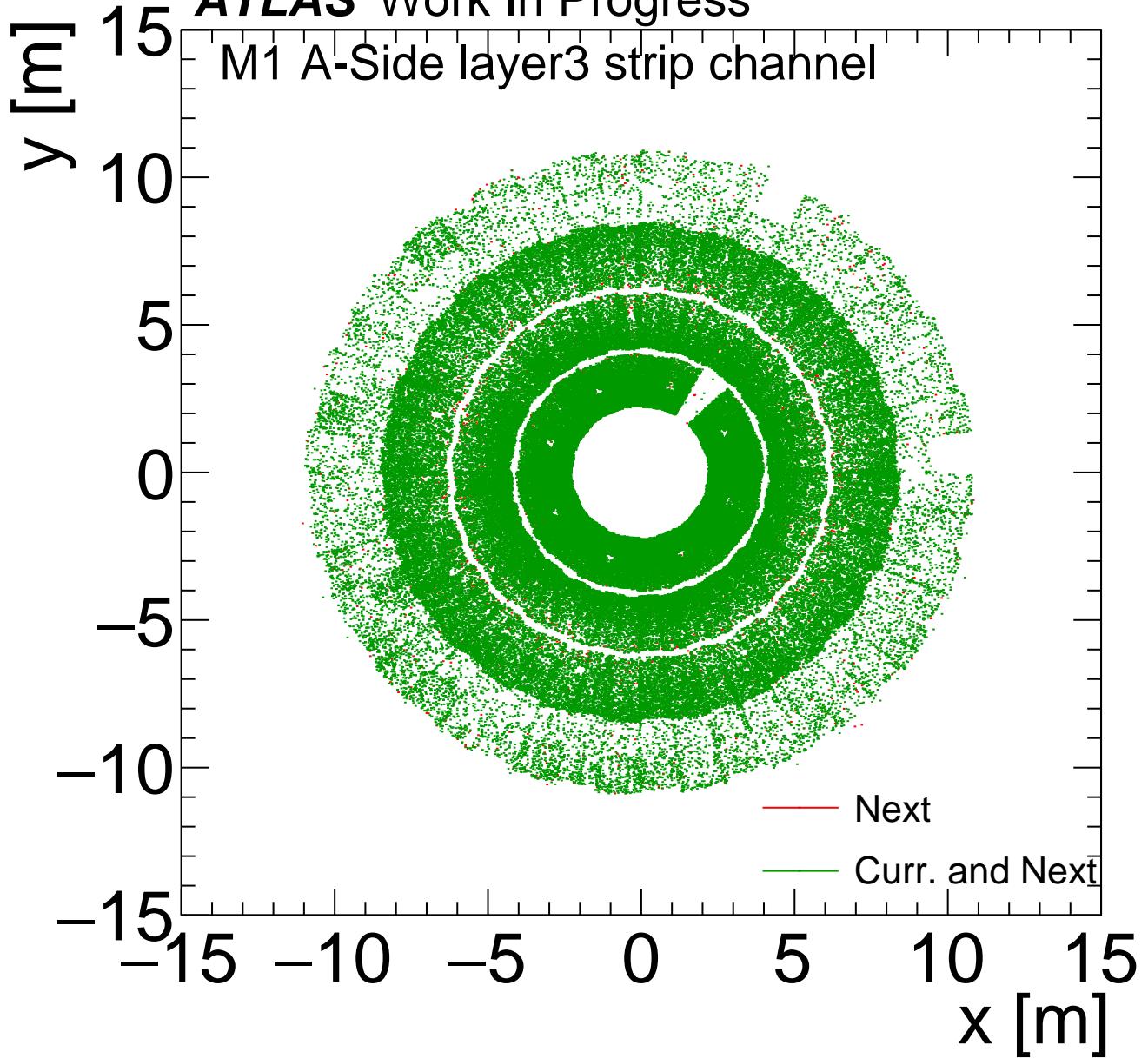
**ATLAS** Work In Progress

M1 A-Side layer3 wire channel



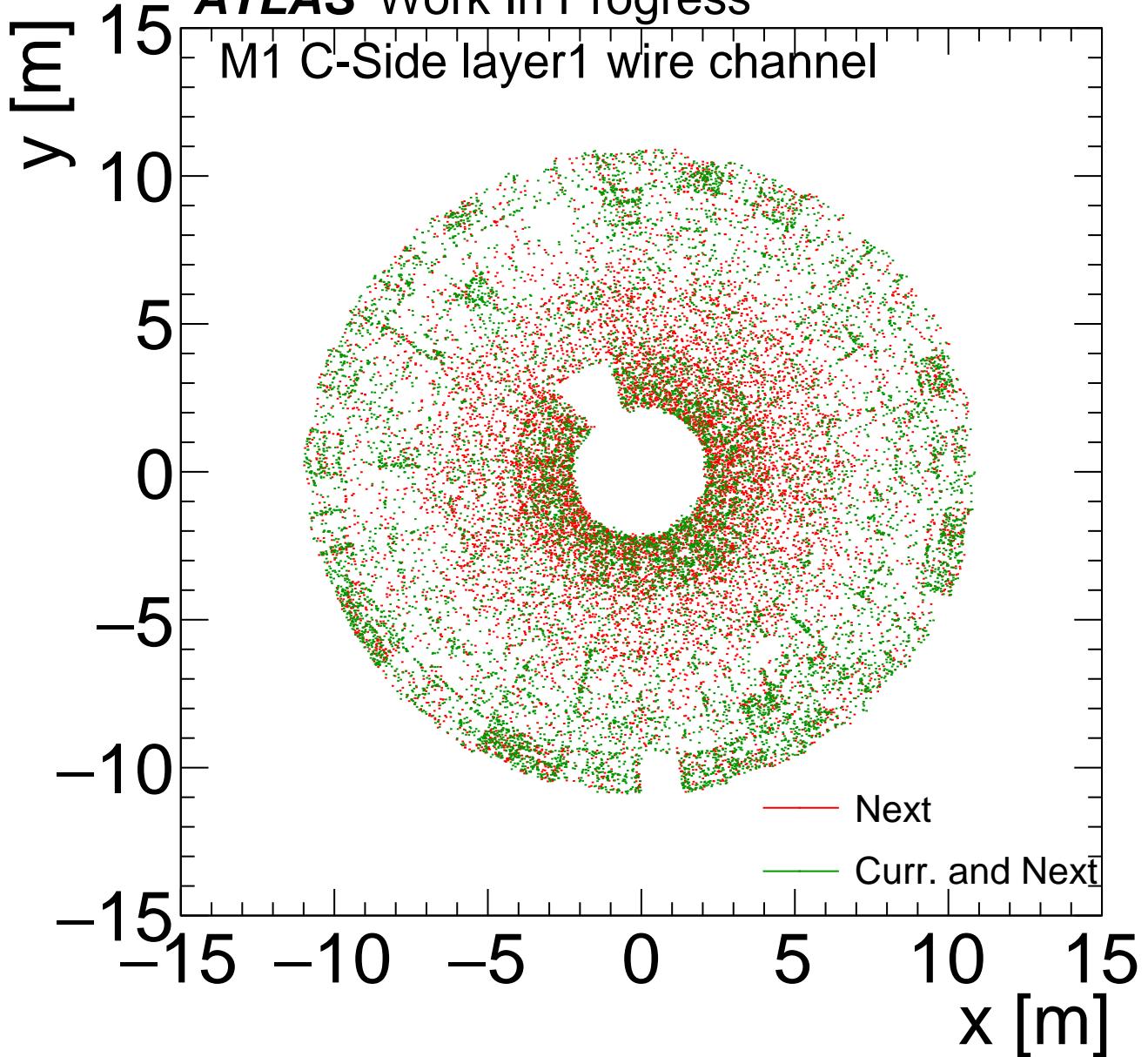
**ATLAS** Work In Progress

M1 A-Side layer3 strip channel



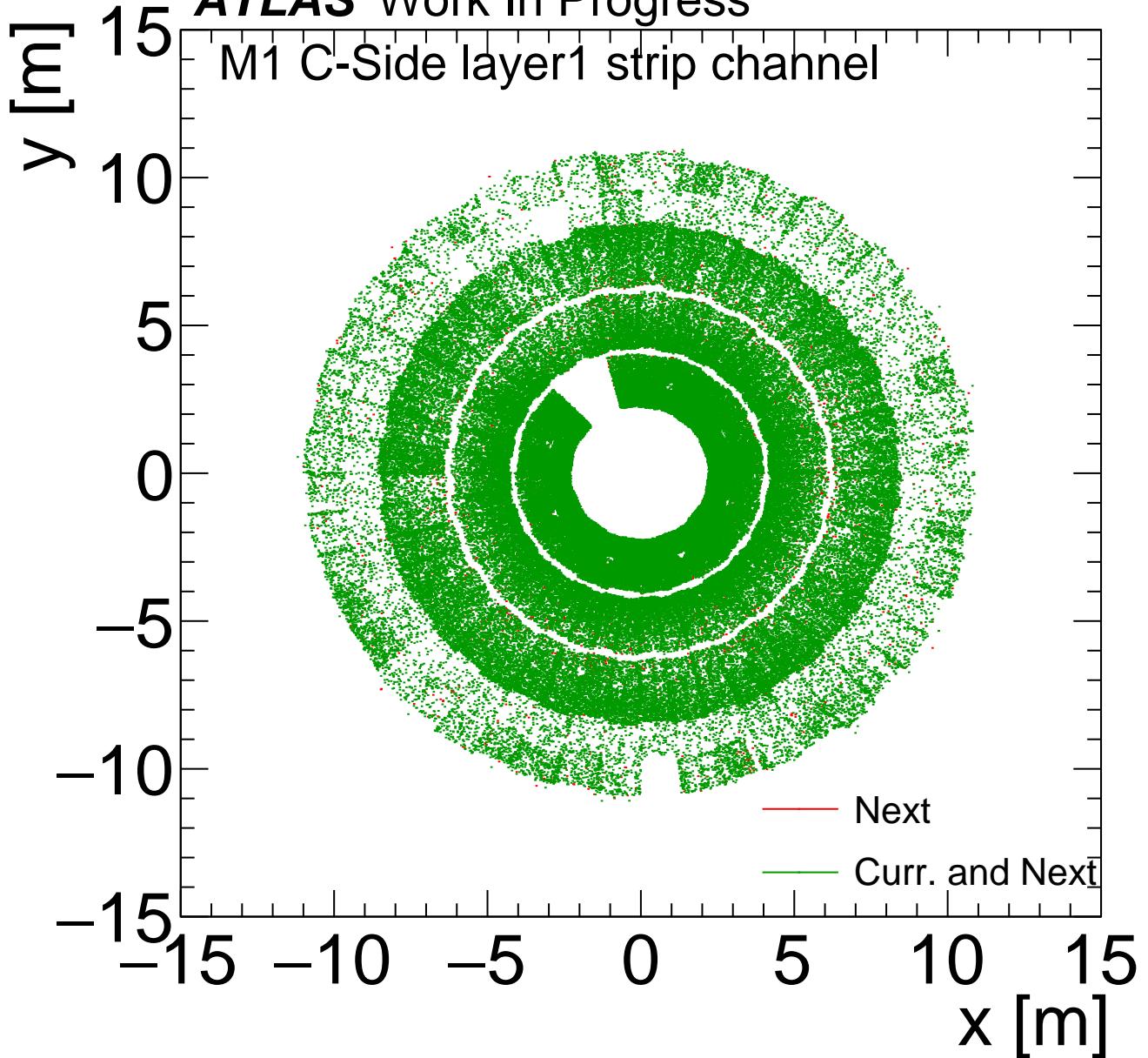
**ATLAS** Work In Progress

M1 C-Side layer1 wire channel



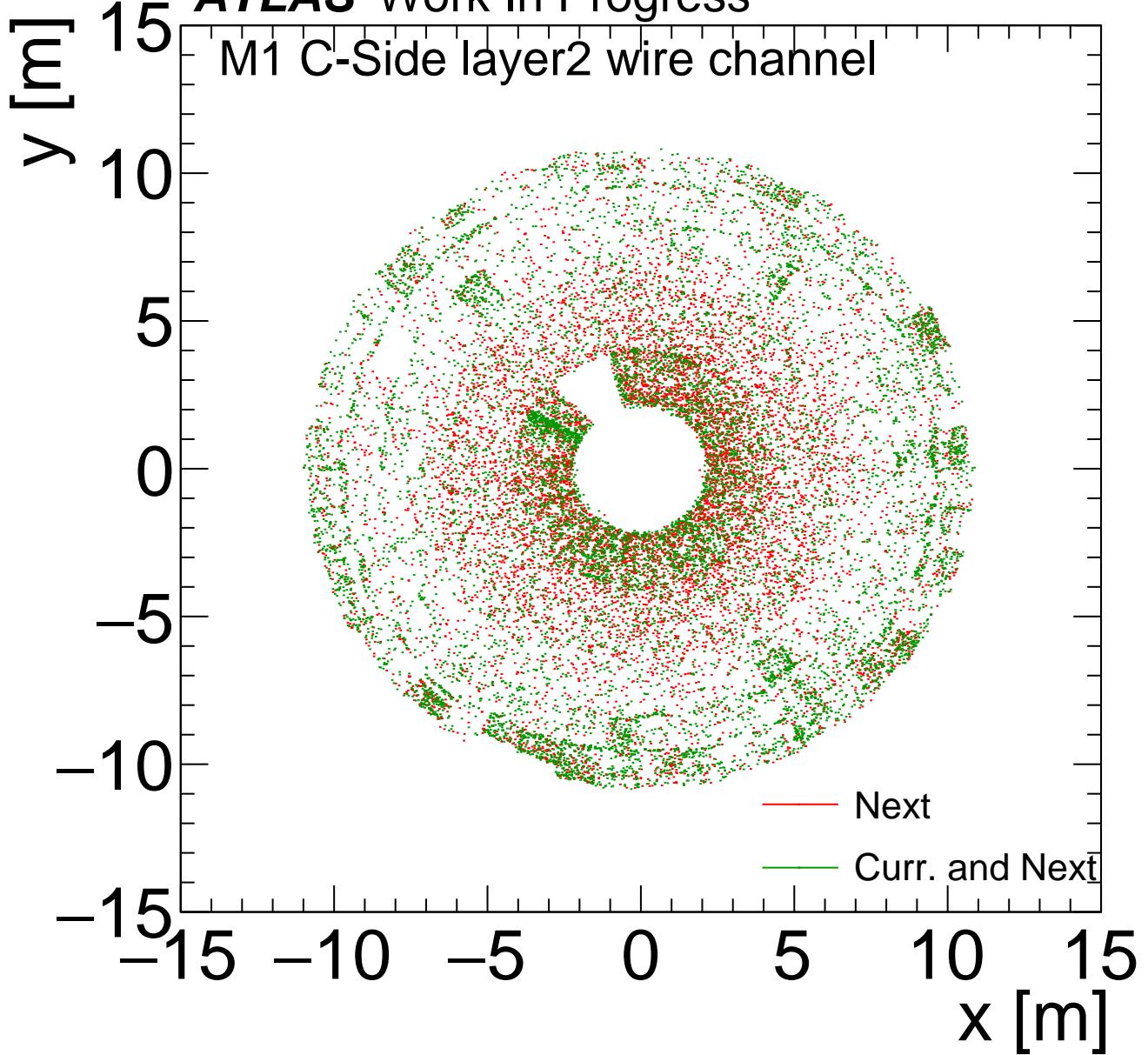
**ATLAS** Work In Progress

M1 C-Side layer1 strip channel



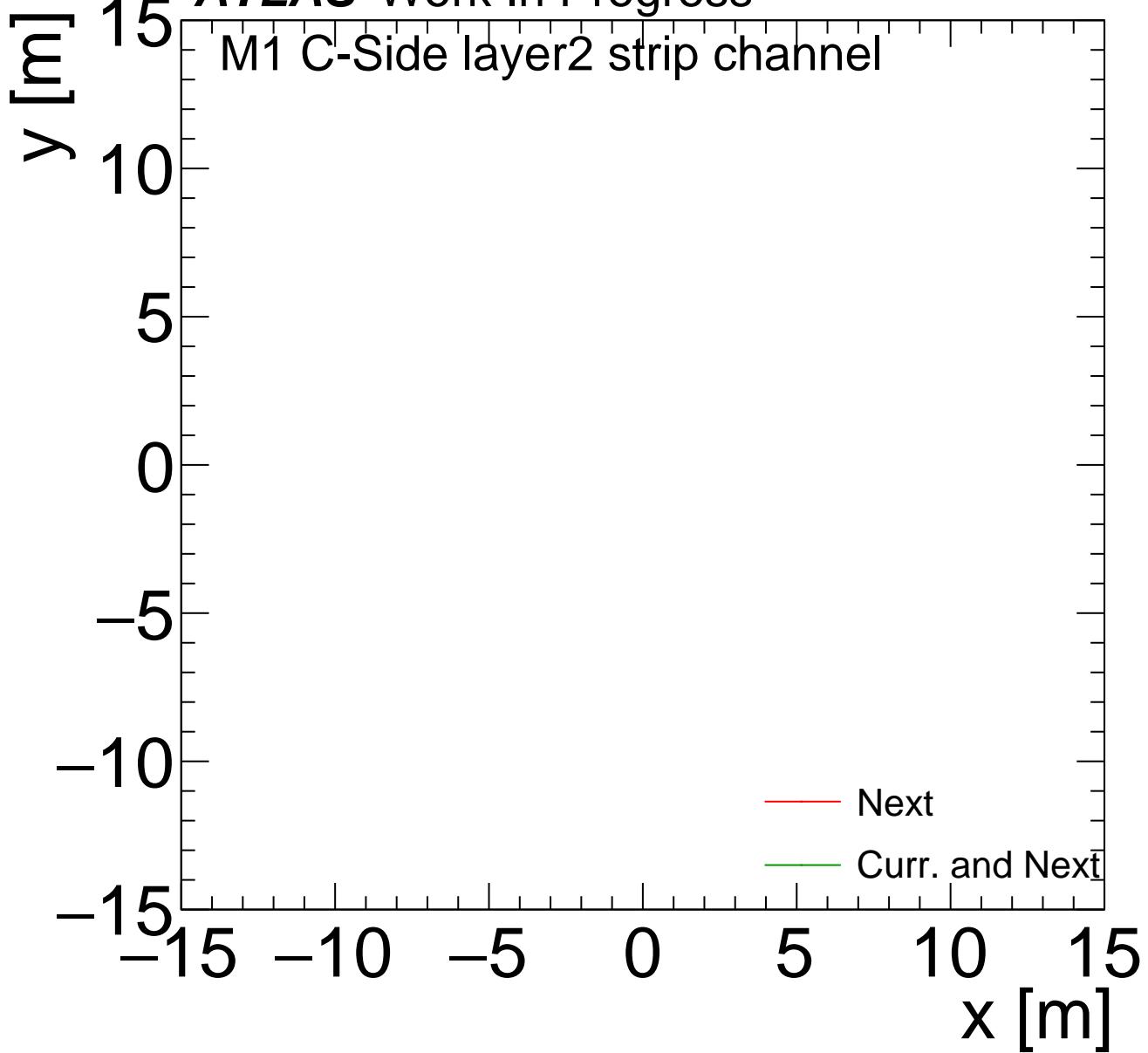
**ATLAS** Work In Progress

M1 C-Side layer2 wire channel



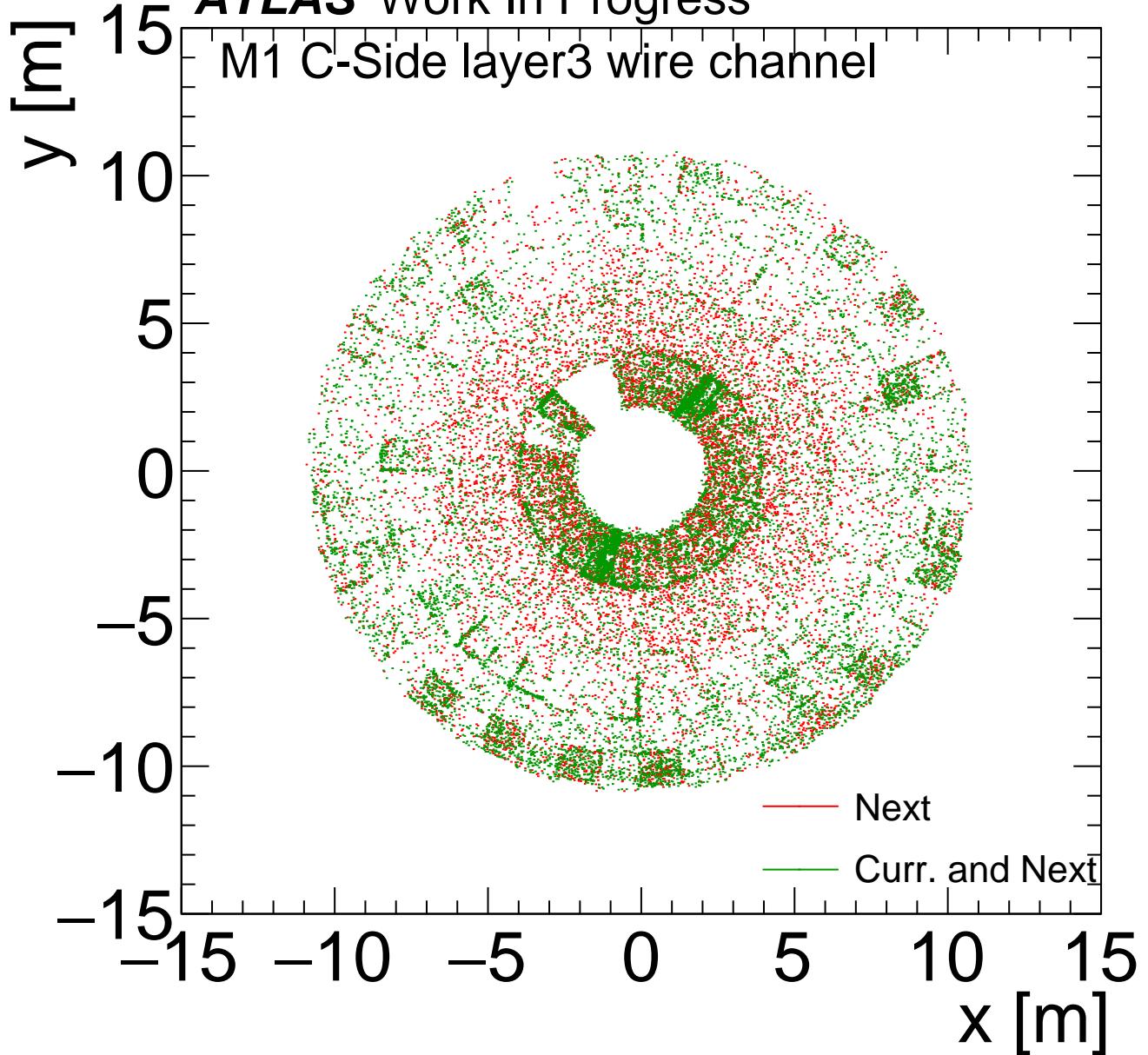
**ATLAS** Work In Progress

M1 C-Side layer2 strip channel



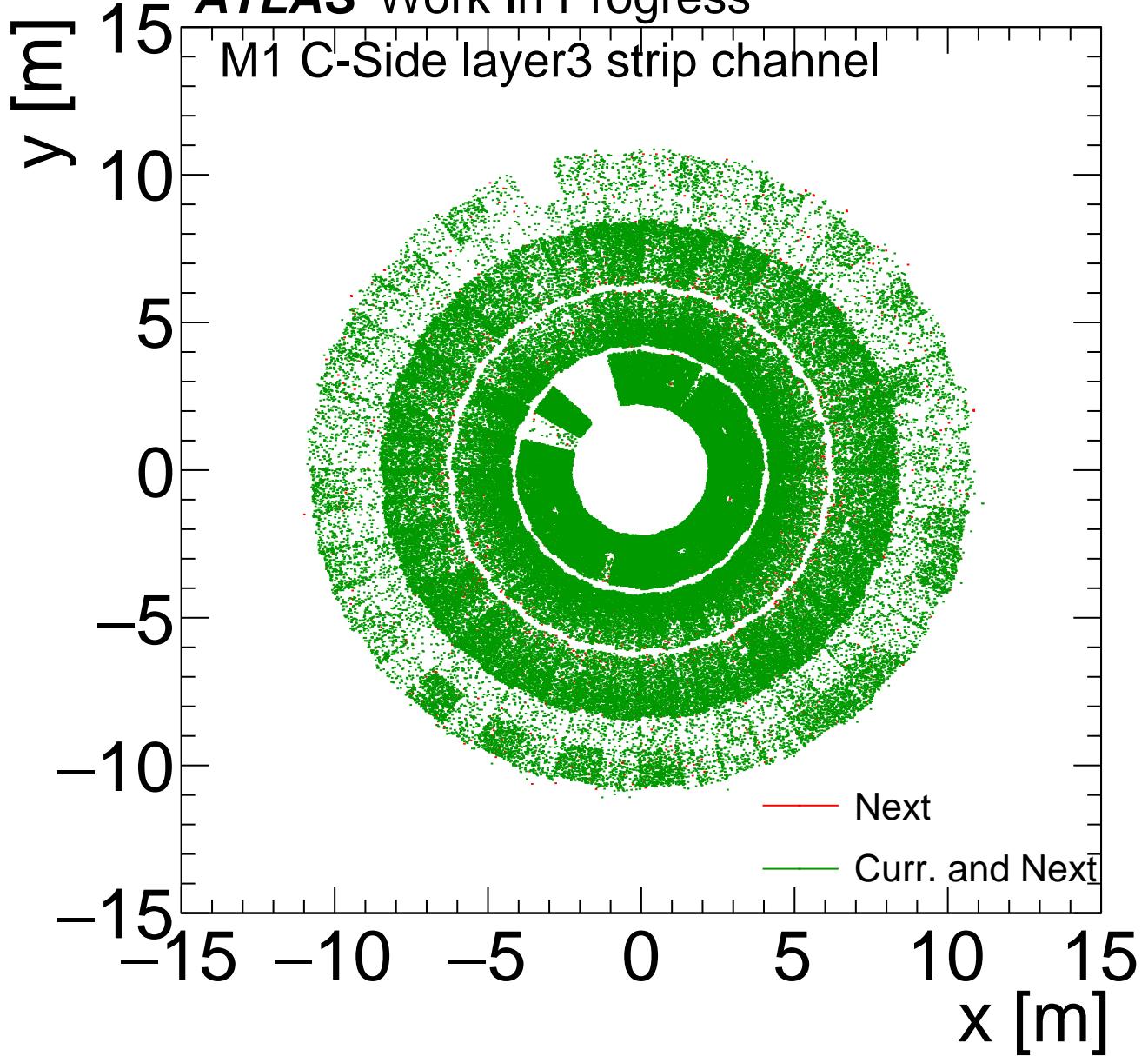
**ATLAS** Work In Progress

M1 C-Side layer3 wire channel



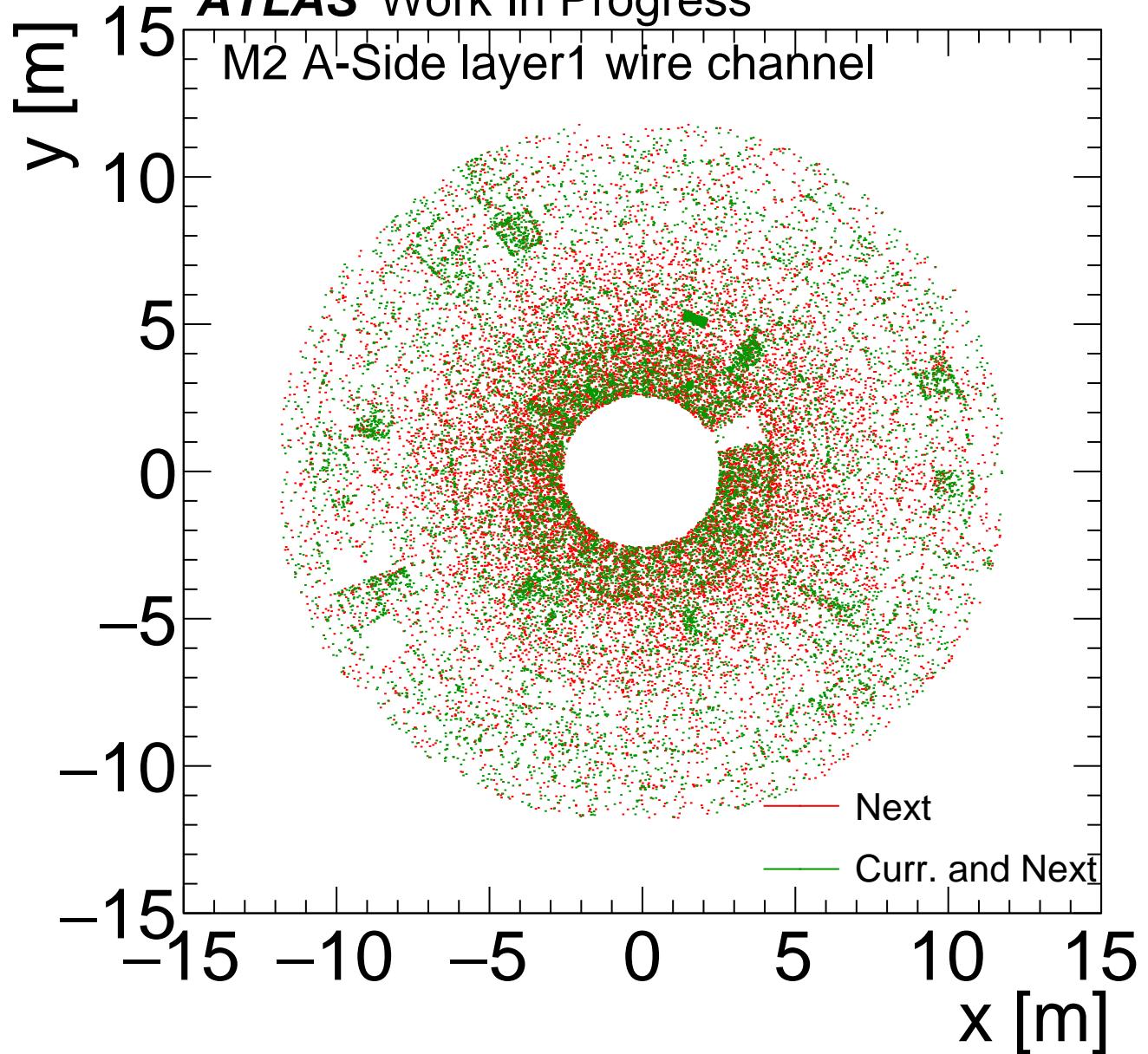
**ATLAS** Work In Progress

M1 C-Side layer3 strip channel



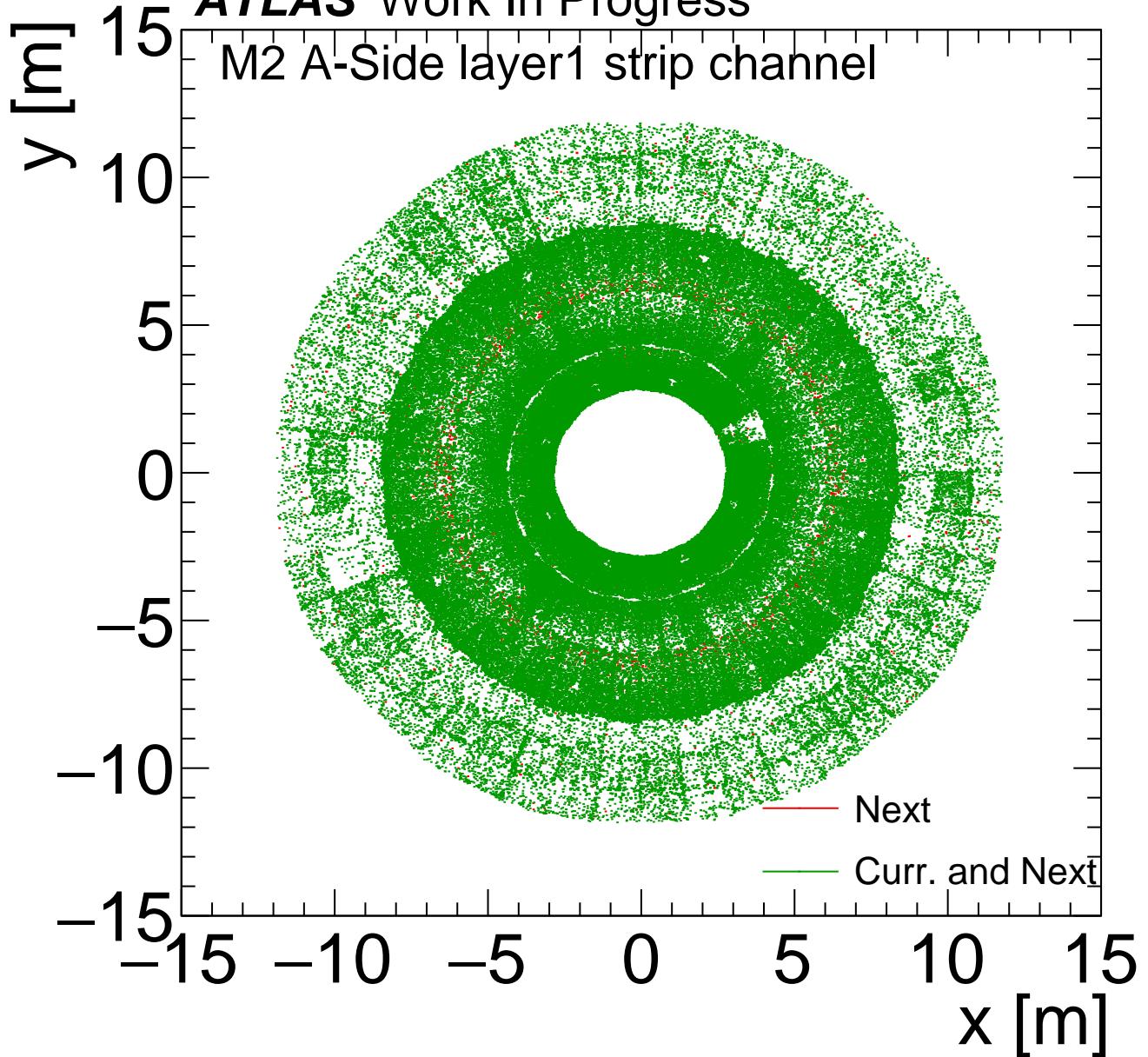
**ATLAS** Work In Progress

M2 A-Side layer1 wire channel



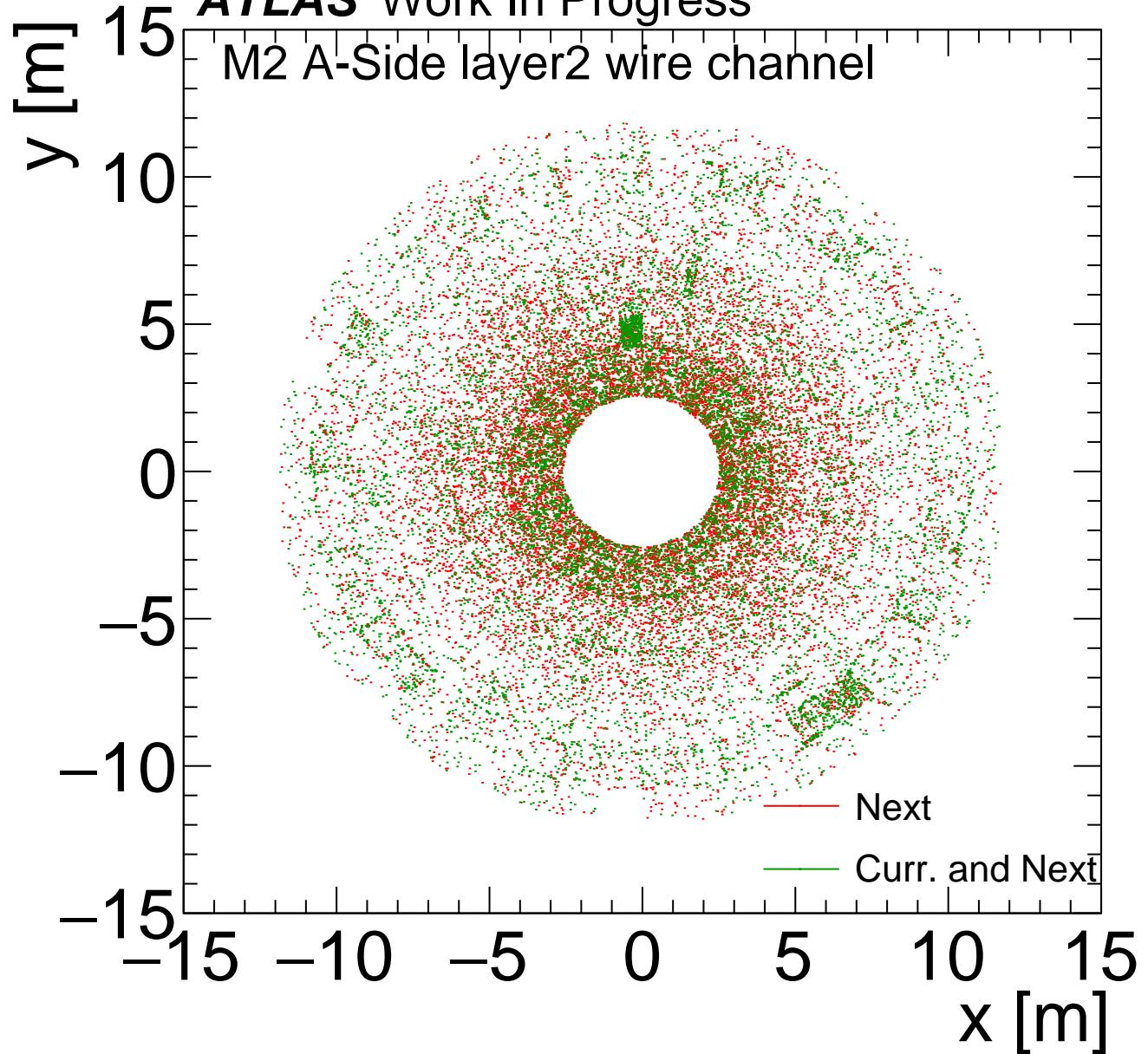
**ATLAS** Work In Progress

M2 A-Side layer1 strip channel



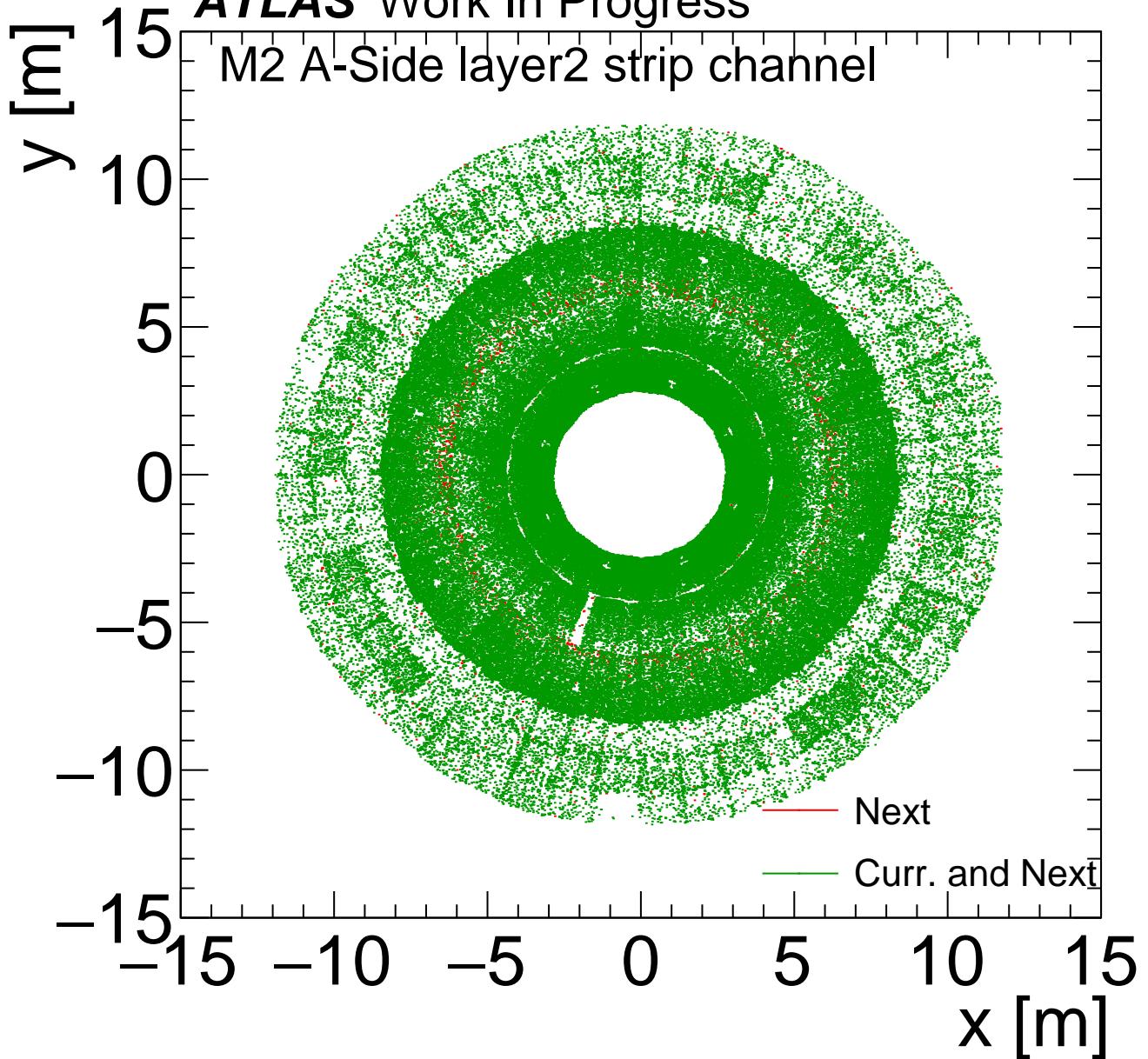
**ATLAS** Work In Progress

M2 A-Side layer2 wire channel



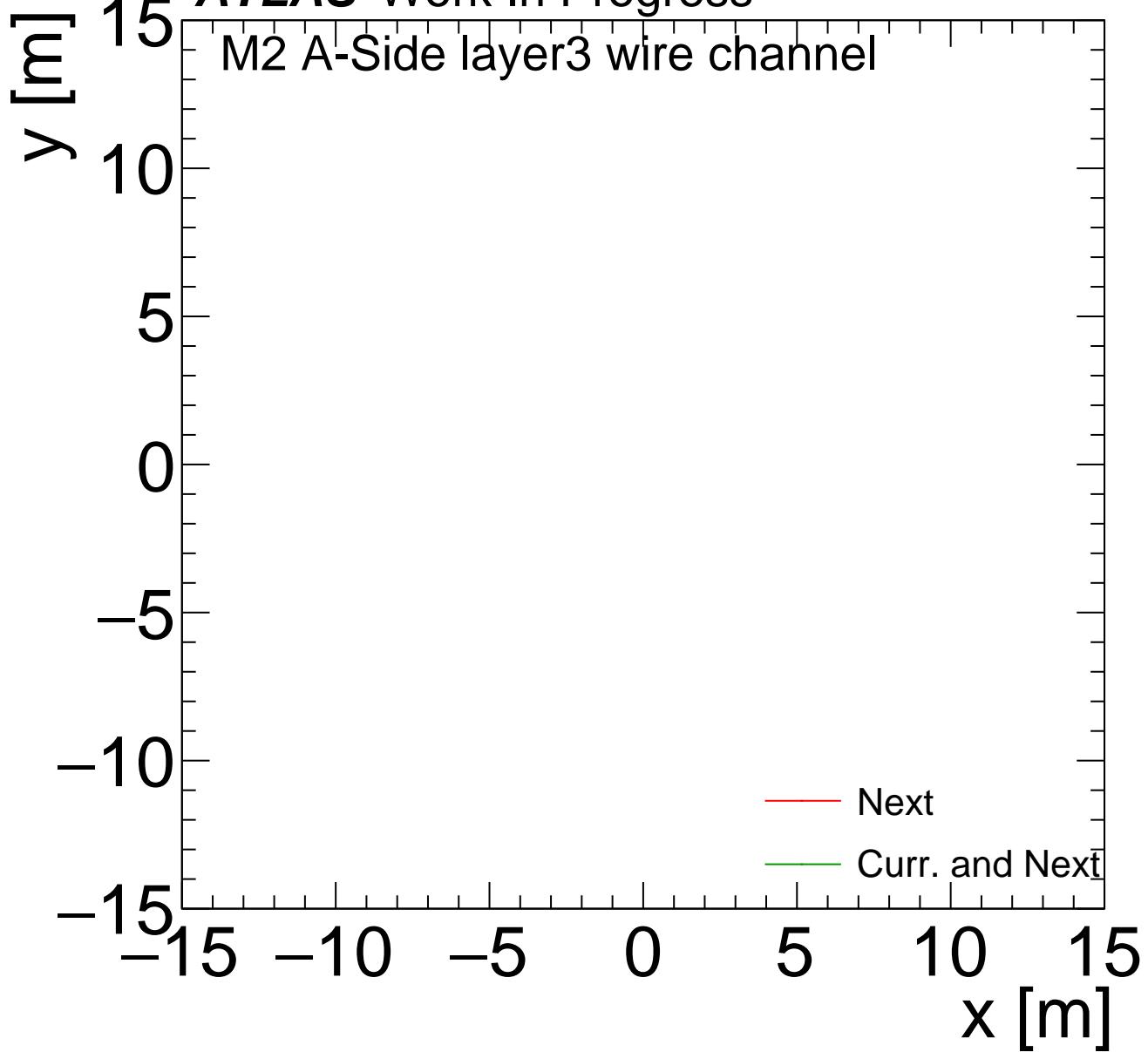
**ATLAS** Work In Progress

M2 A-Side layer2 strip channel



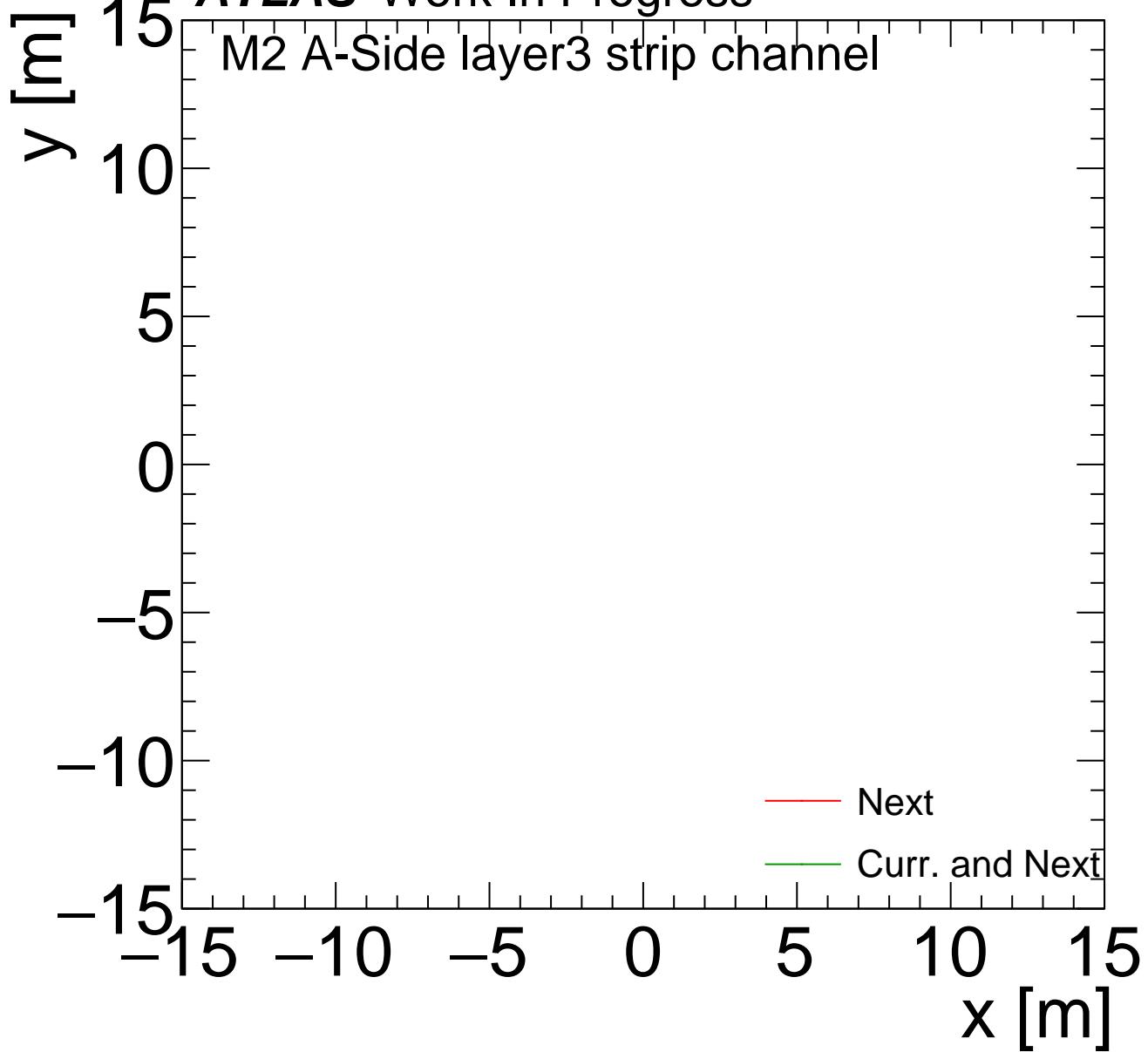
**ATLAS** Work In Progress

M2 A-Side layer3 wire channel



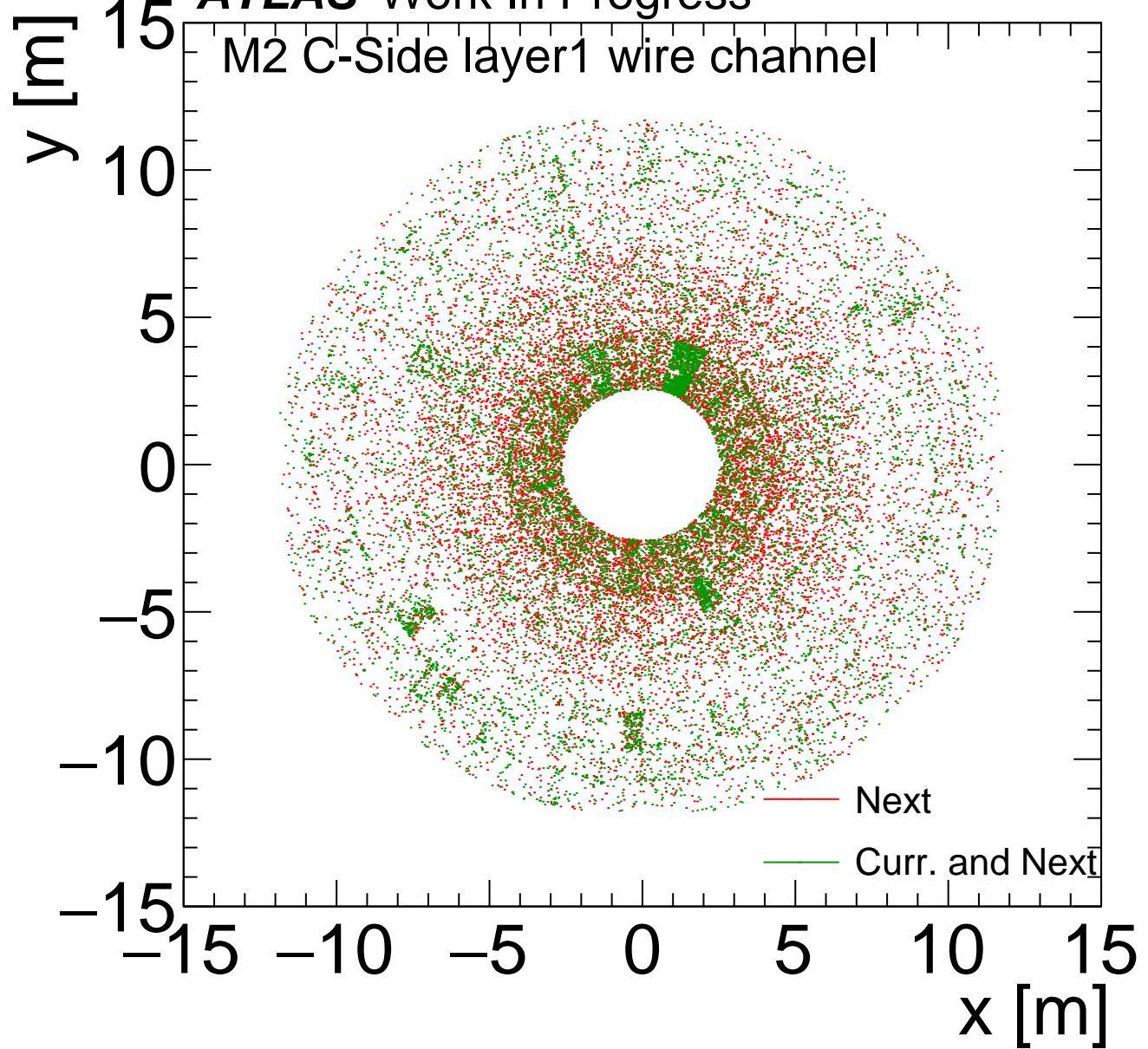
**ATLAS** Work In Progress

M2 A-Side layer3 strip channel



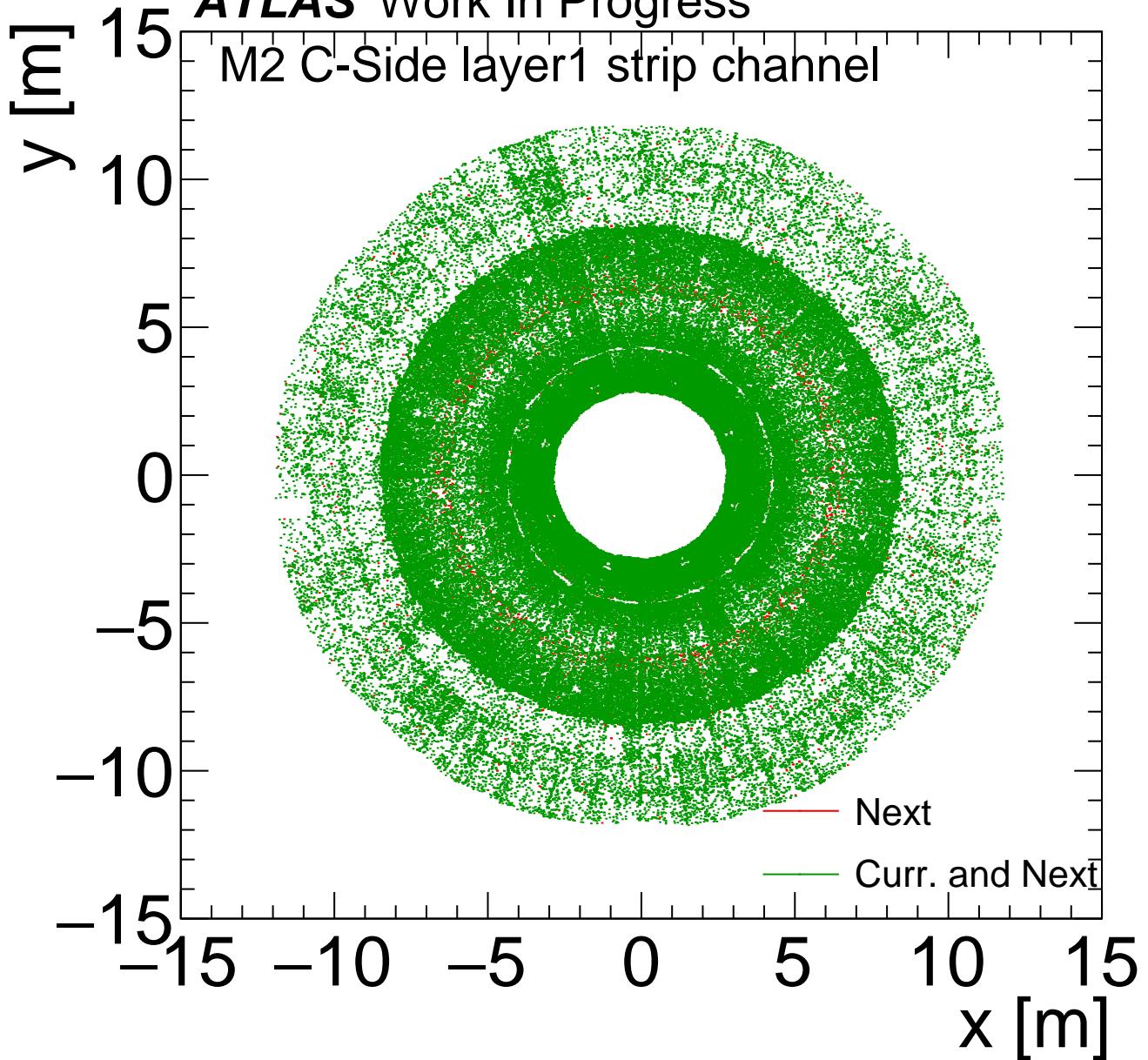
**ATLAS** Work In Progress

M2 C-Side layer1 wire channel



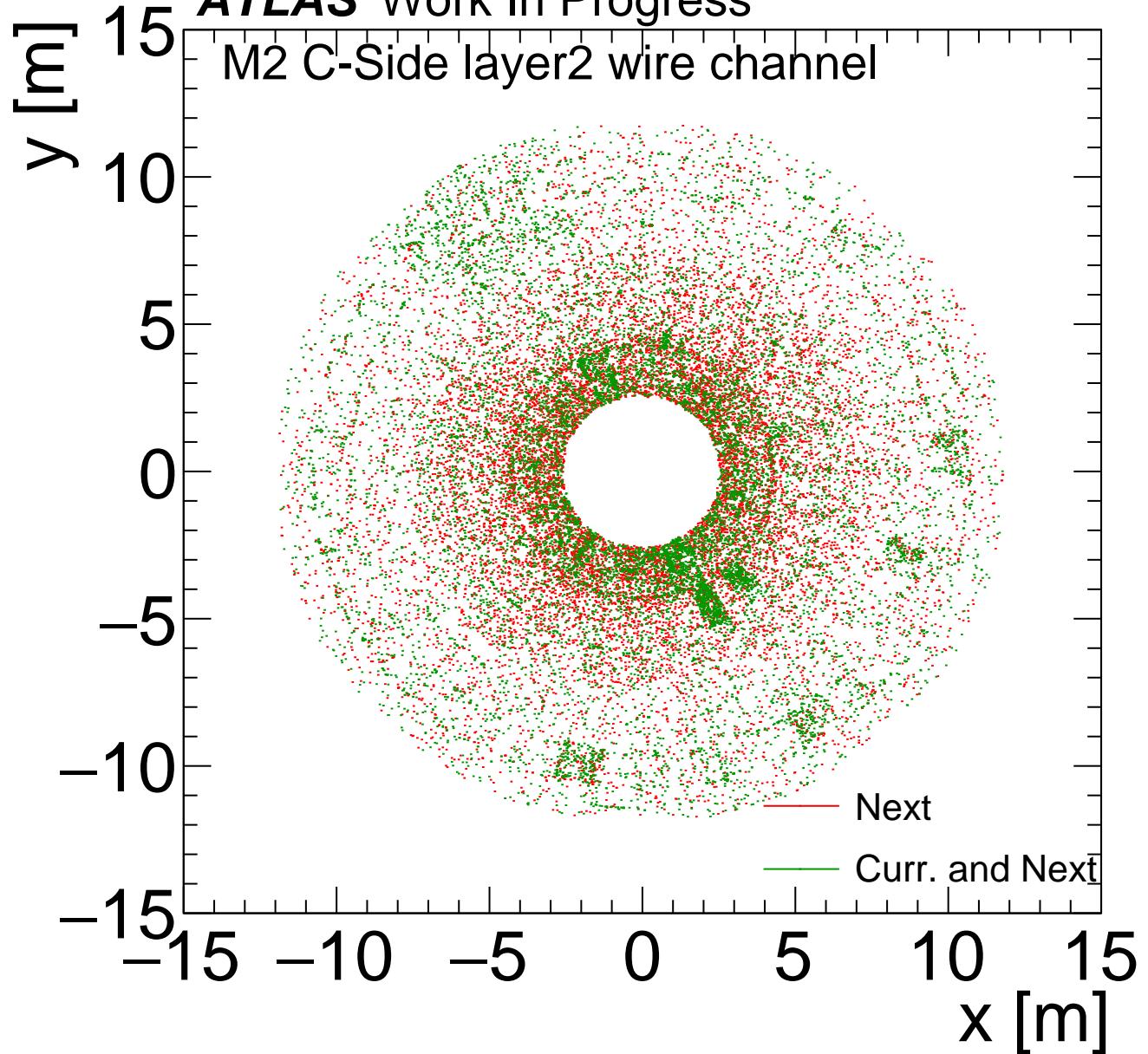
**ATLAS** Work In Progress

M2 C-Side layer1 strip channel



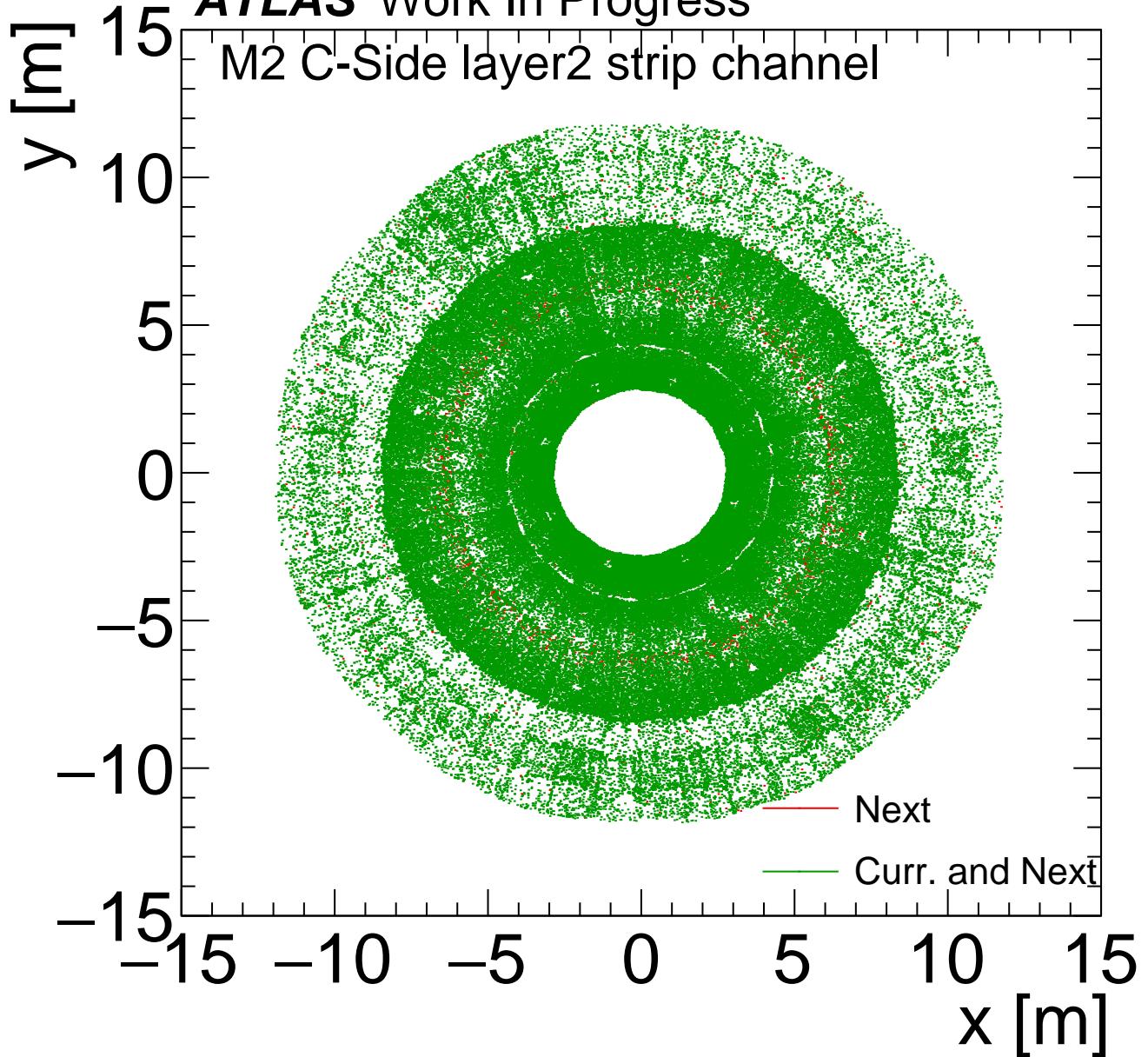
**ATLAS** Work In Progress

M2 C-Side layer2 wire channel



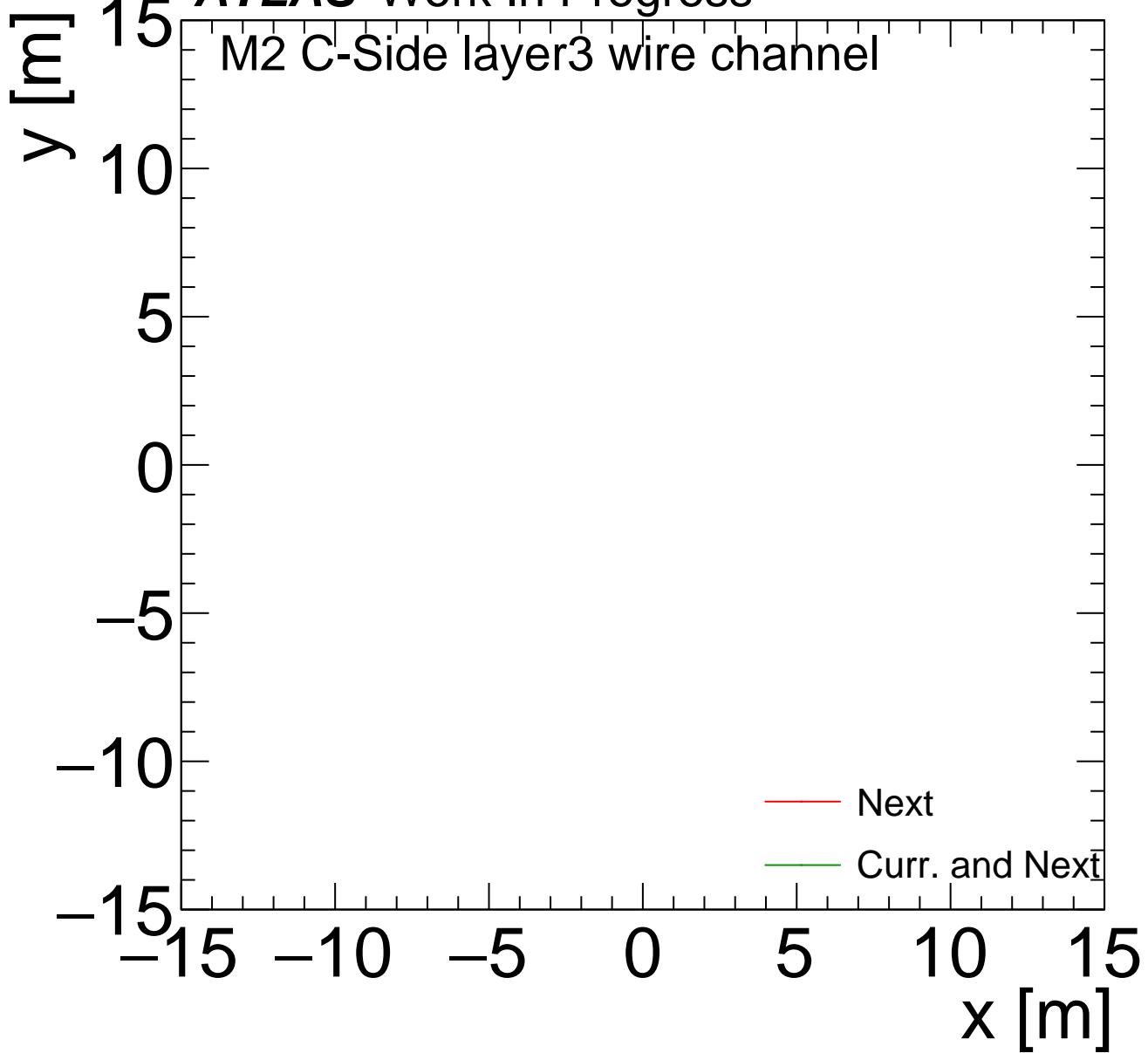
**ATLAS** Work In Progress

M2 C-Side layer2 strip channel



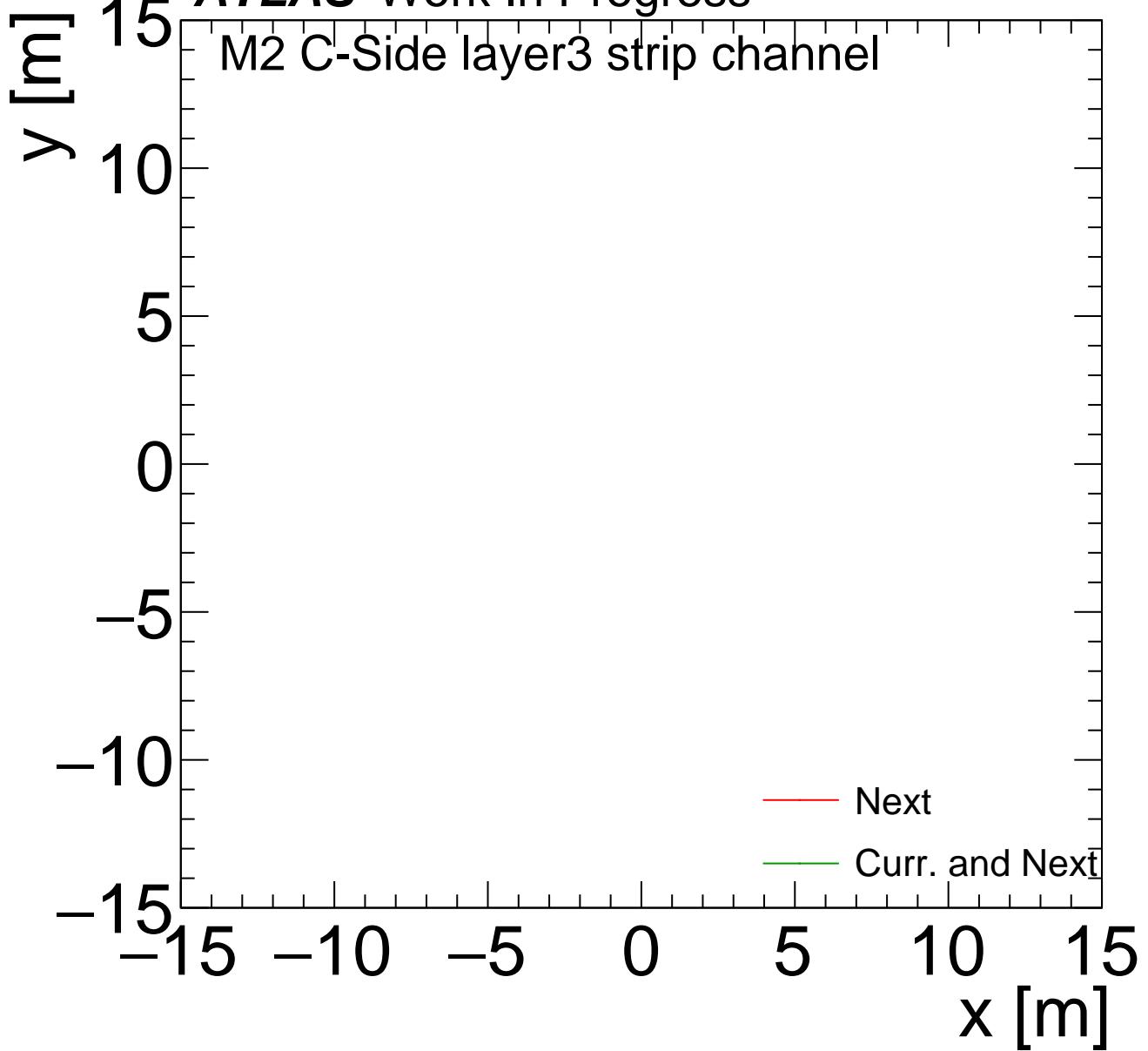
**ATLAS** Work In Progress

M2 C-Side layer3 wire channel



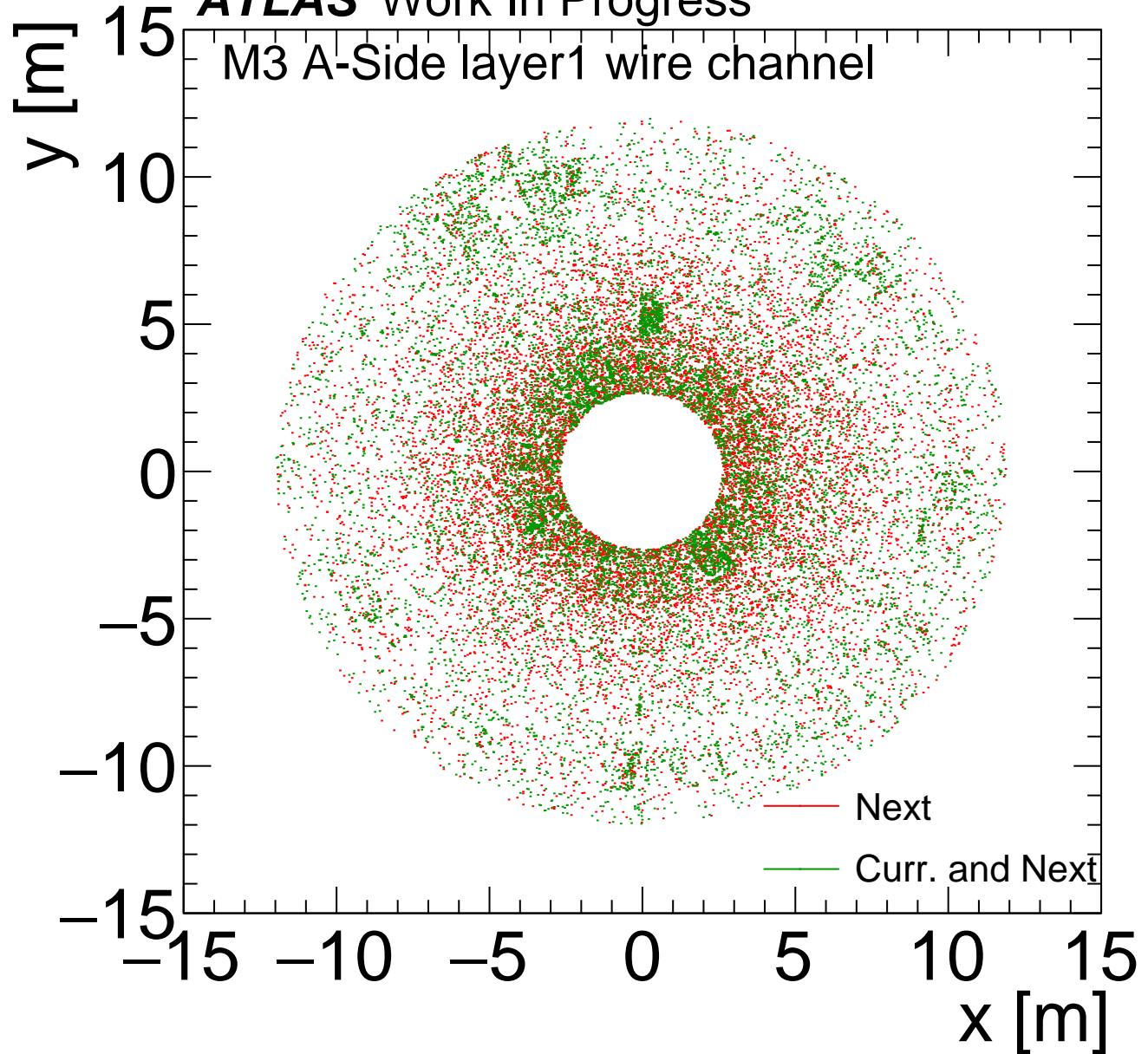
**ATLAS** Work In Progress

M2 C-Side layer3 strip channel



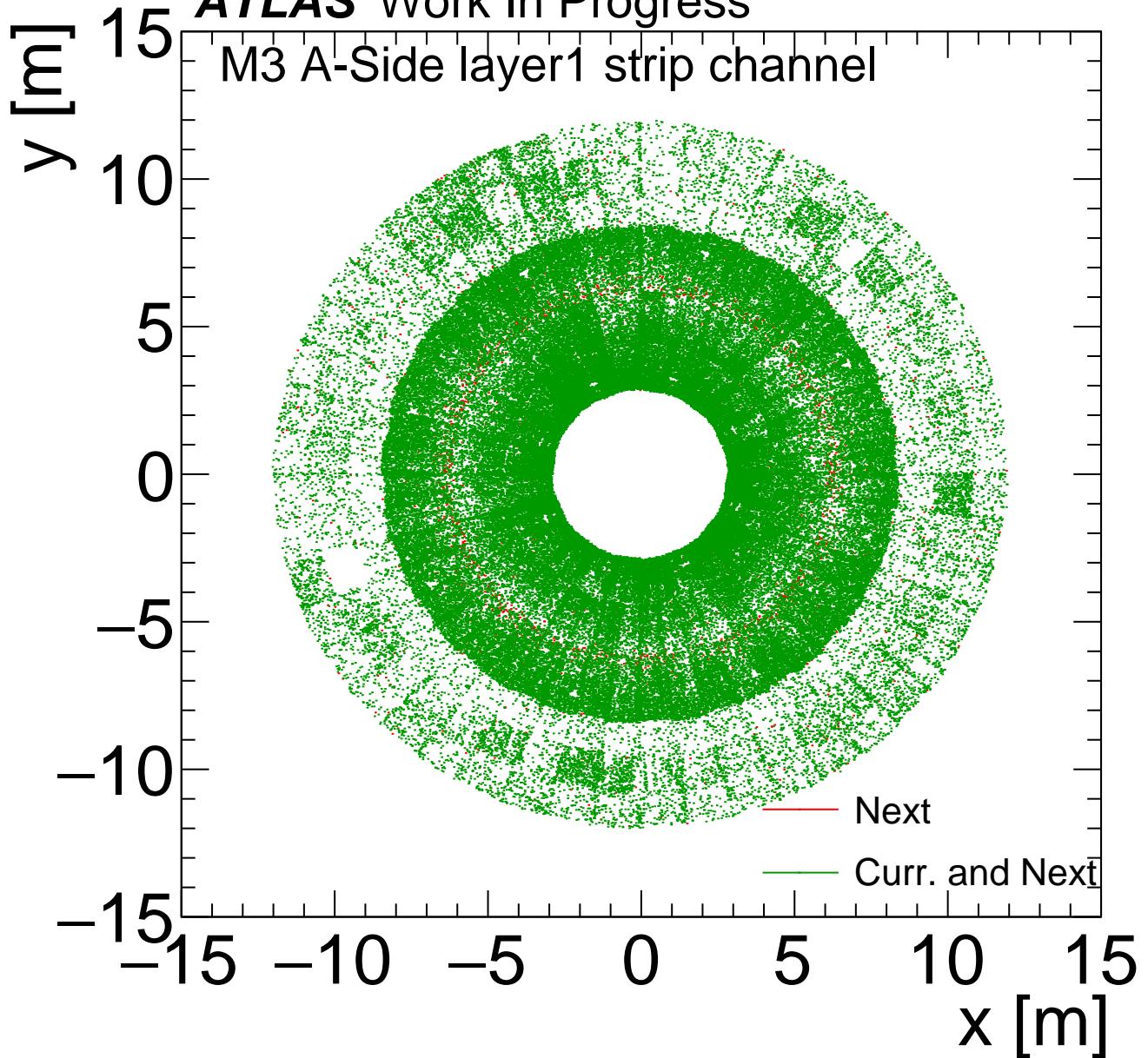
**ATLAS** Work In Progress

M3 A-Side layer1 wire channel



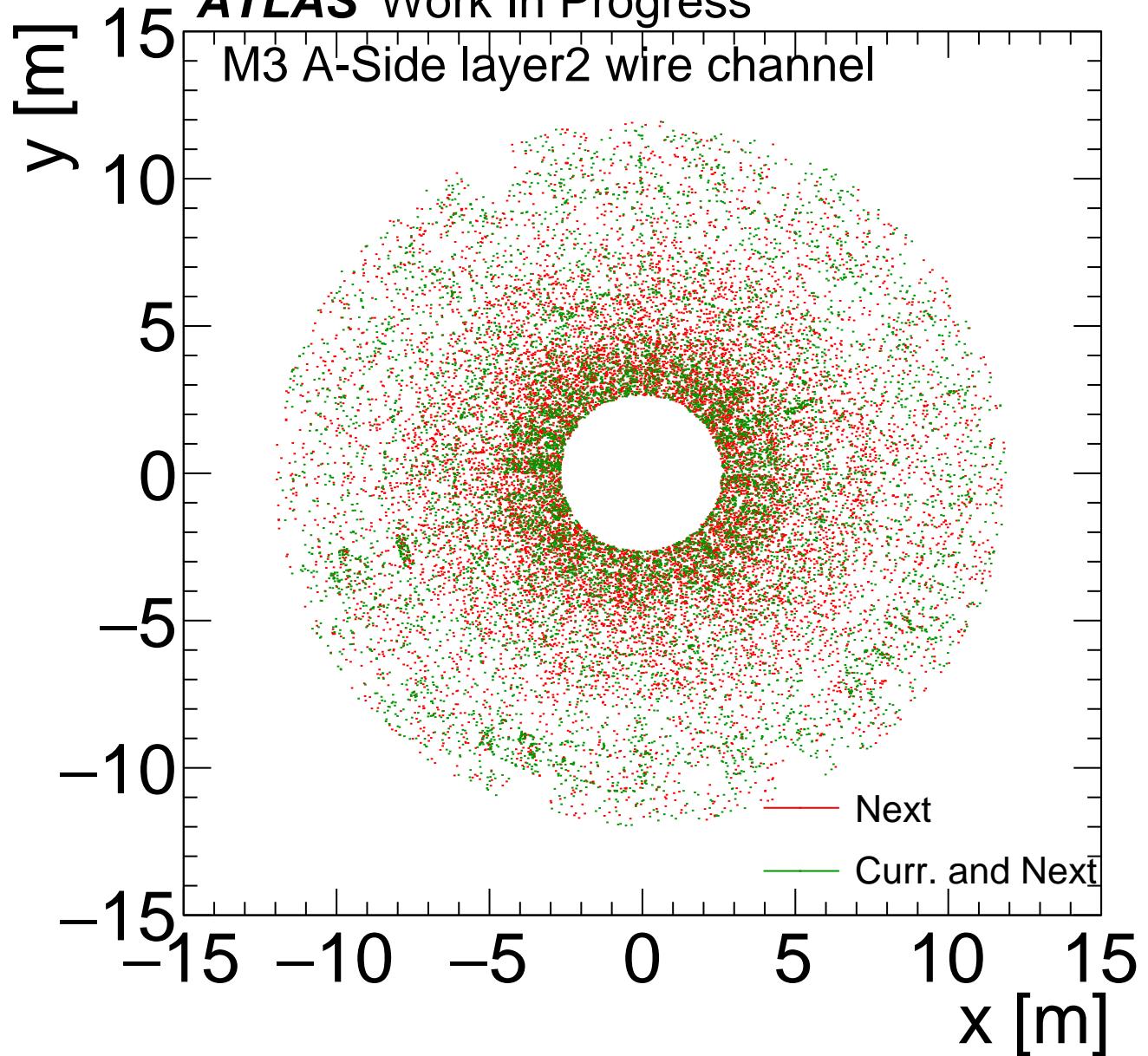
**ATLAS** Work In Progress

M3 A-Side layer1 strip channel



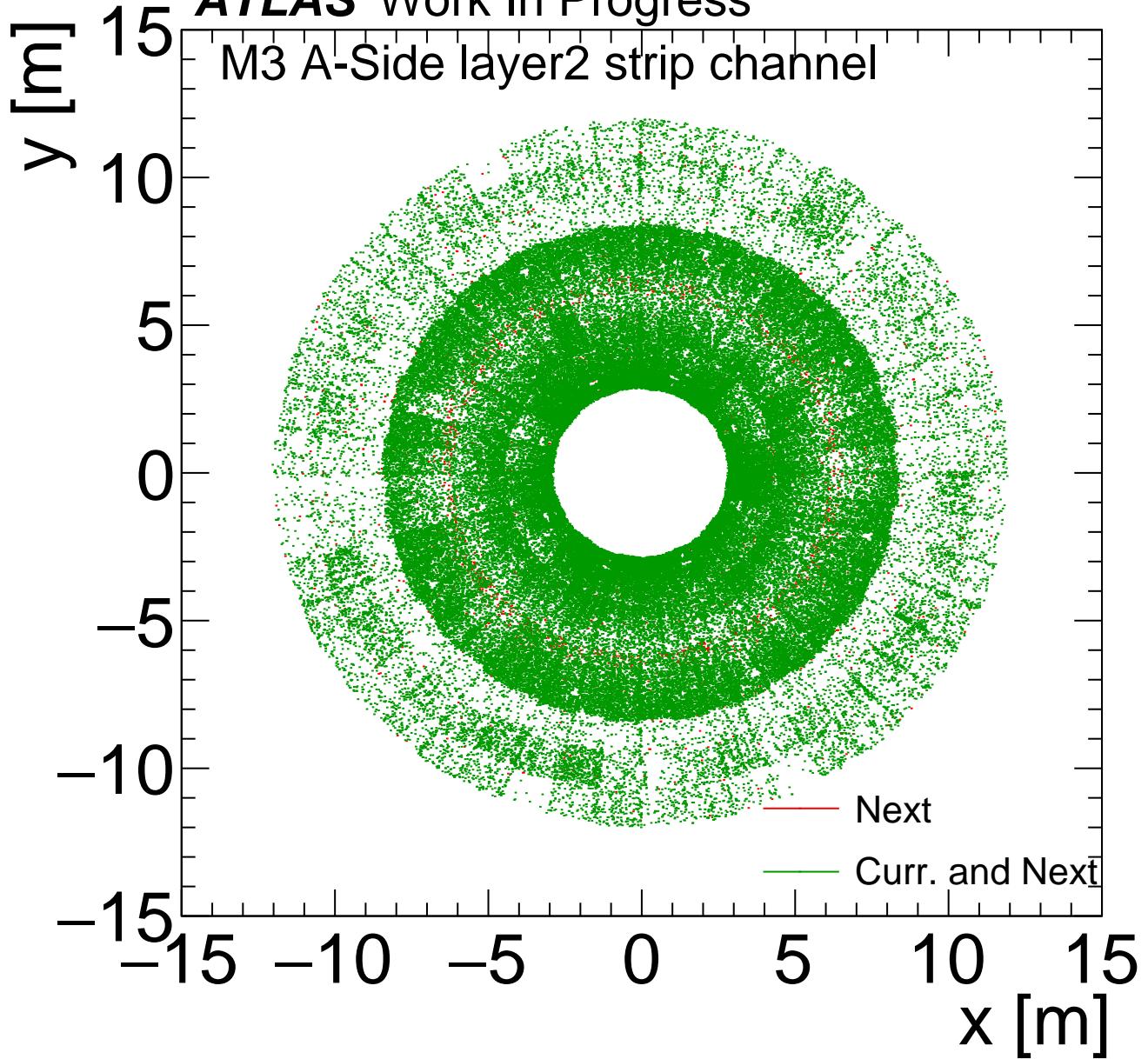
**ATLAS** Work In Progress

M3 A-Side layer2 wire channel



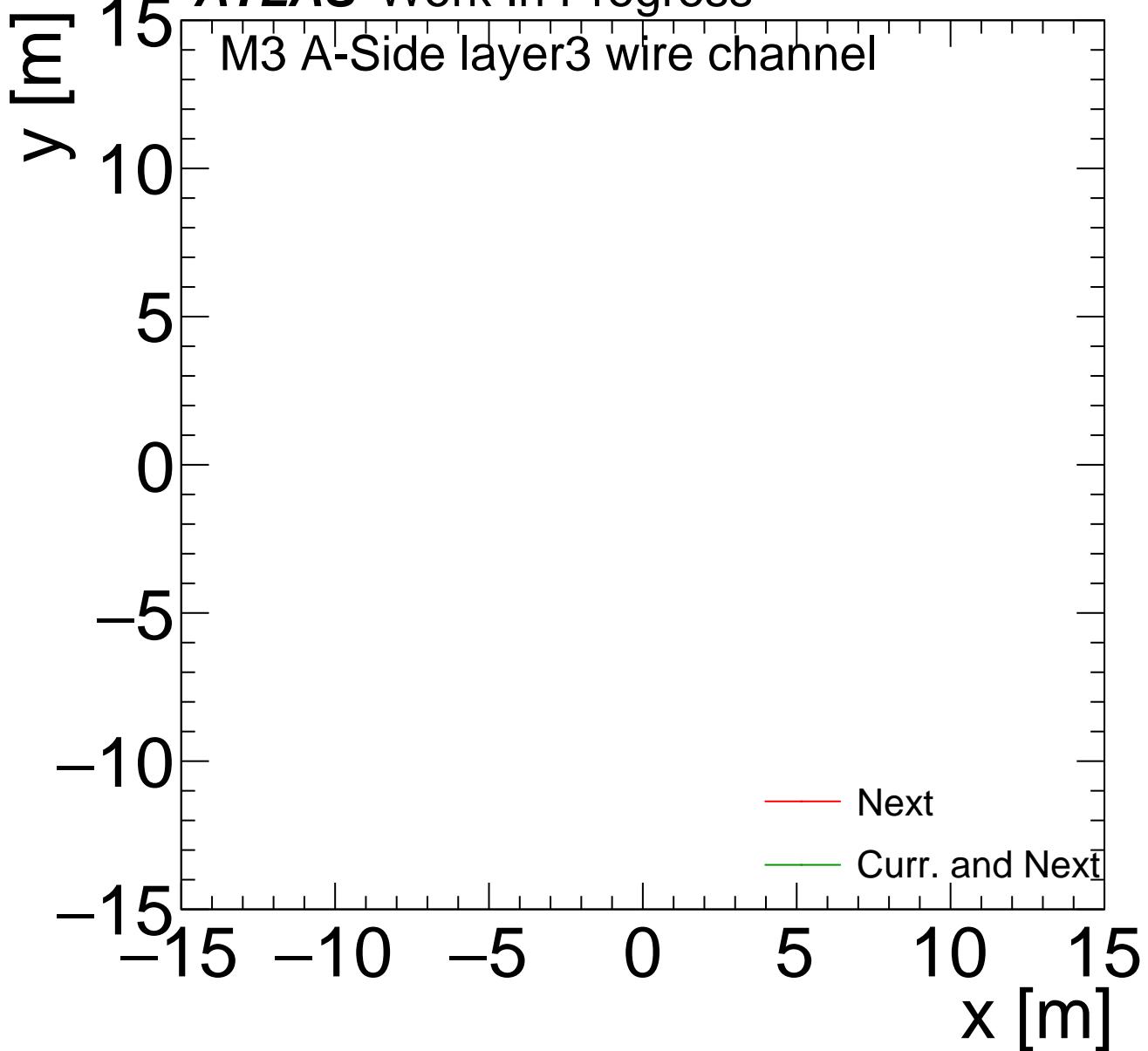
**ATLAS** Work In Progress

M3 A-Side layer2 strip channel



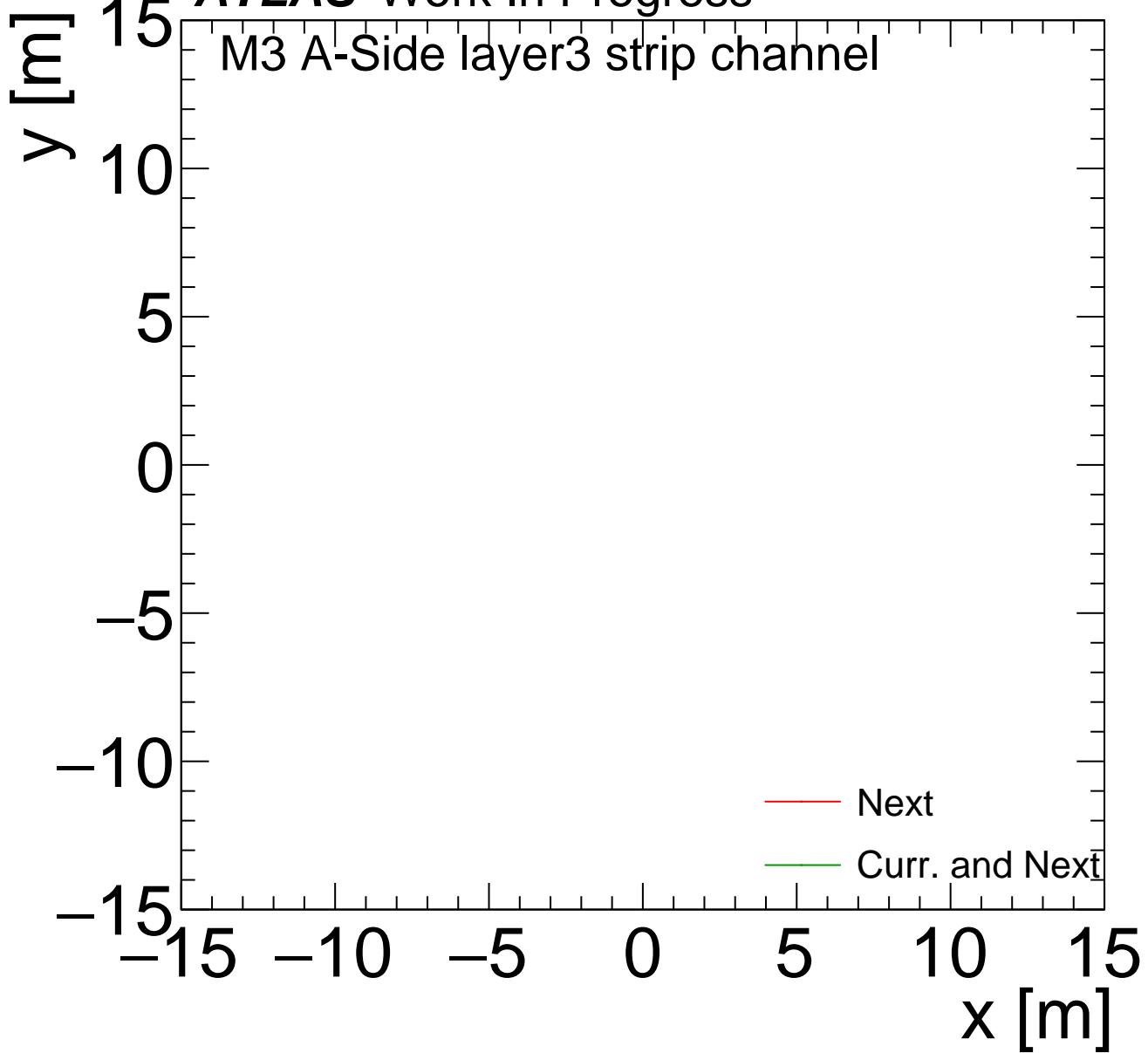
**ATLAS** Work In Progress

M3 A-Side layer3 wire channel



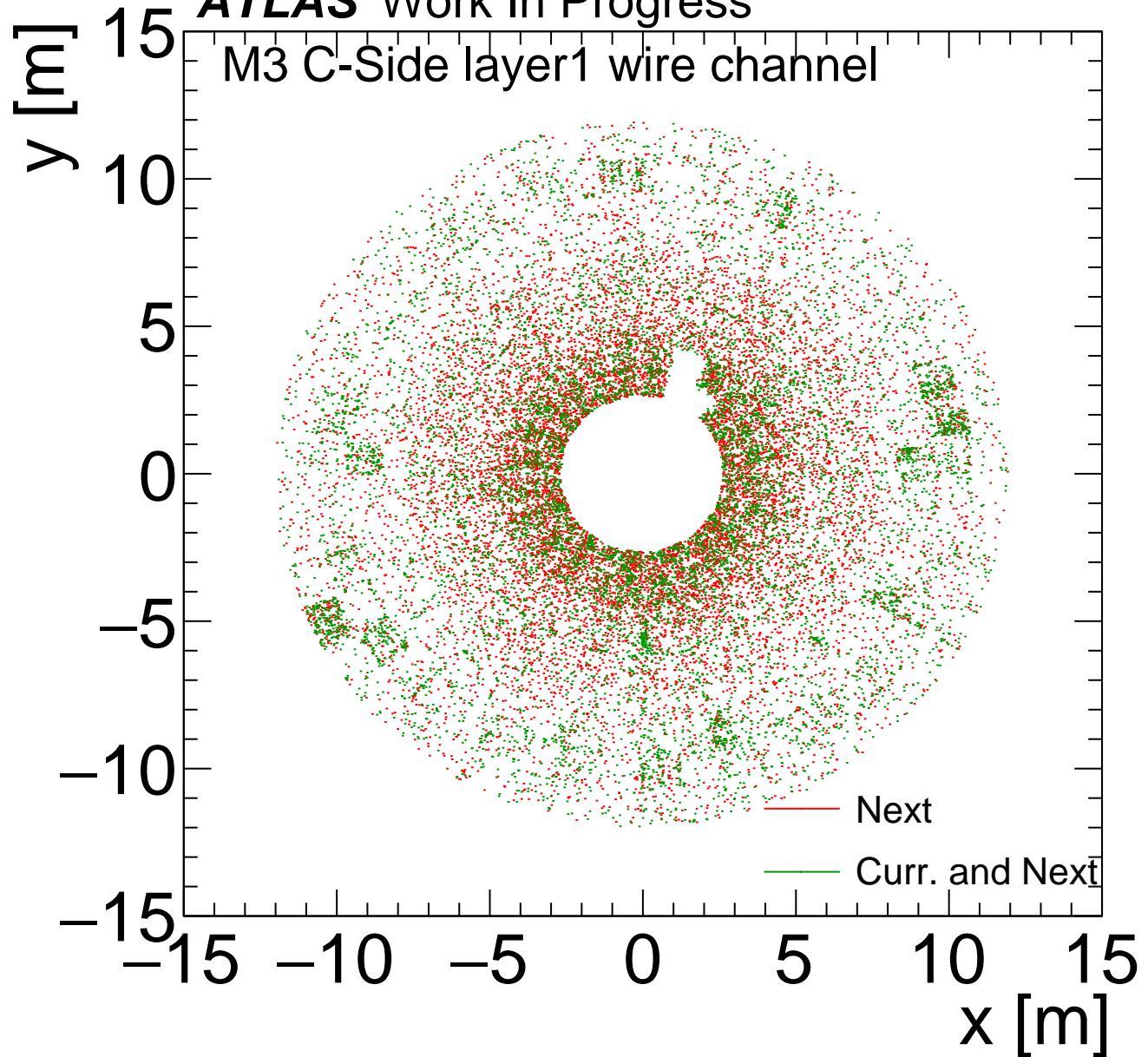
**ATLAS** Work In Progress

M3 A-Side layer3 strip channel



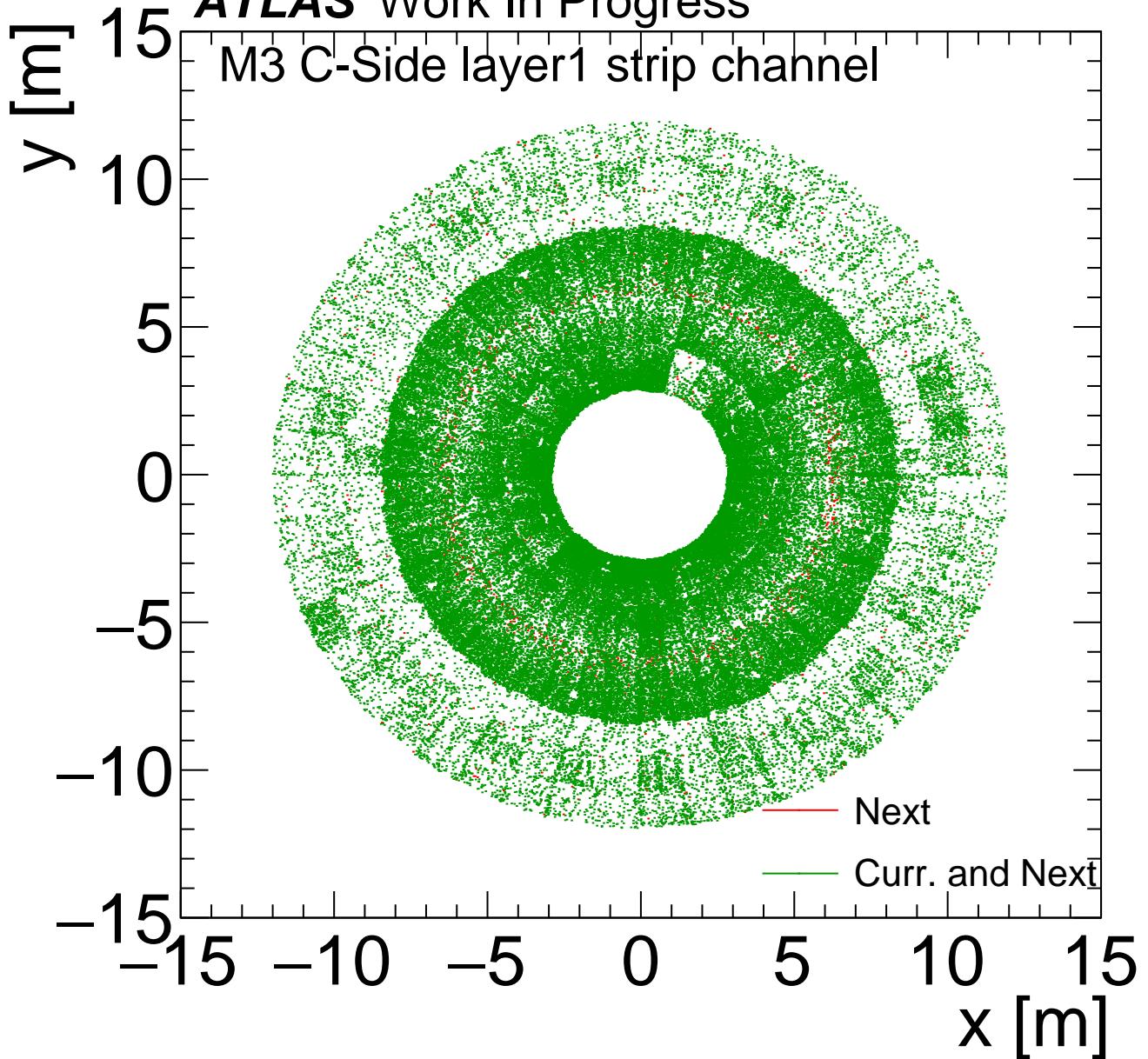
**ATLAS** Work In Progress

M3 C-Side layer1 wire channel



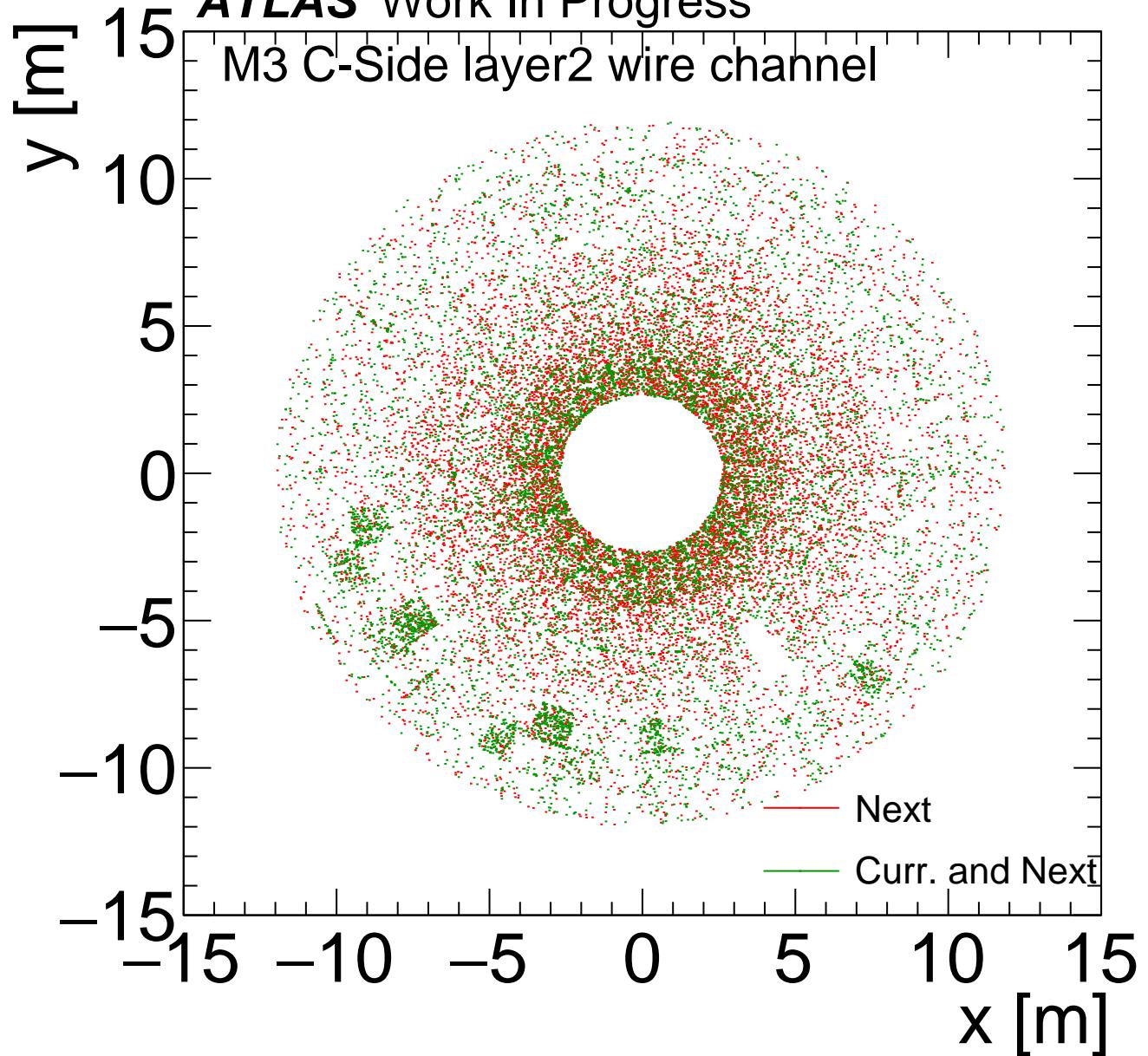
**ATLAS** Work In Progress

M3 C-Side layer1 strip channel



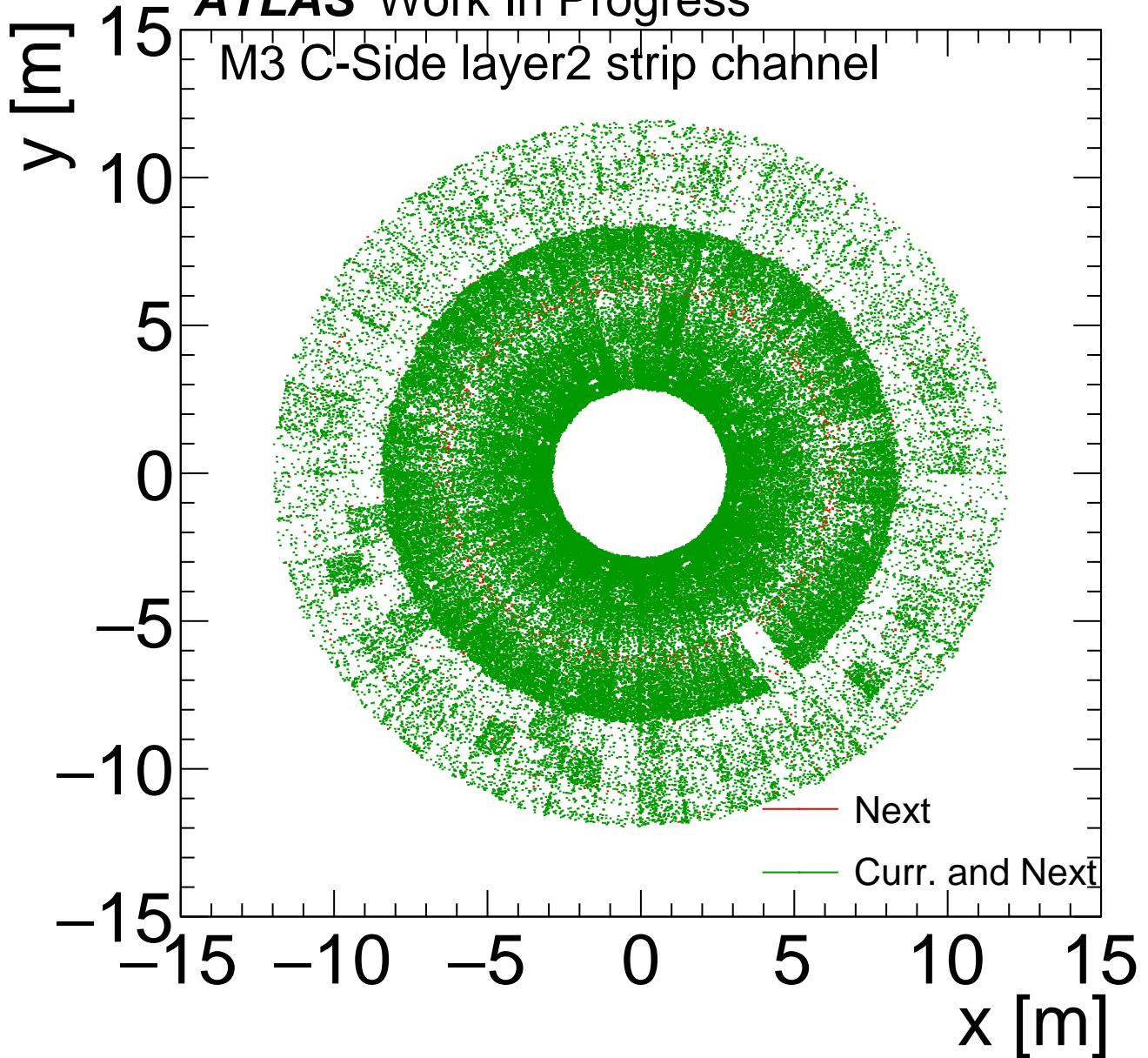
**ATLAS** Work In Progress

M3 C-Side layer2 wire channel



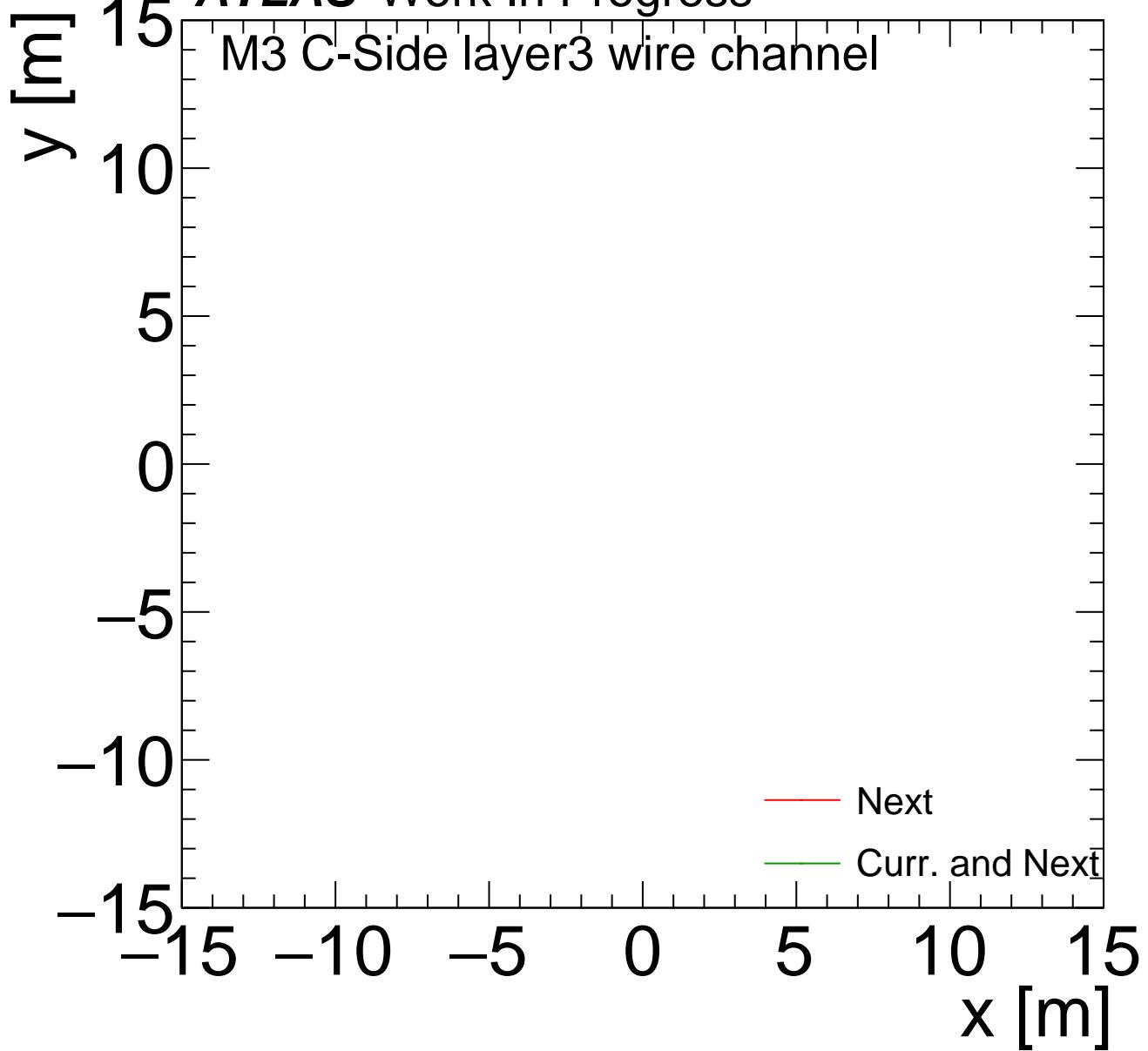
**ATLAS** Work In Progress

M3 C-Side layer2 strip channel



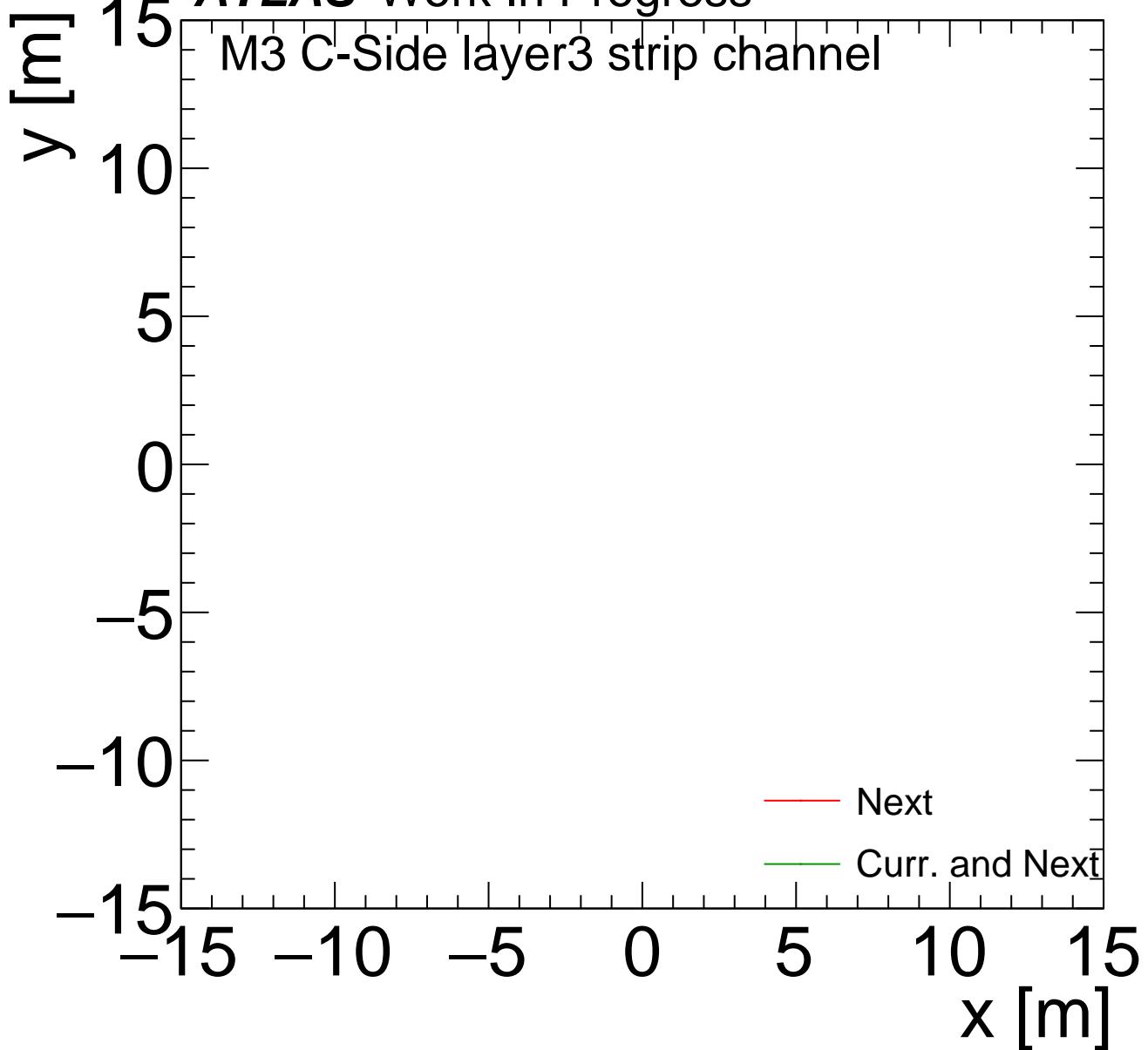
**ATLAS** Work In Progress

M3 C-Side layer3 wire channel



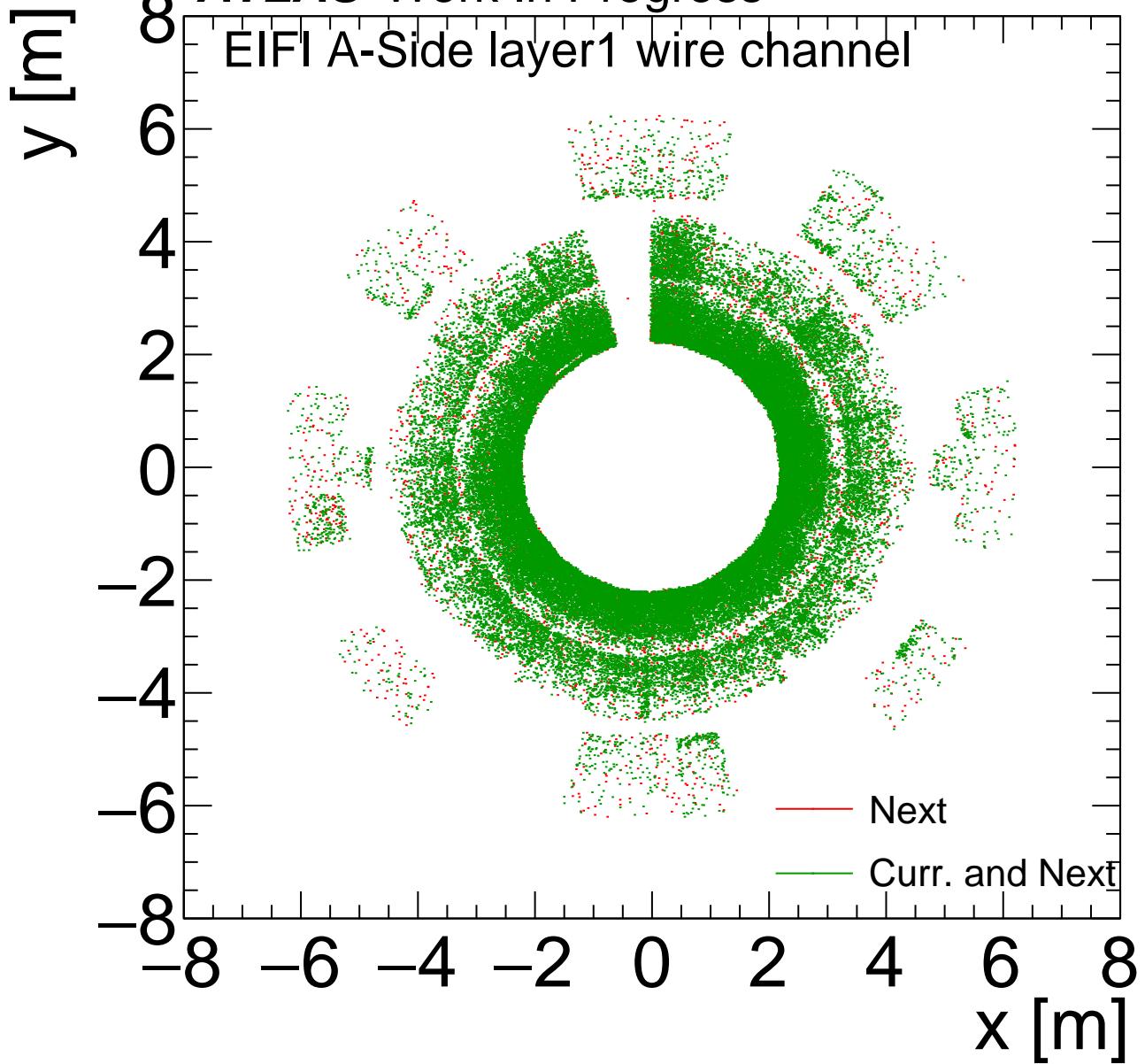
**ATLAS** Work In Progress

M3 C-Side layer3 strip channel



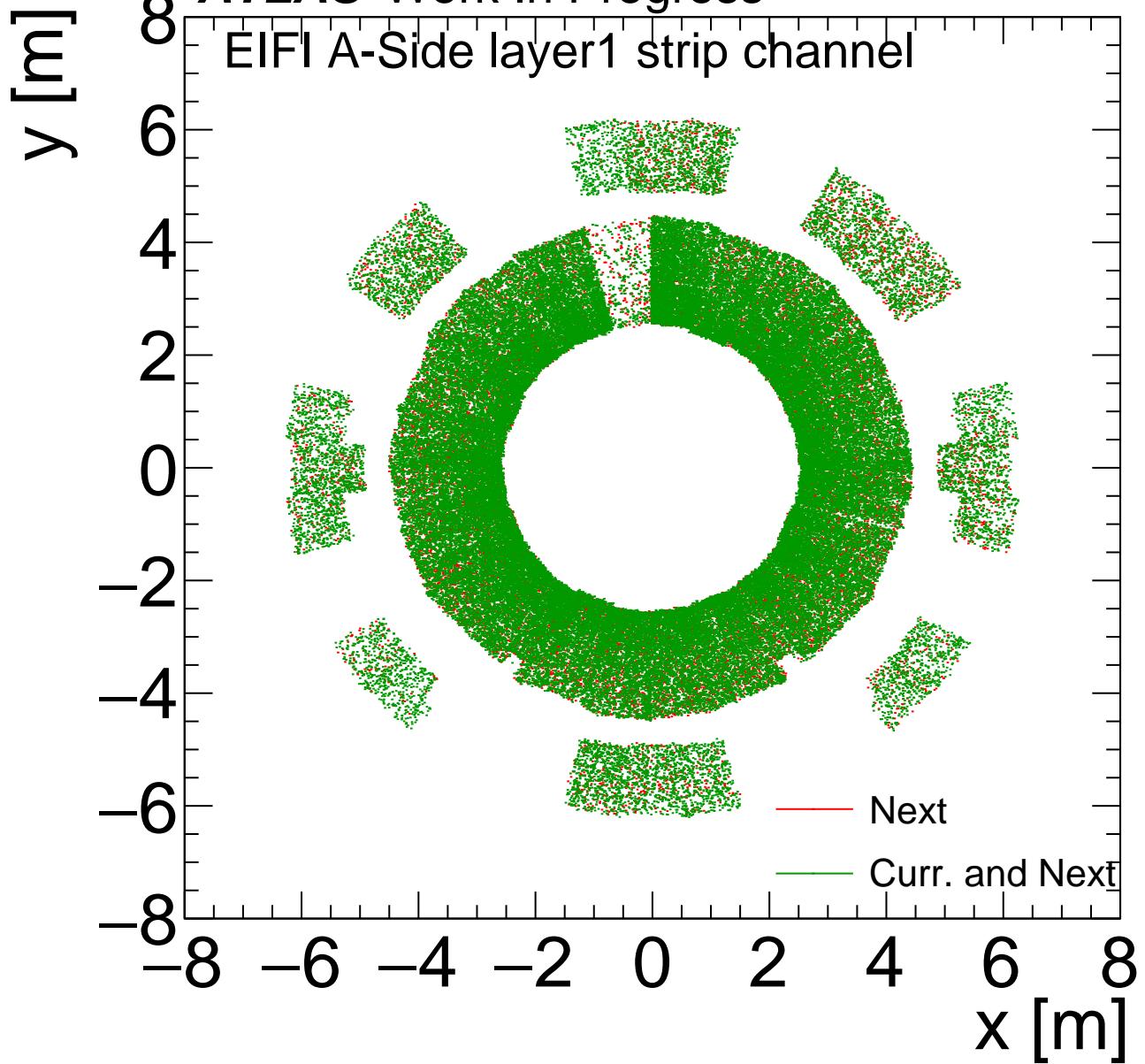
**ATLAS** Work In Progress

EIFI A-Side layer1 wire channel



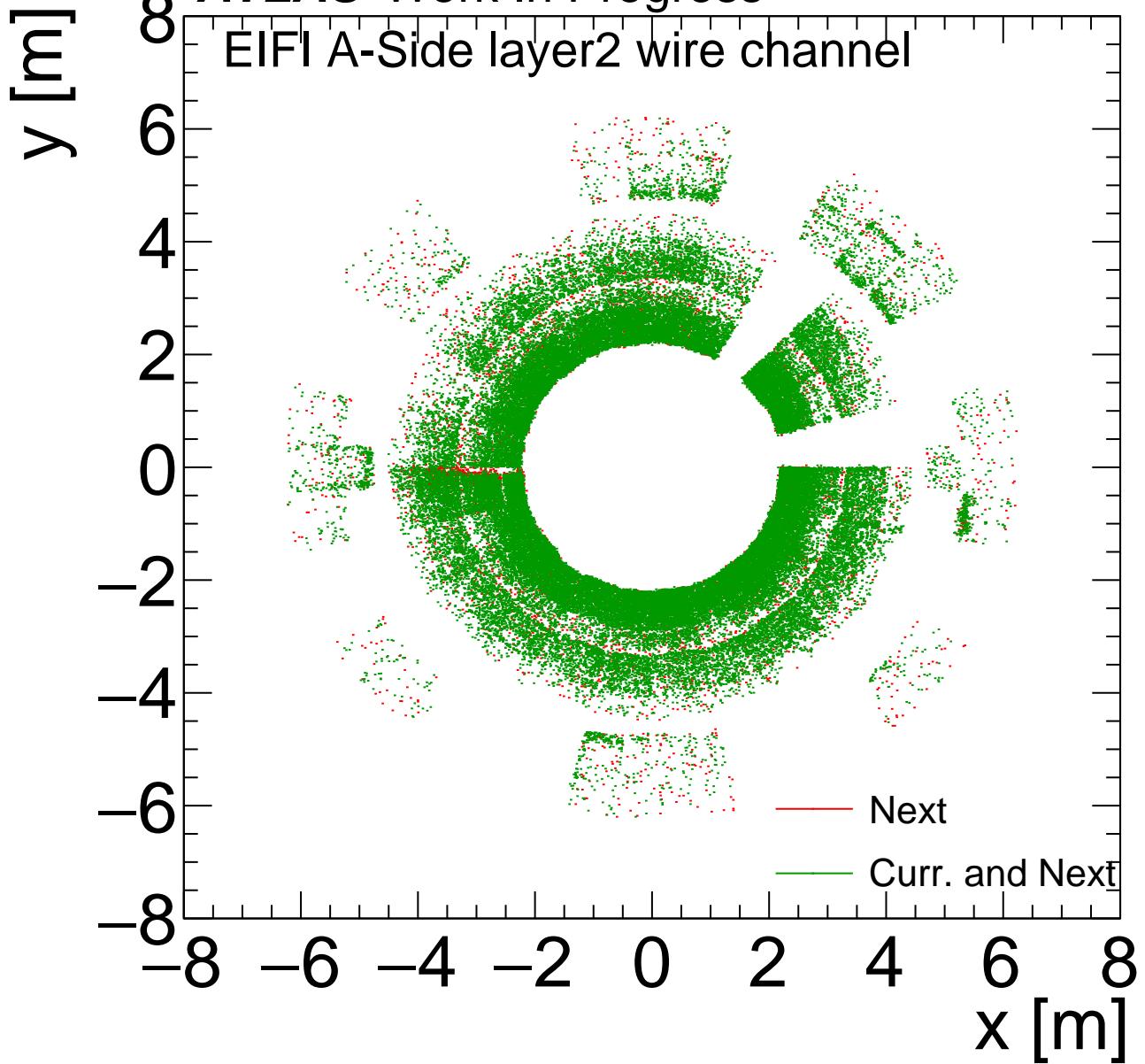
**ATLAS** Work In Progress

EIFI A-Side layer1 strip channel



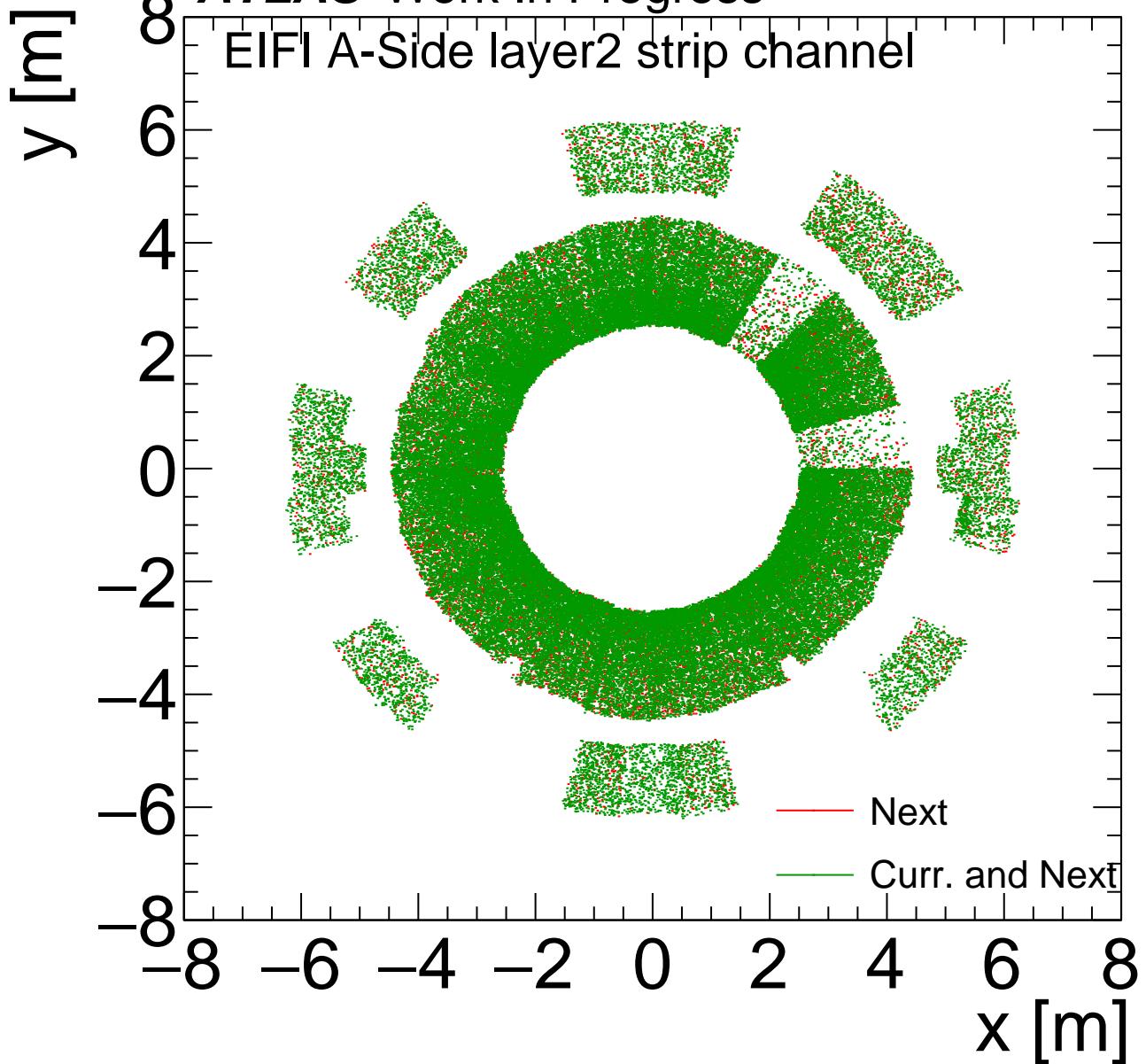
**ATLAS** Work In Progress

EIFI A-Side layer2 wire channel



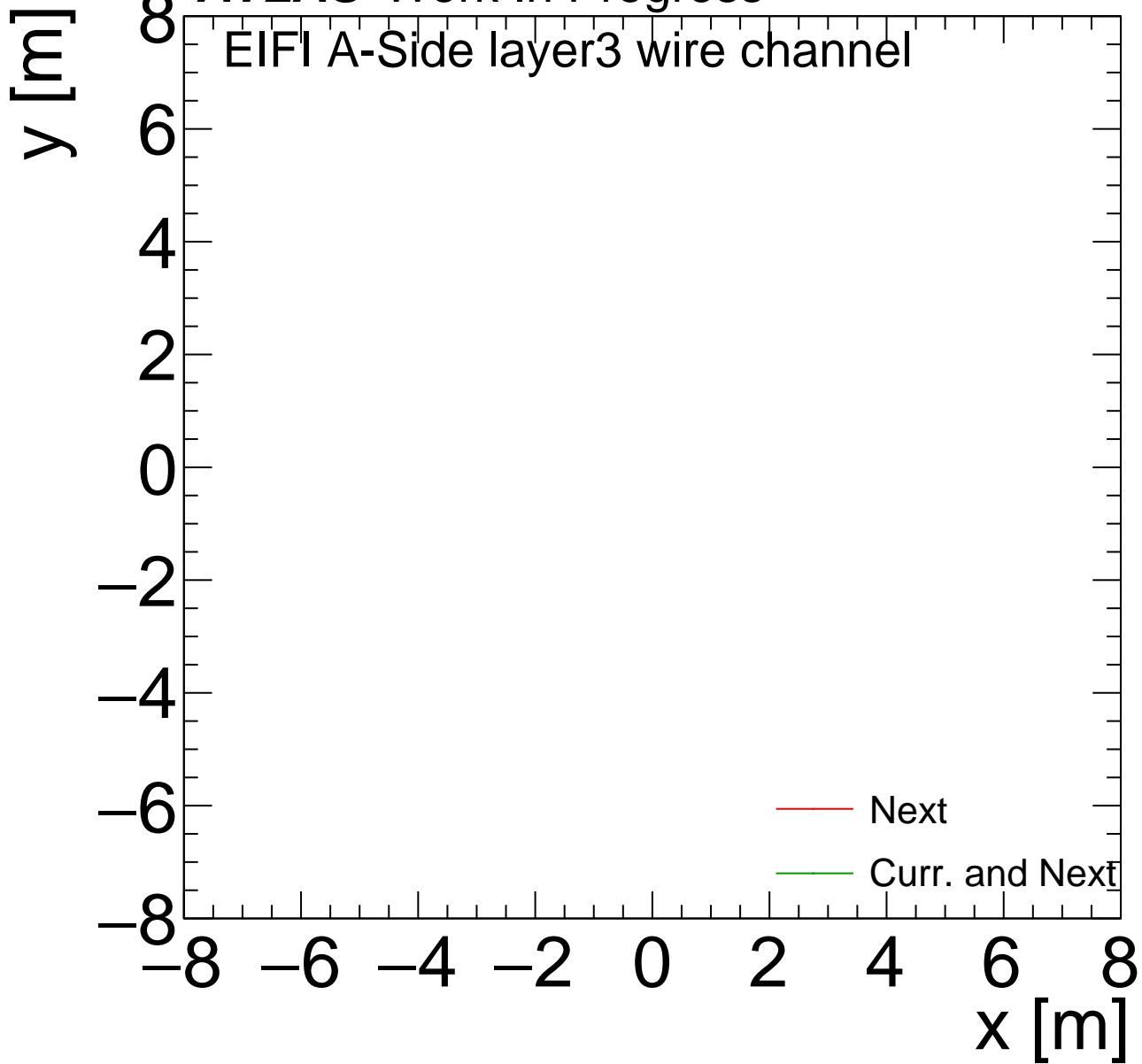
**ATLAS** Work In Progress

EIFI A-Side layer2 strip channel



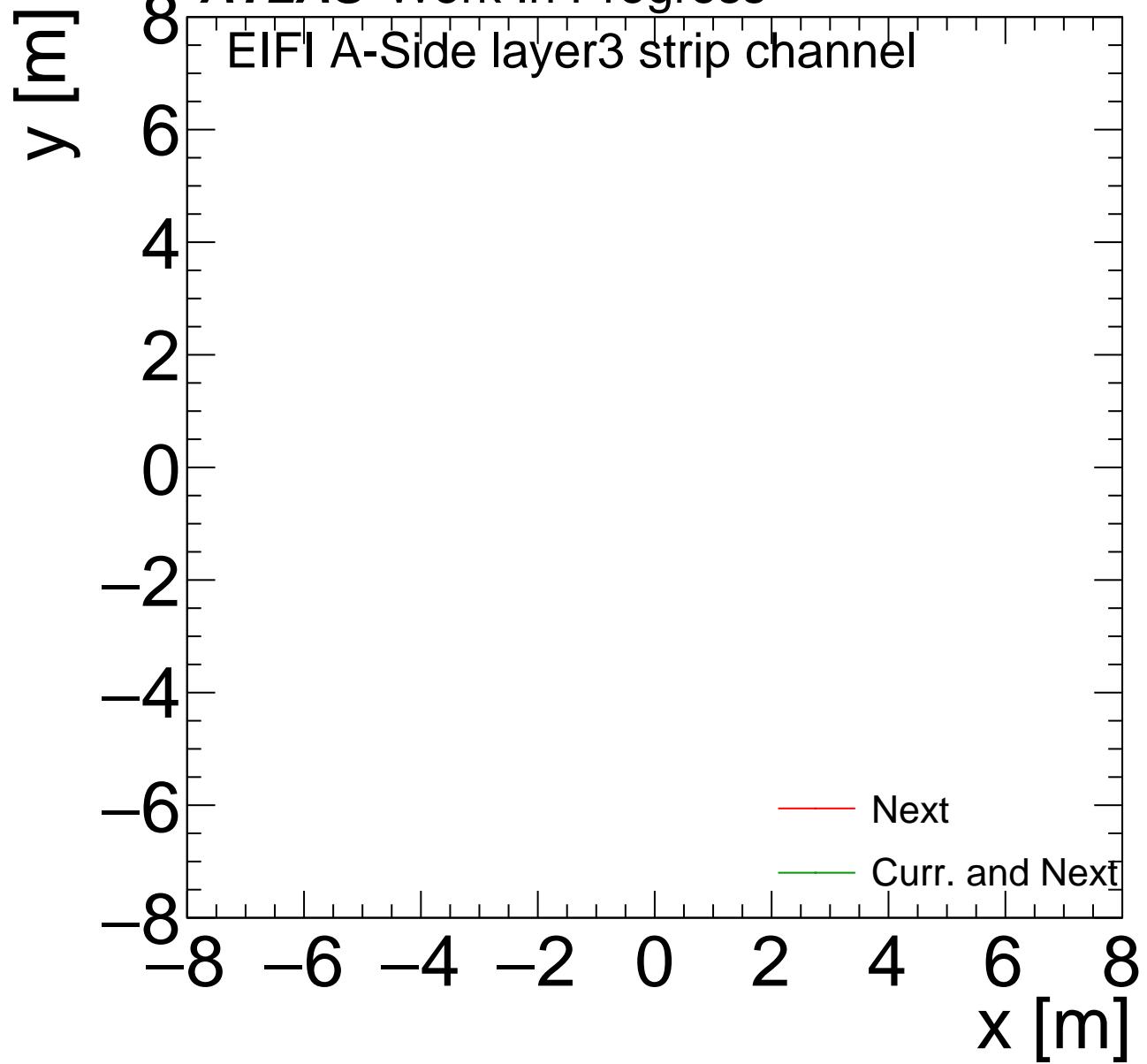
**ATLAS** Work In Progress

EIFI A-Side layer3 wire channel



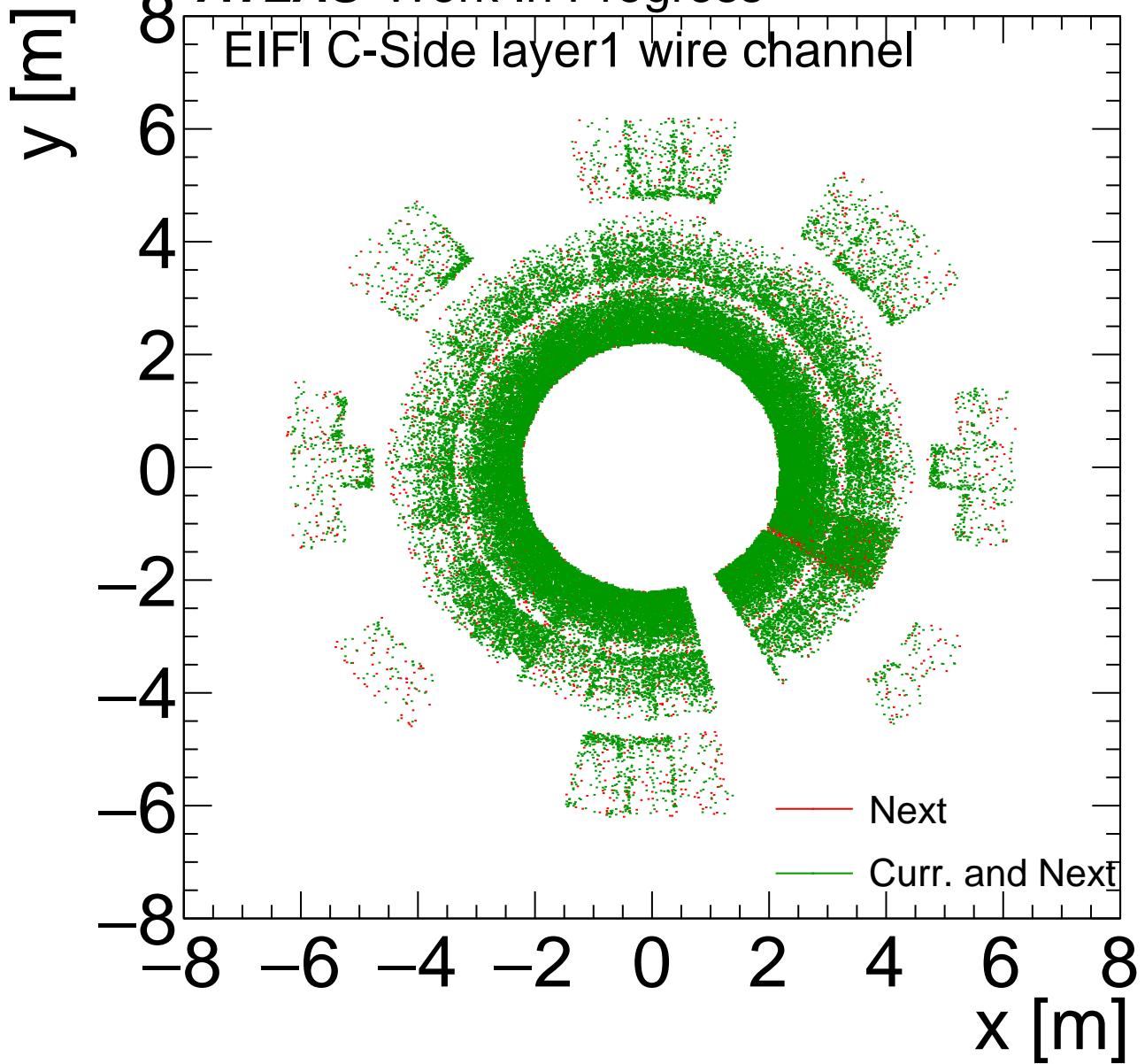
**ATLAS** Work In Progress

EIFI A-Side layer3 strip channel



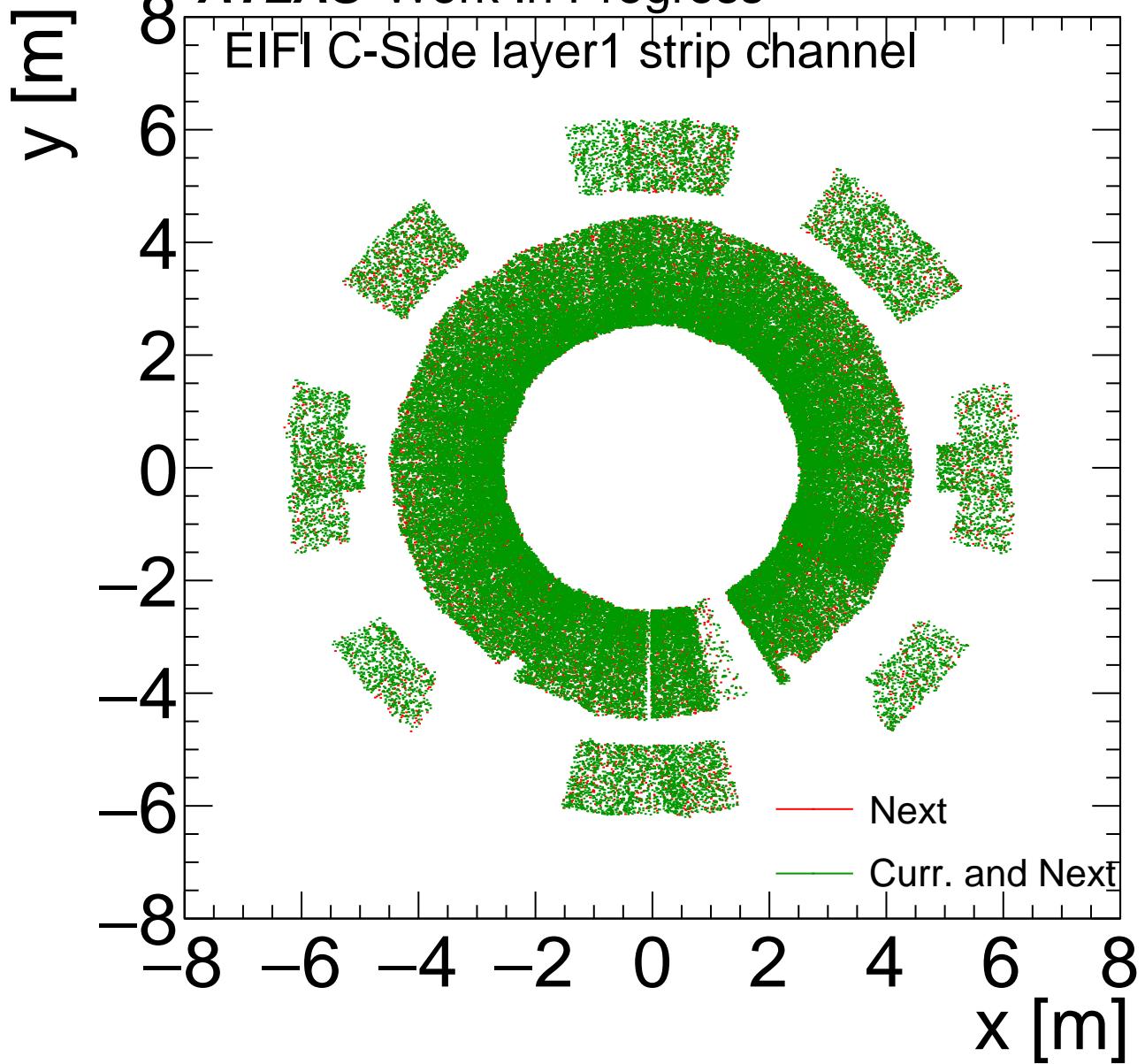
**ATLAS** Work In Progress

EIFI C-Side layer1 wire channel



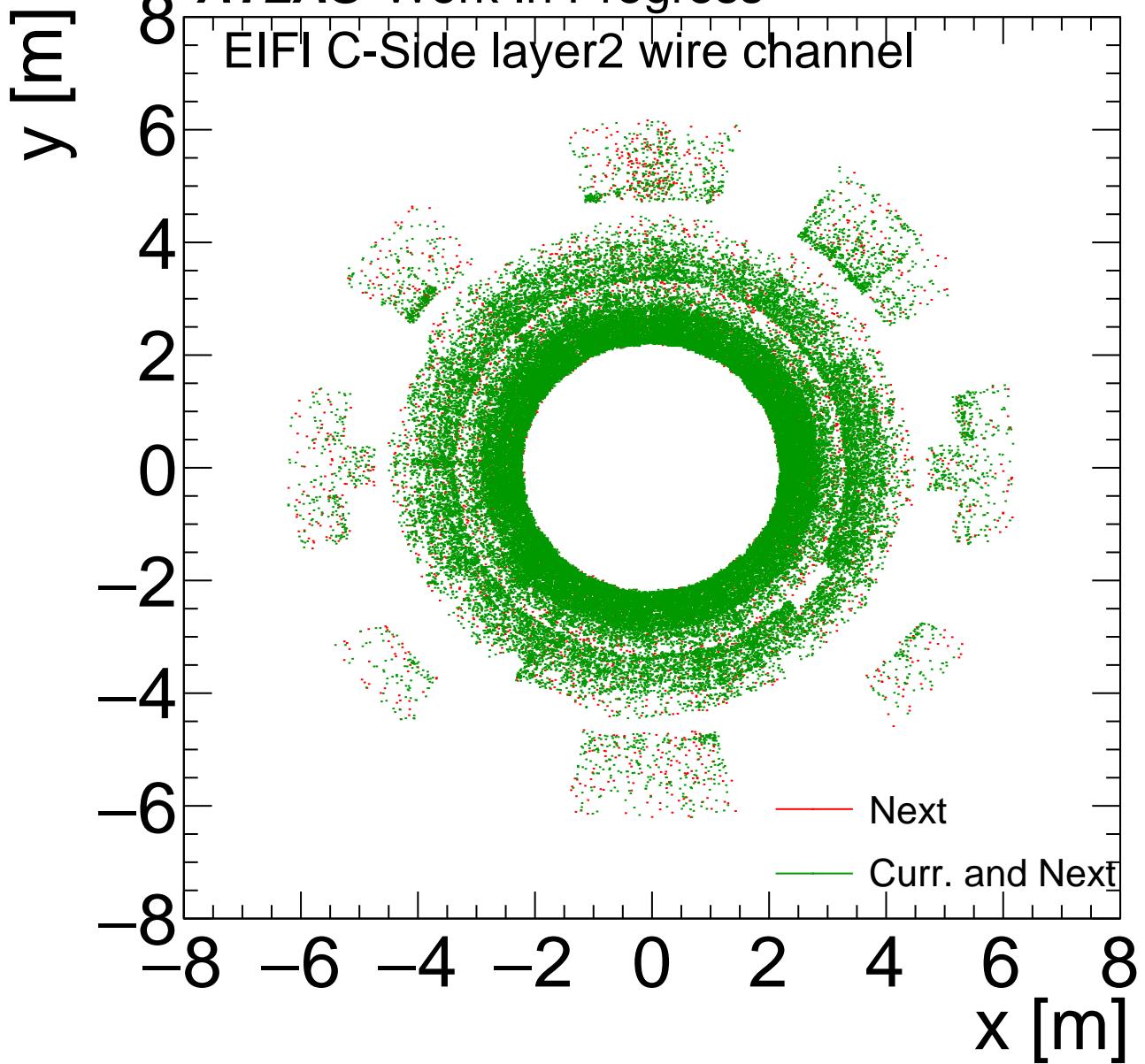
**ATLAS** Work In Progress

EIFI C-Side layer1 strip channel

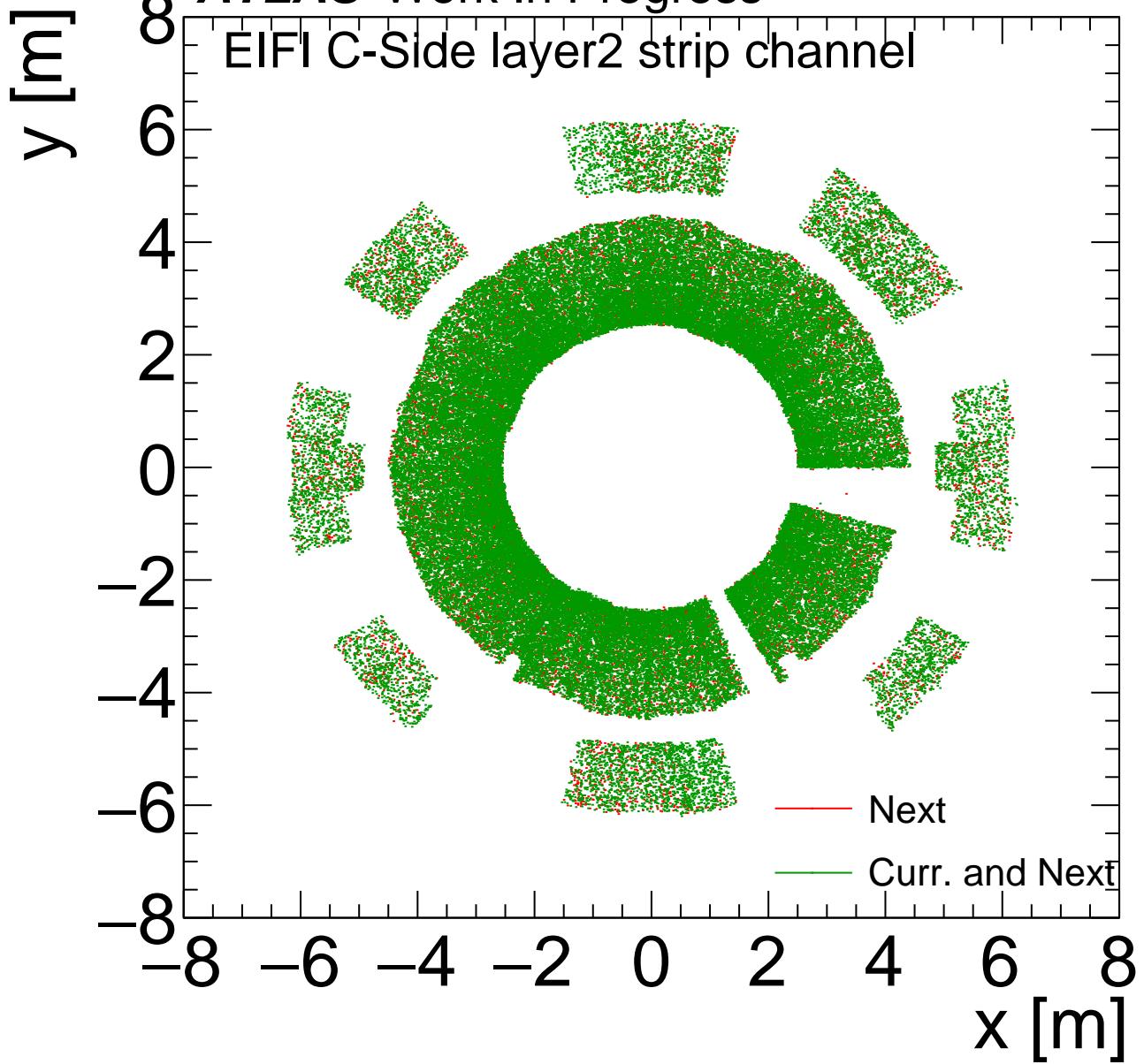


**ATLAS** Work In Progress

EIFI C-Side layer2 wire channel

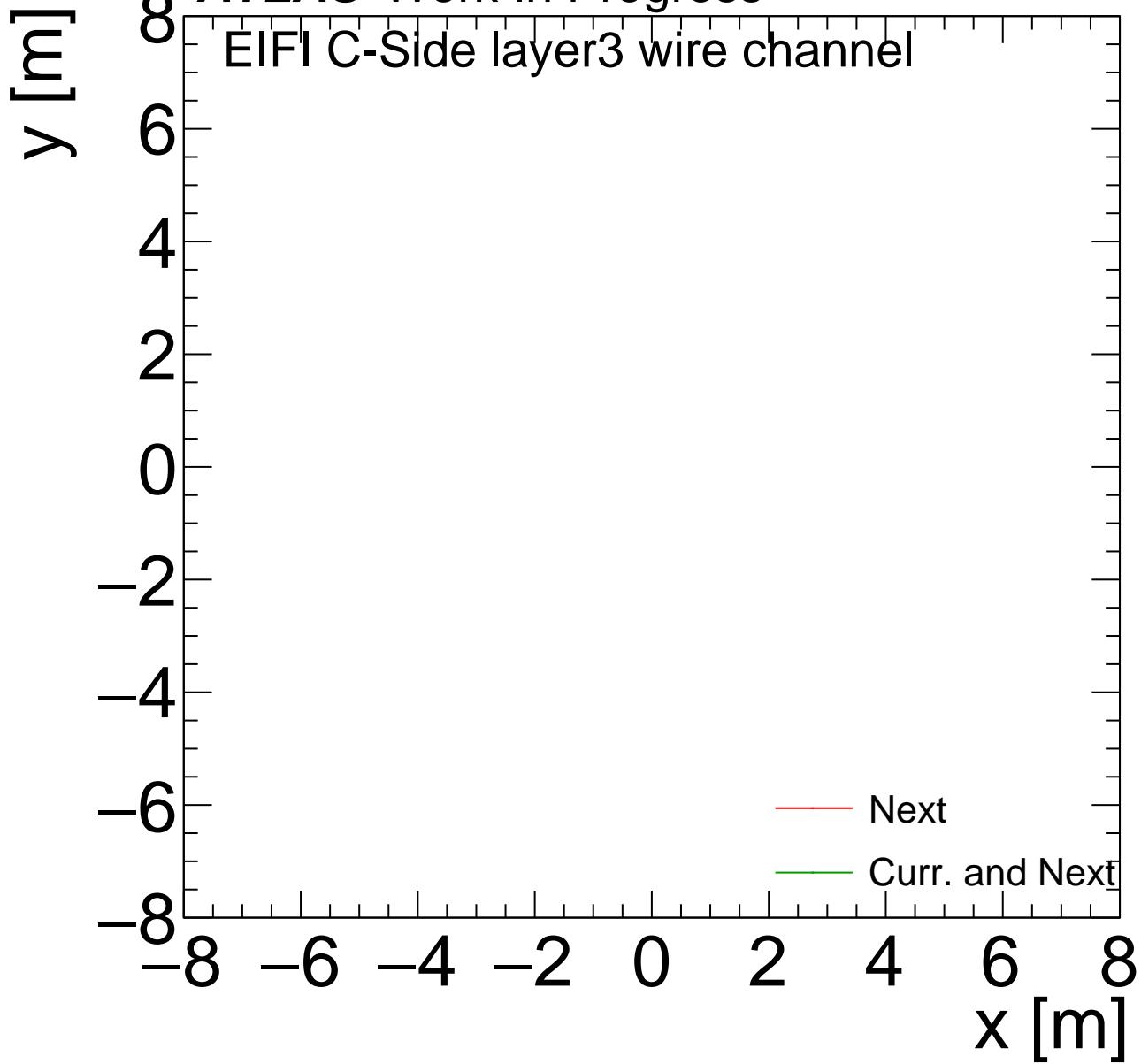


**ATLAS** Work In Progress  
EIFI C-Side layer2 strip channel



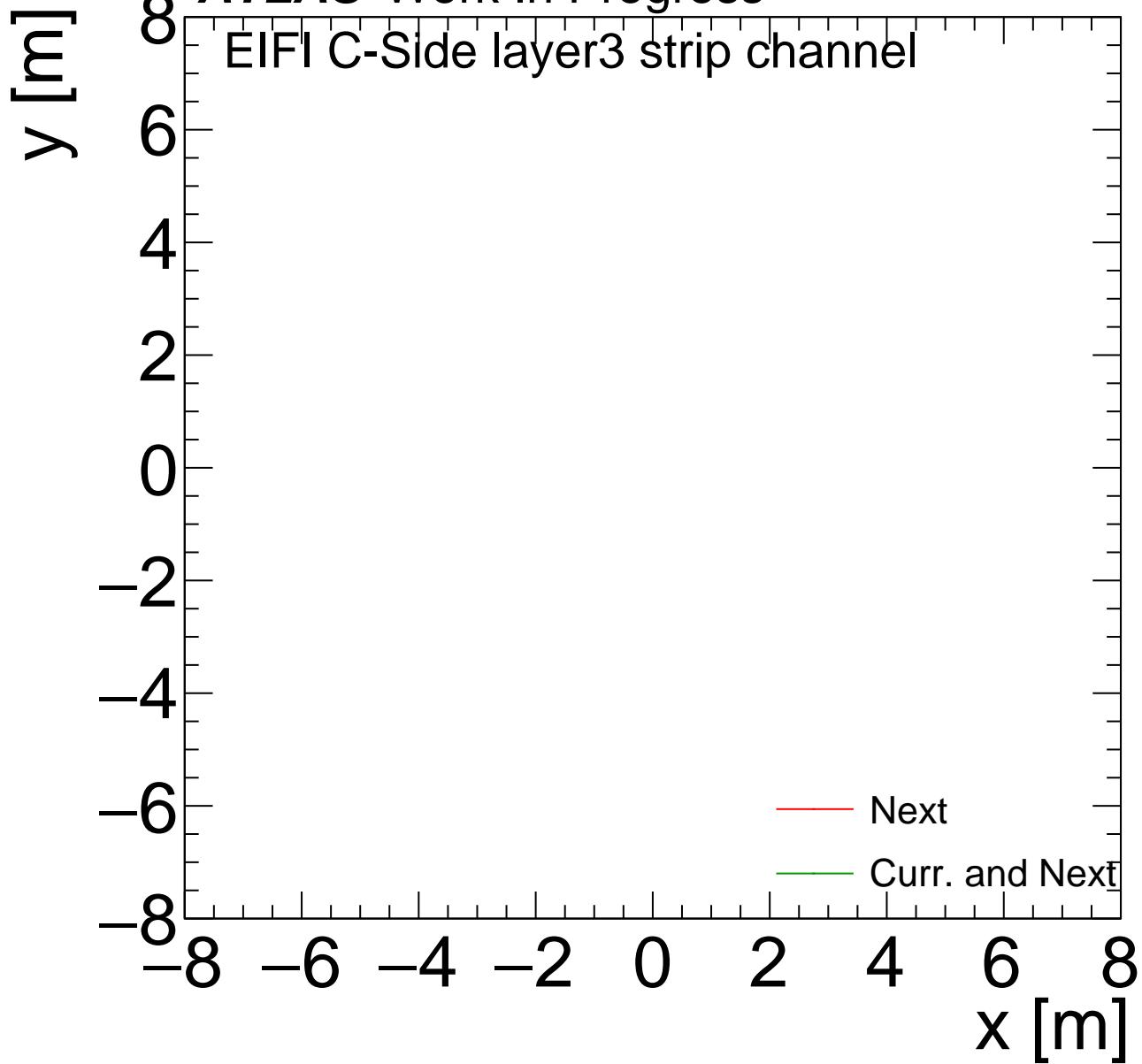
**ATLAS** Work In Progress

EIFI C-Side layer3 wire channel



**ATLAS** Work In Progress

EIFI C-Side layer3 strip channel



**ATLAS** Work In Progress

EIFI C-Side layer3 strip channel

