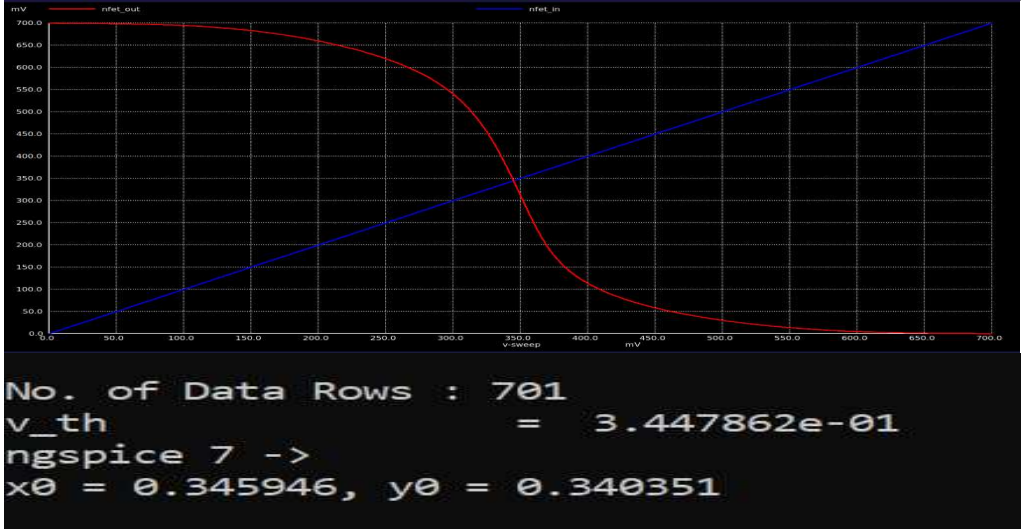
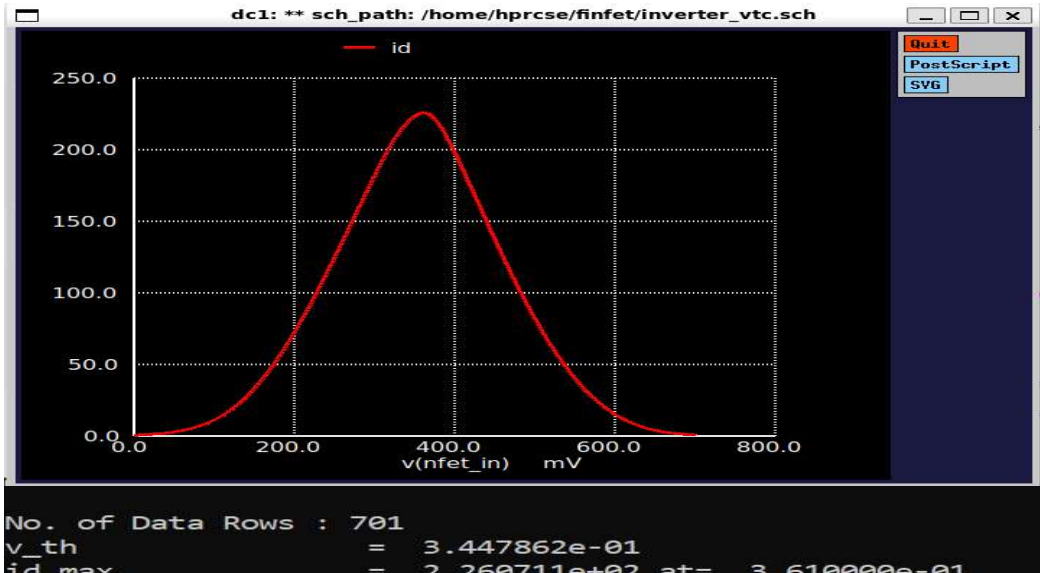


Sunday, January 04, 2026 3:21 AM

Sr. No	W (Width nm) pmos	L (Lengt h) pmos nm	(W/L Ratio) pmos	W (Width ) nmos nm	L (Lengt h) nmos nm	(W/L Ratio) nmos	Switchi ng Thresh old Volutag e (VTC) -mV	Drain Current (Id) (μA)	Power Consu mptio n (P)	Propag ation Delay (t_pd) (ps)	Gain (Av)	Noise Margin (NM)	Transc onduct ance (gm) X10 <sup>-3</sup>	Freque ncy (f) (GHz)	Output Resista nce (Ro)
1	14	7	2	14	7	2	345	226.07 at 0.361v	29.736 65 μW	26.050 92 ps	6.43		36.18	22.46	38.18k



2. Id current  
let id=abs(v2#branch)\*1e6  
plot id vs v(nfet\_in)  
\* from measure get max dc value  
\* 1\_meas 2\_dc(type of simulation) 3\_variable 4\_operation 5\_experssion to be evalted  
meas dc Id\_max MAX id



## 3. Power cons

```

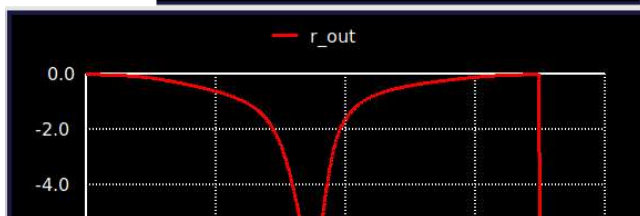
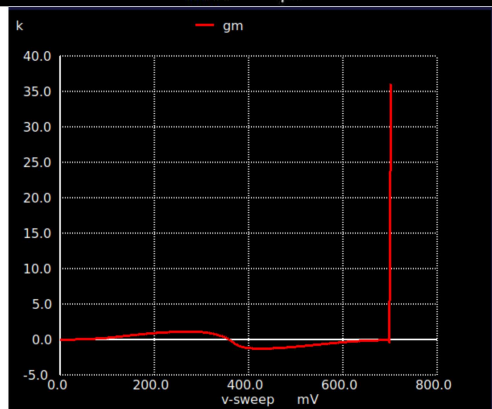
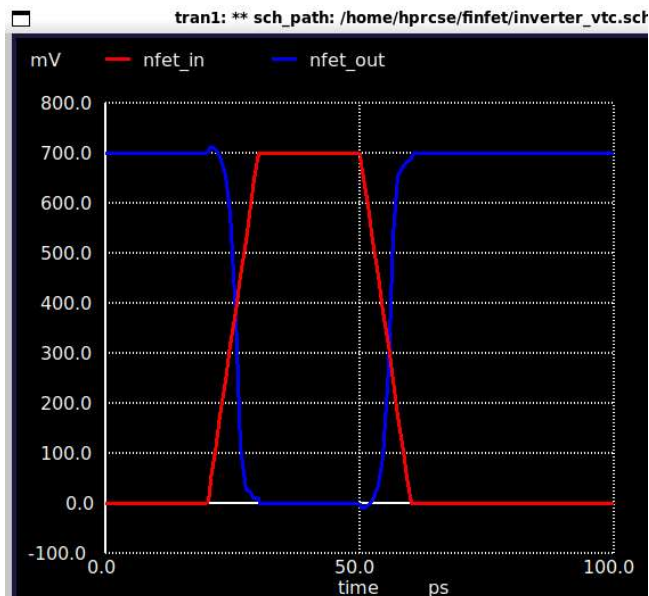
Initial Transient Solution
-----

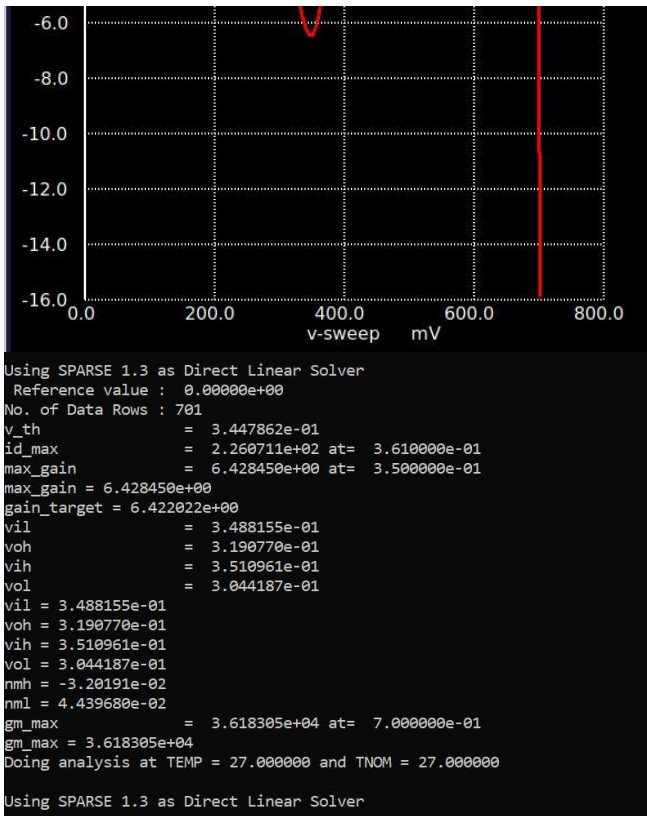
ode
---
fet_out      0.699647
fet_in       0
dd           0.7
2#branch     -8.07134e-07
1#branch      7.22294e-12

Reference value : 9.05000e-11
o. of Data Rows : 120
d_pwr       = -1.69924e-15 from= 2.00000e-11 to= 6.00000e-11
** sch_path: /home/hprcse/finfet/inverter_vtc.sch
Transient Analysis Sun Jan 4 04:09:39 2026
-----

```

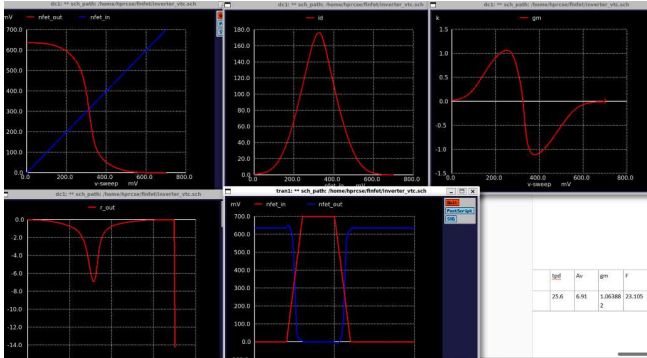
index	time	power
0.000000e+00	2.973665e-05	
1.000000e-14	2.973665e-05	
2.000000e-14	2.973665e-05	
4.000000e-14	2.973665e-05	
8.000000e-14	2.973665e-05	
1.600000e-13	2.973665e-05	
3.200000e-13	2.973665e-05	
6.400000e-13	2.973665e-05	
1.280000e-12	2.973665e-05	
2.280000e-12	2.973665e-05	





ASSIGNMENT---

Naveen—asic-0.637V so V2 set max to 0.637



Sr no	W pmos	L pmos	W/L (pmos)	W nmos	L nmos	W/l	Vth	Id	Power	tpd	Av	gm	F	R
1	14	7	2	14	7	2	312	176.5491	18.8	25.6	6.91	1.063882	23.105	38.2
2	12	7	1.7	8	7	1.14	335.48mv	120.419	12.9886	25.7	6.913	0.66539	22.896	6.8
3	20	7	2.857	14	7	2	332.67	206.5815	22.25	25.77	6.908	1.153153	22.92712	7.2

