

Lab 4: Translinear Circuits

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Experiment 1: Bipolar Transistor Matching

In this experiment we looked at the MMPQ3904 bipolar transistor array. There are four transistors in this chip. We measured the base and emitter currents as a function of the base voltage. The collector is connected to V_{dd} .

We were not able to complete the rest of this experiment due to the fact that amidst moving out of the dorms, we lost the data.

Experiment 2: Translinear Circuit 1

In this experiment, we constructed the circuit shown in figure 1. We tied the output voltage to V_{dd} . We measured I_z as a function of I_x for 3 different values of I_y and I_x as a function of I_y for 3 different values of I_x .

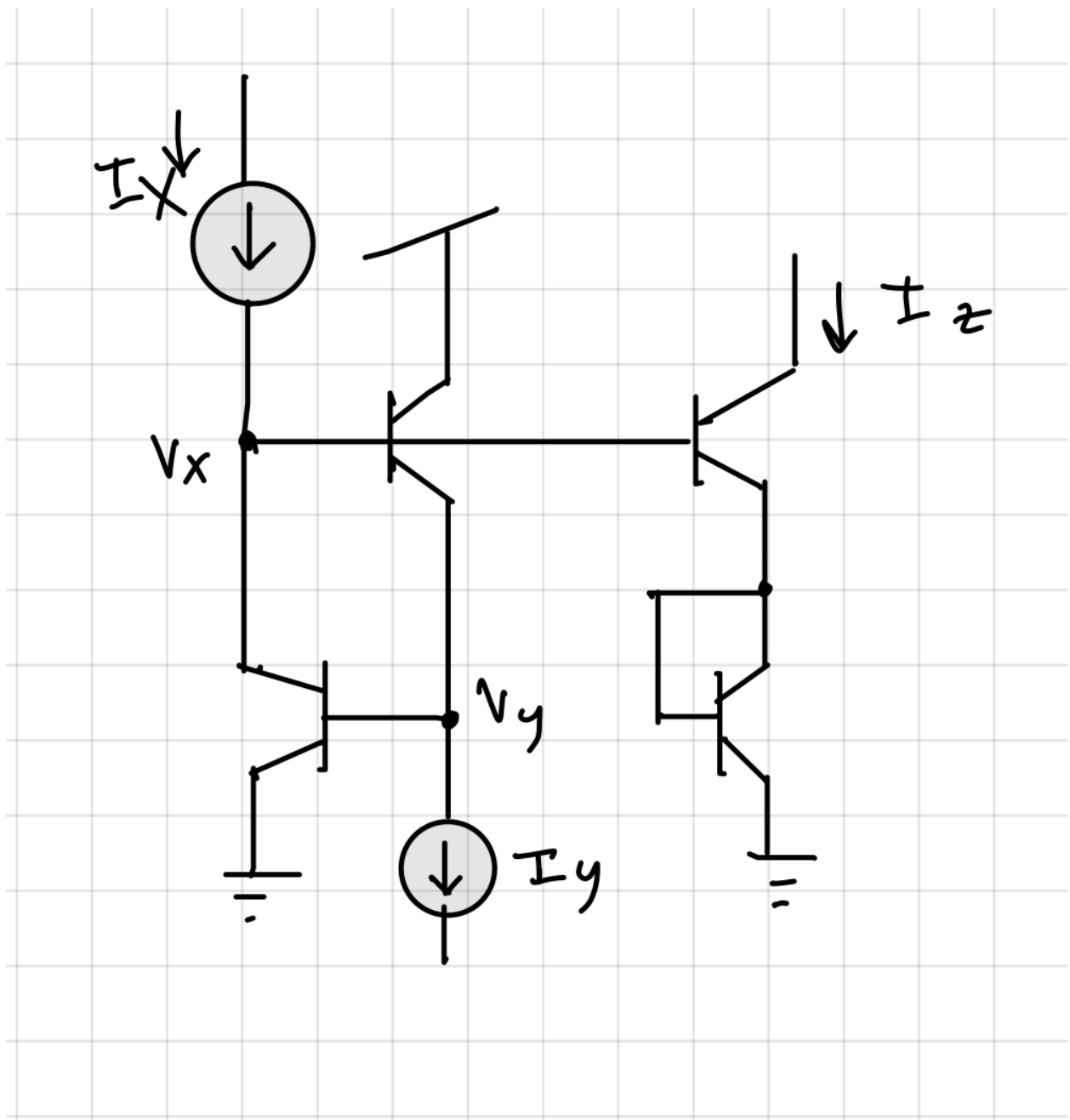


Figure 1: Circuit diagram of experiment 2

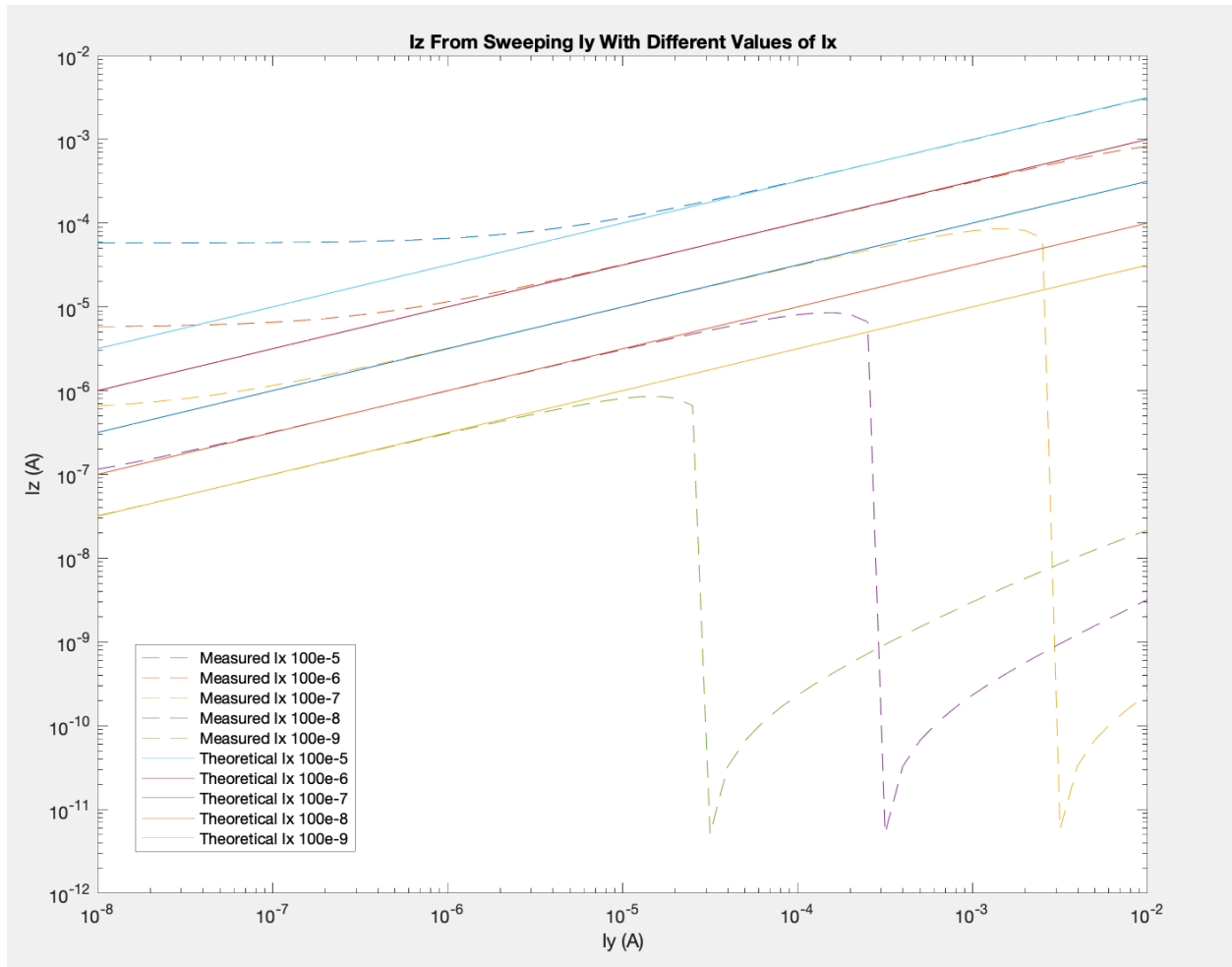


Figure 2: A plot of measured and theoretical I_z values at different I_x values with swept I_y values.

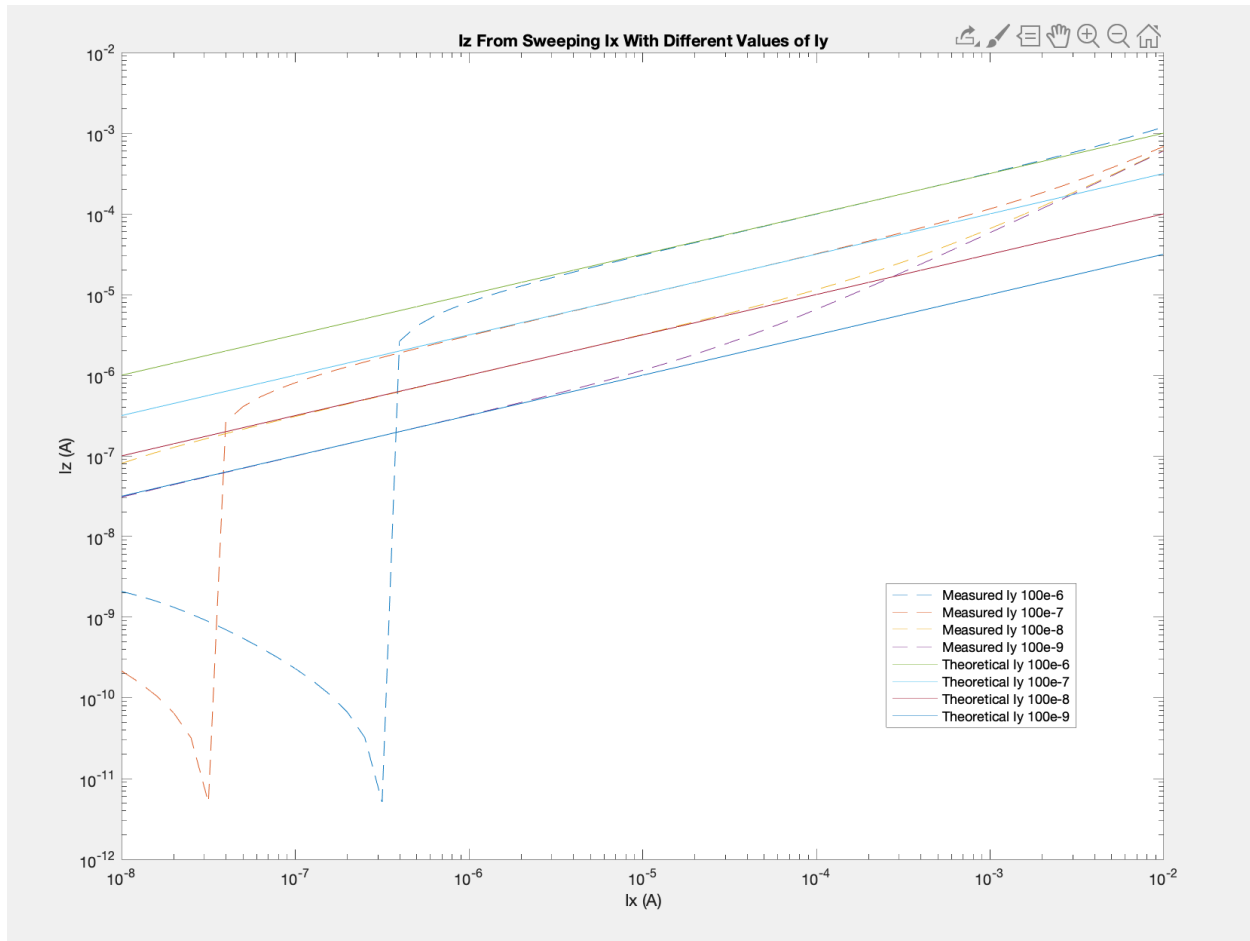


Figure 2: A plot of measured and theoretical I_z values at different I_y values with swept I_x values.

As found according to the prelab,

$$I_z = \sqrt{I_x \cdot I_y}$$

As seen in the figures above, the plots seem to follow the equation found in the prelab for some duration. They deviate from the duration at higher and lower swept values. When sweeping I_y , at higher I_y values the circuit enters saturation mode. When sweeping I_x , at lower I_x values the circuit enters saturation mode. This explains the deviations from the expected line.

Experiment 3: Translinear Circuit 2

In this experiment we constructed the circuit shown below in figure 4.

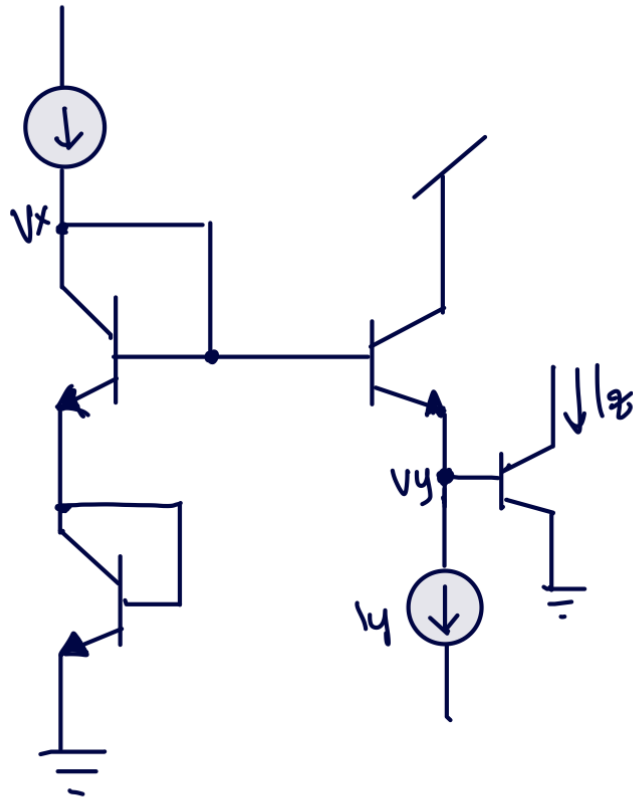


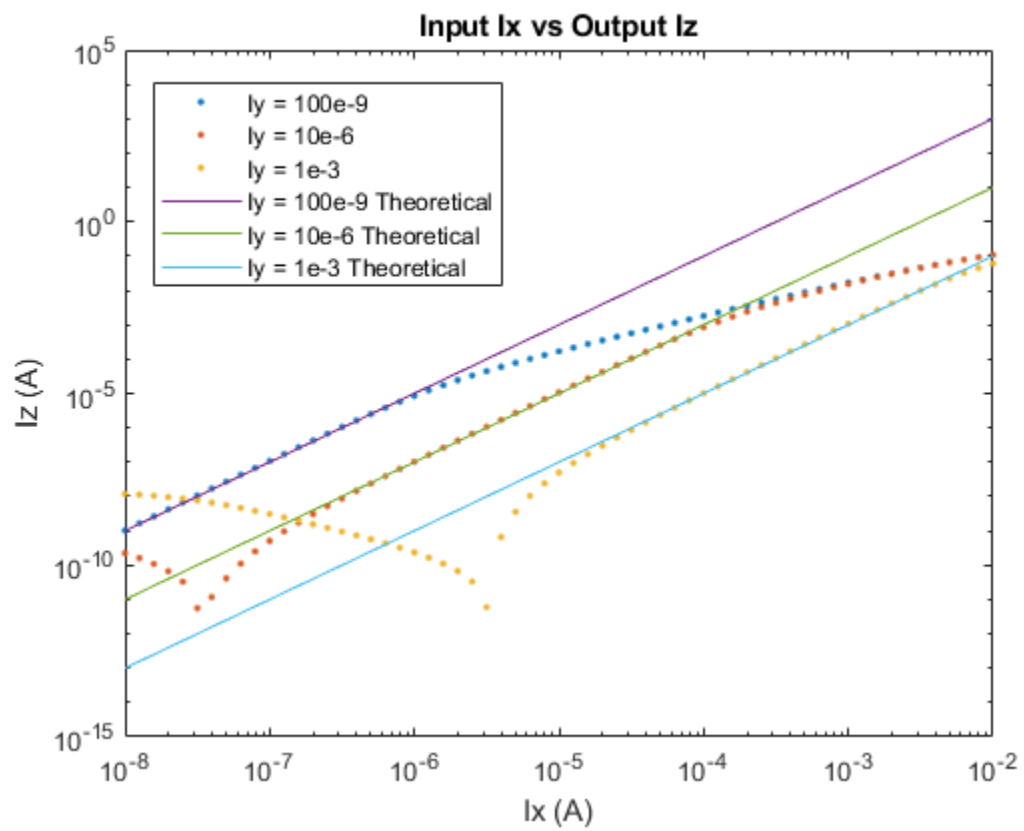
Figure 4: Circuit diagram for experiment 3

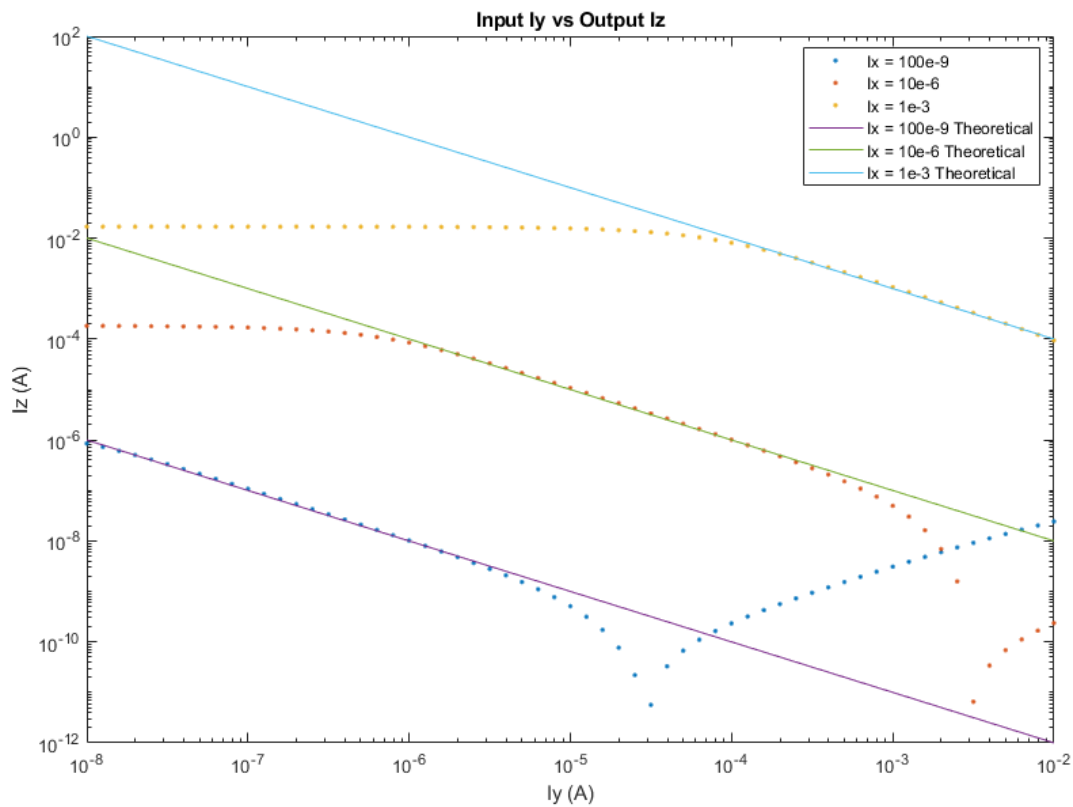
Because we have an even number of translinear elements with an equal number of clockwise and counterclockwise currents, we can evaluate the circuit using the TLP

$$\prod I_{CW} = \prod I_{CCW}$$

Applied to circuit 4.1c, we find the relationship of the currents to be

$$I_x^2 = I_y I_z$$





As shown in the graphs, the outputs follow the calculated theoretical models for current values in the middle of the input range. There are current nonlinearities on the high and low ends of the input range. This is due to the breakdown of the assumption of infinite beta. Small values of V_{be} lead to the -1 term having a non-negligible effect on forward current.