

Date: 01.08.2018

Due: 13.08.2018

Assignment
Department of Electronics and Telecommunication Engineering
Department of Electrical and Electronic Engineering
Course Title: Electronics Devices and Circuits I
Course Code: ETE 202/EEE 201
Total Marks: 30

- P.1** Use PSpice or LTspice to simulate the circuit in Figure 1. Plot v_i and v_o . Determine the value of V_{dc} of the output. Consider $f = 1$ kHz. 5

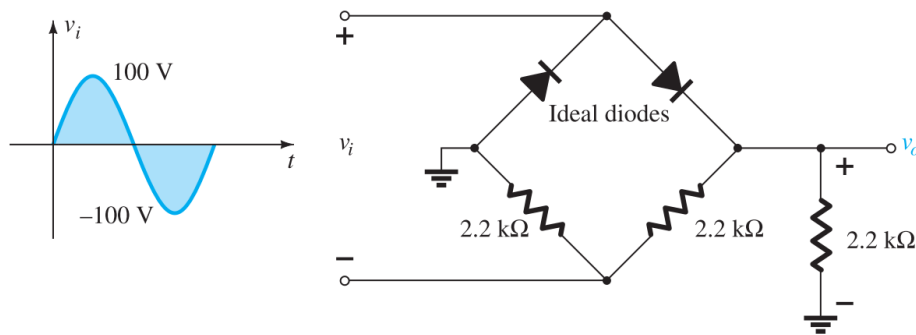


Figure 1 [for P.1]

- P.2** Design a clamper to perform the function indicated in Figure 2. Use PSpice or LTspice to simulate the circuit. Plot v_i and v_o . Consider $f = 1000$ Hz. 5

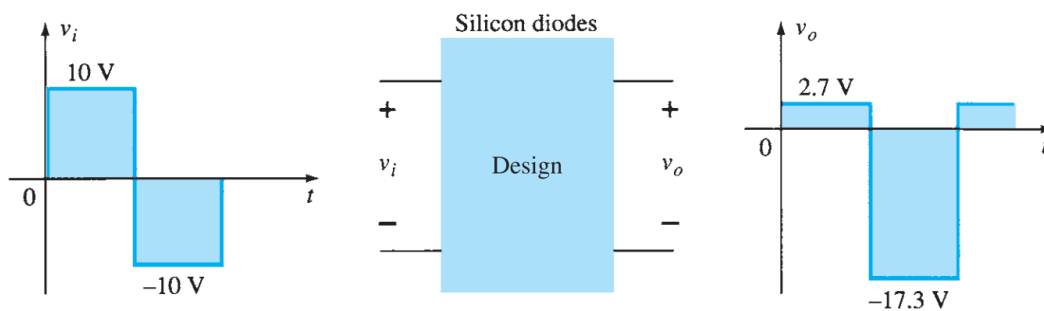


Figure 2 [for P.2]