

micro-instruction	alucontrol [2:0]	immsrc [1:0]	alusrcb [1:0]	alusrca [1:0]	aluOP [1:0]	resultsrc [1:0]	adrsrc	irwrite	memwrite	regwrite	pcwrite	address
fetch	XXX	XX	XX	XX	00	XX	0	1	0	0	0	11110
Decode	XXX	00	XX	XX	00	XX	X	0	0	0	0	11111
MemAdr (lw)	000	00	01	10	00	XX	X	0	0	0	0	00000
MemRead	XXX	00	XX	XX	XX	00	1	0	0	0	0	01110
MemWB	XXX	00	XX	XX	XX	01	X	0	0	1	0	01111
MemWrite	XXX	01	XX	XX	XX	00	1	0	1	0	0	11100
ExecuteR - add	000	XX	00	10	10	XX	X	0	0	0	0	01100
ExecuteR - add	001	XX	00	10	10	XX	X	0	0	0	0	01100
ExecuteR - and	010	XX	00	10	10	XX	X	0	0	0	0	01100
ExecuteR - or	011	XX	00	10	10	XX	X	0	0	0	0	01100
ExecuteR - slt	101	XX	00	10	10	XX	X	0	0	0	0	01100
ALUWB	XXX	XX	XX	XX	XX	00	X	0	0	1	0	01010
Executel - addi	000	XX	01	10	10	XX	X	0	0	0	0	00100
Executel - andi	001	XX	01	10	10	XX	X	0	0	0	0	00100
Executel - ori	010	XX	01	10	10	XX	X	0	0	0	0	00100
Executel - slti	101	XX	01	10	10	XX	X	0	0	0	0	00100
JAL	000	11	10	01	00	00	X	0	0	0	1	11011
BEQ	001	10	00	10	01	00	X	0	0	0	zero	11000