

Title High Level VLC Schematic		
Size A4	Number	Revision
Date: 5/07/2024	Sheet 2 of 6	
File: C:\Users\...\VLC.SchDoc	Drawn By: Navnith Reddy	

A

B

C

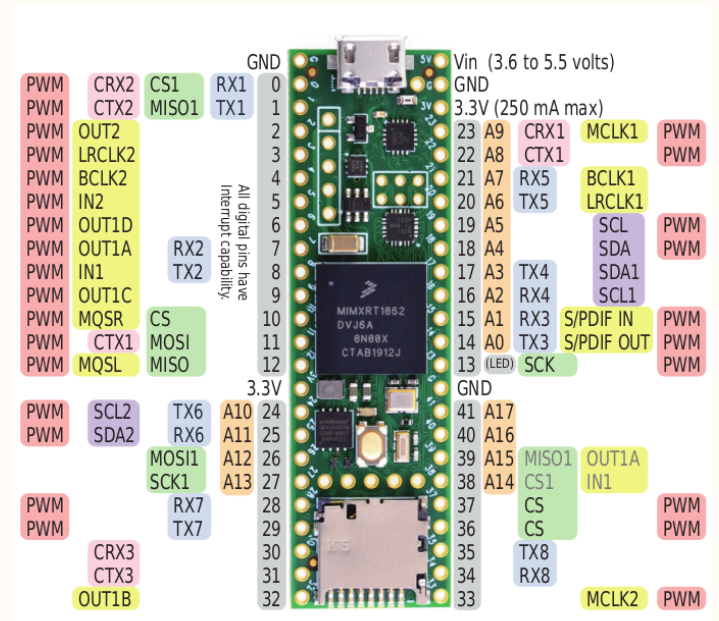
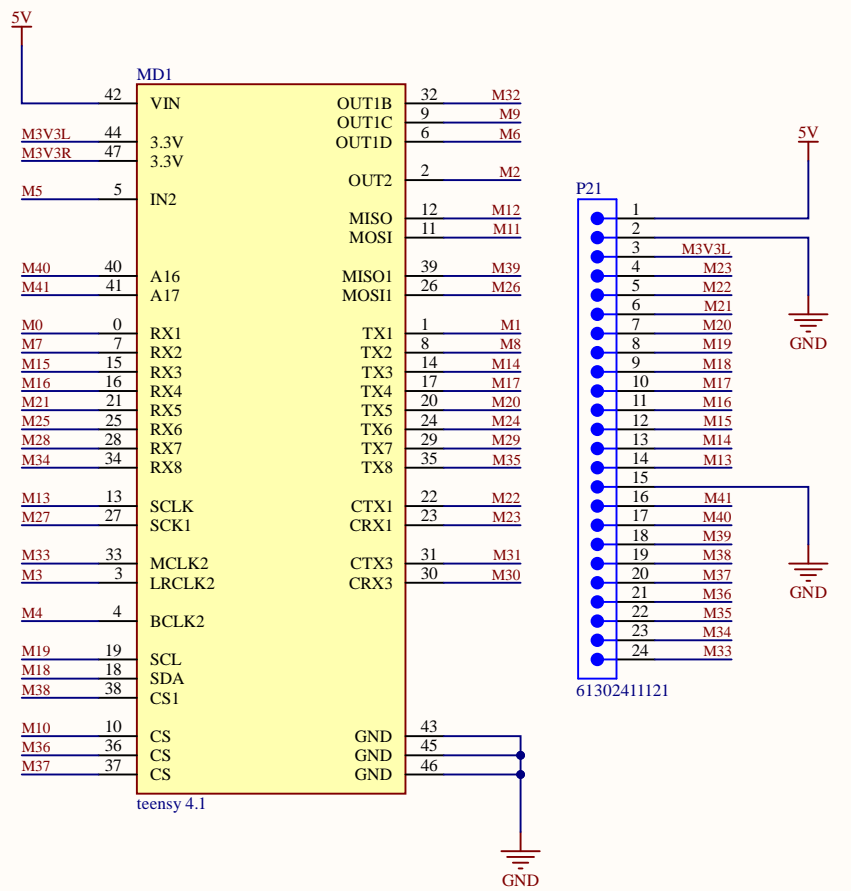
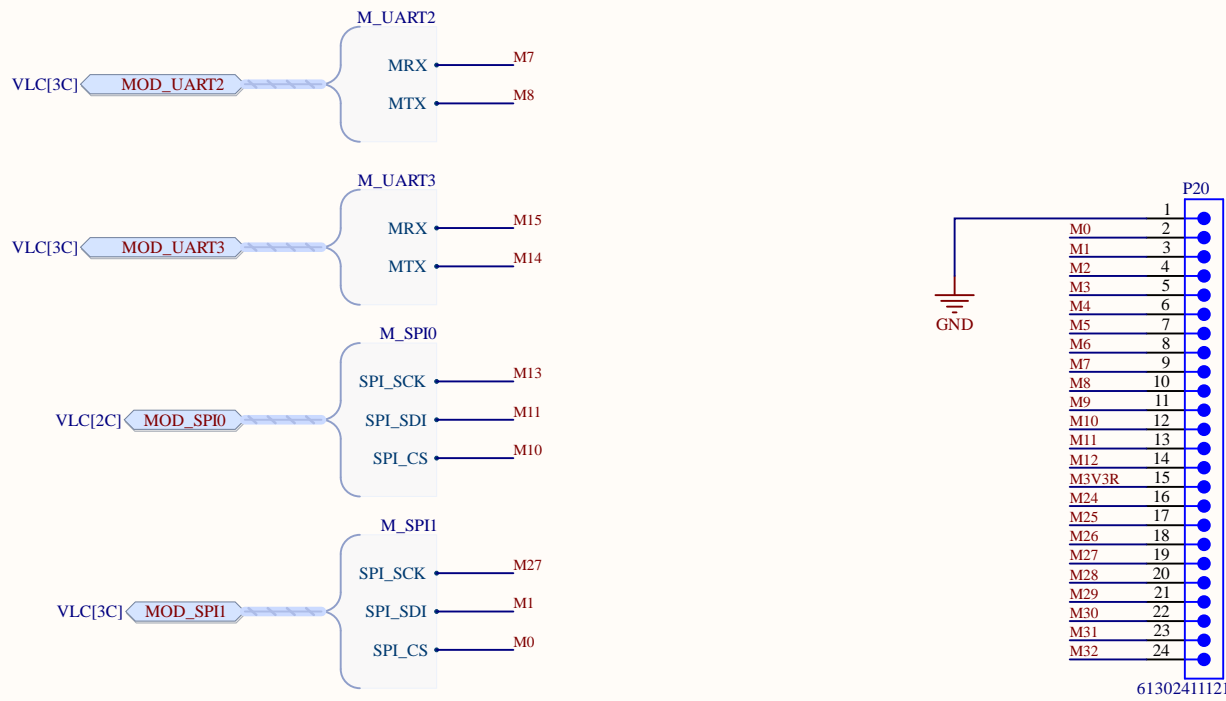
D

A

B

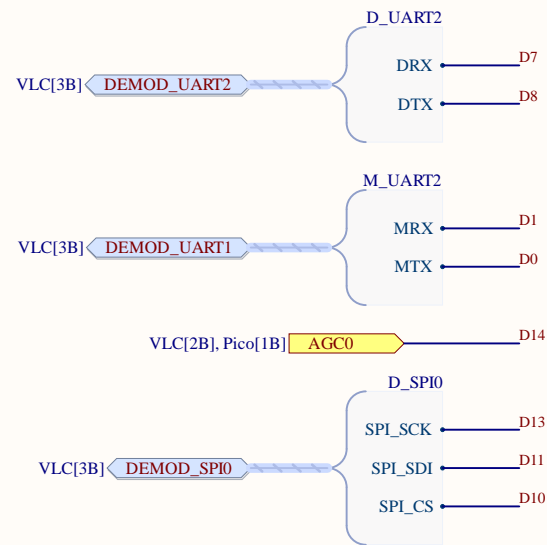
C

D

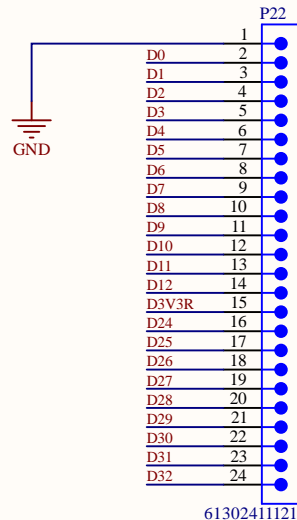


Title		
Modulation Teensy Schematic		
Size	Number	Revision
A4		
Date:	5/07/2024	Sheet 2 of 6
File:	C:\Users\...\ModTeensy.SchDoc	Drawn By: Navnith Reddy

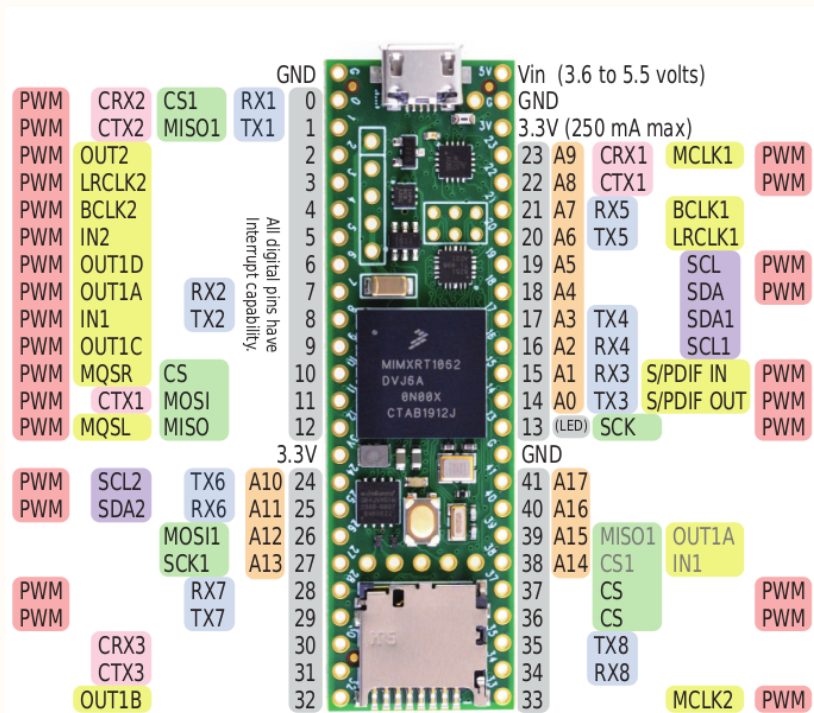
A



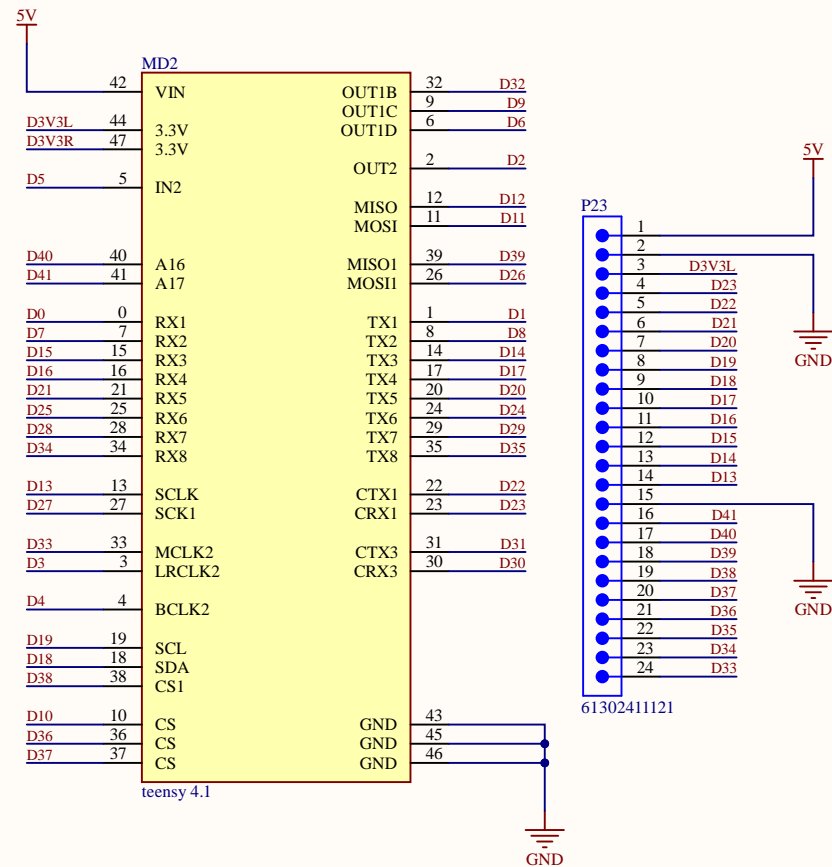
B



C



D



A

B

C

D

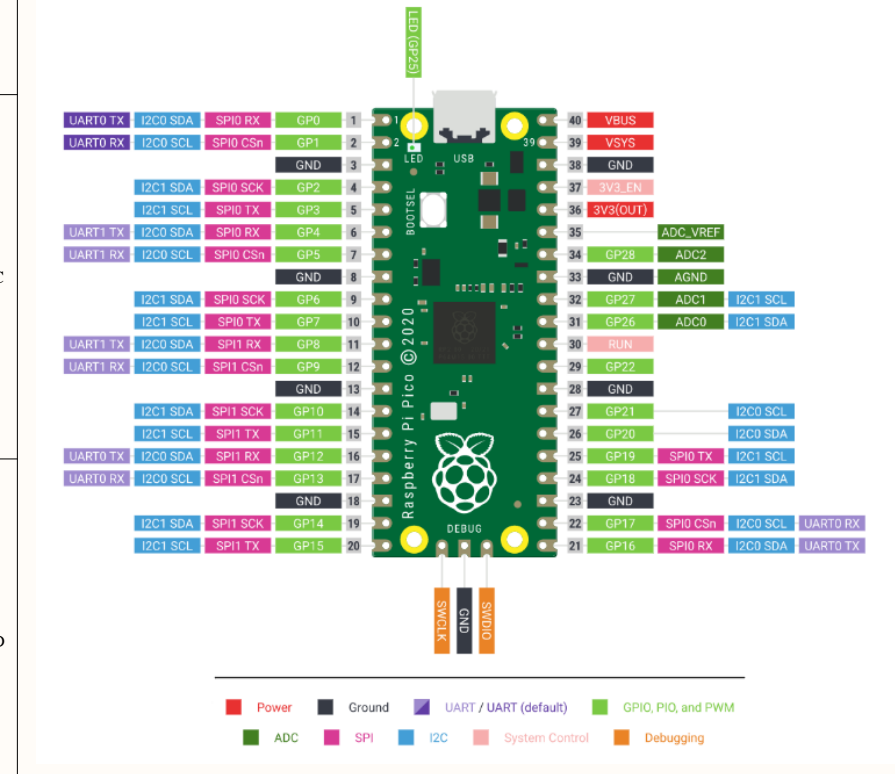
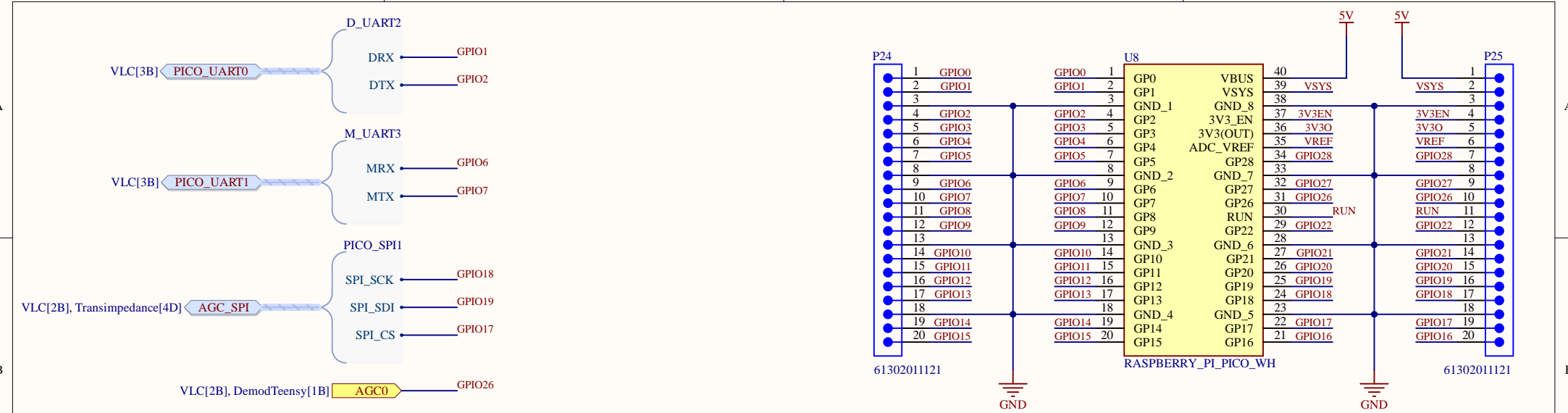
Title		
Demodulation Teensy Schematic		
Size	Number	Revision
A4		
Date:	5/07/2024	Sheet 3 of 6
File:	C:\Users\...\DemodTeensy.SchDoc	Drawn By: Navnith Reddy

A

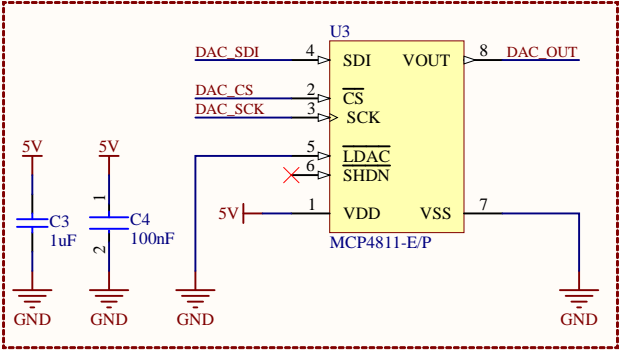
B

C

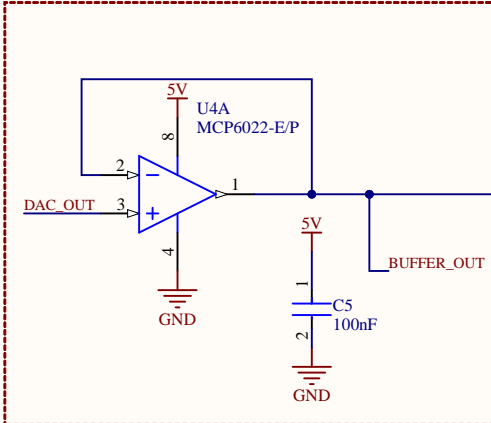
D



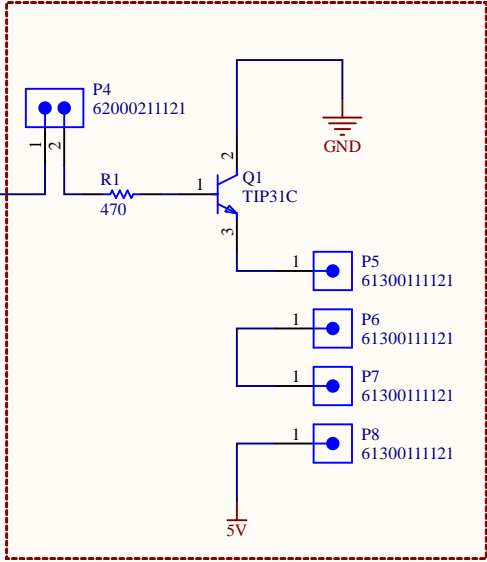
Title		
Raspberry Pi Pico Interface Schematic		
Size	Number	Revision
A4		
Date:	5/07/2024	Sheet 4 of 6
File:	C:\Users\...\Pico.SchDoc	Drawn By: Navnith Reddy



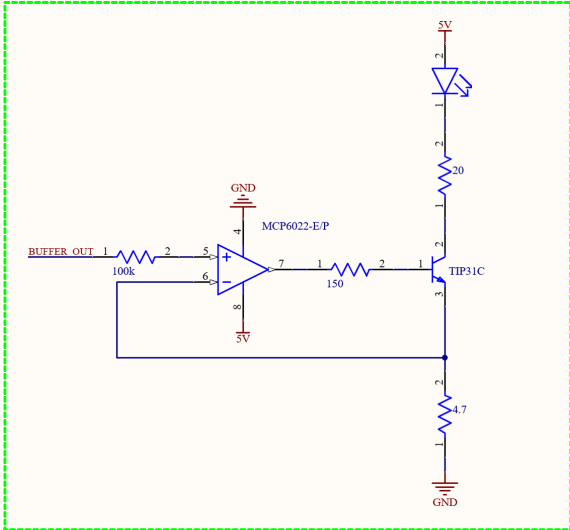
Digital to Analog Converter



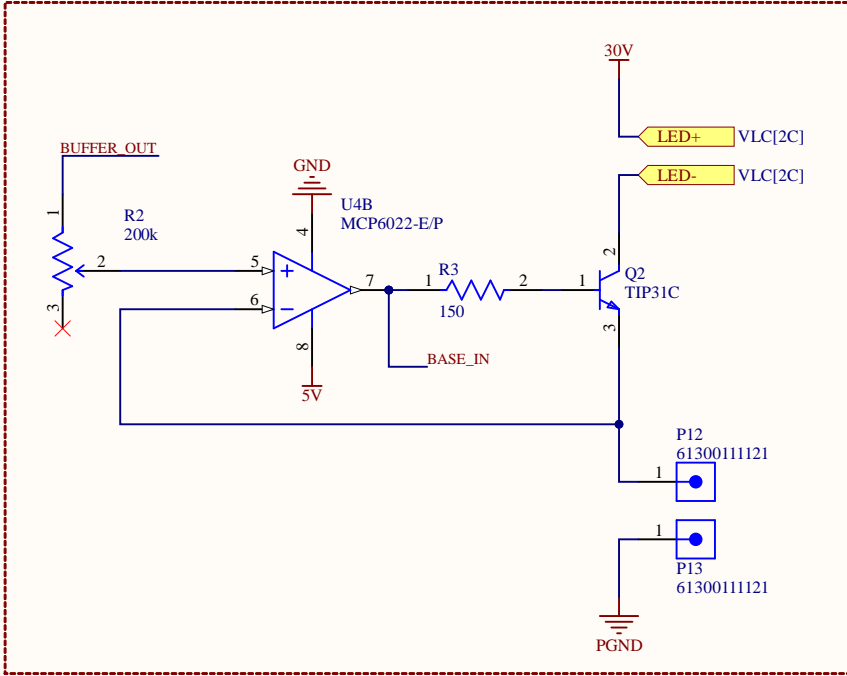
Voltage Buffer



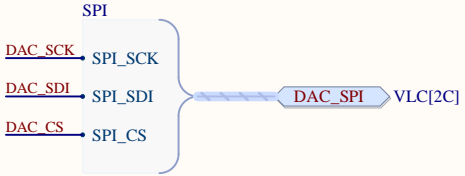
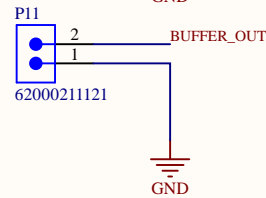
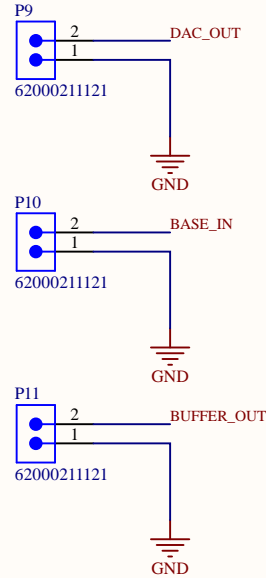
Circuit designed by the voices in my head



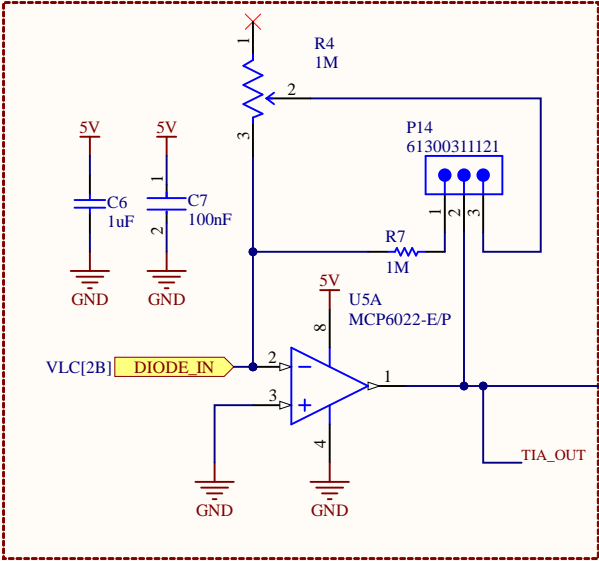
Reference Transconductance Amplifier



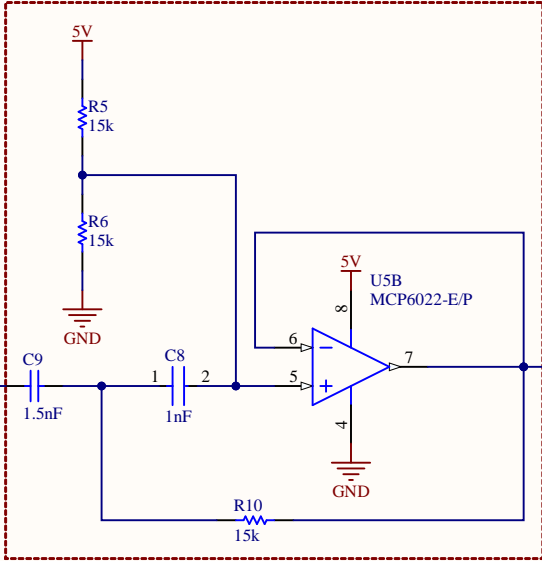
Transconductance Amplifier



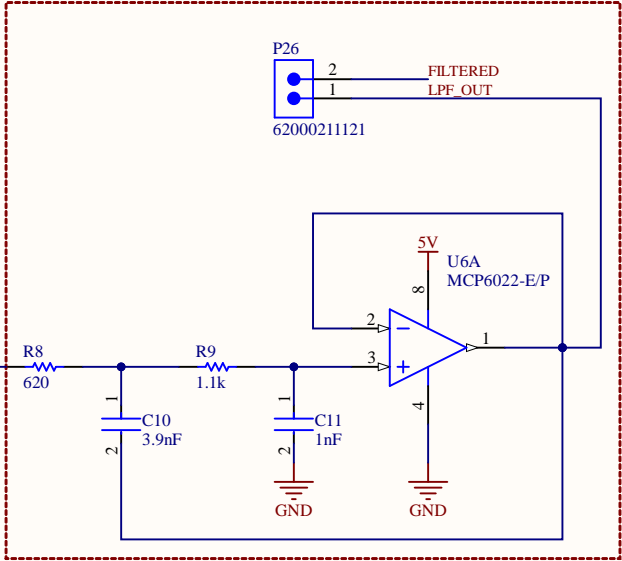
Title		
DAC and Transconductance Amplifier Schematic		
Size	Number	Revision
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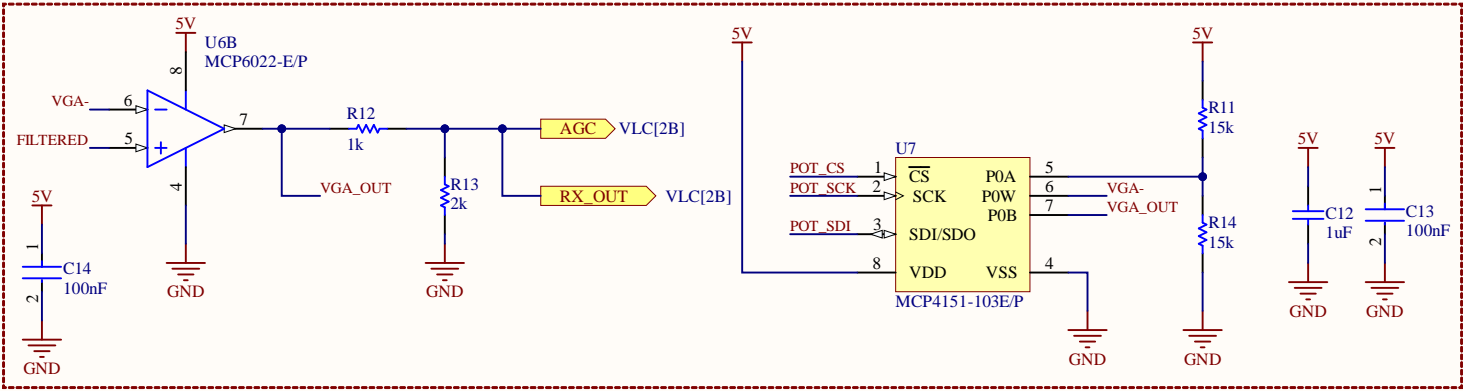
Transimpedance Amplifier



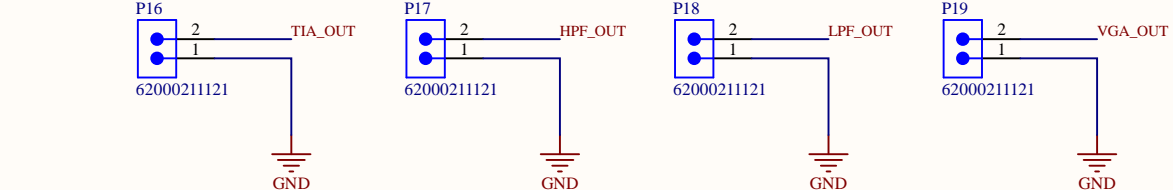
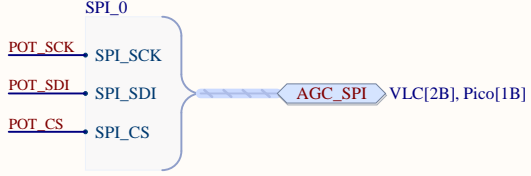
High Pass Filter designed by Nigel Kent



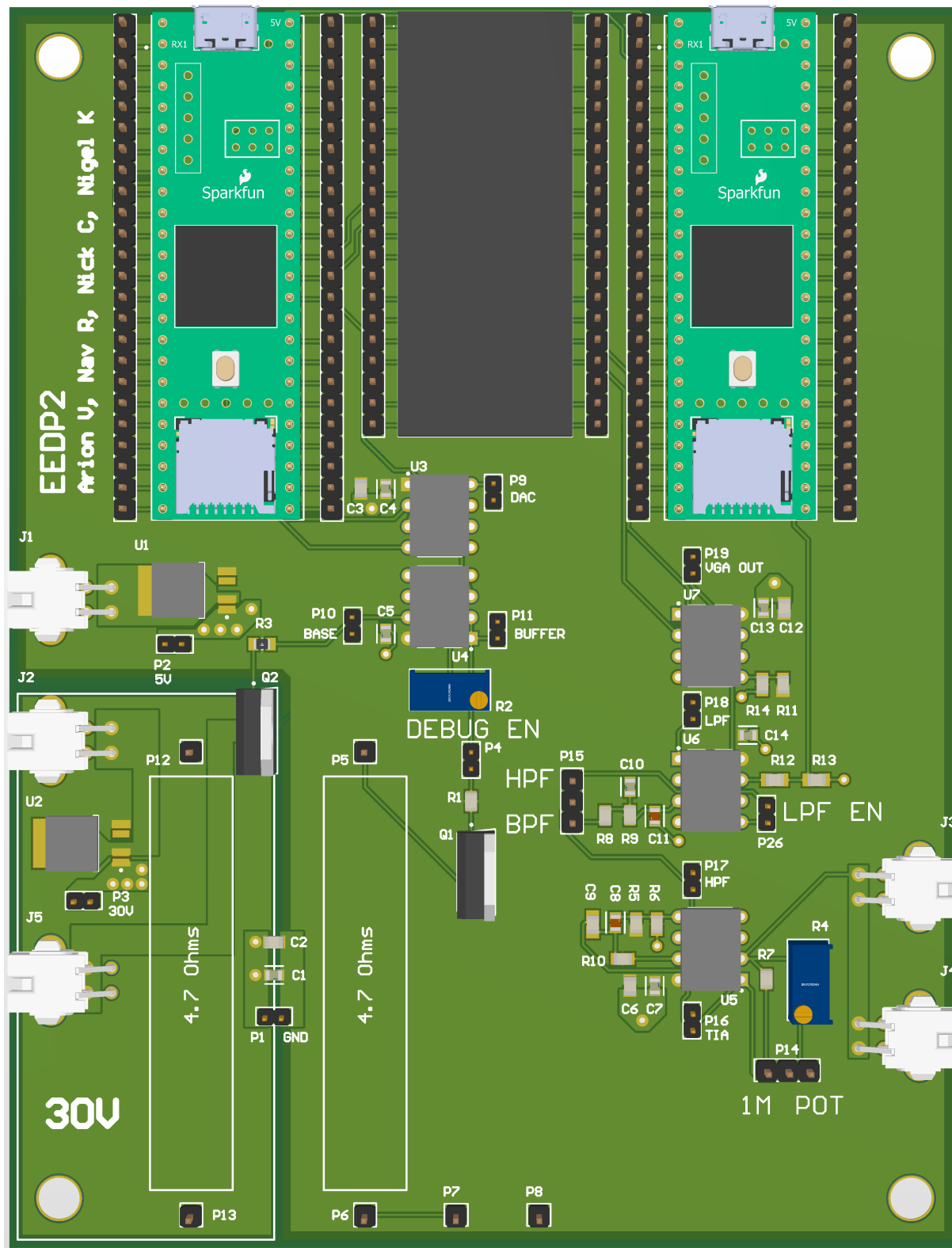
Low Pass Filter designed by Nigel Kent



Variable Gain Amplifier



Title Transimpedance Amplifier and Filter Schematics		
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EEDP2

Arion U, Nav R, Nick C, Nigel K

U1

P12

4.7 Ohms

4.7 Ohms

P6

P7

P8

P9
DAC

P11
BUFFER

DEBUG EN

HPF

BPF

P19
UGA OUT

P18
LPF

R14 R11

C13 C12

R12 R13

P26

P17
HPF

P16
TIA

1M POT

R4

P14

P15

P13

P10
BASE

C5

U3

C3 C4

U4

R3

P2

5V

Q2

P3

30V

J5

U2

J2

J1

J3

J4

J6

J7

J8

J9

J10

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EEDP2

Arion U, Nav R, Nick C, Nigel K

