

# GANESH SUNKARA

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## OBJECTIVE

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As a final year B.Tech student from SRI VASAVI ENGINEERING COLLEGE, Tadepalligudem, I bring expertise in Verilog and basic knowledge in Java, Python, and C. Combining hardware and software skills, I have experience in digital circuit design and am eager to contribute to innovative Verilog RTL design projects.

## EDUCATION

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**B.Tech in ECE**, Sri Vasavi Engineering college,Tadepalligudem Expected 2025

**Intermediate in MPC**, Narayana Junior college 2019 - 2021

**Schooling in**, Sree Bharathi Public School 2019

## SKILLS

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**Technical Skills** C,Java,Python,Verilog,VHDL

**Soft Skills** Teamwork,Problem-solving,Time management

## PROJECTS

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### IOT Based Smart Agriculture System

- Designed and implemented a comprehensive IoT system for smart agriculture, integrating ultrasonic sensors to detect approaching objects and trigger alerts via a buzzer, pH sensors to monitor soil acidity levels, and DHT sensors for environmental monitoring
- Utilized ultrasonic sensors for object detection and buzzer alert system.
- pH sensors to monitor soil acidity for optimal crop growth.
- Implemented DHT sensors for real-time environmental data collection.
- Increased crop yield through real-time monitoring of soil conditions.

## CERTIFICATION

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### Nptel online certification

- Hardware Modelling using Verilog Jun-Sep(23)
- Microelectronics-Devices to circuits Jul-Oct(23)
- Introduction to Embedded System Jan-Apr(24)
- Design and Analysis of VLSI Subsystems Jan-Apr(24)

## INTERN

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**BIST Technologies - VLSI Design for Testability** May-July 2024

- Successfully implemented DFT in multiple digital circuits enhancing fault detection and repair capabilities.
- Successfully troubleshoot and resolve DFT-related issues,ensuring robust design validation.

**SkillTronics - Arduino Uno Projects** May-July 2023

- Gained practical experience with the Arduino Uno board by connecting and programming various sensors, including ultrasonic, DHT, and IR, to create functional prototypes and gather data for testing and analysis.,