Assembler Directives

Selected [MIPS](#) Assembler Directives

- Consists of an integer processing unit (CPU) and a collection of coprocessors

Co Processors

- Coprocessor 0 - handles exceptions and interrupts
- Coprocessor 1 - floating point unit

Load and store instructions

- Most load and store instructions operate only on aligned data
- A quantity is aligned if its memory address is a multiple of its size in bytes