

CSE345 Digital Logic Design

Project-1

4-bit Binary to Odd Parity Code Converter

A 4-bit code converter for odd parity converts the 4-bit input to 5-bit output so that odd parity is ensured.

Project-2

4-bit Binary to Even Parity Code Converter

A 4-bit code converter for even parity converts the 4-bit input to 5-bit output so that even parity is ensured.

Marks Distribution

Assessment Area	Mark
C3: Cognitive: Applying	2
C6: Cognitive: Creating	5
P2: Psychomotor: Manipulation	1
P3: Psychomotor: Precision	1
A2: Affective: Responding	1
Total	10

Project Report will include

1. Problem Statement
2. Design Details
3. Circuit Diagram
4. Structural and Behavioral Verilog Code and Simulation Results

Note: Odd digit number will undertake Project-1 and even digit number will undertake Project-2.