

# 1. Description

## 1.1. Project

Project Name	EFG_LPWR
Board Name	custom
Generated with:	STM32CubeMX 6.10.0
Date	08/01/2024

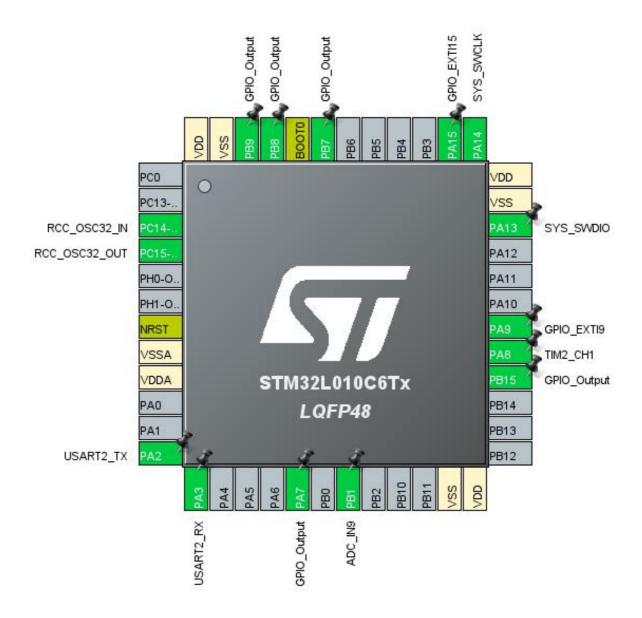
### 1.2. MCU

MCU Series	STM32L0
MCU Line	STM32L0x0 Value Line
MCU name	STM32L010C6Tx
MCU Package	LQFP48
MCU Pin number	48

## 1.3. Core(s) information

Core(s)	Arm Cortex-M0+

## 2. Pinout Configuration

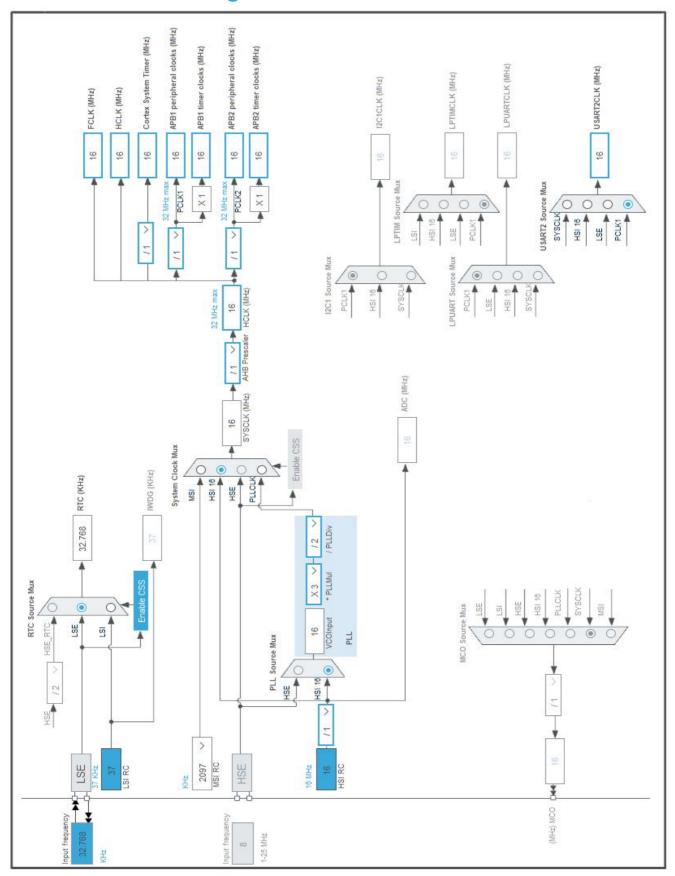


# 3. Pins Configuration

Pin Number LQFP48	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
3	PC14-OSC32_IN	I/O	RCC_OSC32_IN	
4	PC15-OSC32_OUT	I/O	RCC_OSC32_OUT	
7	NRST	Reset		
8	VSSA	Power		
9	VDDA	Power		
12	PA2	I/O	USART2_TX	
13	PA3	I/O	USART2_RX	
17	PA7 *	I/O	GPIO_Output	
19	PB1	I/O	ADC_IN9	
23	VSS	Power		
24	VDD	Power		
28	PB15 *	I/O	GPIO_Output	
29	PA8	I/O	TIM2_CH1	
30	PA9	I/O	GPIO_EXTI9	
34	PA13	I/O	SYS_SWDIO	
35	VSS	Power		
36	VDD	Power		
37	PA14	I/O	SYS_SWCLK	
38	PA15	I/O	GPIO_EXTI15	
43	PB7 *	I/O	GPIO_Output	
44	BOOT0	Boot		
45	PB8 *	I/O	GPIO_Output	
46	PB9 *	I/O	GPIO_Output	
47	VSS	Power		
48	VDD	Power		

<sup>\*</sup> The pin is affected with an I/O function

## 4. Clock Tree Configuration



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# 5. Software Project

## 5.1. Project Settings

Name	Value	
Project Name	EFG_LPWR	
Project Folder	C:\Work Folder\Projects\STM32 Learning Projects\Workspace\EFG_LPWR	
Toolchain / IDE	STM32CubeIDE	
Firmware Package Name and Version	STM32Cube FW_L0 V1.12.2	
Application Structure	Advanced	
Generate Under Root	Yes	
Do not generate the main()	No	
Minimum Heap Size	0x200	
Minimum Stack Size	0x400	

## 5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Keep User Code when re-generating	Yes
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power	No
consumption)	
Enable Full Assert	No

## 5.3. Advanced Settings - Generated Function Calls

Rank	Function Name	Peripheral Instance Name	
1	SystemClock_Config	RCC	
2	MX_GPIO_Init	GPIO	
3	MX_ADC_Init	ADC	
4	MX_RTC_Init	RTC	
5	MX_TIM2_Init	TIM2	
6	MX_USART2_UART_Init	USART2	
7	MX_TIM21_Init	TIM21	

# 1. Power Consumption Calculator report

#### 1.1. Microcontroller Selection

Series	STM32L0
Line	STM32L0x0 Value Line
MCU	STM32L010C6Tx
Datasheet	DS12324_Rev1

### 1.2. Parameter Selection

Temperature	25
Vdd	3.0

### 1.3. Battery Selection

Battery	Li-SOCL2(AAA700)
Capacity	700.0 mAh
Self Discharge	0.08 %/month
Nominal Voltage	3.6 V
Max Cont Current	10.0 mA
Max Pulse Current	30.0 mA
Cells in series	1
Cells in parallel	1

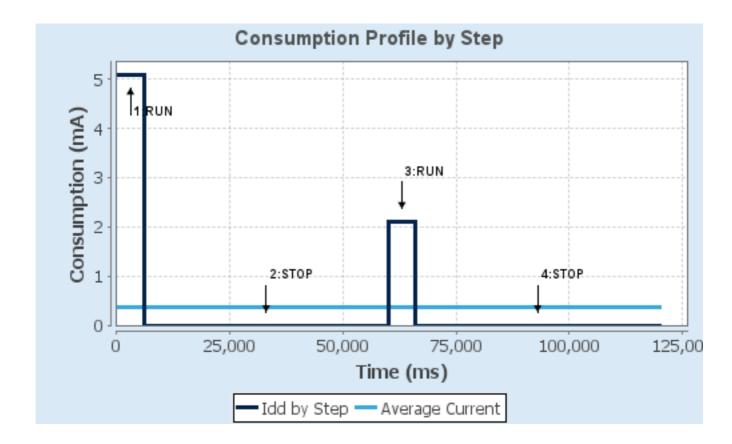
## 1.4. Sequence

Step	Step1	Step2	Step3	Step4
Mode	RUN	STOP	RUN	STOP
Vdd	3.0	3.0	3.0	3.0
Voltage Source	Battery	Battery	Battery	Battery
Range	Range1-High	NoRange	Range1-High	NoRange
Fetch Type	FLASH	n/a	RAM	n/a
CPU Frequency	32 MHz	0 Hz	16 MHz	0 Hz
Clock	HSI PLL	ALL CLOCKS	HSEBYP	ALL CLOCKS
Configuration		OFF		OFF
Clock Source	16 MHz	0 Hz	16 MHz	0 Hz
Frequency				
Peripherals				
Additional	0 mA	0 mA	0 mA	0 mA
Cons.				
Average	5.1 mA	380 nA	2.1 mA	380 nA
Current				
Duration	6 s	54 s	6 s	54 s
DMIPS	30.0	0.0	15.0	0.0
Ta Max	104.13	105	104.64	105
Category	In DS Table	In DS Table	In DS Table	In DS Table

### 1.5. Results

Sequence Time	120 s	Average Current	360.34 µA
Battery Life	2 months, 19	Average DMIPS	22.8 DMIPS
	days, 22 hours		

### 1.6. Chart



## 2. Peripherals and Middlewares Configuration

## 2.1. ADC

## mode: IN9

## 2.1.1. Parameter Settings:

#### ADC\_Settings:

Clock Prescaler Synchronous clock mode divided by 1

Resolution ADC 12-bit resolution

Data Alignment Right alignment

Scan Direction Forward
Continuous Conversion Mode Disabled
Discontinuous Conversion Mode Disabled
DMA Continuous Requests Disabled

End Of Conversion Selection End of single conversion

Overrun behaviour Overrun data preserved

Low Power Auto WaitDisabledLow Frequency ModeDisabledAuto OffDisabledOversampling ModeDisabled

#### ADC\_Regular\_ConversionMode:

Sampling Time 160.5 Cycles \*

External Trigger Conversion Source Regular Conversion launched by software

External Trigger Conversion Edge None

WatchDog:

Enable Analog WatchDog Mode false

#### 2.2. RCC

#### Low Speed Clock (LSE): Crystal/Ceramic Resonator

#### 2.2.1. Parameter Settings:

#### **System Parameters:**

VDD voltage (V) 3.3
Buffer Cache Enabled
Prefetch Disabled
Preread Enabled

Flash Latency(WS) 0 WS (1 CPU cycle)

**RCC Parameters:** 

HSI Calibration Value 16

MSI Calibration Value 0
HSE Startup Timout Value (ms) 100
LSE Startup Timout Value (ms) 5000

LSE Drive Capability

LSE oscillator low drive capability

**Power Parameters:** 

Power Regulator Voltage Scale Power Regulator Voltage Scale 1

#### 2.3. RTC

mode: Activate Clock Source mode: Activate Calendar Alarm A: Internal Alarm A 2.3.1. Parameter Settings:

#### General:

Hour Format Hourformat 24

Asynchronous Predivider value 127
Synchronous Predivider value 255

**Calendar Time:** 

Data Format BCD data format

Hours 15 \*
Minutes 49 \*
Seconds 0

Day Light Saving: value of hour adjustment Daylightsaving None Store Operation Storeoperation Reset

#### **Calendar Date:**

Week Day Wednesday \*

Month July \*
Date 26 \*
Year 24 \*

#### Alarm A:

Hours 0
Minutes 0
Seconds 0
Sub Seconds 0

Alarm Mask Date Week day

Alarm Mask Hours

Enable \*

Alarm Mask Minutes

Disable

Alarm Mask Seconds

Disable

Alarm Sub Second Mask

All Alarm SS fields are masked.

Alarm Date Week Day Sel Date
Alarm Date 1

2.4. SYS

mode: Debug Serial Wire Timebase Source: SysTick

2.5. TIM2

**Channel1: PWM Generation CH1** 

2.5.1. Parameter Settings:

**Counter Settings:** 

auto-reload preload

Prescaler (PSC - 16 bits value)

Counter Mode

Counter Period (AutoReload Register - 16 bits value)

Internal Clock Division (CKD)

No Division

Trigger Output (TRGO) Parameters:

Master/Slave Mode (MSM bit)

Disable (Trigger input effect not delayed)

Disable

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

**PWM Generation Channel 1:** 

Mode PWM mode 1

Pulse (16 bits value) 0

Output compare preload Enable
Fast Mode Disable
CH Polarity High

2.6. TIM21

**Clock Source : Internal Clock** 

2.6.1. Parameter Settings:

**Counter Settings:** 

Prescaler (PSC - 16 bits value) 31 \*
Counter Mode Up

Counter Period (AutoReload Register - 16 bits value ) 49999 \*

Internal Clock Division (CKD)

auto-reload preload

Disable

**Trigger Output (TRGO) Parameters:** 

Master/Slave Mode (MSM bit) Disable (Trigger input effect not delayed)

Trigger Event Selection Reset (UG bit from TIMx\_EGR)

#### 2.7. **USART2**

#### **Mode: Asynchronous**

#### 2.7.1. Parameter Settings:

#### **Basic Parameters:**

Baud Rate 115200

Word Length 8 Bits (including Parity)

Parity None Stop Bits 1

**Advanced Parameters:** 

Data Direction Receive and Transmit

Over Sampling 16 Samples
Single Sample Disable

**Advanced Features:** 

TX Pin Active Level Inversion Disable
RX Pin Active Level Inversion Disable
Data Inversion Disable
TX and RX Pins Swapping Disable
Overrun Enable
DMA on RX Error Enable
MSB First Disable

#### \* User modified value

# 3. System Configuration

## 3.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
ADC	PB1	ADC_IN9	Analog mode	No pull-up and no pull-down	n/a	
RCC	PC14- OSC32_IN	RCC_OSC32_IN	n/a	n/a	n/a	
	PC15- OSC32_OU T	RCC_OSC32_O UT	n/a	n/a	n/a	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14	SYS_SWCLK	n/a	n/a	n/a	
TIM2	PA8	TIM2_CH1	Alternate Function Push Pull	No pull-up and no pull-down	Low	
USART2	PA2	USART2_TX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
	PA3	USART2_RX	Alternate Function Push Pull	No pull-up and no pull-down	Very High	
GPIO	PA7	GPIO_Output	Output Push Pull	Pull-down *	Low	
	PB15	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PA9	GPIO_EXTI9	External Interrupt Mode with Falling edge trigger detection	No pull-up and no pull-down	n/a	
	PA15	GPIO_EXTI15	External Interrupt  Mode with  Rising/Falling edge	No pull-up and no pull-down	n/a	
	PB7	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	
	PB9	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	

## 3.2. DMA configuration

nothing configured in DMA service

## 3.3. NVIC configuration

## 3.3.1. NVIC

Interrupt Table	Enable	Preenmption Priority	SubPriority	
Non maskable Interrupt	true	0	0	
Hard fault interrupt	true	0	0	
System service call via SWI instruction	true	0	0	
Pendable request for system service	true	0	0	
System tick timer	true	3	0	
RTC global interrupt through EXTI lines 17, 19 and 20 and LSE CSS interrupt through EXTI line 19	true	0	0	
RCC global interrupt	true	0	0	
EXTI line 4 to 15 interrupts	true	0	0	
TIM2 global interrupt	true	0	0	
TIM21 global interrupt	true	0	0	
Flash and EEPROM global interrupt	unused			
ADC global interrupt	unused			
USART2 global interrupt / USART2 wake-up interrupt through EXTI line 26	unused			

## 3.3.2. NVIC Code generation

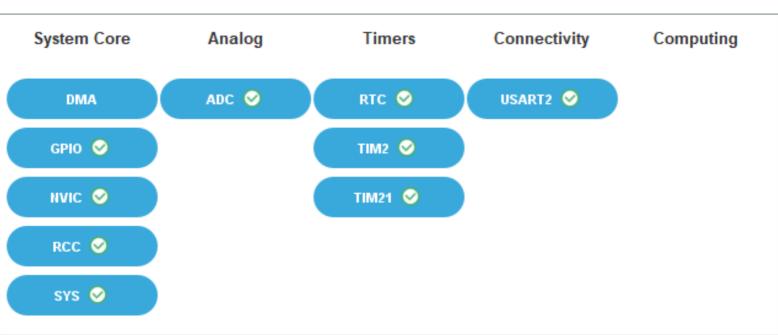
Enabled interrupt Table	Select for init sequence ordering	Generate IRQ handler	Call HAL handler
Non maskable Interrupt	false	true	false
Hard fault interrupt	false	true	false
System service call via SWI instruction	false	true	false
Pendable request for system service	false	true	false
System tick timer	false	true	true
RTC global interrupt through EXTI lines 17, 19 and 20 and LSE CSS interrupt through EXTI line 19	false	true	true
RCC global interrupt	false	true	false
EXTI line 4 to 15 interrupts	false	true	true
TIM2 global interrupt	false	true	true
TIM21 global interrupt	false	true	true

#### \* User modified value

## 4. System Views

- 4.1. Category view
- 4.1.1. Current





## 5. Docs & Resources

Type Link

IBIS models https://www.st.com/resource/en/ibis\_model/stm32l0\_ibis.zip

System View https://www.st.com/resource/en/svd/stm32l0-svd.zip

Description

Presentations https://www.st.com/resource/en/product\_presentation/gt\_stm32f0-l0.pdf

Presentations https://www.st.com/resource/en/product\_presentation/stm32-

stm8\_embedded\_software\_solutions.pdf

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tools\_portfolio.pdf

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- Application Notes https://www.st.com/resource/en/application\_note/an4013-stm32-crossseries-timer-overview-stmicroelectronics.pdf
- Application Notes https://www.st.com/resource/en/application\_note/an4221-i2c-protocol-used-in-the-stm32-bootloader-stmicroelectronics.pdf
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- firewall-embedded-in-stm32l0l4l4-series-mcus-for-secure-access-to-sensitive-parts-of-code-and-data-stmicroelectronics.pdf
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