CSC322: Spring 2018

Date

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Computer Organization Lab

February 9, 2018 1/1 Lab 4: Name: ID Logic Design Nazareth H. Keshishian 201701143



1 Introduction

During the lab 4, we were introduced to the Logic Design.

2 Discussion

We first started learning logical operators such as AND/OR/NAND/NOR/XOR and their functions in a logic board. Then using a specific program, we created a circuit with these logic gates. We designed in a way that the circuit will add the 3 inputs and output them after being executed. The output can be 1(true) or 0(false).

3 Results Analysis

After running the program, we analyzed the output which contained 1s and 0s (according to the inputs).

4 Conclusion

During the lab 4, we studied the basics of Logic Design using a simulator.