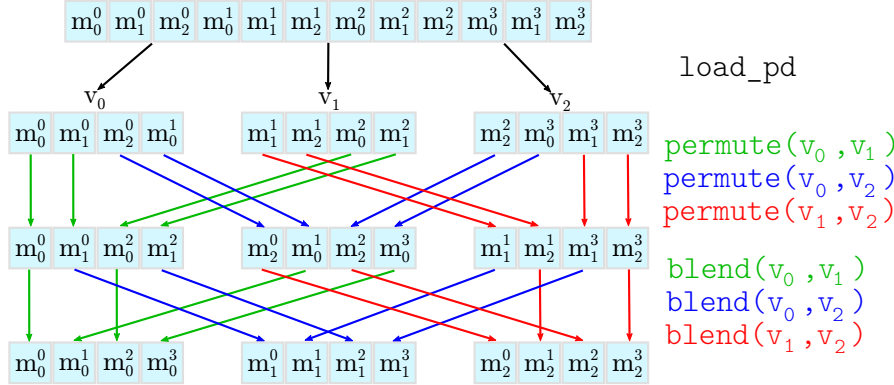


a) SSE3



b) AVX