1. (Completed at home)
2. (Completed at home)
3. A = 0x83F1 and B = 0x0FA50
   1. 0x
   2. 0x
   3. 0x
   4. 0x
   5. 0x
   6. 0x
   7. 0x
   8. 0x
4. SPI Control Reg 1

A screenshot of a cell phone

Description automatically generated

* 1. Clear SPI Enabled: REG = REG & ~0x0040
  2. Enable 16-Bit CRC: REG = REG | 0x1000
  3. Set the Frame Format to MSB first: REG = REG & ~0x00800
  4. Enable Full Duplex: REG = REG & ~0x0400
  5. Enable Master Configuration: REG = REG & 0x0004
  6. Set the Clock Polarity to 0 when idle: REG = REG & ~0x0002
  7. Enable Hardware CRC Calculation: REG = REG & 0x2000
  8. Set SPI Enabled: REG = REG & 0x0040

1. Register definitions
   1. Flash access control register

Table, calendar

Description automatically generated

* 1. DAC control register

A close up of a keyboard

Description automatically generated

* 1. GPIO port mode register

Table

Description automatically generated

* 1. RTC time register

Table, calendar

Description automatically generated

* 1. I2C control register 1

Table

Description automatically generated

* 1. USART control register 1

Table

Description automatically generated