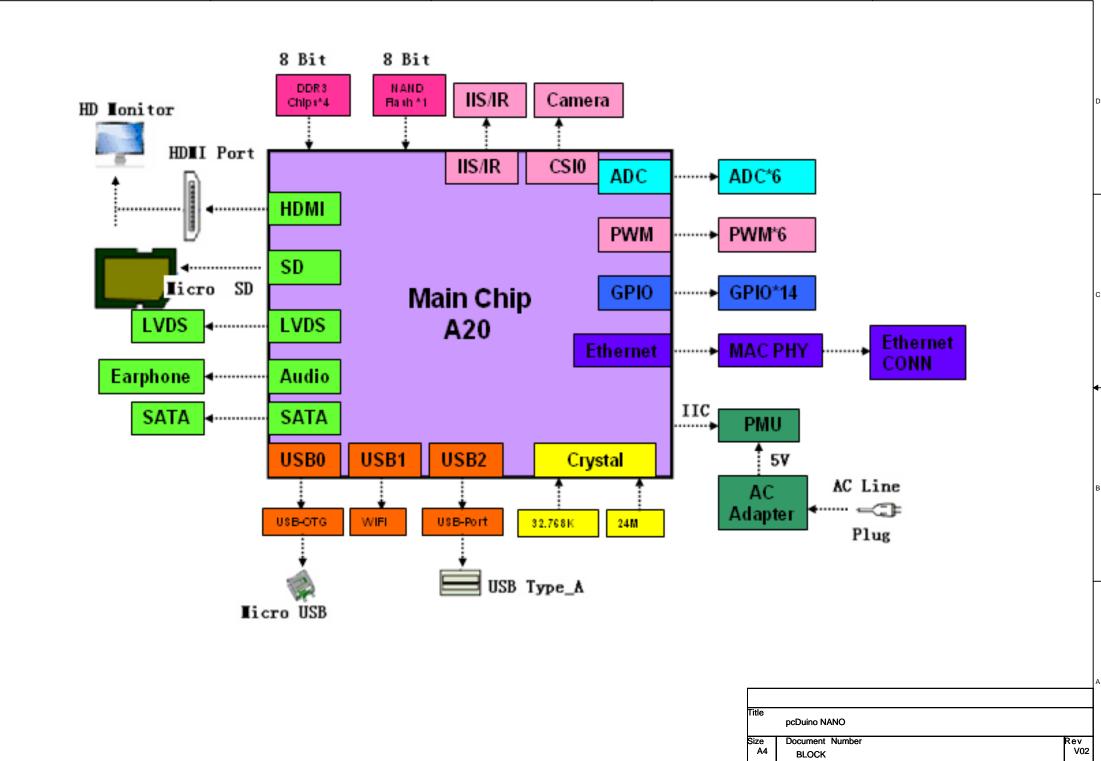
Description Rev Date Drawn 2014-06-13 V01 First release V02 2014-07-23 Modify ETH RXCLK net pcDuino NANO Document Number Rev V02

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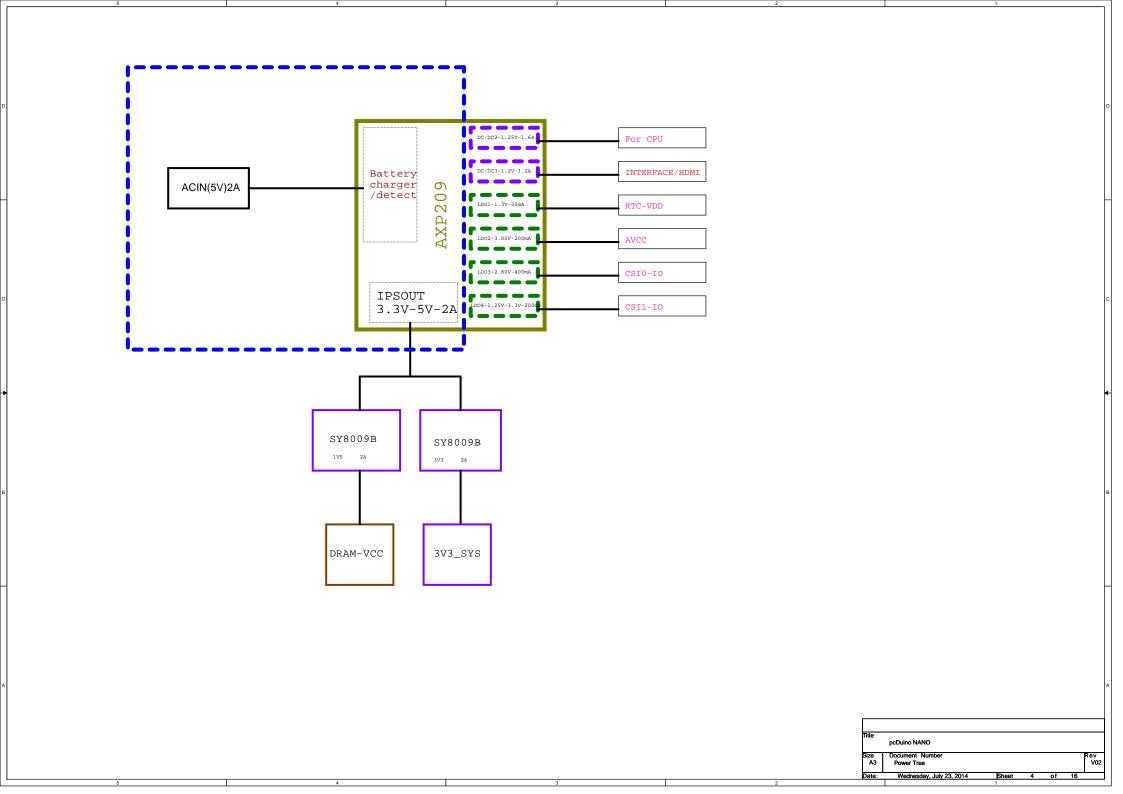
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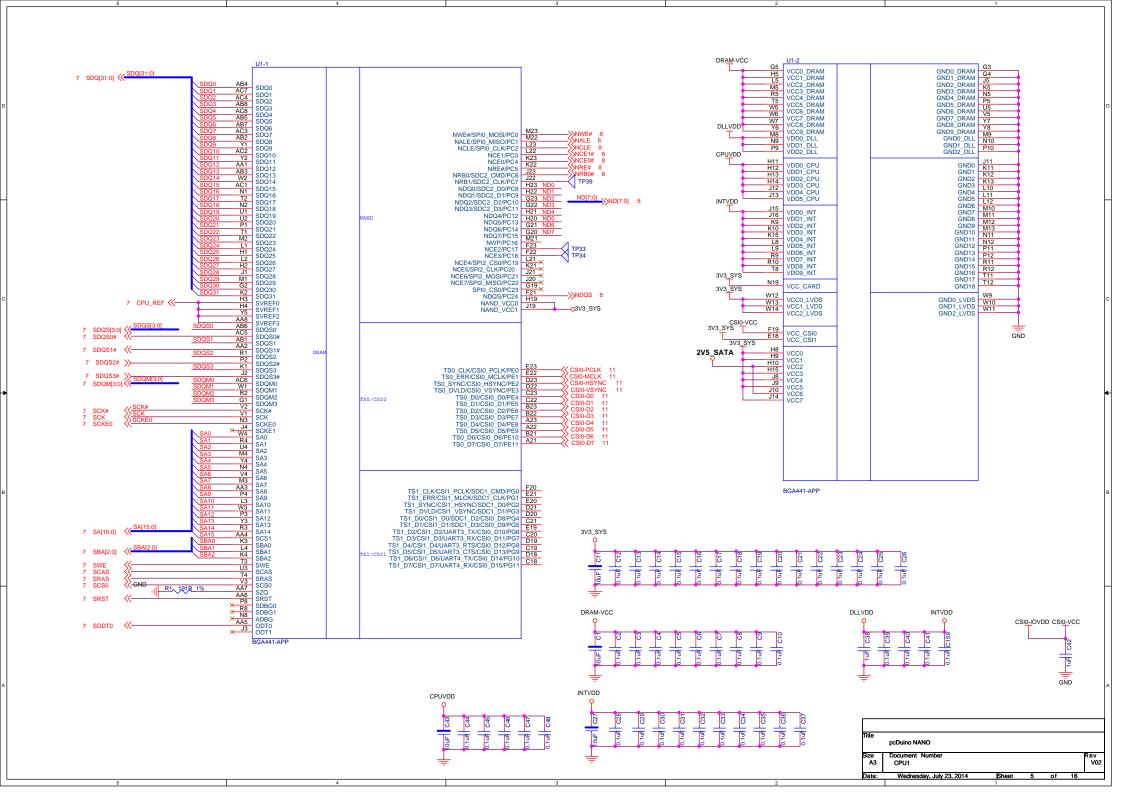
unction	GPIO List	Pin Name	Direction	Net Name	ACCIV	Default	Description		
	ERXD2/SPI1_CLK/PA1	E5	0	SPI1_CLK					
SPI1	ERXD1/SPI1_MOSI/PA2	D6	0	SPI1_MOSI			SPI1_LINK		
	ERXD0/SPI1_MISO/PA3	E6	1	SPI1_MISO					
	SPIO0_CSO/UART5_TX/EINT22/PI10	C17	0	SPIO_CS			SPIO_LINK/PWM		
SPTO	SPI00_CLK/UART5_RX/EINT23/PI11	D17	0	SPI0_CLK			SPI01_LINK		
SLIO _	SPIO0_MOSI/UART6_TX/EINT24/PI12	C16	0	SPIO_MOSI			SPIO_LINK/PWM		
	SPIO0_MISO/UART6_RX/EINT25/PI13	D16	1	SPIO_MISO			SPIO_LINK		
TTCO	TWI0_SCK/PB0	A15	0	PMU-SCK			PMU IIC LINK		
1100	TWI0_SDA/PB1	B15	1/0	PMU-SDA			PINIO_IIC_LINK		
IIC1	TWI1_SCK/PB18	A8	0	TWI1-SCK			IIC_LINK		
1101	TWI1_SDA/PB19	BS	1/0	TWI1-SDA			IIC_EINK		
IIC2	TWI2_SCK/PB20	C8	0	TW12-SCK			CONN_HC_LINK		
1112	TWI2_SDA/PB21	C7	1/0	TWI2-SDA					
	I2S_MCLK/AC97_MCLK/PB5	B13	0	HS_MCLK					
	I2S_BCLK/AC97_BCLK/PB6	A12	0	HS_BCLK					
	I2S_LRCK/AC97_SYNC/PB7	B12	0	IIS_LRCLK			12S_LINK		
IIS	12S_D00/AC97_D0/PB8	A11	0	IIS_DO0					
	I2S_D02/PB10	C11	0	IIS_D02					
	I2S_D03/PB11	C10	0	HS_DO3					
	I2S_DI/AC97_DI/SPDIF_DI/PB12	C9	1	IIS_DI					
UARTO -	UARTO_TX/IR1_TX/PB22	A7	0	UARTO-TX			UARTO LINK Fordebug		
OVETO	UARTO_RX/IR1_RX/PB23	B7	1	UARTO-RX			OAKTO_CINK FOR debug		
	JTAG_MS0/SPI2_CS0/PB14	A10	0	JTAG-MS					
	JTAG_CK0/SPI2_CLK/PB15	B10	0	JTAG-CK					
JTAG	JTAG_D00/SPI2_M0SI/PB16	A9	0	JTAG-DO			JJTAG_LINK		
	JTAG_DI0/SPI2_MISO/PB17	B9	1	JTAG-DI					
	JTAGSEL	T10	1	JTAG-SEL					
	NMI#	F5	0	NIMI#			PMU_INT		
	SDC0_DI/JATG_MS1/PF0	M20	1/0	SD0-D1					
	SDC0_D0/JATG_DI1/PF1	M19	1/0	SD0-D0					
spo 🗀	SDC0_CLK/UART0_TX/PF2	L20	0	SD0-CLK			SD CARD		
200	SDC0_CMD/JTAG_D01/PF3	L19	0	SD0-CMD			SD_CARD		
	SDC0_D3/UART0_RX/PF4	K20	1/0	SD0-D3					
	SDC0_D2/JATG_CK1/PF5	K19	1/0	SD0-D2					
UART2	EINT30/SPI1_MOSI/UART2_TX/PI18	E15	0	UART2_TX			UART2 LINK/GPIO		
ORBIZ	EINT30/SPI1_MISO/UART2_RX/PI19	D14	1	UART2_RX			OAKTZ_LINK/GPIO		
	I2S_D01/PB9	C12	1	USB0-DRV	н	н	PMU_CHANEL_SEL/IIS		

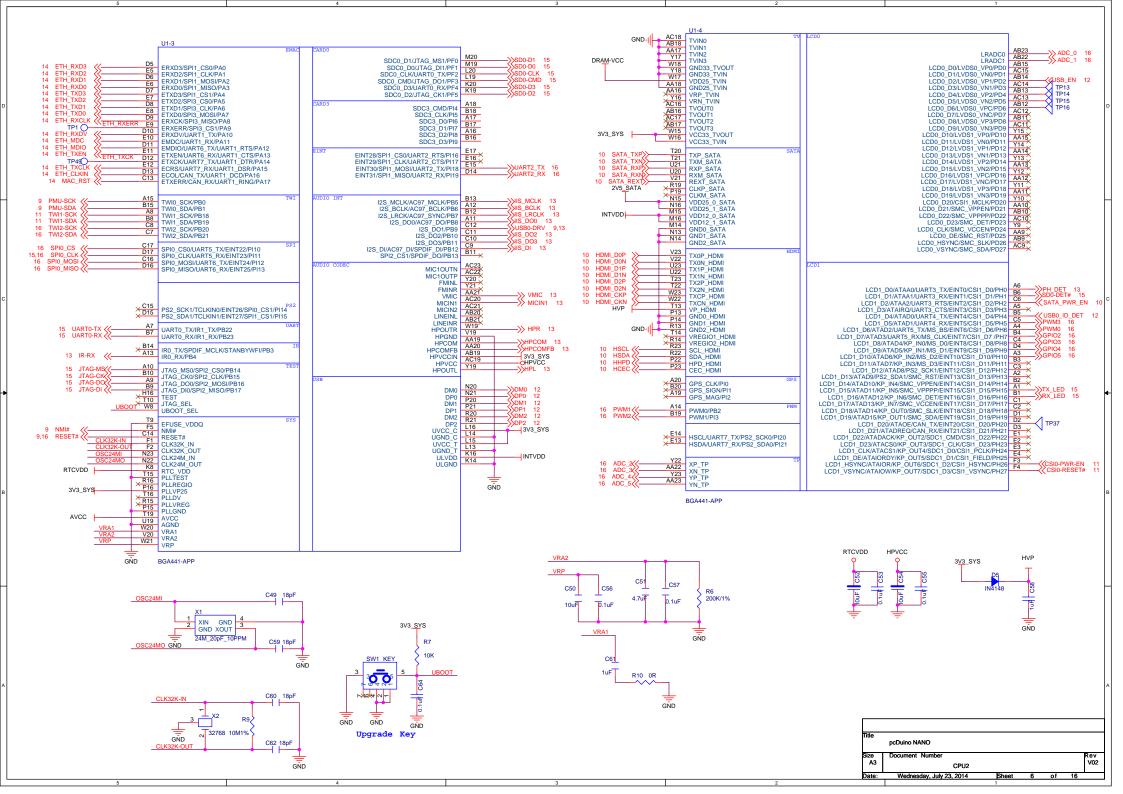
USBO	DM0	N20	1/0	DM0			OTG USB	
0000	DPO	N21	1/0	DPO				
USB1	DM1	P20	1/0	DM1			USB WIFI	
UDDI	DP1	P21	1/0	DP1				
USB2	DM2	R20	1/0	DM2			USB_Type_A	
UDDE	DP2	R21	1/0	DP2				
	SCL_HDMI	R23	0	HSCL			HDMI_DDC	
ношт	SDA_HDMI	R22	1/0	HSDA				
<u>-</u>	HPD_HDMI	P22	I	HHPD	Н	L	HDMI_HPD	
	CEC_HDMI	P23	1	HCEC	Н	L	HDMI_CEC	
PTE -	PWM0/PB2	A14	0	PWM1			PWM	
***	PWM1/PI3	B19	0	PWM2			PWM	
	XP_TP	Y22	1	ADC_2				
L	XN_TP	AA22	1	ADC_3				
ADC	YP_TP	Y23	1	ADC_4			6_CHANEL_ADC	
KUC	YN_TP	AA23	1	ADC_5				
	LRAD C0	AB23	I	ADC_0				
	LRAD C1	AB22	1	ADC_1				
	LCD1_D1/ATAA1/UART3_RX/EINT1/CSI1_D1/PH1	B6		SD0-DET#	L	Н	SD_CARD_DET	
	LCD4_D1/ATAD0/UART4_TX/EINT4/CSI1_D4/PH4	B5	I	USB0_ID_DET	L	н	OTG_USB_DET	
PTE	LCD5_D1/ATAD1/UART4_RX/EINT5/CSI1_D5/PH5	C5	1/0	PWM3			PWM	
r	LCD6_D1/ATAD2/UART5_TX/EINT6/CSI1_D6/PH6	A4	1/0	PWM0			PWM	
	VMIC	AA21	I	VMIC				
	MICIN1	AC20	1	MICIN1				
Earphone	HPOUTR	W19	0	HPR			HP_LINK	
Larphone	HPCOM	AA19	I	HPCOM			HF_LINK	
	HPCOMFB	AA20	1	HPCOMFB				
Ī	HPOUTL	Y19	0	HPL				
IR	IRO_RX/PB4	A13	I	IR-RX		н	IR.	
	TSO_CLK/CSIO_PCLK/PE0	E23	1	CS10-P CLK				
Γ	TS0_ERR/CSI0_MCLK/PE1	E22	I	CS10-MICLK				
1	TS0_SYNC/CSI0_HSYNC/PE2	D23	ı	CS10-HSYNC				
1	TS0_DVLD/CSI0_VSYNC/PE3	D22	I	CS10-VSYNC				
Ī	TS0_D0/CSI0_D0/PE4	C23	1	CS10-D0				
_ [TSO_D1/CSIO_D1/PE5	C22	I	CSI0-D1			CEL LINK	
Camera	TS0_D2/CSI0_D2/PE6	B23		CS10-D2			CSI_LINK	

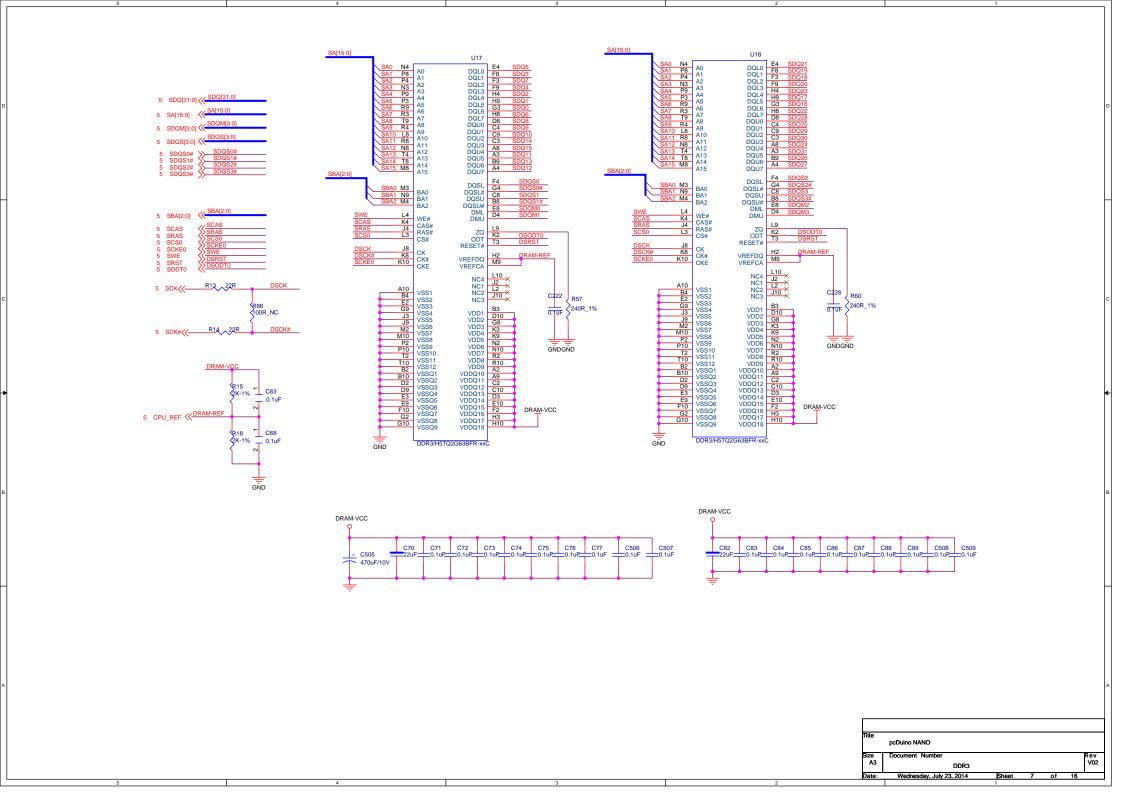
	TSO D3/CSIO D3/PE7	B22	1	CS10-D3	1		1
	TSO D4/CSIO D4/PE8	A23	1	CS10-D4			1
	TSO D5/CSIO D5/PE9	A22	i	CS10-D5			1
	TS0 D6/CS10 D6/PE10	B21	1	CS10-D6			1
	TS0_D7/CSI0_D7/PE11	A21	i i	CS10-D7			i
	TXP SATA	T20	0	SATA TXP			
	TXM SATA	T21	0	SATA_TXN			1
SATA	RXP SATA	U21	0	SATA RXP			SATA_LINK
	RXM_SATA	U20	0	SATA_RXN			1
	LCD0_D10/LVDS1_VP0/PD10	Y15	ő	LVDS_TA+			
	LCD0 D11/LVDS1 VN0/PD11	AA15	0	LVDS TA-			1
	LCD0 D12/LVDS1 VP1/PD12	Y14	ő	LVDS TB+			1
	LCD0 D13/LVDS1 VN1/PD13	AA14	0	LVDS TB-			1
	LCD0 D14/LVDS1 VP2/PD14	Y13	0	LVDS TC+			1
LVDS	LCD0 D15/LVDS1 VN2/PD15	AA13	0	LVDS TC-			LVDS_LINK
	LCD0 D16/LVDS1 VPC/PD16	Y12	0	LVDS TCLK+			1
	LCD0 D17/LVDS1 VNC/PD17	AA12	0	LVDS TCLK-			1
	LCD0 D18/LVDS1 VP3/PD18	Y11	0	LVDS TD+			1
	LCD0_D19/LVDS1_VN3/PD19	AA11	0	LVDS_TD-			1
	LCD1_D0/ATAD0/UART3_TX/EINT0/CSI1_D0/PH0	A6		PH DET	Н	L	PH DET
	LCD1 D2/ATAD2/UART3 RTS/EINT2/CSI1 D2/PH2	C6	Ö	SATA PWR EN	Н	ī	SATA PWR EN
	LCD1 D7/ATAD3/UART5 RX/EINT7/CSI1 D7/PH7	B4	1/0	GP102			GPIO
	LCD1 D8/ATAD4/EINT8/CSI1 D8/PH8	C4	1/0	GP103			GPIO
	LCD1 D9/ATAD5/EINT9/CSI1 D9/PH9	D4	1/0	GP104			GPIO
	LCD1 D10/ATAD6/EINT10/CSI1 D10/PH10	A3	1/0	GP105			GPIO
	LCD1 D11/ATAD7/EINT11/CSI1 D11/PH11	B3	1/0	GP106			GPIO
	LCD12_D12/ATAD8/EINT12/CSI1_D12/PH12	C3	1/0	GP107			GPIO
GPIO	LCD1 D13/ATAD9/EINT13/CSI1 D13/PH13	A2	1/0	GP108			GPIO
	LCD14_D14/ATAD10/EINT14/CSI1_D14/PH14	B2	1/0	GP109			GP10
	LCD1_D15/ATAD11/EINT15/CSI1_D15/PH15	A1	0	TX LED	L	н	TX LED
	LCD1 D16/ATAD12/EINT16/CSI1 D16/PH16	B1	0	RX_LED	ī	Н	RX_LED
	LCD1 D17/ATAD13/EINT17/CSI1 D16/PH17	C1	i	Key Back	ī	Н	Key Back
	LCD1 D18/ATAD14/EINT18/CSI1 D16/PH18	C2	i	Key Home	ī	Н	Key Home
	LCD1 D19/ATAD15/EINT19/CSI1 D16/PH19	D1	1	Key Menu	L	н	Key Menu
	LCD1 HSYNC/ATAIOR/SDC1 D2/CSI1 HSYNC/PH26	F3	0	CS10-P W R-EN	н	н	CS10-PWR-EN
	LCD1 VSYNC/ATAIOW/SDC1 D3/CSI1 VSYNC/PH27	F4	0	CS10-RESET#	1 7	н	CSIO-RESET#

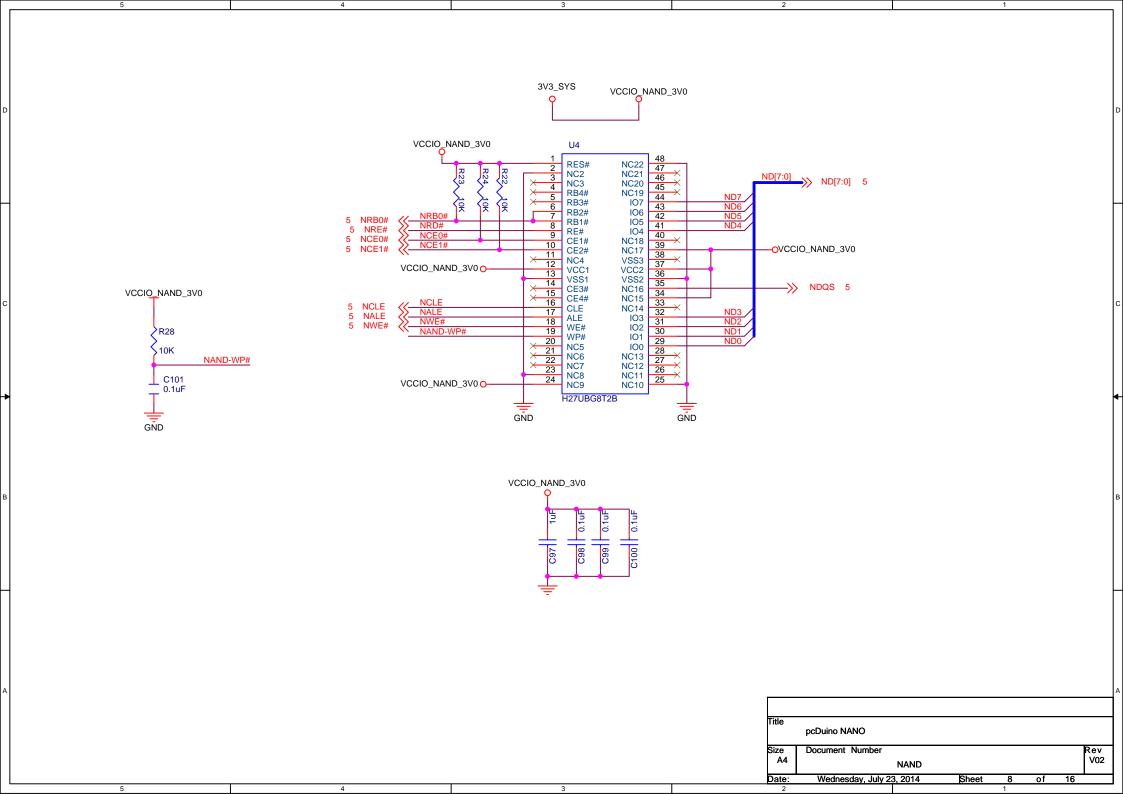
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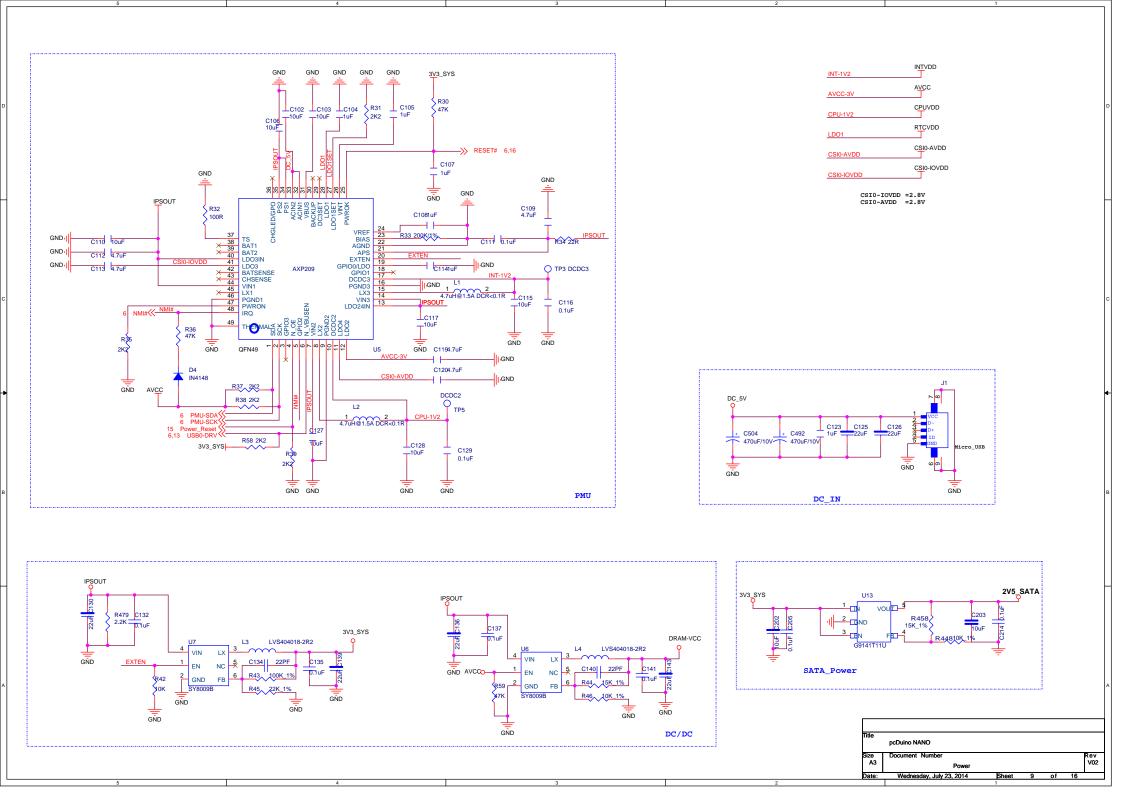


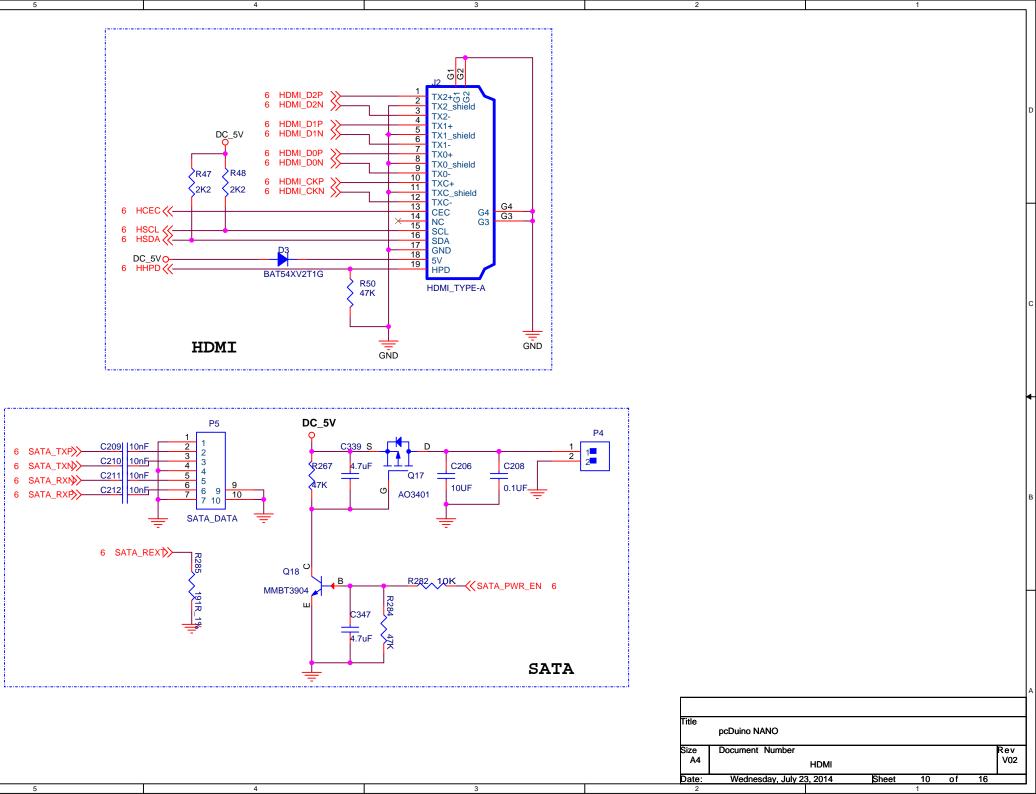


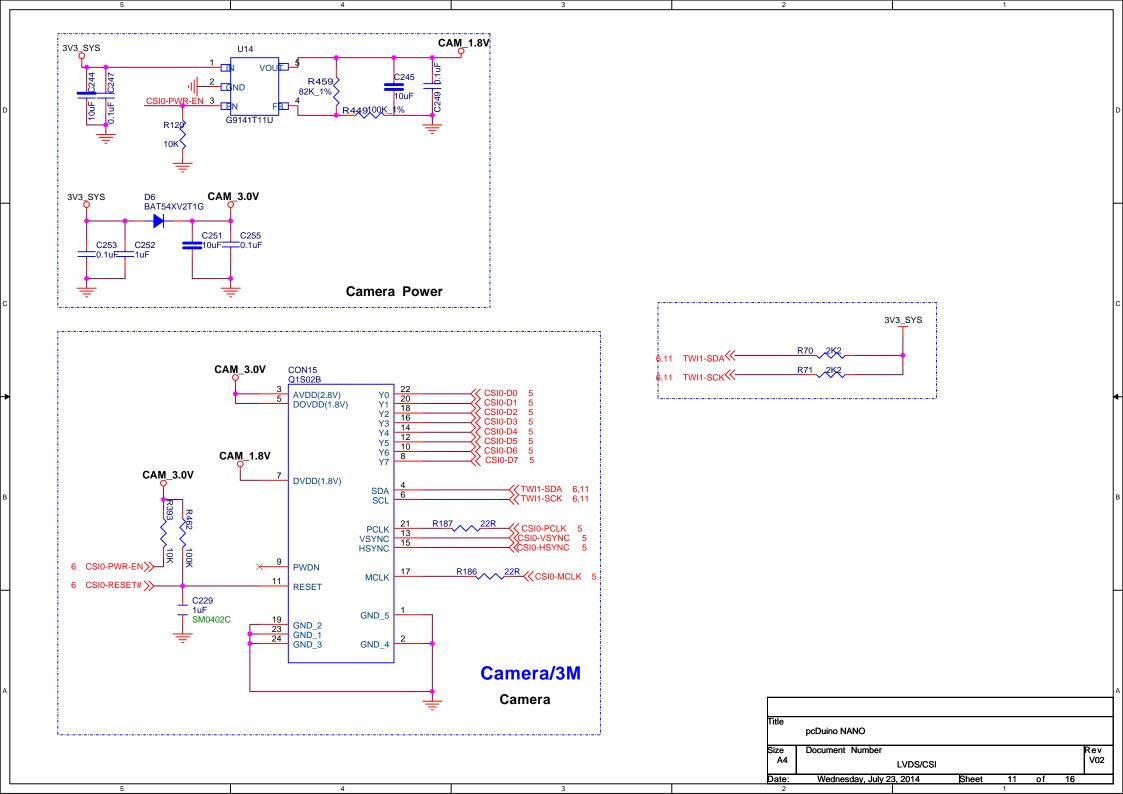


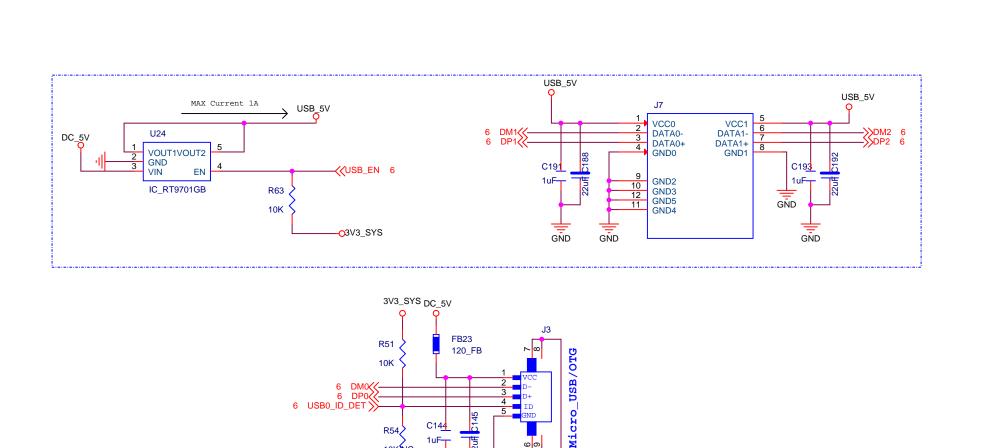












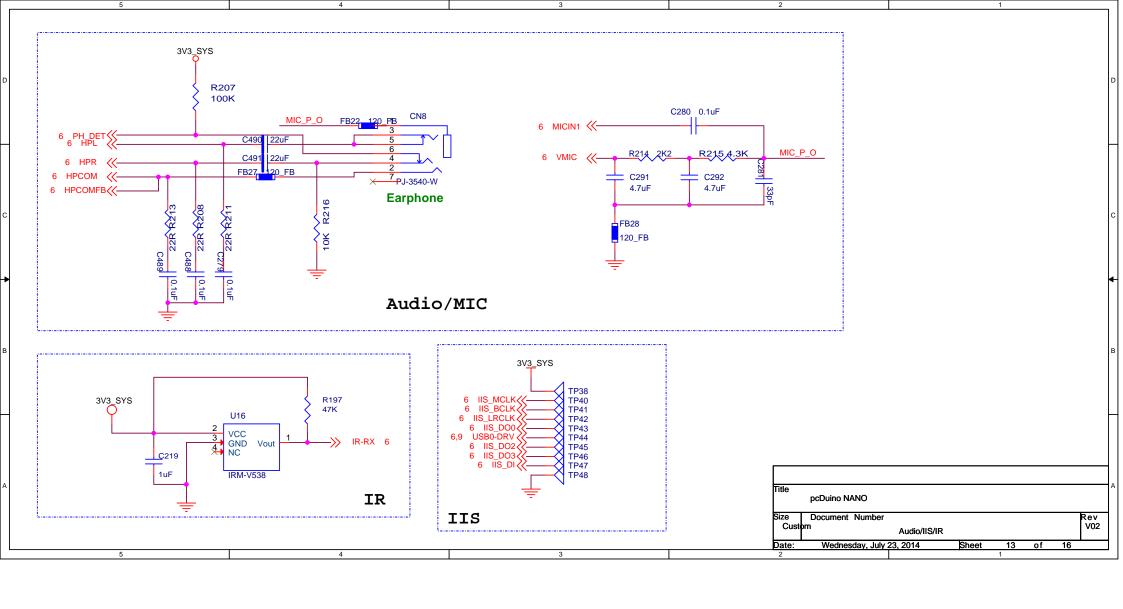
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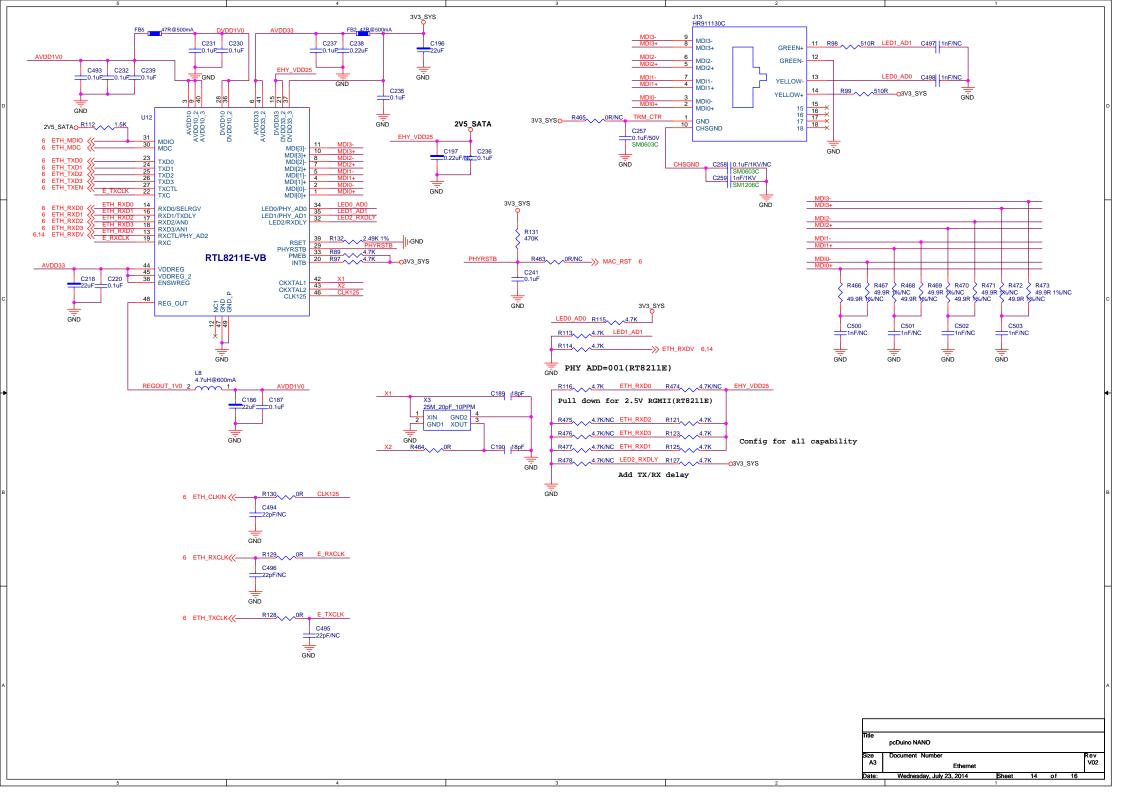
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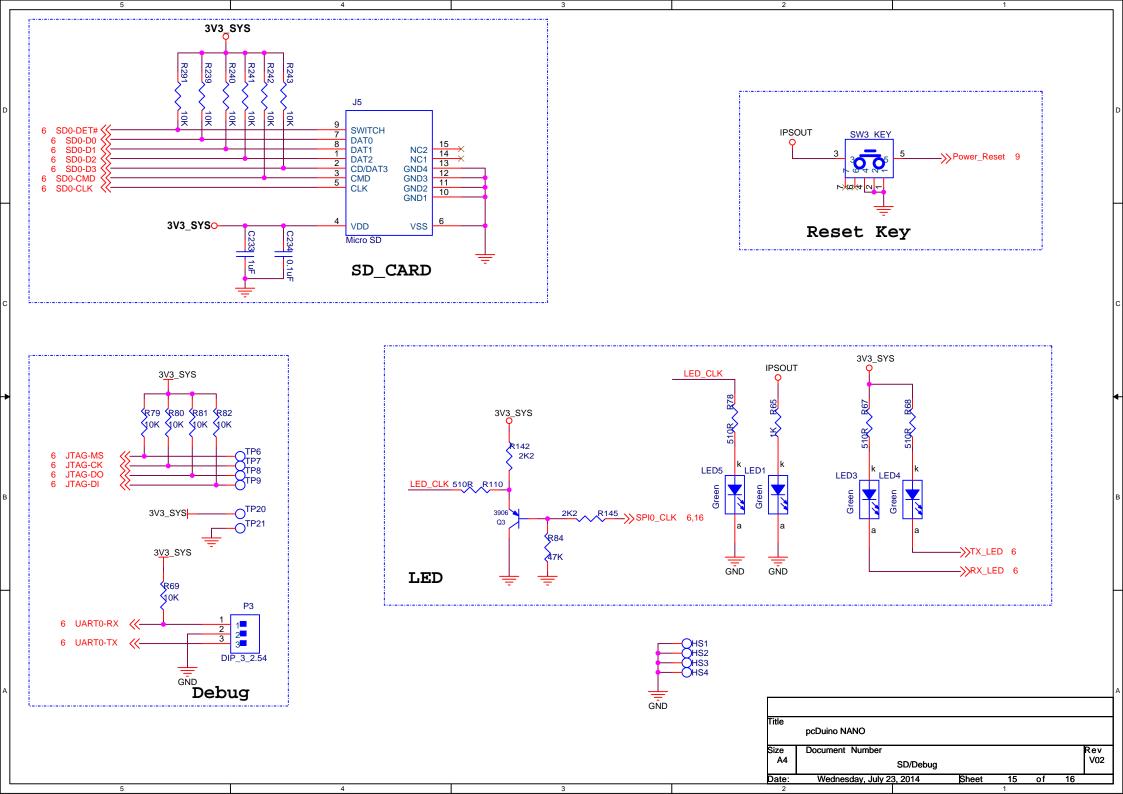
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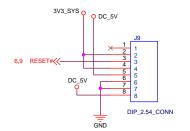
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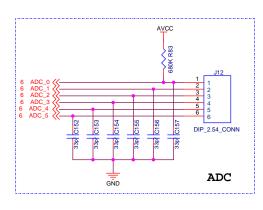
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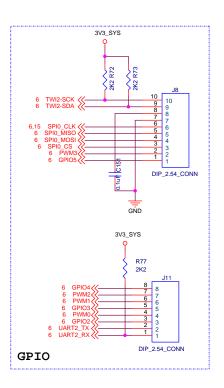












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