

	Part Number			ELF CPLD(130nm)		ELF2 FPGA(55nm)				AL3 FPGA(65nm)			EG4 FPGA(55nm)			
				ELF-300	ELF-650	EF2L1500	EF2L2500	EF2L4500	EF2M4500	AL3A10	AL3S10	AL3A100IP	EG4X20	EG4A20	EG4D20	EG4S20
Logic Resources	DFFs			336	688	1500	2500	4480	4480	8,640	8,640	110,592	19,600	19,600	19,600	19,600
	LUTs			336	688	1500	2500	4480	4480	10,386	10,386	132,710	23,520	23,520	23,520	23,520
Memory	Distribute RAM Bits			2K	7K	12K	20K	35K	35K	69,120	69,120	884,736	156,800	156,800	156,800	156,800
	BRAM(9K)			/	/	6	3	1	1	48	48	442	64	64	64	64
	BRAM (32K)			/	/	9	4	1	1	2	2	16	16	16	16	16
	BRAM (128K)			/	/	12	6	1	1	/	/	/	/	/	/	/
	BRAM (256K)			/	/	12	6	1	1	/	/	/	/	/	/	/
	Total BRAM Bits			/	/	546K	607K	700K	700K	507,904	507,904	4,595,712	1,114,112	1,114,112	1,114,112	1,114,112
	SDRAM			/	/	/	/	/	/	/	2M*32bits SDRAM	/	/	/	8M*16bits DDR	2M*32bits SDRAM
Cfg Flash	User flash			128Kbits	128Kbits	2.6Mbits	2.6Mbits	2.6Mbits	2.6Mbits	/	/	/	/	/	/	/
	MCU			/	/	/	/	/	Cortex M3	/	/	/	/	/	/	/
	Bitstream (Mbits)			128Kbits	128Kbits	1.4	1.4	1.4	1.4	2.2	2.2	26.5	6.0	6.0	6.0	6.0
Multipler	M18x18			/	/	8	12	15	15	3	3	316	29	29	29	29
Clock Resource	PLL			/	/	1	1	1	1	2	2	4	4	4	4	4
	GCLK			8	8	16	16	16	16	16	16	16	16	16	16	16
IO Resource	I/O Banks			4	4	4	4	4	4	8	8	16	8	4	4	4
	Maximum User IOs			84	120	206	30	206	114	184	111	672	183+6	189+6	135	71
IP Resource	Hard IP					8通道,12位,1MSPS ADC/电源监控模块/OSC/温度传感器										
Footprint	Package Foorprint	Package Dimension(mm)	Pitch(mm)	IO / LVDS pairs												
	XWFN42	4.2x4.2	0.35				30/14									
	LQFP64	10x10	0.5						51 **							
	QFN88	10x10	0.4							60/7	60/7	IP Core		71/7(14)		71/7(14)
	LQFP100	14x14	0.5			80										
	TQFP100	16x16	0.5	84	84											
	LQFP144	18x18	0.4								111/42					
	TQFP144	20x20	0.5		120	114		114	114 **	91/31						
	TQFP176	22x22	0.4												135/48	
	FBGA256	17x17	1.0			206		206		184/92				189/93	195/96	

**Note1:** ELF2的User flash容量：BRAM使用了初始化文件时，bit文件会增大，User flash的容量会相应减小，所有BRAM均使用了初始化文件时, bit文件约在2Mbits，User flash的容量约在2Mbits