

TOWARDS A COMPOSITIONAL SESSION LOGIC FOR COMMUNICATION PROTOCOLS

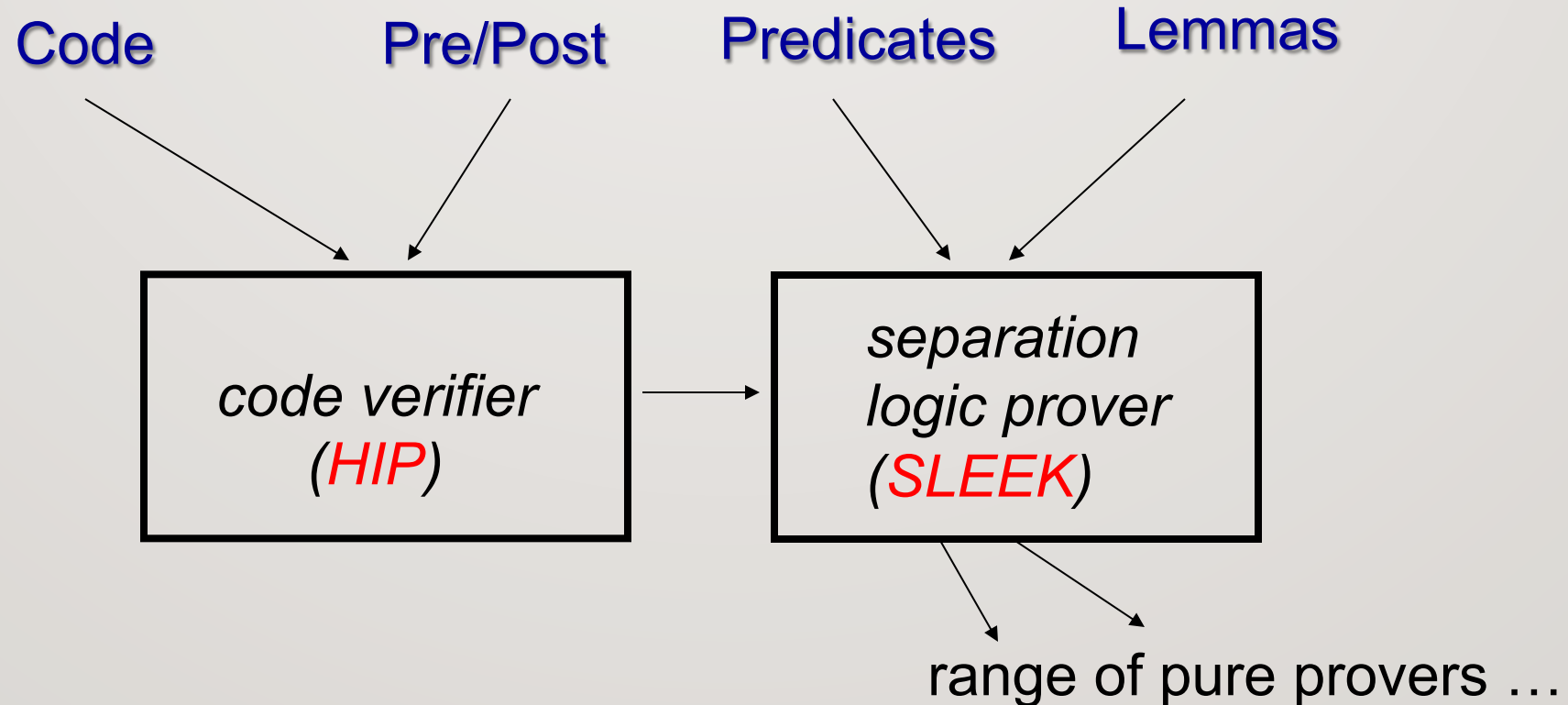
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PROJECT

Under development since 2006 (200K lines of OCaml)
Currently : 3 current PhD students; 7 graduated PhD



Omega, MONA, Isabelle, Coq, SMT, Redlog, MiniSAT, Mathematica

FEATURES OF HIP/SLEEK

Can specify *complex* data structures to support symbolic verification.

(i) *expressive* (shapes+size, term, //ism)

(ii) *automation* (with lemma, inference)

(iii) *modular* (per method)

(iv) *more scalable* (proof slicing)

<http://loris-5.d2.comp.nus.edu.sg/Tut1/>

TOWARDS A COMPOSITIONAL
SESSION LOGIC FOR
COMMUNICATION PROTOCOLS

COMMUNICATION PROTOCOLS

Are **everywhere**: OS, parallel computation, transportation system, etc.

But,
writing such software is **error-prone**!

Therefore,
need support to **formally specify** and **verify** their correctness.

COMMUNICATION PROTOCOLS

Session type theory^{1,2,3}

- intensively studied: maturity (multiparty, delegation, hybrid solutions)
- correct message type sequencing
- deadlock freedom, progress and liveness

But, it should also be important to:

- go beyond types and reason about the content of the message (value, resource)
- support specialized protocols in a general context
- add fewer constraints over the input language

1. K. Honda, N. Yoshida, M. Carbone. Multiparty Asynchronous Session Types. In POPL 2008

2. D. Orchard No. Yoshida. Effects as Sessions, Sessions as Effects. In POPL 2016

3. M. Carbone, S. Lindley, F. Montesi, C. Shürmann, and P. Wadler. Coherence generalises duality: a logical explanation of multiparty session types. In CONCUR 2016.

TOWARDS A **SESSION LOGIC** FOR COMMUNICATION PROTOCOLS

KEY ISSUES

Type safety → Logic which treats communication as resource

Variance or Flow-Awareness

Higher-Order Predicates

Multiparty & Synchronization

COMMUNICATION ASSUMPTIONS

Bidirectional channels / Mailbox channel per party

Asynchronous send and blocking receive

Explicit Synchronization

SESSION LOGIC – BINARY PROTOCOLS – Operators (extension of Separation Logic)

$P =$

$?r \cdot \phi$: *input*

$!r \cdot \phi$: *output*

$P_1 ; P_2$: *sequence*

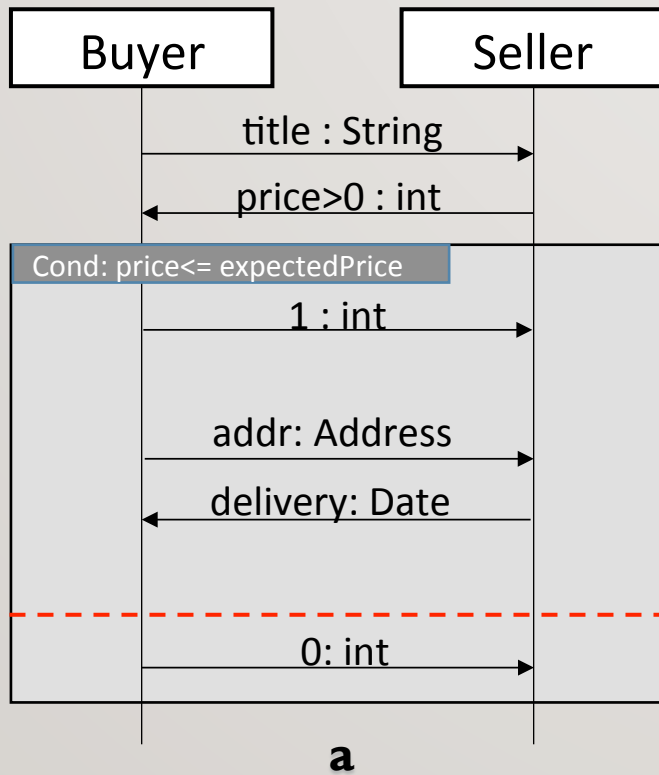
$P_1 \vee P_2$: *choice*

$P_1 * P_2$: *non-deterministic*

Shape pred.	$spre$	$:= c\langle v^* \rangle \equiv \phi \text{ inv } \pi$
Formula	ϕ	$:= \bigvee (\exists v^* \cdot \kappa \wedge \pi)^*$
Pure formula	π	$:= \gamma \wedge \varphi$
Ptr. eq./diseq.	γ	$:= v=v \mid v=\text{null} \mid v \neq v \mid v \neq \text{null} \mid \gamma_1 \wedge \gamma_2$
Heap formula	κ	$:= \text{emp} \mid v \mapsto c(v^*) \mid p\langle v^* \rangle \mid \mathcal{C}\langle v, P \rangle \mid \kappa * \kappa$
	Δ	$:= \Phi \mid \Delta \vee \Delta \mid \Delta \wedge \pi \mid \Delta * \Delta \mid \exists v \cdot \Delta$
	φ	$:= \varphi \mid b \mid c \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \neg \varphi \mid \exists v \cdot \varphi \mid \forall v \cdot \varphi$
	b	$:= \text{true} \mid \text{false} \mid v \mid b = b$
	c	$:= a = a \mid a \leq a$
Presburger arith.	a	$:= k^{\text{int}} \mid v \mid k^{\text{int}} \times a \mid a + a \mid -a$

$\mathcal{C}(v, P)$ – associates spec P to channel v

SESSION LOGIC – BINARY PROTOCOLS – Example



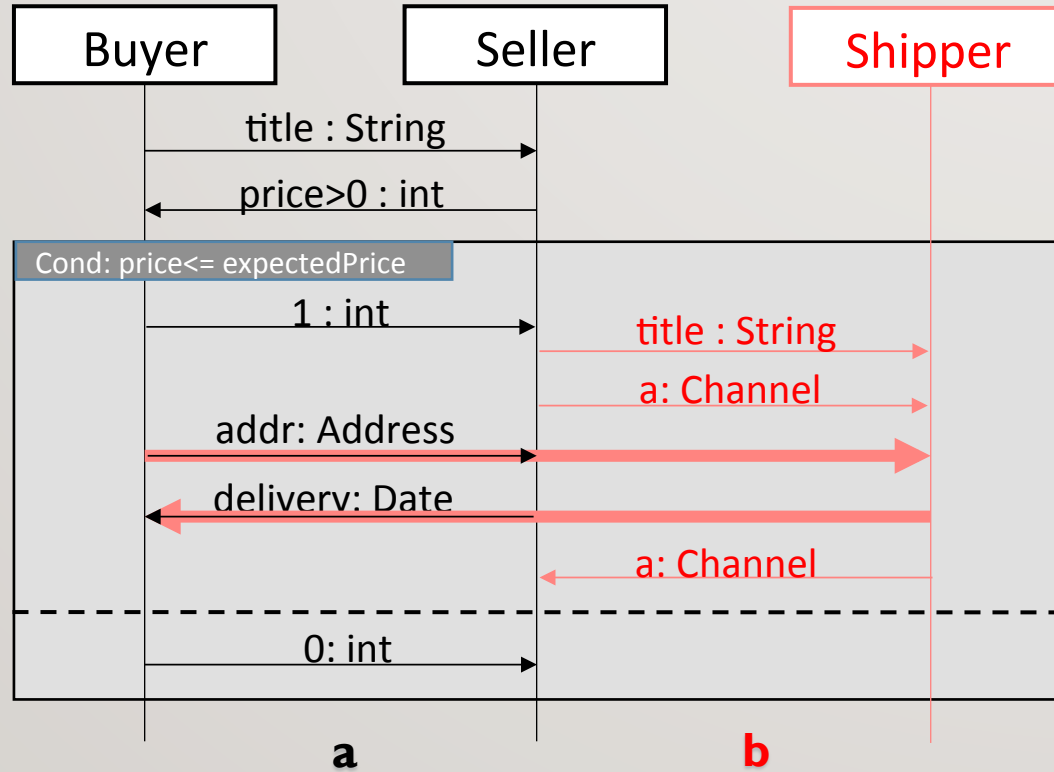
$!r \cdot \text{emp} \wedge r:\text{String} \wedge \text{true}$

$?r \cdot \text{emp} \wedge r:\text{int} \wedge r > 0$

$\text{buyer}_a \triangleq !\text{String}; ?v : \text{int} \cdot v > 0; ((!1; !\text{Address}; ?\text{Date})\textcolor{red}{V}!0)$
 $\text{seller}_a \triangleq ?\text{String}; !v : \text{int} \cdot v > 0; ((?1; ?\text{Address}; !\text{Date})\textcolor{red}{V}?0)$

$!r \cdot \text{emp} \wedge r:\text{int} \wedge r = 1$

SESSION LOGIC – BINARY PROTOCOLS – delegation



$\text{buyer}_a \triangleq !\text{String}; ?v : \text{int} \cdot v > 0; ((!1; !\text{Address}; ?\text{Date}) \vee !0)$

$\text{seller}_a \triangleq ?\text{String}; !v : \text{int} \cdot v > 0; ((?1; ?\text{Address}; !\text{Date}) \vee ?0)$

$\text{seller}_b \triangleq !\text{String}; !v \cdot \mathcal{C}(v, ?\text{Address}; !\text{Date}); ?v \cdot \mathcal{C}(v, \text{emp})$

$\text{shipper}_b \triangleq ?\text{String}; ?v \cdot \mathcal{C}(v, ?\text{Address}; !\text{Date}); !v \cdot \mathcal{C}(v, \text{emp})$

PLUGGING THE LOGIC INTO A SOFTWARE VERIFIER⁴

4. W. N. Chin, C. David, H.H. Nguyen, S. Qin. Automated verification of shape, size and bag properties via user-defined predicates in separation logic. *Science of Computer Programming*, Volume 77, Number 9, August 2012

SESSION LOGIC – Selected Entailment Rules

$$\frac{\boxed{\text{ENT-CHAN}} \quad P_a \vdash P_c \rightsquigarrow S' \quad S = \{\pi_i^e \mid \pi_i^e \in S'\}}{\mathcal{C}(v, P_a) \vdash \mathcal{C}(v, P_c) \rightsquigarrow S}$$

$$\frac{\boxed{\text{ENT-SEQ}} \quad \square_a \vdash \square_c \rightsquigarrow S_1 \quad P_a \vdash P_c \rightsquigarrow S_2 \quad \text{where } \square := ?v \cdot \Delta \mid !v \cdot \Delta \mid \zeta}{\square_a; P_a \vdash \square_c; P_c \rightsquigarrow \{\text{emp} \wedge \pi_1 \wedge \pi_2 \mid \pi_1 \in S_1 \wedge \pi_2 \in S_2\}}$$

$$\frac{\boxed{\text{ENT-LHS-OR}} \quad P_i; P_a \vdash P_c \rightsquigarrow S_i \quad S = \{\bigvee_i \Delta_i \mid \Delta_i \in S_i\}}{(\bigvee_i P_i); P_a \vdash P_c \rightsquigarrow S}$$

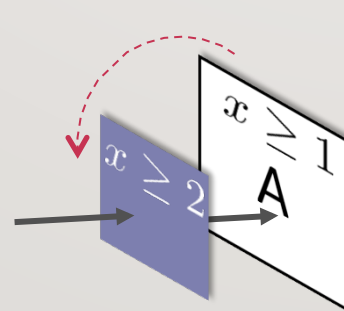
$$\frac{\boxed{\text{ENT-RECV}} \quad \Delta_a \vdash \Delta_c \rightsquigarrow S' \quad S = \{\pi_i^e \mid \pi_i^e \in S'\}}{?v \cdot \Delta_a \vdash ?v \cdot \Delta_c \rightsquigarrow S}$$

$$\frac{\boxed{\text{ENT-SEND}} \quad \Delta_c \vdash \Delta_a \rightsquigarrow S' \quad S = \{\pi_i^e \mid \pi_i^e \in S'\}}{!v \cdot \Delta_a \vdash !v \cdot \Delta_c \rightsquigarrow S}$$

SESSION LOGIC – BINARY PROTOCOLS – Variance

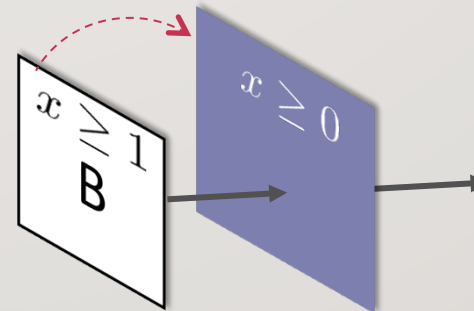
Example:

A: $\mathcal{C}(v, !x \cdot x \geq 1)$ || B: $\mathcal{C}(v, ?x \cdot x \geq 1)$



$$!x \cdot x \geq 1 \vdash !x \cdot x \geq 2$$

contravariant



$$?x \cdot x \geq 1 \vdash ?x \cdot x \geq 0$$

covariant

COMMUNICATION PRIMITIVES

$\boxed{\text{FV-OPEN}}$

$\vdash \{\text{emp}\} \text{ open}(c) \text{ with } P \{ \mathcal{C}(c, S_1) * \mathcal{C}(c, S_2) \}$

$\boxed{\text{FV-CLOSE}}$

$\vdash \{ \mathcal{C}(c, \text{emp}) * \mathcal{C}(c, \text{emp}) \} \text{ close}(c) \{ \text{emp} \}$

$\boxed{\text{FV-SEND}}$

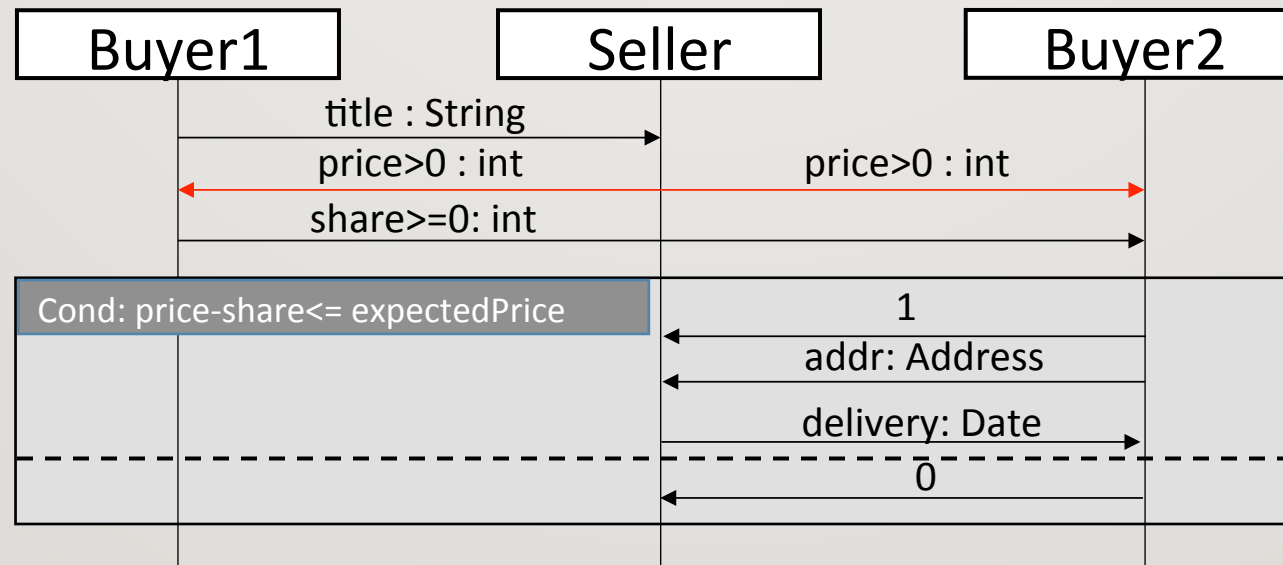
$\vdash \{ \mathcal{C}(c, !v \cdot L(v); P) * L(x) \} \text{ send}(c, x) \{ \mathcal{C}(c, P) \}$

$\boxed{\text{FV-RECV}}$

$\vdash \{ \mathcal{C}(c, ?v \cdot L(v); P) \} x = \text{receive}(c) \{ L(x) * \mathcal{C}(c, P) \}$

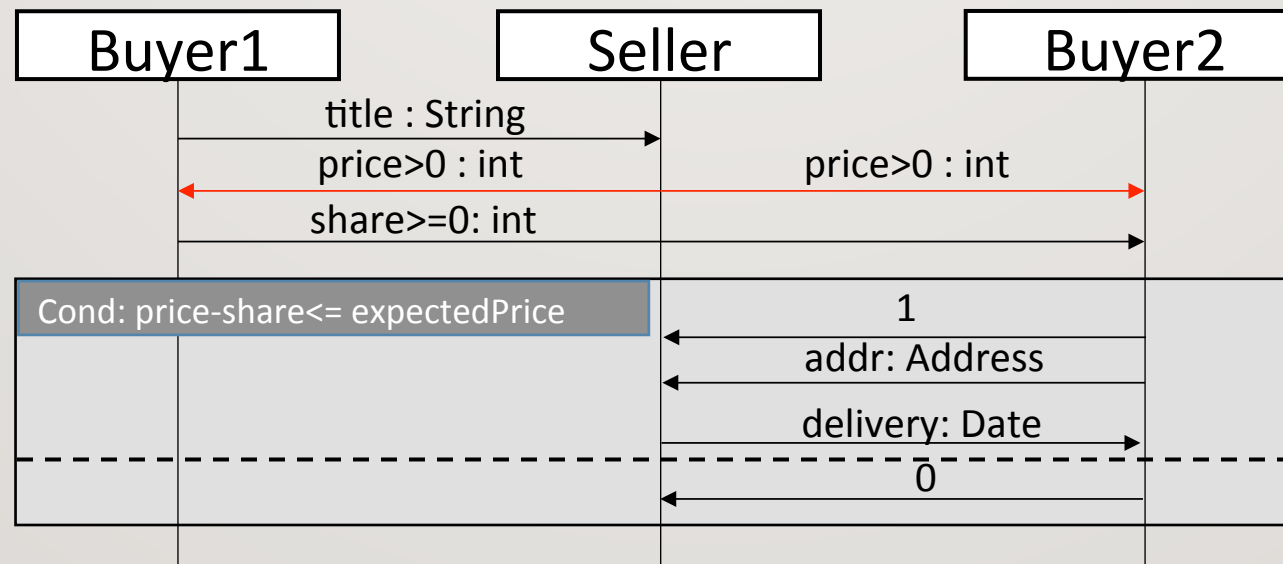
MULTI-PARTY SPECIFICATION

SESSION LOGIC – MULTIPARTY PROTOCOLS



$$G ::= A \rightarrow B : \Phi \mid A \xrightarrow{d} B : G \mid p(v^*) \mid \zeta_{id} \mid G \circledast G \mid G \vee G \mid G ; G$$

SESSION LOGIC – MULTIPARTY PROTOCOLS – Specs



$$G_{BBS}(B_1, B_2, S) \triangleq B_1 \rightarrow S : \text{String};$$

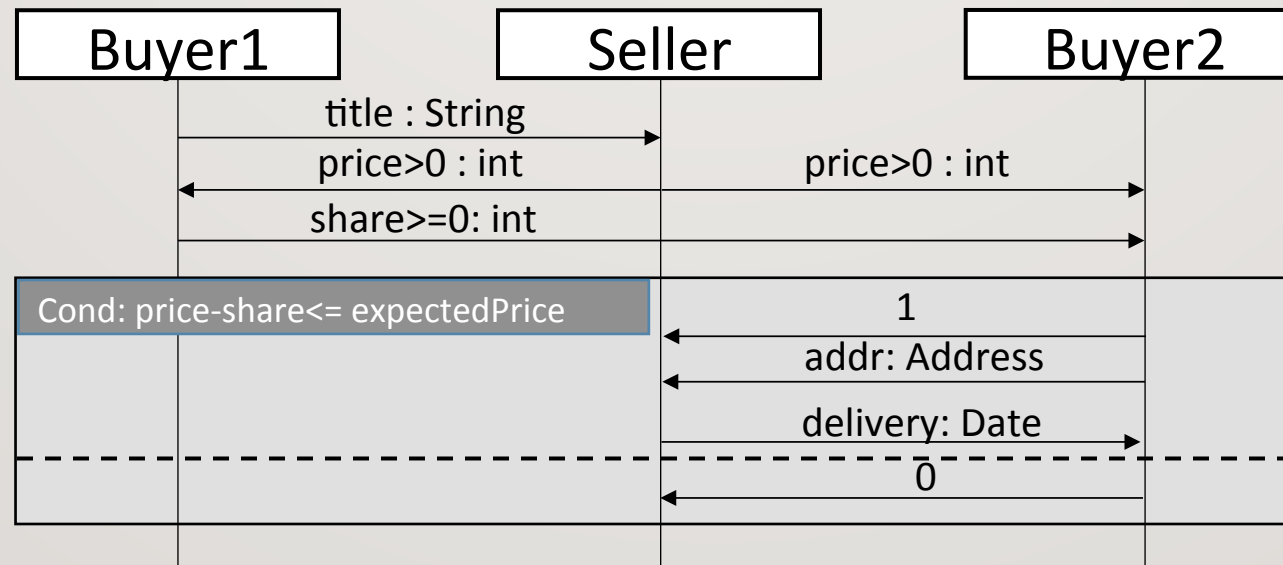
$$((S \rightarrow B_1 : v \cdot v > 0) \text{ } \textcircled{*} \text{ } (S \rightarrow B_2 : v \cdot v > 0));$$

$$B_2 \rightarrow B_1 : v \cdot v \geq 0;$$

$$((B_2 \rightarrow S : 1; B_2 \rightarrow S : \text{Addr}; S \rightarrow B_2 : \text{Date}) \vee (B_2 \rightarrow S : 0))$$

arbitrary order

SESSION LOGIC – MULTIPARTY PROTOCOLS – Specs (enforcing sequentiality between different channels)



$$\begin{aligned}
 G_{\text{BBS}}(B_1, B_2, S) \triangleq & \boxed{B_1 \rightarrow S : \text{String}; \zeta_1;} \\
 & ((\boxed{S \rightarrow B_1 : v \cdot v > 0}) \circledast \boxed{S \rightarrow B_2 : v \cdot v > 0}); \zeta_2; \\
 & \boxed{B_2 \rightarrow B_1 : v \cdot v \geq 0; \zeta_3;} \\
 & ((\boxed{B_2 \rightarrow S : 1; B_2 \rightarrow S : \text{Addr}; S \rightarrow B_2 : \text{Date}}) \vee \boxed{B_2 \rightarrow S : 0})
 \end{aligned}$$

ζ_{id} - local or
global sync
instrument

SYNCHRONIZATION

IMPLICIT (NO) SYNCHRONIZATION

Consider: $A \rightarrow B : t_1$; $C \rightarrow D : t_2$

What are expected ordering?

$A \rightarrow$ before $\rightarrow B$

$C \rightarrow$ before $\rightarrow D$

EXPLICIT SYNCHRONIZATION

Consider: $A \rightarrow B : t_1$; $C \rightarrow D : t_2$

How about the other sequencing?

SS: $A \rightarrow$ before $C \rightarrow$ **SR:** $A \rightarrow$ before $\rightarrow D$

RS: $\rightarrow B$ before $C \rightarrow$ **RR:** $\rightarrow B$ before $\rightarrow D$

Which is the strongest? cf sequential consistency..

EXPLICIT SYNCHRONIZATION

Consider: $A \rightarrow B : t_1 \ ;_{RS} \ C \rightarrow D : t_2$

Strict Sequencing $RS: \rightarrow B$ before $C \rightarrow$

Explicit enforcement:

$A \rightarrow : t_1 \quad \rightarrow B : t_1 \quad \text{AWAIT}(c) \quad C \rightarrow : t_2 \quad \rightarrow D : t_2$
 $\text{DEC}(c)$

STRICT SEQUENCING

Advantage: simpler to understand

Disadvantage: less concurrency;
need for explicit synchronization

MAILBOX COMMUNICATION

Consider: $A \rightarrow B : t_1$; $C \rightarrow B : t_2$

Assume mailbox communication + no synchronization

$$\begin{array}{ccc} A \rightarrow :t_1 & C \rightarrow :t_2 & \rightarrow B :t_1 \\ & & \rightarrow B :t_2 \end{array}$$

*What if C sends before A to mailbox of B?
Type error in communication!*

SEND-SEND SYNCHRONIZATION

Consider: $A \rightarrow B : t_1$; ss $C \rightarrow B : t_2$

Enforce SS synchronization for mailbox communication

$ss: A \rightarrow$ before $C \rightarrow$

Explicit synchronization:

$A \rightarrow : t_1$	$AWAIT(c)$	$\rightarrow B : t_1$
$DEC(c)$	$C \rightarrow : t_2$	$\rightarrow B : t_2$

WITHOUT SYNCHRONIZATION

Consider: $A \rightarrow B : t_1 ; A \rightarrow C : t_2$

Decomposition without synchronization

$$\begin{array}{ccccc} A \rightarrow : t_1 & \rightarrow B : t_1 & \rightarrow C : t_2 \\ A \rightarrow : t_2 & & & & \end{array}$$

What if B is our privileged customer whom we wish to notify ahead of our regular customer C?

RECEIVE-RECEIVE SYNCHRONIZATION

Consider: $A \rightarrow B : t_1$; **RR** $A \rightarrow C : t_2$

Enforce RR synchronization for priority receive.

RR: $\rightarrow B$ before $C \rightarrow$

Explicit synchronization:

$A \rightarrow : t_1$

$A \rightarrow : t_2$

$\rightarrow B : t_1$

DEC(c)

AWAIT(c)

$\rightarrow C : t_2$

SYNCHRONIZATION

Both explicit and implicit synchronization can be used.

Priority (i) safety (ii) correctness (iii) user requirement (iv) performance.

Expressive logic + spec synthesis provide flexibility

TAKE AWAY

Type safety → Logic which treats communication as resource: concurrency, sequence, nondeterminism

Variance or Flow-Awareness: support for precise specification and verification

Higher-Order Predicates: expressive protocols

Multiparty → Locality: synchronization

Work in Progress

Synthesize local projections → Multiparty view

Explore more abstractions (e.g. specify security protocols)