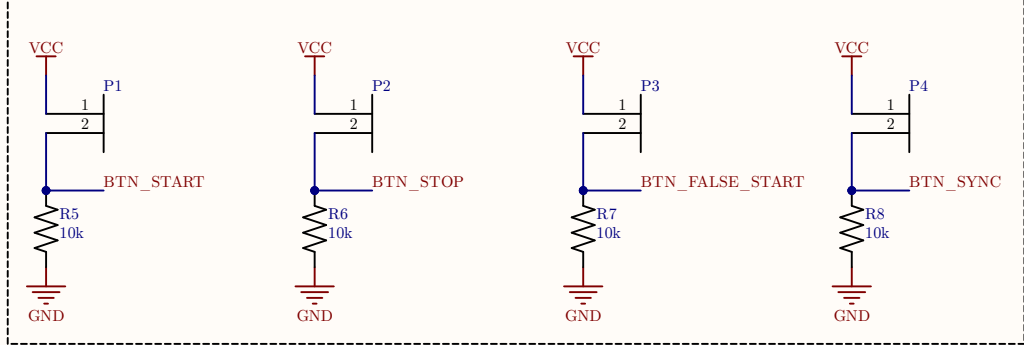


Project: Arana LA Race Timing System  
Title: Display Board System

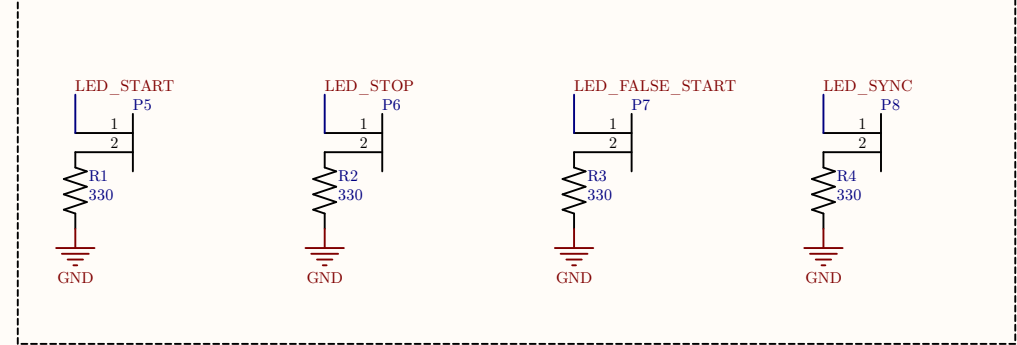
Drawn By: N.dePaola  
Checked By: B.Park, C.Murray  
Revision: 1.1

Organisation: QUT  
Date: 28/09/2020  
Sheet 1 of 3

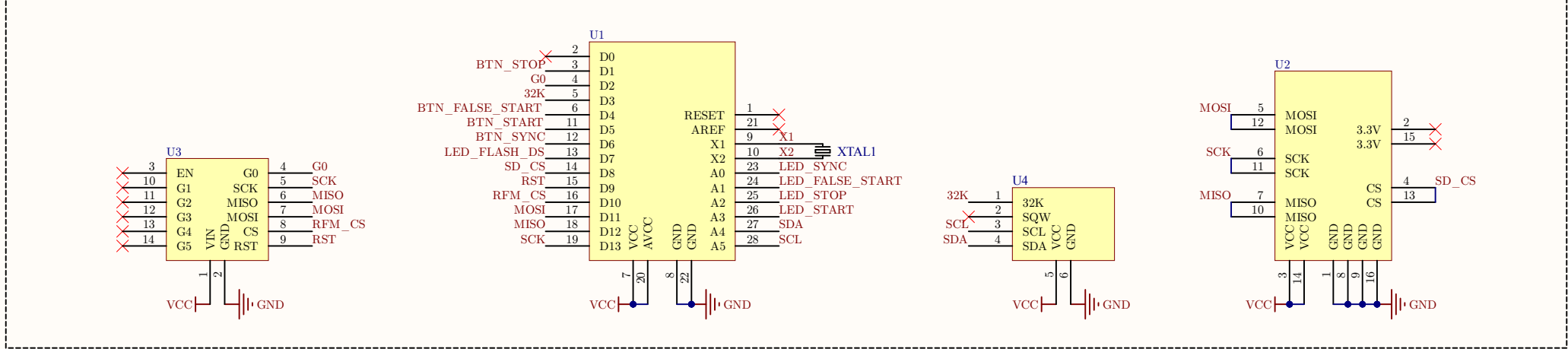
Push Buttons



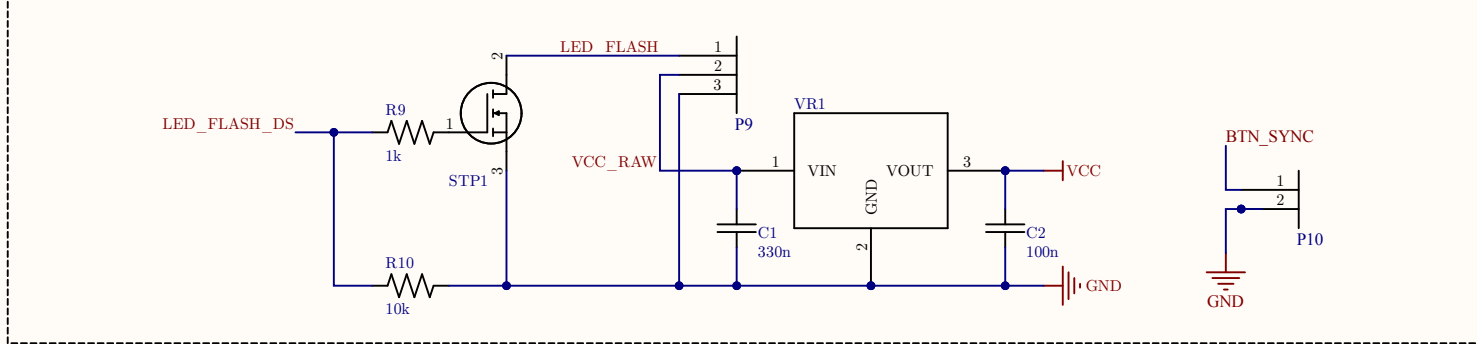
Push Button LEDs



Integrated Circuits

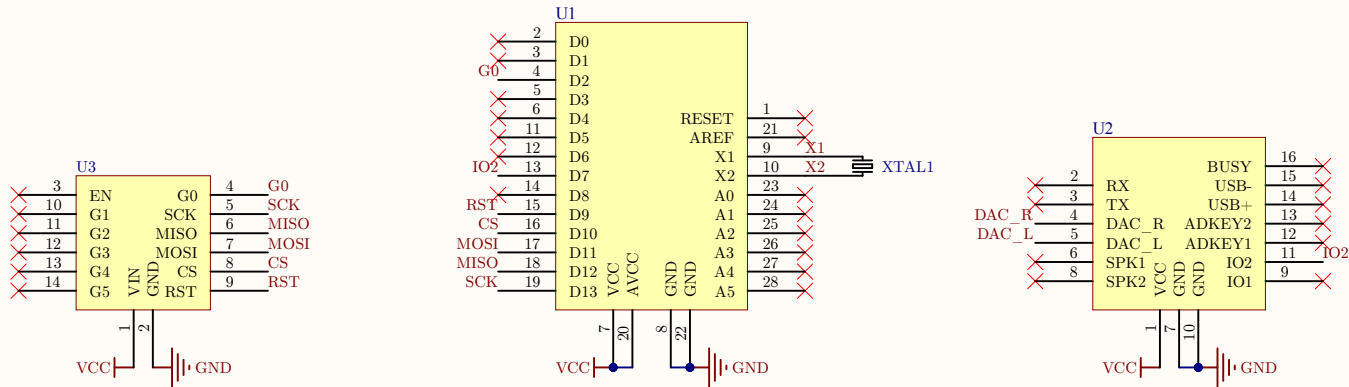


Voltage Regulation & LED Flash Switching

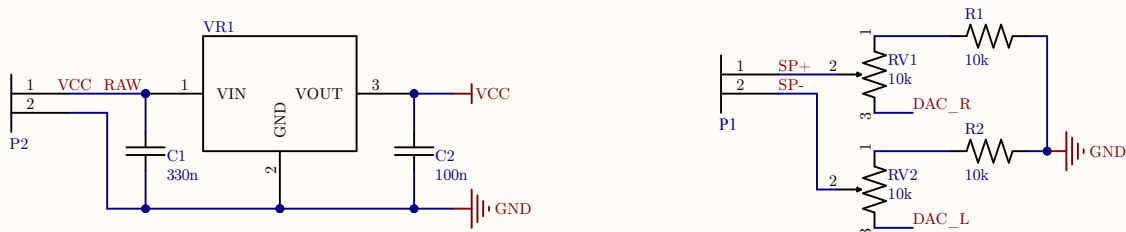


Project: Arana LA Race Timing System		
Title: Start Line System		
Drawn By: N.de Paola	Checked By: B.Park, C.Murray	Revision: 1.1
Organisation: QUT	Date: 7/10/2020	Sheet 2 of 3

Integrated Circuits



Voltage Regulation & Speaker Output



Project: Arana LA Race Timing System		
Title: Speaker System		
Drawn By: N.de Paola	Checked By: B.Park, C.Murray	Revision: 1.1
Organisation: QUT	Date: 28/09/2020	Sheet 3 of 3