xv6 is a re-implementation of Dennis Ritchie's and Ken Thompson's Unix Version 6 (v6). xv6 loosely follows the structure and style of v6, but is implemented for a modern x86-based multiprocessor using ANSI C.

ACKNOWLEDGMENTS

xv6 is inspired by John Lions's Commentary on UNIX 6th Edition (Peer to Peer Communications; ISBN: 1-57398-013-7; 1st edition (June 14, 2000)). See also http://pdos.csail.mit.edu/6.828/2007/v6.html, which provides pointers to on-line resources for v6.

xv6 borrows code from the following sources:
 JOS (asm.h, elf.h, mmu.h, bootasm.S, ide.c, console.c, and others)
 Plan 9 (bootother.S, mp.h, mp.c, lapic.c)
 FreeBSD (ioapic.c)
 NetBSD (console.c)

The following people made contributions: Russ Cox (context switching, locking) Cliff Frey (MP) Xiao Yu (MP)

The code in the files that constitute xv6 is Copyright 2006-2007 Frans Kaashoek, Robert Morris, and Russ Cox.

ERROR REPORTS

If you spot errors or have suggestions for improvement, please send email to Frans Kaashoek and Robert Morris (kaashoek,rtm@csail.mit.edu).

BUILDING AND RUNNING XV6

To build xv6 on an x86 ELF machine (like Linux or FreeBSD), run "make". On non-x86 or non-ELF machines (like OS X, even on x86), you will need to install a cross-compiler gcc suite capable of producing x86 ELF binaries. See http://pdos.csail.mit.edu/6.828/2007/tools.html. Then run "make TOOLPREFIX=i386-jos-elf-".

To run xv6, you can use Bochs or QEMU, both PC simulators. Bochs makes debugging easier, but QEMU is much faster. To run in Bochs, run "make bochs" and then type "c" at the bochs prompt. To run in QEMU, run "make qemu". Both log the xv6 screen output to standard output.

To create a typeset version of the code, run "make xv6.pdf". This requires the "mpage" text formatting utility. See http://www.mesa.nl/pub/mpage/.

The numbers to the left of the file names in the table are sheet numbers. The source code has been printed in a double column format with fifty lines per column, giving one hundred lines per sheet (or page). Thus there is a convenient relationship between line numbers and sheet numbers.

# basic headers	# system calls	# pipes
01 types.h	23 traps.h	51 pipe.c
01 param.h	24 vectors.pl	
02 defs.h	24 trapasm.S	# string operations
03 x86.h	25 trap.c	53 string.c
05 asm.h	26 syscall.h	3
06 mmu.h	26 syscall.c	# low-level hardware
08 elf.h	28 sysproc.c	54 mp.h
		55 mp.c
# startup	# file system	56 lapic.c
09 bootasm.S	29 buf.h	58 ioapic.c
10 bootother.S	29 dev.h	59 picirq.c
11 bootmain.c	30 fcntl.h	60 kbd.h
12 main.c	30 stat.h	62 kbd.c
	31 file.h	62 console.c
# locks	31 fs.h	66 timer.c
13 spinlock.h	32 fsvar.h	
13 spinlock.c	33 ide.c	# user-level
	35 bio.c	67 initcode.S
# processes	36 fs.c	67 init.c
15 proc.h	44 file.c	68 usys.S
16 proc.c	45 sysfile.c	68 sh.c
21 swtch.S	50 exec.c	
22 kalloc.c		

The source listing is preceded by a cross-reference that lists every defined constant, struct, global variable, and function in xv6. Each entry gives, on the same line as the name, the line number (or, in a few cases, numbers) where the name is defined. Successive lines in an entry list the line numbers where the name is used. For example, this entry:

```
swtch 2256
0311 1928 1962 2255
2256
```

indicates that swtch is defined on line 2256 and is mentioned on five lines on sheets 03. 19. and 22.

acquire 1375	2911 3387 3413 3418 3460 3479 3618 bfree 3730 3730 4060 4070 bget 3565 3565 3596 3606 binit 3538 0210 1224 3538 bmap 4010	7020 7021 7025 bufhead 3535 3535 3551 3552 3554 3555 3556 3557 3573 3587 3633 3634 3635 3636 buf_table_lock 3530 3530 3542 3569 3577 3581 3592 3629 3641 B_VALID 2910	7248 7250 7253 7255
0311 1375 1379 1632	3460 3479 3618 bfree 3730	bufhead 3535	7258 7260 7263 7264
	bfree 3730	3535 3551 3552 3554	7275 7278 7281 7285
1967 1980 2023 2058	3730 4060 4070	3555 3556 3557 3573	7300 7303 7308 7312
2265 2312 2549 2870	bget 3565	3587 3633 3634 3635	7313 7316 7321 7322
3406 3465 3569 3629	3565 3596 3606	3636	7328 7337 7338 7344
3807 3840 3860 3889	bget 3565 3565 3596 3606 binit 3538 0210 1224 3538 bmap 4010 4010 4047 4119 4169 4222 bootmain 1116 0975 1116 bootothers 1267 1207 1236 1267 BPB 3193 3193 3196 3712 3714 3740 bread 3602 0211 3602 3683 3694 3713 3739 3867 3961	buf_table_lock 3530	7345 7351 7352 7361
3904 3914 4423 4440	0210 1224 3538	3530 3542 3569 3577	7364 7366 7372 7373
4456 5217 5255 5278	bmap 4010	3581 3592 3629 3641	7378 7384 7390 7391
6385 6440 6466 6508	4010 4047 4119 4169	3381 3392 3629 3641 B_VALID 2910 2910 3417 3460 3479 3574 3607 bwrite 3614 0213 3614 3617 3696 3718 3745 3966 3990 4041 4172 bzero 3690 3690 3736 C 6081 6459 6081 6129 6154 6155 6156 6157 6158 6160 6459 6469 6472 6479 6489 6519 CAPSLOCK 6062 6062 6095 6236 cga_putc 6301 6301 6342 cli 0464 0464 0466 0914 1027 1460 6336 6570 cmd 6865 6865 6877 6886 6887	7394
allocproc 1627	4222	2910 3417 3460 3479	CONSOLE 2957
162/ 1/15	bootmain 1116	35/4 360/	2957 6556 6557
alltraps 2456	0975 1116	bwrite 3614	console_init 6551
2410 2418 2432 2437	bootothers 1267	0213 3614 3617 3696	0216 1231 6551
2455 2456	1207 1236 1267	3718 3745 3966 3990	console_intr 6462
ALT 6060	BPB 3193	4041 4172	0218 6248 6462
6060 6088 6090	3193 3196 3712 3714	bzero 3690	console_lock 6270
argfd 4564	3740	3690 3736	6270 6385 6431 6440
4564 4607 4619 4630	bread 3602	C 6081 6459	6443 6553
4644 4656	0211 3602 3683 3694	6081 6129 6154 6155	console_read 6501
argint 2694	3713 3739 3867 3961	6156 6157 6158 6160	6501 6557
0329 2694 2708 2724	3740 bread 3602 0211 3602 3683 3694 3713 3739 3867 3961 3982 4032 4066 4119 4169 4222 brelse 3624 0212 3624 3627 3685 3697 3719 3723 3746 3875 3967 3970 3991	6459 6469 6472 6479	console_write 6435
2837 2856 2868 4569	4169 4222	6489 6519	6435 6556
4607 4619 4858 4909	brelse 3624	CAPSLOCK 6062	cons_putc 6333
4910 4957	0212 3624 3627 3685	6062 6095 6236	6333 6372 6396 6414
argptr 2704	3697 3719 3723 3746	cga_putc 6301	6417 6421 6422 6442
0330 2704 4607 4619	3875 3967 3970 3991	6301 6342	6476 6482 6488
4656 4982	4037 4043 4072 4122	cli 0464	context 1515
argstr 2721	4173 4233 4237	0464 0466 0914 1027	0201 0308 1515 1540
0331 2721 4668 4758	BSIZE 3157	1460 6336 6570	1560 1745 1746 1747
4858 4908 4923 4935	3157 3169 3187 3193	cmd 6865	1832 1864 2129
4957	3695 4119 4120 4121	6865 6877 6886 6887	copyproc 1709
BACK 6861	4165 4166 4169 4170	6865 6865 6877 6886 6887 6892 6893 6898 6902 6906 6915 6918 6923 6931 6937 6941 6951 6975 6977 7052 7055 7057 7058 7059 7060 7063 7064 7066 7068	0292 1709 1762 2812
6861 6974 7120 7389	4171 4221 4222 4224	6906 6915 6918 6923	cp 1573
backcmd 6896 7114	buf 2900	6931 6937 6941 6951	1573 1657 1660 1661
6896 6909 6975 7114	0200 0211 0212 0213	6975 6977 7052 7055	1662 1663 1664 1665
7116 7242 7355 7390	0253 2900 2904 2905	7057 7058 7059 7060	1666 1667 1857 1864
BACKSPACE 6266	2906 3310 3325 3375	7063 7064 7066 7068	1872 1886 1905 1923
6266 6284 6313 6476	3404 3454 3456 3459	7069 7070 7071 7072	1924 1928 2009 2014
6482	3527 3529 3535 3540	7073 7074 7075 7076	2015 2016 2020 2021
balloc 3704	3553 3564 3567 3577	7079 7080 7082 7084	2026 2030 2038 2039
3704 3725 4019 4030	3601 3604 3614 3624	7085 7086 7087 7088	2066 2084 2090 2537
4040	3639 3669 3681 3692	7089 7100 7101 7103	2539 2541 2571 2579
BBLOCK 3196	3707 3732 3854 3955	7105 7106 7107 7108	2580 2586 2591 2696
3196 3713 3739	3979 4013 4055 4105	7109 7110 7113 7114	2710 2712 2726 2778
4607 4619 4858 4909 4910 4957 argptr 2704 0330 2704 4607 4619 4656 4982 argstr 2721 0331 2721 4668 4758 4858 4908 4923 4935 4957 BACK 6861 6861 6974 7120 7389 backcmd 6896 7114 6896 6909 6975 7114 7116 7242 7355 7390 BACKSPACE 6266 6266 6284 6313 6476 6482 balloc 3704 3704 3725 4019 4030 4040 BBLOCK 3196 3196 3713 3739 B_BUSY 2909 2909 3458 3574 3576	4155 4215 6354 6366	7116 7118 7119 7120	2780 2783 2784 2812
2909 3458 3574 3576	6369 6372 6435 6442	7121 7122 7212 7213	2845 2873 4361 4571
3580 3588 3589 3616	6453 6474 6487 6518	7214 7215 7217 7221	4588 4589 4646 4943
3626 3638	6984 6987 6988 6989	7224 7230 7231 7234	4944 4963 4969 4989
B_DIRTY 2911	7003 7015 7016 7019	7237 7239 7242 7246	5097 5101 5102 5103

F104 F10F F10C F3F0	4227 4220 4201 4665	E170 C070 C022 C024	0226 2052 2072 4021
5104 5105 5106 5258	4327 4328 4391 4665	51/0 68/8 6933 6934	0236 3952 3972 4821
5280 6511	4327 4328 4391 4665 4755 4805 disk_1_present 3327 3327 3364 3462 DPL_USER 0664 0664 1691 1692 1767 1768 2522 2586 E0ESC 6066	/004 /0/2 /2/2	0236 3952 3972 4821 IBLOCK 3190 3190 3867 3961 3982 I_BUSY 3266 3266 3861 3863 3886 3890 3907 3909 3915 ICRHI 5674 5674 5737 5821 5833 ICRLO 5667 5667 5738 5739 5822
cprintf 6377	disk_1_present 3327	filealloc 4419	3190 3867 3961 3982
0217 1221 1255 1262	3327 3364 3462	0225 4419 4874 5176	T_RN21 3500
2127 2131 2133 2235	DPL_USER 0664	fileclose 4452	3200 3801 3803 3880
2328 2565 2573 2578	0664 1691 1692 1767	U220 2U13 4432 4438	3890 3907 3909 3913
2782 5637 5761 5912	1768 2522 2586	44/3 404/ 48/0 4990	ICKHI 30/4
6377 6572 6573 6574		4991 3203 3209	30/4 3/3/ 3821 3833
6577	6066 6220 6224 6225	filedup 4438	ICKLU 5007
cpu 1557 5751	6227 6230	filedup 4438 0227 1740 4438 4442 4634	500/ 5/38 5/39 5822
0256 0269 1221 1255	elfhdr 0805	4034	3024 3034
1257 1258 1260 1262	0805 1118 1122 5014	fileinit 4412	ID 5660
1271 1279 1306 1367	ELF_MAGIC 0802	0228 1229 4412 fileread 4502	5660 5693 5766
1391 1408 1442 1461	0802 1128 5029	fileread 4502	IDE_BSY 3312
1462 1470 1472 1557	ELF_PROG_LOAD 0836	0229 4502 4517 4609	3312 3336
1568 1675 1678 1794	0836 5034 5061 EOI 5663	0229 4502 4517 4609 filestat 4478 0230 4478 4658	IDE_CMD_READ 3317 3317 3391
1811 1814 1861 1864		0230 4478 4038	331/ 3391
2548 2565 2566 2573	5663 5734 5775	file_table_lock 4408 4408 4414 4423 4428 4432 4440 4444 4456	IDE_CMD_WRITE 3318
2574 2578 2579 5512	ERROR 5681	4408 4414 4423 4428	3318 3388
5513 5751 5761 6572	5681 5727	4432 4440 4444 4450	IDE_DF 3314
create 4801	ESR 5666	4460 4466	3314 3338
4801 4843 4862 4911	5666 5730 5731	filewrite 4522	IDE_DRDY 3313
4923	EXEC 6857	0231 4522 4537 4621	3313 3336
CRTPORT 6264	6857 6922 7059 7365 execcmd 6869 7053	FL_IF 0610	IDE_ERR 3315
6264 6306 6307 6308	execcmd 6869 7053	0610 1462 1468 1771	3315 3338
6309 6325 6326 6327	6869 6910 6923 7053 7055 7321 7327 7328 7356 7366 exit 2004	1855 5758	ide_init 3351
6328	7055 7321 7327 7328	fork1 7039	0251 1232 3351
CTL 6059	7356 7366	6900 6942 6954 6961 6976 7024 7039	1de_1ntr 3402
6059 6085 6089 6235	exit 2004	69/6 /024 /039	0252 2557 3402
curproc 1789	0294 2004 2041 2336	forkret 1880	0252 2557 3402 ide_lock 3324 3324 3355 3406 3408 3425 3465 3480 3482 ide_rw 3454 0253 3454 3459 3461 3608 3619 ide_start_request 3375 3328 3375 3378 3423
0293 1559 1573 1789	2542 2587 2822 6715	1014 1/40 1880	3324 3333 3400 3408
1794 1829 1836	6718 6776 6781 6811	forkret1 2484	3425 3465 3480 3482
devsw 2950	6916 6925 6935 6980	1015 1886 2483 2484	1de_rw 3454
2950 2955 4108 4110	7028 7035	gatedesc 0751	0253 3454 3459 3461
4158 4160 4407 6556 6557	TGAIIOC 4583	0414 0417 0751 2510	3608 3619
000/ dinada 2172	4383 4032 48/4 498/	getcallerpcs 1422	10e_Start_request 3375
dinode 3173	fdalloc 4583 4583 4632 4874 4987 fetchint 2666 0332 2666 2696 4963 fetchstr 2678 0333 2678 2726 4969	0312 1392 1422 2129	3328 3375 3378 3423
3173 3187 3855 3868	U332 2666 2696 4963	6575	3475
3956 3962 3980 3983	TETCHSTR 2678	getcmd 6984 6984 7015 gettoken 7156 7156 7241 7245 7257	ide_wait_ready 3332
dirent 3203	0333 2678 2726 4969	0984 7015	3332 3358 3380 3413
3203 4216 4223 4224	file 3100 0202 0225 0226 0227 0229 0230 0231 0286 1538 3100 4403 4409 4418 4425 4426 4427 4429 4437 4438 4452	gettoken 7156	idtinit 2528
4255 4705 4754	0202 0225 0226 0227	/156 /241 /245 /25/	0340 1256 2528
dirlink 4252	0229 0230 0231 0286		idup 3838
0234 4252 4267 4275	1538 3100 4403 4409	7333	0237 1741 3838 4361
4684 4831 4842	4418 4425 4426 4427	growproc 1653	iget 3803
dirlookup 4212	4429 4437 4438 4452	0295 1653 2858	3803 3823 3968 4234
0235 4212 4219 4259	4454 4478 4502 4522	holding 1440	4359
4374 4770 4811	4558 4564 4567 4583	0313 1378 1404 1440 1859	11n1t 3/89
DIRSIZ 3201	4603 4615 4627 4642	1033	0230 1230 3703
3201 3205 4205 4272	4653 4855 4979 5155	ialloc 3952	ilock 3852

	CO74
0239 3852 3858 3878 5884 5909 5910 4371 4483 4514 4534 KEY_RT 4364 4481 4511 4531 ioapic_write 5891 4679 4880 4942 6439 607	74 6117 6139 6163
4364 4481 4511 4531 ioapic_write 5891 4679 4880 4942 6439 607 4672 4683 4693 4762 5891 5917 5918 5931 6506 KEY_UP	
-	71 6115 6137 6161
4867 4937 5020 6444	
	62 1664 1731 2069
	70 2236 2255 2260
	01 5111 5202 5228
3336 3363 5646 6214	
	96 1976 2578 2839
INDIRECT 3168 6016 6017 6020 6029 4925 4939 5069 5112 681	
·	63 1227 2225
initlock 1363 5800 5813 5814 4178 4678 4695 4790 KSTACKS	SIZE 0152
	52 1283 1284 1681
	19 1723 1731 2070
	eoi 5772
	71 2554 2558 2562
	67 5772
	init 5701
· · · · · · · · · · · · · · · · · · ·	72 1220 1258 5701
	startap 5805
·	73 1286 5805
3675 3785 3802 3805 IRQ_ERROR 2384 2312 2316 2322 2326 Tapicw	
	90 5707 5713 5714
	15 5718 5719 5724
-	27 5730 5731 5734
•	37 5738 5743 5775
-	21 5822 5824 5833
4388 4395 4666 4702 IRQ_OFFSET 2379 0266 2561 6246 583	34
4753 4800 4804 4856 2379 2547 2556 2560 KBS_DIB 6053 1gdt 04	403
4903 4921 4933 5015 2564 2591 5707 5714 6053 6215 040	03 0411 0954 1054
6435 6501 5727 5917 5931 5997 KBSTATP 6052 170	00
INPUT_BUF 6450 6016 6052 6214 1idt 04	1 17
6450 6453 6474 6486 IRQ_SLAVE 5960 KEY_DEL 6078 041	17 0425 2530
6487 6489 6518 5960 5964 6002 6017 6078 6119 6141 6165 LINTO 5	5679
insl 0362	79 5718
0362 1173 3414 2385 2564 5707 6072 6115 6137 6161 LINT1 5	5680
	80 5719
5869 5917 2381 2547 2591 5714 6070 6118 6140 6164 LIST 68	360
	60 6940 7107 7383
5858 5908 isdirempty 4702 6069 6118 6140 6164 listcmo	d 6890 7101
ioapic_enable 5923 4702 4709 4778 KEY_INS 6077 689	90 6911 6941 7101
0256 3357 5923 6561 ismp 5514 6077 6119 6141 6165 710	03 7246 7357 7384
ioapic_id 5516 0276 1233 5514 5613 KEY_LF 6073 LPTPORT	
0257 5516 5628 5911 5905 5925 6073 6117 6139 6163 626	65 6282 6286 6287
10apic_enable 5923 4702 4709 4778 KEY_INS 6077 688 0256 3357 5923 6561 ismp 5514 6077 6119 6141 6165 710 ioapic_id 5516 0276 1233 5514 5613 KEY_LF 6073 LPTPORT 0257 5516 5628 5911 5905 5925 6073 6117 6139 6163 626 5912 itrunc 4052 KEY_PCDN 6076 628 ioanic init 5901 33675 3911 4052 6076 6116 6138 6162 Int put	
10ap (C_1111c 3301	
	78 6341
ioapic_read 5884 0241 3884 3887 3926 6075 6116 6138 6162 ltr 042	29

0429 0431 1701	5428 5606 5619 5624	nulterminate 7352	4537 4709 4777 4786
MAXARGS 6863	mp_search 5556	7215 7230 7352 7373	4843 5638 6565 6572
6863 6871 6872 7340	5556 5585	7379 7380 7385 7386	6901 6920 6953 7032
MAXFILE 3170	mp_search1 5537	7391	7045 7228 7272 7306
3170 4165 4166	5537 5564 5568 5571	NUMLOCK 6063	7310 7336 7341
memcmp 5315	NADDRS 3166	6063 6096	parseblock 7301
0320 5315 5543 5588	3166 3179 3263	O_CREATE 3003	7301 7306 7325
memmove 5331	namecmp 4203	3003 4861 7278 7281	parsecmd 7218
0321 1276 1660 1727	0244 4203 4228 4765	O_RDONLY 3000	6902 7025 7218
1736 1780 3684 3874	namei 4389	3000 7275	parseexec 7317
3989 4121 4171 4328	0245 1765 4389 4670	O_RDWR 3002	7214 7255 7317
4330 5080 5331 6320	4865 4935 5018	3002 4868 4886 6764	•
memset 5303	_namei 4354	6766 7007	7212 7224 7235 7246
0322 1217 1661 1745	4354 4392 4398	outb 0371	7308
1766 2263 3695 3964	nameiparent 4396	0371 0933 0941 1164	parsepipe 7251
4784 4959 5055 5067	0246 4396 4681 4760	1165 1166 1167 1168	7213 7239 7251 7258
5303 6322 6987 7058	4807	1169 3361 3370 3381	parseredirs 7264
7069 7085 7106 7119	NBUF 0156	3382 3383 3384 3385	7264 7312 7331 7342
microdelay 5781	0156 3529 3553	3386 3388 3391 5645	PCINT 5678
5781 5823 5825 5835	NCPU 0153	5646 5813 5814 5970	5678 5724
min 3674	0153 1568 5512	5971 5985 5986 5994	peek 7201
3674 4120 4170	NDEV 0158	5997 6002 6012 6015	7201 7225 7240 7244
mp 5402	0158 4108 4158 4407	6016 6017 6020 6026	7256 7269 7305 7309
5402 5507 5536 5542	NDIRECT 3167	6027 6029 6030 6286	7324 7332
5543 5544 5555 5560	3166 3167 3170 4015	6287 6288 6306 6308	pic_enable 5975
5564 5565 5568 5569	4023 4058	6325 6326 6327 6328	0282 3356 5975 6560
5580 5583 5585 5587	NELEM 0346	6627 6628 6629	6630
5594 5604 5610 5642	0346 2123 2779 4961	outsl 0383	pic_init 5982
mp_bcpu 5519	NFILE 0155	0383 3389	0283 1225 5982
0277 1220 1257 5519	0155 4409 4424	outw 0377	pic_setmask 5967
MPBUS 5452	NINDIRECT 3169	0377 1143 1144	5967 5977 6033
5452 5631	3169 3170 4025 4068	O_WRONLY 3001	pinit 1618
mpconf 5413	NINODE 0157	3001 4868 4885 4886	0297 1223 1618
5413 5579 5582 5587	0157 3785 3811	7278 7281	pipe 5160
5605	NO 6056	PAGE 0151	0204 0287 0288 0289
<pre>mp_config 5580</pre>	6056 6102 6105 6107	0151 0152 1763 2233	3105 4469 4509 4529
5580 5610	6108 6109 6110 6112	2235 2236 2259 2309	5160 5172 5178 5184
mp_init 5601	6124 6127 6129 6130	5049 5051 5178 5202	5188 5192 5215 5251
0278 1219 5601 5637	6131 6132 6134 6152	5228	5274 6813 6952 6953
5638	6153 6155 6156 6157	panic 6565 7032	pipealloc 5170
mpioapic 5439	6158	0219 1379 1405 1469	0286 4984 5170
5439 5607 5627 5629	NOFILE 0154	1471 1856 1858 1860	pipeclose 5215
MPIOINTR 5454	0154 1538 1738 2013	1862 1906 1909 2010	0287 4469 5215
5454 5632	4571 4587	2041 2260 2271 2310	pipecmd 6884 7080
MPLINTR 5455	NPROC 0150	2575 3378 3459 3461	6884 6912 6951 7080
5455 5633	0150 1610 1633 1821	3463 3596 3617 3627	7082 7258 7358 7378
mpmain 1253	1957 1981 2029 2062	3725 3743 3823 3858	piperead 5274
1208 1239 1253 1255	2119	3878 3887 3908 3972	0288 4509 5274
1285	NSEGS 1506	4047 4219 4267 4275	PIPESIZE 5158
mpproc 5428	1506 1562	4442 4458 4473 4517	5158 5166 5257 5266

F300	1100 1105	SCROLLLOCK 6064 6064 6097 SECTSIZE 1111 1111 1125 1173 1186 1189 1194 SEG 0654 0654 1686 1687 1691 1692 SEG16 0659 0659 1688 SEG_ASM 0558 0558 0985 0986 1081 1082 Segdesc 0627 0400 0403 0627 0651 0654 0659 1562 SEG_KCODE 1501 1501 1686 2521 2522 SEG_KDATA 1502 1502 1679 1687 SEG_NULL 0651 0651 1685 1694 1695 SEG_TSS 1505 1505 1688 1689 1701 SEG_UCODE 1503 1501 1691 1694 1767 SEG_UDATA 1504 1504 1692 1695 1768 SETGATE 0771 0771 2521 2522 Setupsegs 1673	2200 2224 2526 2520
5290	1160 1195	SCRULLLUCK 6064	3309 3324 3526 3530
pipewrite 5251	readseg 1179	0004 0097	3668 3784 4404 4408
0289 4529 5251	1113 1123 1133 1179	SECISIZE IIII	5156 5165 6258 6270
0217 1417 1466 1460	KEDIK 0000 6050 6020 7070 7271	1111 1125 1173 1180	0432 CTA D 0567 0671
1471 1702 1705	0000 0900 7070 7071	1109 1194	SIA_K U307 U071
14/1 1/02 1/93	607E 6012 6021 7064	0654 1696 1697 1601	0567 0671 0985 1081
6251 6402 6407	7066 7275 7279 7291	1602	1000 1091 c+an+ 0012 1025 6707
0331 0403 0407	7000 7273 7270 7201	2002	0911 0912 0974 1024
020E 0202 0202 0200	7339 7372 DEC TD 5960	0650 1600	1025 1073 1074 2229
0203 0292 0293 0300	5860 5010	CEC VCW U228	2232 2233 2236 6706
1520 1535 1550 1605	REC TARLE 5862	0558 0085 0086 1081	6707
1610 1611 1626 1630	5862 5017 5018 5021	1082	c+a+ 3050
1634 1673 1708 1709	5032	sandasc 0627	0207 0230 0248 3050
1712 1750 1788 1791	REC. VER. 5861	0400 0403 0627 0651	3665 4082 4478 4553
1810 1822 1955 1957	5861 5000	0654 0659 1562	1654 6753
1978 1981 2006 2029	release 1402	SEC KCODE 1501	stati 4082
2055 2063 2115 2120	0315 1402 1405 1638	1501 1686 2521 2522	0248 4082 4482
2504 2578 2654 2666	1642 1839 1874 1883	SEC KDATA 1502	STA W 0566 0670
2678 2804 2810 3306	1919 1932 1969 1987	1502 1679 1687	0566 0670 0986 1082
3667 4555 5003 5154	1991 2076 2085 2293	SEG NULL 0651	1687 1692
5510 5606 5619 5620	2316 2322 2326 2552	0651 1685 1694 1695	STA X 0563 0667
5621 6261	2874 2879 3408 3425	SEG NULLASM 0554	0563 0667 0985 1081
procdump 2104	3482 3581 3592 3641	0554 0984 1080	1686 1691
0298 2104 6470	3814 3830 3842 3864	SEG TSS 1505	sti 0470
proc table lock 1608	3892 3910 3919 4428	1505 1688 1689 1701	0470 0472 1473 1817
1608 1620 1632 1638	4432 4444 4460 4466	SEG_UCODE 1503	strlen 5389
1642 1820 1839 1859	5225 5259 5269 5281	1503 1691 1694 1767	0324 5044 5078 5389
1860 1871 1874 1883	5293 6431 6443 6497	SEG_UDATA 1504	7019 7223
1917 1918 1931 1932	6512 6532	1504 1692 1695 1768	strncmp 5351
1967 1969 1980 1987	ROOTDEV 0159	SETGATE 0771	0325 4205 5351
1991 2023 2058 2076	0159 4359	0771 2521 2522	strncpy 5361
2085 2090	run 2214	setupsegs 1673	0326 4272 5361
proghdr 0824	2111 2214 2215 2218	0300 1259 1666 1673	STS_IG32 0685
0824 1119 1132 5016	2257 2266 2267 2269	1830 1837 5106	0685 0777
pushcli 1455	2307	SHIFT 6058	STS_T32A 0682
0316 1377 1455 1677	runcmd 6906	6058 6086 6087 6235	0682 1688
1793	6906 6920 6937 6943	skipelem 4314	STS_TG32 0686
read_ebp 0392	6945 6959 6966 6977	4314 4363	0686 0777
0392 5762	7025	sleep 1903	STUB 6803 6810 6811 6812 6813 6814
read_eflags 0435	RUNNING 1526	0301 1903 1906 1909	6810 6811 6812 6813
0435 1459 1468 1855	1526 1831 1857 2111	2090 2109 2877 3480	6814 6815 6816 6817
5758	2591	3577 3862 5263 5284	6818 6819 6820 6821
readi 4102	safestrcpy 5375	6516 6829	6822 6823 6824 6825
0247 4102 4266 4512	0323 1781 5097 5375	spinlock 1301	6826 6827 6828 6829
4708 4709 5027 5032	sched 1853	0206 0301 0311 0313	sum 5525
5059 5065	1853 1856 1858 1860	0314 0315 0343 1301	5525 5527 5529 5531
readsb 3679	1862 1873 1925 2040	1358 1363 1375 1402	5532 5543 5592
3679 3711 3738 3959	scheduler 1808	1440 1606 1608 1903	superblock 3160
readsect 1160	1160 1195 readseg 1179 1113 1125 1135 1179 REDIR 6858 6858 6930 7070 7371 redircmd 6875 7064 6875 6913 6931 7064 7066 7275 7278 7281 7359 7372 REG_ID 5860 5860 5910 REG_TABLE 5862 5862 5917 5918 5931 5932 REG_WER 5861 5861 5909 release 1402 0315 1402 1405 1638 1642 1839 1874 1883 1919 1932 1969 1987 1991 2076 2085 2293 2316 2322 2326 2552 2874 2879 3408 3425 3482 3581 3592 3641 3814 3830 3842 3864 3892 3910 3919 4428 4432 4444 4460 4466 5225 5259 5269 5281 5293 6431 6443 6497 6512 6532 ROOTDEV 0159 0159 4359 run 2214 2111 2214 2215 2218 2257 2266 2267 2269 2307 runcmd 6906 6906 6920 6937 6943 6945 6959 6966 6977 7025 RUNNING 1526 1526 1831 1857 2111 2591 safestrcpy 5375 0323 1781 5097 5375 sched 1853 1853 1856 1858 1860 1862 1873 1925 2040 scheduler 1808 0299 1263 1808	2210 2212 2507 2512	7019 7223 strncmp 5351 0325 4205 5351 strncpy 5361 0326 4272 5361 STS_IG32 0685 0685 0777 STS_T32A 0682 0682 1688 STS_TG32 0686 0686 0777 STUB 6803 6810 6811 6812 6813 6814 6810 6811 6812 6813 6814 6815 6816 6817 6818 6819 6820 6821 6822 6823 6824 6825 6826 6827 6828 6829 sum 5525 5525 5527 5529 5531 5532 5543 5592 superblock 3160 3160 3679 3708 3733

20	TD CD - 500-
3957	TDCR 5685
SVR 5664	5685 5713
5664 5707	T_DEV 3184
swtch 2156	3184 4107 4157 4911
0308 1832 1864 2155	T_DIR 3182
2156	3182 4218 4365 4673
syscall 2774	4778 4838 4868 4923
0334 2540 2656 2774	4938
SYS_chdir 2616	T_FILE 3183
2616 2751	3183 4862
SYS_close 2607	ticks 2513
2607 2752	0341 2513 2550 2551
SYS_dup 2617	2871 2872 2877
2617 2753	tickslock 2512
SYS_exec 2609	0343 2512 2524 2549
2609 2754 6711	2552 2870 2874 2877
SYS_exit 2602	2879
2602 2755 6716	TICR 5683
SYS_fork 2601	5683 5715
2601 2756	TIMER 5675
SYS_fstat 2613	5675 5714
2613 2757	TIMER_16BIT 6621
SYS_getpid 2618	6621 6627
2618 2758	TIMER_DIV 6616
SYS_kill 2608	6616 6628 6629
2608 2759	TIMER_FREQ 6615
SYS_link 2614	6615 6616
2614 2760	timer_init 6624
SYS_mkdir 2615	0337 1234 6624
2615 2761	TIMER_MODE 6618
SYS_mknod 2611	6618 6627
2611 2762	TIMER_RATEGEN 6620
SYS_open 2610	6620 6627
2610 2763	TIMER_SELO 6619
SYS_pipe 2604	6619 6627
2604 2764	TPR 5662
SYS_read 2606	5662 5743
2606 2765	trap 2534
SYS_sbrk 2619	2402 2404 2469 2534
2619 2766	2573 2575 2578
SYS_sleep 2620	trapframe 0477
2620 2767	0477 1541 1615 1723
SYS_unlink 2612	2534
2612 2768	trapret 2474
SYS_wait 2603	2473 2474 2486
2603 2769	T_SYSCALL 2376
SYS_write 2605	2376 2522 2536 6712
2605 2770	6717 6807
taskstate 0701	tvinit 2516
0701 1561	0342 1228 2516

userinit 1757	3639 3891 3916	5220
0302 1235 1757	5223 5262 5268	5292
VER 5661	6491	
5661 5723	wakeup1 1953	
wait 2053	1953 1968 2026	2033
0303 2053 2829 63	783 writei 4152	
6812 6944 6970 69	971 0249 4152 4274	4532
7026	4785 4786	
waitdisk 1151	xchg 0451	
1151 1163 1172	0451 1260 1384	1415
wakeup 1965	yield 1869	
0304 1965 2551 3	419 0305 1869 2592	

0100 0101	typedef typedef	unsigned unsigned		uint; ushort;
0102	typedef			uchar;
	cypedei	ulis i glieu	Ciiai	uchai,
0103				
0104				
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```
0150 #define NPROC
                         64 // maximum number of processes
0151 #define PAGE
                       4096 // granularity of user-space memory allocation
0152 #define KSTACKSIZE PAGE // size of per-process kernel stack
                          8 // maximum number of CPUs
0153 #define NCPU
0154 #define NOFILE
                         16 // open files per process
0155 #define NFILE
                        100 // open files per system
0156 #define NBUF
                         10 // size of disk block cache
0157 #define NINODE
                         50 // maximum number of active i-nodes
0158 #define NDEV
                         10 // maximum major device number
0159 #define ROOTDEV
                          1 // device number of file system root disk
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0199
```

Sheet 01 Sheet 01

0200 struct buf;		0250 // ide.c	
0201 struct context;		0251 void	ide_init(void);
0202 struct file;		0252 void	ide_intr(void);
0203 struct inode;		0253 void	ide_rw(struct buf *);
0204 struct pipe;		0254	
0205 struct proc;		0255 // ioapic.c	
0206 struct spinlock	;	0256 void	<pre>ioapic_enable(int irg, int cpu);</pre>
0207 struct stat;	•	0257 extern uchar	ioapic_id;
0208		0258 void	<pre>ioapic_init(void);</pre>
0209 // bio.c		0259	
0210 void	<pre>binit(void);</pre>	0260 // kalloc.c	
0211 struct buf*	bread(uint, uint);	0261 char*	<pre>kalloc(int);</pre>
0212 void	brelse(struct buf*);	0262 void	kfree(char*, int);
0213 void	<pre>bwrite(struct buf*);</pre>	0263 void	kinit(void);
0214	Shirte (Struct Sur),	0264	Killie(Voluy)
0215 // console.c		0265 // kbd.c	
0216 void	<pre>console_init(void);</pre>	0266 void	<pre>kbd_intr(void);</pre>
0210 void	cprintf(char*,);	0267	kbu_intr(void),
0217 void 0218 void			
	console_intr(int(*)(void));	0268 // lapic.c	ما الأرباء أما الأرباء
0219 void	<pre>panic(char*)attribute((noreturn));</pre>	0269 int	cpu(void);
0220		0270 extern volatile	• •
0221 // exec.c		0271 void	lapic_eoi(void);
0222 int	exec(char*, char**);	0272 void	<pre>lapic_init(int);</pre>
0223		0273 void	<pre>lapic_startap(uchar, uint);</pre>
0224 // file.c		0274	
0225 struct file*	filealloc(void);	0275 // mp.c	
0226 void	<pre>fileclose(struct file*);</pre>	0276 extern int	ismp;
0227 struct file*	<pre>filedup(struct file*);</pre>	0277 int	<pre>mp_bcpu(void);</pre>
0228 void	<pre>fileinit(void);</pre>	0278 void	<pre>mp_init(void);</pre>
0229 int	<pre>fileread(struct file*, char*, int n);</pre>	0279 void	<pre>mp_startthem(void);</pre>
0230 int	<pre>filestat(struct file*, struct stat*);</pre>	0280	
0231 int	<pre>filewrite(struct file*, char*, int n);</pre>	0281 // picirq.c	
0232		0282 void	<pre>pic_enable(int);</pre>
0233 // fs.c		0283 void	<pre>pic_init(void);</pre>
0234 int	<pre>dirlink(struct inode*, char*, uint);</pre>	0284	
0235 struct inode*	<pre>dirlookup(struct inode*, char*, uint*);</pre>	0285 // pipe.c	
0236 struct inode*	<pre>ialloc(uint, short);</pre>	0286 int	<pre>pipealloc(struct file**, struct file**);</pre>
0237 struct inode*	<pre>idup(struct inode*);</pre>	0287 void	<pre>pipeclose(struct pipe*, int);</pre>
0238 void	<pre>iinit(void);</pre>	0288 int	<pre>piperead(struct pipe*, char*, int);</pre>
0239 void	<pre>ilock(struct inode*);</pre>	0289 int	<pre>pipewrite(struct pipe*, char*, int);</pre>
0240 void	<pre>iput(struct inode*);</pre>	0290	p.p ,, ,
0241 void	<pre>iunlock(struct inode*);</pre>	0291 // proc.c	
0242 void	<pre>iunlockput(struct inode*);</pre>	0292 struct proc*	<pre>copyproc(struct proc*);</pre>
0243 void	<pre>iupdate(struct inode*);</pre>	0292 struct proc*	curproc(void);
0244 int	namecmp(const char*, const char*);	0294 void	exit(void);
0245 struct inode*	namei(char*);	0294 VOIU 0295 int	
0246 struct inode*		0296 int	<pre>growproc(int); kill(int);</pre>
	nameiparent(char*, char*);		
0247 int	readi(struct inode*, char*, uint, uint);	0297 void	pinit(void);
0248 void	stati(struct inode*, struct stat*);	0298 void	procdump(void);
0249 int	<pre>writei(struct inode*, char*, uint, uint);</pre>	0299 void	<pre>scheduler(void)attribute((noreturn));</pre>

Sheet 02 Sheet 02

```
0300 void
                     setupsegs(struct proc*);
                                                                                  0350 // Routines to let C code use special x86 instructions.
0301 void
                     sleep(void*, struct spinlock*);
                                                                                  0351
0302 void
                     userinit(void);
                                                                                  0352 static inline uchar
0303 int
                                                                                  0353 inb(ushort port)
                     wait(void);
0304 void
                     wakeup(void*);
                                                                                  0354 {
0305 void
                                                                                  0355 uchar data;
                     yield(void);
0306
                                                                                  0356
0307 // swtch.S
                                                                                  0357
                                                                                        asm volatile("in %1,%0" : "=a" (data) : "d" (port));
0308 void
                     swtch(struct context*, struct context*);
                                                                                  0358
                                                                                         return data;
                                                                                  0359 }
0309
0310 // spinlock.c
                                                                                  0360
0311 void
                                                                                  0361 static inline void
                     acquire(struct spinlock*);
0312 void
                     getcallerpcs(void*, uint*);
                                                                                  0362 insl(int port, void *addr, int cnt)
0313 int
                     holding(struct spinlock*);
                                                                                  0363 {
                     initlock(struct spinlock*, char*);
                                                                                  0364
0314 void
                                                                                        asm volatile("cld\n\trepne\n\tinsl"
0315 void
                     release(struct spinlock*);
                                                                                  0365
                                                                                                          "=D" (addr), "=c" (cnt)
                                                                                                          "d" (port), "0" (addr), "1" (cnt) :
0316 void
                     pushcli();
                                                                                  0366
                                                                                                          "memory", "cc");
0317 void
                     popcli();
                                                                                  0367
0318
                                                                                  0368 }
0319 // string.c
                                                                                  0369
0320 int
                     memcmp(const void*. const void*. uint):
                                                                                  0370 static inline void
0321 void*
                     memmove(void*. const void*. uint):
                                                                                  0371 outb(ushort port, uchar data)
0322 void*
                     memset(void*, int, uint);
                                                                                  0372 {
0323 char*
                     safestrcpy(char*, const char*, int);
                                                                                  0373 asm volatile("out %0,%1" : : "a" (data), "d" (port));
0324 int
                     strlen(const char*):
                                                                                  0374 }
0325 int
                     strncmp(const char*, const char*, uint);
                                                                                  0375
0326 char*
                     strncpy(char*, const char*, int);
                                                                                  0376 static inline void
0327
                                                                                  0377 outw(ushort port, ushort data)
0328 // syscall.c
                                                                                  0379 asm volatile("out %0,%1" : : "a" (data), "d" (port));
0329 int
                     argint(int, int*);
0330 int
                     argptr(int, char**, int);
                                                                                  0380 }
0331 int
                     argstr(int, char**);
                                                                                  0381
0332 int
                     fetchint(struct proc*, uint, int*);
                                                                                  0382 static inline void
0333 int
                     fetchstr(struct proc*, uint, char**);
                                                                                  0383 outsl(int port, const void *addr, int cnt)
0334 void
                     syscall(void);
                                                                                  0384 {
                                                                                  0385 asm volatile("cld\n\trepne\n\toutsl"
0335
0336 // timer.c
                                                                                  0386
                                                                                                          "=S" (addr), "=c" (cnt)
0337 void
                     timer_init(void);
                                                                                  0387
                                                                                                          "d" (port), "0" (addr), "1" (cnt) :
0338
                                                                                  0388
                                                                                                          "cc"):
0339 // trap.c
                                                                                  0389 }
0340 void
                     idtinit(void);
                                                                                  0390
                                                                                  0391 static inline uint
0341 extern int
                     ticks:
0342 void
                     tvinit(void);
                                                                                  0392 read_ebp(void)
0343 extern struct spinlock tickslock;
                                                                                  0393 {
0344
                                                                                  0394 uint ebp:
0345 // number of elements in fixed-size array
                                                                                  0395
0346 #define NELEM(x) (sizeof(x)/sizeof((x)[0]))
                                                                                  0396
                                                                                        asm volatile("movl %ebp, %0" : "=a" (ebp));
0347
                                                                                  0397
                                                                                         return ebp;
0348
                                                                                  0398 }
0349
                                                                                  0399
```

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```
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0450 static inline uint
0451 xchg(volatile uint *addr, uint newval)
0452 {
0453 uint result;
0454
0455 // The + in "+m" denotes a read-modify-write operand.
0456
     asm volatile("lock; xchgl %0, %1":
                   "+m" (*addr), "=a" (result) :
0457
0458
                   "1" (newval) :
                   "cc");
0459
0460 return result;
0461 }
0462
0463 static inline void
0464 cli(void)
0465 {
0466 asm volatile("cli");
0467 }
0468
0469 static inline void
0470 sti(void)
0471 {
0472 asm volatile("sti");
0473 }
0474
0475 // Layout of the trap frame built on the stack by the
0476 // hardware and by trapasm.S, and passed to trap().
0477 struct trapframe {
0478 // registers as pushed by pusha
0479 uint edi;
0480 uint esi;
0481 uint ebp;
0482 uint oesp;
                      // useless & ignored
0483 uint ebx;
0484 uint edx;
0485 uint ecx:
0486 uint eax;
0487
0488 // rest of trap frame
0489
     ushort es:
0490
      ushort padding1;
0491 ushort ds;
0492
      ushort padding2;
```

0493

0494

0496

0499

uint trapno;

ushort padding3;

uint err;

0497 uint eip;

0498 ushort cs;

0495 // below here defined by x86 hardware

```
0500
       uint eflags;
                                                                                   0550 //
0501
                                                                                   0551 // assembler macros to create x86 segments
0502
      // below here only when crossing rings, such as from user to kernel
                                                                                   0552 //
       uint esp;
                                                                                   0553
0503
0504
                                                                                   0554 #define SEG_NULLASM
       ushort ss;
0505
       ushort padding4;
                                                                                   0555
                                                                                                .word 0, 0;
                                                                                                                                                         \
0506 };
                                                                                   0556
                                                                                                .byte 0, 0, 0, 0
0507
                                                                                   0557
0508
                                                                                   0558 #define SEG_ASM(type,base,lim)
0509
                                                                                   0559
                                                                                                .word (((lim) >> 12) & 0xffff), ((base) & 0xffff);
0510
                                                                                   0560
                                                                                                .byte (((base) >> 16) & 0xff), (0x90 \mid (type)),
0511
                                                                                   0561
                                                                                                        (0xC0 \mid (((1im) >> 28) \& 0xf)), (((base) >> 24) \& 0xff)
0512
                                                                                   0562
0513
                                                                                   0563 #define STA_X
                                                                                                          0x8
                                                                                                                    // Executable segment
0514
                                                                                   0564 #define STA E
                                                                                                                    // Expand down (non-executable segments)
                                                                                                          0x4
0515
                                                                                   0565 #define STA_C
                                                                                                          0x4
                                                                                                                    // Conforming code segment (executable only)
0516
                                                                                   0566 #define STA_W
                                                                                                          0x2
                                                                                                                    // Writeable (non-executable segments)
0517
                                                                                   0567 #define STA R
                                                                                                          0x2
                                                                                                                    // Readable (executable segments)
0518
                                                                                   0568 #define STA_A
                                                                                                          0x1
                                                                                                                    // Accessed
0519
                                                                                   0569
0520
                                                                                   0570
0521
                                                                                   0571
0522
                                                                                   0572
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```

Sheet 05 Sheet 05

```
0600 // This file contains definitions for the
                                                                                  0650 // Null segment
0601 // x86 memory management unit (MMU).
                                                                                  0651 #define SEG_NULL
                                                                                                                (struct segdesc) { 0.0.0.0.0.0.0.0.0.0.0.0.0 }
0602
                                                                                  0652
0603 // Eflags register
                                                                                  0653 // Normal segment
0604 #define FL_CF
                             0x0000001
                                             // Carry Flag
                                                                                  0654 #define SEG(type, base, lim, dpl) (struct segdesc)
                                             // Parity Flag
0605 #define FL PF
                             0x00000004
                                                                                  0655 { ((lim) >> 12) & 0xffff, (base) & 0xffff, ((base) >> 16) & 0xff,
0606 #define FL AF
                             0x00000010
                                             // Auxiliary carry Flag
                                                                                           type, 1, dpl, 1, (uint) (lim) >> 28, 0, 0, 1, 1,
                                                                                  0656
0607 #define FL_ZF
                             0x00000040
                                                                                  0657
                                                                                           (uint) (base) >> 24 }
                                             // Zero Flag
0608 #define FL_SF
                             0x00000080
                                             // Sign Flag
                                                                                  0658
                                             // Trap Flag
0609 #define FL_TF
                             0x00000100
                                                                                  0659 #define SEG16(type, base, lim, dpl) (struct segdesc)
0610 #define FL_IF
                             0x00000200
                                             // Interrupt Enable
                                                                                  0660 { (lim) & 0xffff, (base) & 0xffff, ((base) >> 16) & 0xff,
0611 #define FL DF
                             0x00000400
                                             // Direction Flag
                                                                                  0661
                                                                                           type, 1, dpl, 1, (uint) (lim) >> 16, 0, 0, 1, 0,
                                             // Overflow Flag
                                                                                  0662
                                                                                           (uint) (base) >> 24 }
0612 #define FL OF
                             0x00000800
0613 #define FL_IOPL_MASK
                             0x00003000
                                             // I/O Privilege Level bitmask
                                                                                  0663
                                                 IOPL == 0
                                                                                  0664 #define DPL USER
0614 #define FL IOPL 0
                             0x00000000
                                                                                                                    // User DPL
0615 #define FL IOPL 1
                             0x00001000
                                             //
                                                  IOPL == 1
                                                                                  0665
0616 #define FL_IOPL_2
                             0x00002000
                                             //
                                                  IOPL == 2
                                                                                  0666 // Application segment type bits
0617 #define FL IOPL 3
                             0x00003000
                                             // IOPL == 3
                                                                                  0667 #define STA X
                                                                                                            0x8
                                                                                                                    // Executable segment
0618 #define FL NT
                             0x00004000
                                             // Nested Task
                                                                                  0668 #define STA E
                                                                                                                    // Expand down (non-executable segments)
                                                                                                            0x4
0619 #define FL_RF
                             0x00010000
                                             // Resume Flag
                                                                                  0669 #define STA_C
                                                                                                            0x4
                                                                                                                   // Conforming code segment (executable only)
0620 #define FL VM
                             0x00020000
                                             // Virtual 8086 mode
                                                                                  0670 #define STA W
                                                                                                            0x2
                                                                                                                   // Writeable (non-executable segments)
0621 #define FL AC
                             0x00040000
                                             // Alianment Check
                                                                                  0671 #define STA R
                                                                                                           0x2
                                                                                                                   // Readable (executable segments)
0622 #define FL_VIF
                             0x00080000
                                             // Virtual Interrupt Flag
                                                                                  0672 #define STA_A
                                                                                                            0x1
                                                                                                                    // Accessed
0623 #define FL_VIP
                             0x00100000
                                             // Virtual Interrupt Pending
                                                                                  0673
0624 #define FL ID
                             0x00200000
                                             // ID flag
                                                                                  0674 // System segment type bits
                                                                                  0675 #define STS_T16A
                                                                                                                    // Available 16-bit TSS
0625
                                                                                                           0x1
0626 // Segment Descriptor
                                                                                  0676 #define STS_LDT
                                                                                                            0x2
                                                                                                                    // Local Descriptor Table
0627 struct segdesc {
                                                                                  0677 #define STS_T16B
                                                                                                            0x3
                                                                                                                   // Busy 16-bit TSS
                                                                                                                   // 16-bit Call Gate
      uint lim_15_0 : 16; // Low bits of segment limit
                                                                                  0678 #define STS_CG16
                                                                                                            0x4
0628
0629
      uint base_15_0 : 16; // Low bits of segment base address
                                                                                  0679 #define STS_TG
                                                                                                            0x5
                                                                                                                   // Task Gate / Coum Transmitions
0630
      uint base_23_16 : 8; // Middle bits of segment base address
                                                                                  0680 #define STS_IG16
                                                                                                            0x6
                                                                                                                   // 16-bit Interrupt Gate
0631
      uint type : 4;
                            // Segment type (see STS_ constants)
                                                                                  0681 #define STS_TG16
                                                                                                            0x7
                                                                                                                   // 16-bit Trap Gate
0632
      uint s : 1;
                            // 0 = system, 1 = application
                                                                                  0682 #define STS T32A
                                                                                                            0x9
                                                                                                                   // Available 32-bit TSS
0633
      uint dpl : 2;
                            // Descriptor Privilege Level
                                                                                  0683 #define STS_T32B
                                                                                                            0xB
                                                                                                                   // Busy 32-bit TSS
                            // Present
0634
      uint p : 1;
                                                                                  0684 #define STS_CG32
                                                                                                            0xC
                                                                                                                   // 32-bit Call Gate
0635
      uint lim_19_16 : 4; // High bits of segment limit
                                                                                  0685 #define STS IG32
                                                                                                            0xE
                                                                                                                    // 32-bit Interrupt Gate
0636
      uint avl : 1;
                            // Unused (available for software use)
                                                                                  0686 #define STS_TG32
                                                                                                           0xF
                                                                                                                   // 32-bit Trap Gate
0637
       uint rsv1 : 1:
                            // Reserved
                                                                                  0687
0638
      uint db : 1:
                            // 0 = 16-bit segment, 1 = 32-bit segment
                                                                                  0688
0639
      uint q : 1;
                            // Granularity: limit scaled by 4K when set
                                                                                  0689
0640
      uint base_31_24 : 8; // High bits of segment base address
                                                                                  0690
0641 };
                                                                                  0691
0642
                                                                                  0692
0643
                                                                                  0693
0644
                                                                                  0694
0645
                                                                                  0695
0646
                                                                                  0696
0647
                                                                                  0697
0648
                                                                                  0698
0649
                                                                                  0699
```

Sheet 06 Sheet 06

```
0700 // Task state segment format
                                                                                 0750 // Gate descriptors for interrupts and traps
0701 struct taskstate {
                                                                                 0751 struct gatedesc {
                         // Old ts selector
0702
      uint link;
                                                                                 0752 uint off_15_0 : 16; // low 16 bits of offset in segment
0703
      uint esp0;
                         // Stack pointers and segment selectors
                                                                                 0753
                                                                                        uint cs : 16;
                                                                                                             // code segment selector
0704
      ushort ss0;
                         // after an increase in privilege level
                                                                                 0754
                                                                                       uint args : 5;
                                                                                                             // # args, 0 for interrupt/trap gates
                                                                                 0755 uint rsv1 : 3;
0705
                                                                                                             // reserved(should be zero I guess)
      ushort padding1;
0706
      uint *esp1;
                                                                                 0756
                                                                                       uint type : 4;
                                                                                                             // type(STS_{TG,IG32,TG32})
0707
      ushort ss1;
                                                                                 0757
                                                                                       uint s : 1;
                                                                                                             // must be 0 (system)
0708
      ushort padding2;
                                                                                 0758 uint dpl : 2;
                                                                                                             // descriptor(meaning new) privilege level
0709
      uint *esp2;
                                                                                 0759
                                                                                       uint p : 1;
                                                                                                             // Present
0710
      ushort ss2;
                                                                                 0760
                                                                                       uint off_31_16 : 16; // high bits of offset in segment
0711
      ushort padding3;
                                                                                 0761 }:
0712
      void *cr3:
                                                                                 0762
                         // Page directory base
0713
      uint *eip;
                         // Saved state from last task switch
                                                                                 0763 // Set up a normal interrupt/trap gate descriptor.
0714
      uint eflags:
                                                                                 0764 // - istrap: 1 for a trap (= exception) gate. 0 for an interrupt gate.
0715
      uint eax:
                         // More saved state (registers)
                                                                                 0765 // interrupt gate clears FL_IF, trap gate leaves FL_IF alone
0716
      uint ecx;
                                                                                 0766 // - sel: Code segment selector for interrupt/trap handler
0717
      uint edx:
                                                                                 0767 // - off: Offset in code segment for interrupt/trap handler
0718
                                                                                 0768 // - dpl: Descriptor Privilege Level -
      uint ebx:
0719
      uint *esp;
                                                                                 0769 //
                                                                                                the privilege level required for software to invoke
0720
      uint *ebp:
                                                                                 0770 //
                                                                                                this interrupt/trap gate explicitly using an int instruction.
0721
      uint esi:
                                                                                 0771 #define SETGATE(gate, istrap, sel, off, d)
0722
      uint edi;
                                                                                 0772 {
0723
      ushort es;
                         // Even more saved state (segment selectors)
                                                                                 0773
                                                                                        (gate).off_15_0 = (uint) (off) & 0xffff;
0724
      ushort padding4:
                                                                                 0774
                                                                                        (gate).cs = (sel):
0725
                                                                                 0775
      ushort cs:
                                                                                        (qate).args = 0;
0726
      ushort padding5;
                                                                                 0776
                                                                                        (gate).rsv1 = 0;
0727
      ushort ss;
                                                                                 0777
                                                                                        (gate).type = (istrap) ? STS_TG32 : STS_IG32;
0728
                                                                                 0778
      ushort padding6;
                                                                                        (qate).s = 0;
0729
                                                                                 0779
                                                                                        (qate).dpl = (d);
      ushort ds;
0730
      ushort padding7;
                                                                                 0780
                                                                                        (gate).p = 1;
0731
      ushort fs:
                                                                                 0781
                                                                                        (gate).off_31_16 = (uint) (off) >> 16;
0732
      ushort padding8;
                                                                                 0782 }
0733
      ushort gs;
                                                                                 0783
0734
      ushort padding9;
                                                                                 0784
0735
                                                                                 0785
      ushort 1dt;
0736
      ushort padding10;
                                                                                 0786
0737
      ushort t:
                         // Trap on task switch
                                                                                 0787
0738
      ushort iomb;
                         // I/O map base address
                                                                                 0788
0739 };
                                                                                 0789
0740
                                                                                 0790
0741
                                                                                 0791
0742
                                                                                 0792
0743
                                                                                 0793
0744
                                                                                 0794
0745
                                                                                 0795
0746
                                                                                 0796
0747
                                                                                 0797
0748
                                                                                 0798
0749
                                                                                 0799
```

Sheet 07 Sheet 07

0800 // Format of an ELF executable file	0850 // Blank page.
0801	0851
0802 #define ELF_MAGIC 0x464C457FU // "\x7FELF" in little endian	0852
0803	0853
0804 // File header	0854
0805 struct elfhdr {	0855
0806 uint magic; // must equal ELF_MAGIC	0856
0807 uchar elf[12];	0857
0808 ushort type;	0858
0809 ushort machine;	0859
0810 uint version;	0860
0811 uint entry;	0861
0812 uint phoff;	0862
0813 uint shoff;	0863
0814 uint flags;	0864
0815 ushort ehsize;	0865
0816 ushort phentsize;	0866
0817 ushort phnum;	0867
0818 ushort shentsize;	0868
0819 ushort shnum;	0869
0820 ushort shstrndx;	0870
0821 };	0871
0822	0872
0823 // Program section header	0873
0824 struct proghdr {	0874
0825 uint type;	0875
0826 uint offset;	0876
0827 uint va;	0877
0828 uint pa;	0878
0829 uint filesz;	0879
0830 uint memsz;	0880
0831 uint flags;	0881
0832 uint align;	0882
0833 };	0883
0834	0884
0835 // Values for Proghdr type	0885
0836 #define ELF_PROG_LOAD 1	0886
0837	0887
0838 // Flag bits for Proghdr flags	0888
0839 #define ELF_PROG_FLAG_EXEC 1	0889
0840 #define ELF_PROG_FLAG_WRITE 2	0890
0841 #define ELF_PROG_FLAG_READ 4	0891
0842	0892
0843	0893
0844	0894
0845	0895
0846	0896
0847	0897
0848	0898
0849	0899

Sheet 08

0900 #include "asm.h" 0901 0902 # Start the first CPU: switch to 32-bit protected mode, jump into C. 0903 # The BIOS loads this code from the first sector of the hard disk into 0904 # memory at physical address 0x7c00 and starts executing in real mode 0905 # with %cs=0 %ip=7c00. 0906 0907 .set PROT_MODE_CSEG, 0x8 # kernel code segment selector 0908 .set PROT_MODE_DSEG, 0x10 # kernel data segment selector 0909 .set CRO_PE_ON, 0x1 # protected mode enable flag 0910 0911 .globl start 0912 start:			# Switch from real to protected mode, using a bootstrap GDT # and segment translation that makes virtual addresses # identical to physical addresses, so that the # effective memory map does not change during the switch. # gdtdesc # scr0, %eax # scr0 # scr0, %eax # movl %cr0, %eax # scr0 # scr0, %eax # sometimes witch # scr0 # scr0, %eax # scr0					irtual addresses that the during the switch.		
0913	.code16		# Assemble for 16-bit mode	0963	.code3				# Assemble	for 32-bit mode
0914	cli		# Disable interrupts		protcseg	_				
0915	c1d		# String operations increment	0965			•	rotected-mode	_	_
0916 0917	# Co+ !!	n the important data	commont registers (DS ES SS)	0966 0967	mo∨w mo∨w		_	MODE_DSEG, %a		data segment selector
0917	# Set u	%ax,%ax	segment registers (DS, ES, SS). # Segment number zero	0968	movw		%ax, %0 %ax, %0			S: Data Segment S: Extra Segment
0918	MOVW	%ax,%ds	# -> Data Segment	0969	movw		%ax, %t %ax, %t		# -> ES	5
0920	movw	%ax,%es	# -> Extra Segment	0970	movw		%ax, %		# -> GS	
0921	movw	%ax,%ss	# -> Stack Segment	0971	movw		%ax, %	•		S: Stack Segment
0922		70ax 17033	" > Seach Segment	0972		,	70 u , 700	33	" / 35	or seach segment
0923	# Enabl	e A20:		0973	# Set	uр	the st	tack pointer	and call int	to C.
0924			lity with the earliest PCs, physical	0974	mov1		\$start	•		
0925		· ·	low, so that addresses higher than	0975	call		bootma			
0926			by default. This code undoes this.	0976						
0927	seta20.1:	•	,	0977	# If b	0001	tmain ı	returns (it s	houldn't),	loop.
0928	inb	\$0x64,%al	# Wait for not busy	0978	spin:					•
0929	testb	\$0x2,%al		0979	jmp	9	spin			
0930	jnz	seta20.1		0980						
0931				0981	# Bootst	trap	p GDT			
0932	movb	\$0xd1,%al	# 0xd1 -> port 0x64		.p2align	1 2				# force 4 byte alignment
0933	outb	%a1,\$0x64		0983	-					
0934				0984	SEG_NU					# null seg
	seta20.2:	40.04.0/ 3		0985		-		STA_R, 0x0, 0		# code seg
0936	inb	\$0x64,%al	# Wait for not busy	0986	SEG_AS	SM(S	SIA_W,	0x0, 0xfffff	†††)	# data seg
0937	testb	\$0x2,%a1		0987		_				
0938 0939	jnz	seta20.2		0988	<pre>gdtdesc: .word</pre>		0x17			# sizeof(gdt) - 1
0939	movb	\$0xdf,%al	# 0xdf -> port 0x60	0990	.long		gdt			# address gdt
0940	outb	%al,\$0x60	# Oxul -> port Oxoo	0991	. Tong	Č	gut			# address gdt
0942	outb	/oa1, \$0x00		0992						
0943				0993						
0944				0994						
0945				0995						
0946				0996						
0947				0997						
0948				0998						
0949				0999						

Sheet 09 Sheet 09

```
1000 #include "asm.h"
                                                                                         # Switch from real to protected mode, using a bootstrap GDT
                                                                                         # and segment translation that makes virtual addresses
1001
1002 # Start an Application Processor. This must be placed on a 4KB boundary
                                                                                  1052
                                                                                         # identical to their physical addresses, so that the
1003 # somewhere in the 1st MB of conventional memory (APBOOTSTRAP). However,
                                                                                  1053
                                                                                         # effective memory map does not change during the switch.
1004 # due to some shortcuts below it's restricted further to within the 1st
                                                                                  1054
                                                                                         1gdt
                                                                                                 gdtdesc
1005 # 64KB. The AP starts in real-mode, with
                                                                                  1055
                                                                                         mov1
                                                                                                 %cr0, %eax
1006 # CS selector set to the startup memory address/16;
                                                                                  1056
                                                                                         orl
                                                                                                 $CRO_PE_ON, %eax
1007 # CS base set to startup memory address;
                                                                                  1057
                                                                                                 %eax, %cr0
                                                                                         mov1
1008 # CS limit set to 64KB;
                                                                                  1058
1009 # CPL and IP set to 0.
                                                                                  1059
                                                                                         # Jump to next instruction, but in 32-bit code segment.
1010 #
                                                                                  1060
                                                                                         # Switches processor into 32-bit mode.
1011 # Bootothers (in main.c) starts each non-boot CPU in turn.
                                                                                  1061
                                                                                                 $PROT_MODE_CSEG, $protcseg
1012 # It puts the correct %esp in start-4,
                                                                                  1062
1013 # and the place to jump to in start-8.
                                                                                  1063
                                                                                        .code32
                                                                                                                     # Assemble for 32-bit mode
1014 #
                                                                                  1064 protcsea:
1015 # This code is identical to bootasm.S except:
                                                                                  1065
                                                                                         # Set up the protected-mode data segment registers
1016 # - it does not need to enable A20
                                                                                  1066
                                                                                         movw
                                                                                                 $PROT_MODE_DSEG, %ax # Our data segment selector
1017 # - it uses the address at start-4 for the %esp
                                                                                  1067
                                                                                         movw
                                                                                                 %ax. %ds
                                                                                                                         # -> DS: Data Segment
1018 # - it jumps to the address at start-8 instead of calling bootmain
                                                                                  1068
                                                                                                 %ax. %es
                                                                                                                         # -> ES: Extra Segment
                                                                                         movw
                                                                                                                         # -> FS
1019
                                                                                  1069
                                                                                         movw
                                                                                                 %ax, %fs
1020 .set PROT MODE CSEG. 0x8
                                      # kernel code seament selector
                                                                                  1070
                                                                                         movw
                                                                                                 %ax. %as
                                                                                                                         # -> GS
1021 .set PROT MODE DSEG. 0x10
                                      # kernel data segment selector
                                                                                  1071
                                                                                                 %ax, %ss
                                                                                                                         # -> SS: Stack Segment
                                                                                         movw
1022 .set CRO_PE_ON,
                         0x1
                                      # protected mode enable flag
                                                                                  1072
1023
                                                                                  1073
                                                                                         mov1
                                                                                                 start-4, %esp
1024 .globl start
                                                                                  1074
                                                                                         mov1
                                                                                                 start-8. %eax
1025 start:
                                                                                  1075
                                                                                         qmj
                                                                                                 *%eax
                                   # Assemble for 16-bit mode
1026
      .code16
                                                                                  1076
1027
      cli
                                   # Disable interrupts
                                                                                  1077 # Bootstrap GDT
1028
      c1d
                                   # String operations increment
                                                                                  1078 .p2align 2
                                                                                                                                 # force 4 byte alignment
1029
                                                                                  1079 gdt:
1030
      # Set up the important data segment registers (DS, ES, SS).
                                                                                  1080 SEG_NULLASM
                                                                                                                                 # null seq
1031
              %ax,%ax
                                   # Segment number zero
                                                                                         SEG_ASM(STA_X|STA_R, 0x0, 0xffffffff)
                                                                                                                                 # code seq
      xorw
                                                                                  1081
                                                                                         SEG_ASM(STA_W, 0x0, 0xffffffff)
1032
      movw
              %ax,%ds
                                   # -> Data Segment
                                                                                  1082
                                                                                                                                 # data seg
1033
                                   # -> Extra Segment
                                                                                  1083
      movw
              %ax,%es
1034
      movw
              %ax,%ss
                                   # -> Stack Segment
                                                                                  1084 gdtdesc:
1035
                                                                                  1085
                                                                                         .word
                                                                                                 0x17
                                                                                                                                 # sizeof(gdt) - 1
1036
                                                                                  1086
                                                                                         .long
                                                                                                 gdt
                                                                                                                                 # address gdt
1037
                                                                                  1087
1038
                                                                                  1088
1039
                                                                                  1089
1040
                                                                                  1090
1041
                                                                                  1091
1042
                                                                                  1092
1043
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1047
                                                                                  1097
1048
                                                                                  1098
1049
                                                                                  1099
```

Sheet 10 Sheet 10

```
1100 // Boot loader.
                                                                                1150 void
                                                                                1151 waitdisk(void)
1101 //
1102 // Part of the boot sector, along with bootasm.S, which calls bootmain().
                                                                                1152 {
1103 // bootasm.S has put the processor into protected 32-bit mode.
                                                                                1153 // Wait for disk ready.
1104 // bootmain() loads an ELF kernel image from the disk starting at
                                                                                1154 while((inb(0x1F7) & 0xC0) != 0x40)
1105 // sector 1 and then jumps to the kernel entry routine.
                                                                                1155
1106
                                                                                1156 }
1107 #include "types.h"
                                                                                1157
1108 #include "elf.h"
                                                                                1158 // Read a single sector at offset into dst.
1109 #include "x86.h"
                                                                                1159 void
1110
                                                                                1160 readsect(void *dst, uint offset)
1111 #define SECTSIZE 512
                                                                                1161 {
1112
                                                                                1162 // Issue command.
1113 void readseg(uint, uint, uint);
                                                                                1163
                                                                                      waitdisk();
1114
                                                                                       outb(0x1F2, 1); // count = 1
1115 void
                                                                                1165
                                                                                       outb(0x1F3, offset);
1116 bootmain(void)
                                                                                1166
                                                                                       outb(0x1F4, offset >> 8);
1117 {
                                                                                1167
                                                                                      outb(0x1F5, offset >> 16):
1118 struct elfhdr *elf;
                                                                                       outb(0x1F6. (offset \gg 24) | 0xE0):
                                                                                1168
1119
      struct proghdr *ph, *eph;
                                                                                1169
                                                                                       outb(0x1F7, 0x20); // cmd 0x20 - read sectors
1120
      void (*entrv)(void):
                                                                                1170
1121
                                                                                1171 // Read data.
1122 elf = (struct elfhdr*)0x10000; // scratch space
                                                                                1172 waitdisk();
1123
                                                                                insl(0x1F0, dst, SECTSIZE/4);
1124 // Read 1st page off disk
                                                                                1174 }
1125
      readseg((uint)elf, SECTSIZE*8, 0);
                                                                                1175
1126
                                                                                1176 // Read 'count' bytes at 'offset' from kernel into virtual address 'va'.
                                                                                1177 // Might copy more than asked.
1127 // Is this an ELF executable?
1128 if(elf->magic != ELF_MAGIC)
                                                                                1178 void
1129
        goto bad;
                                                                                1179 readseg(uint va, uint count, uint offset)
1130
                                                                                1180 {
1131 // Load each program segment (ignores ph flags).
                                                                                1181 uint eva;
1132
      ph = (struct proghdr*)((uchar*)elf + elf->phoff);
                                                                                1182
1133
      eph = ph + elf->phnum;
                                                                                1183
                                                                                       eva = va + count;
1134 for(; ph < eph; ph++)
                                                                                1184
1135
        readseg(ph->va & 0xFFFFFF, ph->memsz, ph->offset);
                                                                                1185
                                                                                      // Round down to sector boundary.
1136
                                                                                1186
                                                                                       va &= ~(SECTSIZE - 1);
1137 // Call the entry point from the ELF header.
                                                                                1187
1138 // Does not return!
                                                                                1188
                                                                                      // Translate from bytes to sectors; kernel starts at sector 1.
1139 entry = (void(*)(void))(elf->entry & 0xFFFFFF);
                                                                                1189
                                                                                       offset = (offset / SECTSIZE) + 1;
1140 entry();
                                                                                1190
1141
                                                                                1191 // If this is too slow, we could read lots of sectors at a time.
1142 bad:
                                                                                1192 // We'd write more to memory than asked, but it doesn't matter --
1143 outw(0x8A00, 0x8A00);
                                                                                1193
                                                                                       // we load in increasing order.
1144 outw(0x8A00, 0x8E00);
                                                                                1194
                                                                                       for(; va < eva; va += SECTSIZE, offset++)</pre>
                                                                                         readsect((uchar*)va, offset);
1145
      for(;;)
                                                                                1195
1146
                                                                                1196 }
       ;
1147 }
                                                                                1197
                                                                                1198
1148
1149
                                                                                1199
```

Sheet 11 Sheet 11

```
1200 #include "types.h"
1201 #include "defs.h"
1202 #include "param.h"
1203 #include "mmu.h"
1204 #include "proc.h"
1205 #include "x86.h"
1206
1207 static void bootothers(void);
1208 static void mpmain(void) __attribute__((noreturn));
1209
1210 // Bootstrap processor starts running C code here.
1211 int
1212 main(void)
1213 {
1214 extern char edata[], end[];
1215
1216 // clear BSS
1217
      memset(edata, 0, end - edata):
1218
1219
      mp_init(); // collect info about this machine
1220
      lapic_init(mp_bcpu());
1221
      cprintf("\ncpu%d: starting xv6\n\n", cpu());
1222
1223
      pinit();
                       // process table
1224
                       // buffer cache
      binit():
1225
                       // interrupt controller
      pic_init();
1226
      ioapic_init(); // another interrupt controller
                       // physical memory allocator
1227
      kinit();
1228 tvinit();
                       // trap vectors
1229 fileinit();
                       // file table
1230 iinit();
                       // inode cache
1231
      console_init(); // I/O devices & their interrupts
1232 ide_init();
                       // disk
1233 if(!ismp)
1234
        timer_init(); // uniprocessor timer
1235
      userinit():
                       // first user process
1236
      bootothers();
                     // start other processors
1237
1238 // Finish setting up this processor in mpmain.
1239
      mpmain();
1240 }
1241
1242
1243
1244
1245
1246
1247
1248
1249
```

```
1250 // Bootstrap processor gets here after setting up the hardware.
1251 // Additional processors start here.
1252 static void
1253 mpmain(void)
1254 {
1255 cprintf("cpu%d: mpmain\n", cpu());
1256
      idtinit();
1257
      if(cpu() != mp_bcpu())
1258
        lapic_init(cpu());
1259
      setupseqs(0);
1260
      xchg(&cpus[cpu()].booted, 1);
1261
1262 cprintf("cpu%d: scheduling\n");
1263 scheduler();
1264 }
1265
1266 static void
1267 bootothers(void)
1268 {
1269 extern uchar _binary_bootother_start[], _binary_bootother_size[];
1270
      uchar *code:
1271
      struct cpu *c:
1272
      char *stack;
1273
1274
      // Write bootstrap code to unused memory at 0x7000.
1275
      code = (uchar*)0x7000;
1276
      memmove(code, _binary_bootother_start, (uint)_binary_bootother_size);
1277
1278
       for(c = cpus; c < cpus+ncpu; c++){
1279
        if(c == cpus+cpu()) // We've started already.
1280
           continue;
1281
1282
        // Fill in %esp, %eip and start code on cpu.
1283
         stack = kalloc(KSTACKSIZE);
1284
         *(void**)(code-4) = stack + KSTACKSIZE;
1285
        *(void**)(code-8) = mpmain;
1286
        lapic_startap(c->apicid, (uint)code);
1287
1288
        // Wait for cpu to get through bootstrap.
1289
        while(c->booted == 0)
1290
           ;
1291 }
1292 }
1293
1294
1295
1296
1297
1298
1299
```

```
1300 // Mutual exclusion lock.
                                                                                 1350 // Mutual exclusion spin locks.
1301 struct spinlock {
                                                                                 1351
1302
      uint locked; // Is the lock held?
                                                                                 1352 #include "types.h"
                                                                                 1353 #include "defs.h"
1303
1304 // For debugging:
                                                                                 1354 #include "param.h"
1305
      char *name;
                     // Name of lock.
                                                                                 1355 #include "x86.h"
1306
      int cpu;
                     // The number of the cpu holding the lock.
                                                                                 1356 #include "mmu.h"
1307
      uint pcs[10]; // The call stack (an array of program counters)
                                                                                 1357 #include "proc.h"
1308
                     // that locked the lock.
                                                                                 1358 #include "spinlock.h"
1309 };
                                                                                 1359
1310
                                                                                 1360 extern int use_console_lock;
1311
                                                                                 1361
1312
                                                                                 1362 void
1313
                                                                                 1363 initlock(struct spinlock *lock, char *name)
1314
1315
                                                                                 1365 lock->name = name;
1316
                                                                                 1366
                                                                                       lock \rightarrow locked = 0;
1317
                                                                                       lock->cpu = 0xffffffff;
1318
                                                                                 1368 }
1319
                                                                                 1369
1320
                                                                                 1370 // Acquire the lock.
1321
                                                                                 1371 // Loops (spins) until the lock is acquired.
1322
                                                                                 1372 // Holding a lock for a long time may cause
1323
                                                                                 1373 // other CPUs to waste time spinning to acquire it.
1324
                                                                                 1374 void
1325
                                                                                 1375 acquire(struct spinlock *lock)
1326
                                                                                 1376 {
1327
                                                                                 1377
                                                                                        pushcli();
1328
                                                                                 1378
                                                                                       if(holding(lock))
1329
                                                                                 1379
                                                                                          panic("acquire");
1330
                                                                                 1380
1331
                                                                                 1381 // The xchg is atomic.
                                                                                 1382 // It also serializes, so that reads after acquire are not
1332
1333
                                                                                 1383 // reordered before it.
1334
                                                                                 1384 while(xchg(&lock->locked, 1) == 1)
1335
                                                                                 1385
1336
                                                                                 1386
1337
                                                                                       // Record info about lock acquisition for debugging.
1338
                                                                                 1388 // The +10 is only so that we can tell the difference
1339
                                                                                 1389
                                                                                       // between forgetting to initialize lock->cpu
1340
                                                                                 1390
                                                                                       // and holding a lock on cpu 0.
1341
                                                                                 1391 lock -> cpu = cpu() + 10;
1342
                                                                                 1392
                                                                                        getcallerpcs(&lock, lock->pcs);
1343
                                                                                 1393 }
                                                                                 1394
1344
                                                                                 1395
1345
1346
                                                                                 1396
1347
                                                                                 1397
1348
                                                                                 1398
1349
                                                                                 1399
```

```
1400 // Release the lock.
                                                                                 1450 // Pushcli/popcli are like cli/sti except that they are matched:
1401 void
                                                                                 1451 // it takes two popcli to undo two pushcli. Also, if interrupts
1402 release(struct spinlock *lock)
                                                                                 1452 // are off, then pushcli, popcli leaves them off.
                                                                                 1453
1403 {
1404 if(!holding(lock))
                                                                                 1454 void
1405
        panic("release");
                                                                                 1455 pushcli(void)
1406
                                                                                 1456 {
1407
      lock - pcs[0] = 0;
                                                                                 1457 int eflags;
1408
      lock->cpu = 0xffffffff;
                                                                                 1458
1409
                                                                                 1459
                                                                                        eflags = read_eflags();
1410 // The xchg serializes, so that reads before release are
                                                                                 1460
                                                                                        cli();
1411 // not reordered after it. (This reordering would be allowed
                                                                                 1461
                                                                                       if(cpus[cpu()].ncli++ == 0)
1412 // by the Intel manuals, but does not happen on current
                                                                                 1462
                                                                                          cpus[cpu()].intena = eflags & FL_IF;
1413 // Intel processors. The xchg being asm volatile also keeps
                                                                                 1463 }
1414 // gcc from delaying the above assignments.)
                                                                                 1464
1415 xchg(&lock->locked, 0);
                                                                                 1465 void
                                                                                 1466 popcli(void)
1416
1417 popcli();
                                                                                 1467 {
1418 }
                                                                                 1468 if(read_eflags()&FL_IF)
1419
                                                                                 1469
                                                                                          panic("popcli - interruptible");
1420 // Record the current call stack in pcs[] by following the %ebp chain.
                                                                                 1470
                                                                                        if(--cpus[cpu()].ncli < 0)</pre>
1421 void
                                                                                 1471
                                                                                          panic("popcli");
1422 getcallerpcs(void *v, uint pcs[])
                                                                                 1472
                                                                                        if(cpus[cpu()].ncli == 0 && cpus[cpu()].intena)
1423 {
                                                                                 1473
                                                                                          sti();
                                                                                 1474 }
1424 uint *ebp:
1425
                                                                                 1475
      int i;
1426
                                                                                 1476
1427
      ebp = (uint*)v - 2;
                                                                                 1477
1428
      for(i = 0; i < 10; i++){
                                                                                 1478
1429
        if(ebp == 0 || ebp == (uint*)0xffffffff)
                                                                                 1479
1430
                                                                                 1480
          break;
                             // saved %eip
1431
                                                                                 1481
        pcs[i] = ebp[1];
1432
        ebp = (uint*)ebp[0]; // saved %ebp
                                                                                 1482
1433 }
                                                                                 1483
1434 for(; i < 10; i++)
                                                                                 1484
1435
        pcs[i] = 0;
                                                                                 1485
1436 }
                                                                                 1486
1437
                                                                                 1487
1438 // Check whether this cpu is holding the lock.
                                                                                 1488
1439 int
                                                                                 1489
1440 holding(struct spinlock *lock)
                                                                                 1490
1441 {
                                                                                 1491
1442 return lock->locked && lock->cpu == cpu() + 10;
                                                                                 1492
1443 }
                                                                                 1493
1444
                                                                                 1494
1445
                                                                                 1495
1446
                                                                                 1496
1447
                                                                                 1497
1448
                                                                                 1498
1449
                                                                                 1499
```

Sheet 14 Sheet 14

```
1500 // Seaments in proc->adt
                                                                                 1550 // Process memory is laid out contiguously, low addresses first:
1501 #define SEG_KCODE 1 // kernel code
                                                                                 1551 // text
1502 #define SEG_KDATA 2 // kernel data+stack
                                                                                1552 //
                                                                                          original data and bss
                                                                                1553 // fixed-size stack
1503 #define SEG_UCODE 3
1504 #define SEG_UDATA 4
                                                                                1554 // expandable heap
1505 #define SEG_TSS 5 // this process's task state
                                                                                1555
1506 #define NSEGS
                                                                                1556 // Per-CPU state
                      6
1507
                                                                                1557 struct cpu {
1508 // Saved registers for kernel context switches.
                                                                                       uchar apicid;
                                                                                                                   // Local APIC ID
                                                                                1558
1509 // Don't need to save all the %fs etc. segment registers,
                                                                                1559
                                                                                       struct proc *curproc;
                                                                                                                   // Process currently running.
1510 // because they are constant across kernel contexts.
                                                                                1560
                                                                                       struct context context;
                                                                                                                   // Switch here to enter scheduler
1511 // Save all the regular registers so we don't need to care
                                                                                1561 struct taskstate ts:
                                                                                                                   // Used by x86 to find stack for interrupt
1512 // which are caller save, but not the return register %eax.
                                                                                1562 struct segdesc gdt[NSEGS]; // x86 global descriptor table
1513 // (Not saving %eax just simplifies the switching code.)
                                                                                1563
                                                                                       volatile uint booted;
                                                                                                                   // Has the CPU started?
1514 // The layout of context must match code in swtch.S.
                                                                                                                   // Depth of pushcli nesting.
                                                                                1564
                                                                                      int ncli:
1515 struct context {
                                                                                1565 int intena;
                                                                                                                   // Were interrupts enabled before pushcli?
1516 int eip;
                                                                                1566 };
1517
      int esp:
                                                                                1567
1518 int ebx:
                                                                                1568 extern struct cpu cpus[NCPU];
1519
      int ecx;
                                                                                 1569 extern int ncpu;
1520
      int edx:
                                                                                1570
1521 int esi:
                                                                                1571 // "cp" is a short alias for curproc().
1522 int edi;
                                                                                 1572 // It gets used enough to make this worthwhile.
1523 int ebp;
                                                                                1573 #define cp curproc()
1524 }:
                                                                                1574
                                                                                1575
1525
1526 enum proc_state { UNUSED, EMBRYO, SLEEPING, RUNNABLE, RUNNING, ZOMBIE };
                                                                                1576
1527
                                                                                1577
1528 // Per-process state
                                                                                 1578
1529 struct proc {
                                                                                 1579
1530 char *mem;
                                // Start of process memory (kernel address)
                                                                                1580
1531
                                // Size of process memory (bytes)
                                                                                1581
      uint sz;
1532
      char *kstack;
                                // Bottom of kernel stack for this process
                                                                                1582
1533
                                // Process state
                                                                                1583
      enum proc_state state;
1534
      int pid;
                                // Process ID
                                                                                1584
                                // Parent process
1535
      struct proc *parent;
                                                                                1585
1536 void *chan;
                                // If non-zero, sleeping on chan
                                                                                1586
1537
      int killed:
                                // If non-zero, have been killed
                                                                                 1587
1538
      struct file *ofile[NOFILE]; // Open files
                                                                                1588
1539
      struct inode *cwd;
                                // Current directory
                                                                                1589
1540
      struct context context; // Switch here to run process
                                                                                 1590
1541
      struct trapframe *tf;
                                // Trap frame for current interrupt
                                                                                1591
1542
      char name[16];
                                // Process name (debugging)
                                                                                1592
1543 };
                                                                                 1593
1544
                                                                                1594
1545
                                                                                1595
1546
                                                                                 1596
1547
                                                                                1597
1548
                                                                                1598
1549
                                                                                1599
```

Sheet 15 Sheet 15

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```
1650 // Grow current process's memory by n bytes.
1651 // Return old size on success, -1 on failure.
1652 int
1653 growproc(int n)
1654 {
1655 char *newmem, *oldmem;
1656
1657
       newmem = kalloc(cp->sz + n);
1658 if(newmem == 0)
1659
         return -1;
1660
      memmove(newmem, cp->sz);
1661 memset(newmem + cp->sz, 0, n);
1662 oldmem = cp->mem;
1663 cp->mem = newmem;
1664 kfree(oldmem, cp->sz);
1665
       cp->sz += n;
1666 setupsegs(cp);
      return cp->sz - n;
1668 }
1669
1670 // Set up CPU's segment descriptors and task state for a given process.
1671 // If p==0, set up for "idle" state for when scheduler() is running.
1672 void
1673 setupseqs(struct proc *p)
1674 {
1675 struct cpu *c;
1676
1677
      pushcli();
1678 c = \&cpus[cpu()];
     c->ts.ss0 = SEG_KDATA << 3;</pre>
1679
1680 if(p)
1681
         c->ts.esp0 = (uint)(p->kstack + KSTACKSIZE);
1682
1683
         c->ts.esp0 = 0xffffffff;
1684
1685 c \rightarrow gdt[0] = SEG_NULL;
1686
      c \rightarrow gdt[SEG_KCODE] = SEG(STA_X|STA_R, 0, 0x100000 + 64*1024-1, 0);
       c->gdt[SEG_KDATA] = SEG(STA_W, 0, 0xffffffff, 0);
1688 c \rightarrow qdt[SEG_TSS] = SEG16(STS_T32A, (uint)&c \rightarrow ts, sizeof(c \rightarrow ts)-1, 0);
1689
       c->gdt[SEG_TSS].s = 0;
1690
     if(p){
1691
         c->gdt[SEG_UCODE] = SEG(STA_X|STA_R, (uint)p->mem, p->sz-1, DPL_USER);
1692
         c->gdt[SEG_UDATA] = SEG(STA_W, (uint)p->mem, p->sz-1, DPL_USER);
1693 } else {
1694
         c->qdt[SEG_UCODE] = SEG_NULL;
1695
         c->gdt[SEG_UDATA] = SEG_NULL;
1696 }
1697
1698
1699
```

1649

```
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                                                                                 Jan 23 18:04 2008 xv6/proc.c Page 4
1700 lgdt(c->gdt, sizeof(c->gdt));
                                                                                 1750 // Clear %eax so that fork system call returns 0 in child.
1701 ltr(SEG_TSS << 3);
                                                                                 1751 np->tf->eax = 0;
1702 popcli();
                                                                                 1752 return np;
1703 }
                                                                                 1753 }
1704
                                                                                 1754
                                                                                 1755 // Set up first user process.
1705 // Create a new process copying p as the parent.
1706 // Sets up stack to return as if from system call.
                                                                                 1756 void
1707 // Caller must set state of returned proc to RUNNABLE.
                                                                                 1757 userinit(void)
1708 struct proc*
                                                                                 1758 {
1709 copyproc(struct proc *p)
                                                                                 1759 struct proc *p;
1710 {
                                                                                        extern uchar _binary_initcode_start[], _binary_initcode_size[];
                                                                                 1760
1711 int i:
                                                                                 1761
                                                                                 1762
1712 struct proc *np;
                                                                                        p = copyproc(0);
1713
                                                                                 1763 p \rightarrow sz = PAGE;
                                                                                 1764 p \rightarrow mem = kalloc(p \rightarrow sz):
1714 // Allocate process.
1715 if((np = allocproc()) == 0)
                                                                                 1765 p->cwd = namei("/");
1716
        return 0;
                                                                                 1766 memset(p->tf, 0, sizeof(*p->tf));
1717
                                                                                 1767 p\rightarrow tf\rightarrow cs = (SEG\ UCODE\ <<\ 3)\ |\ DPL\ USER:
1718 // Allocate kernel stack.
                                                                                 1768 p->tf->ds = (SEG_UDATA << 3) | DPL_USER;
1719
      if((np->kstack = kalloc(KSTACKSIZE)) == 0){
                                                                                 1769
                                                                                        p->tf->es = p->tf->ds;
1720
        np->state = UNUSED:
                                                                                 1770 p->tf->ss = p->tf->ds:
1721
        return 0:
                                                                                 1771 p->tf->eflags = FL_IF;
1722 }
                                                                                 1772
                                                                                        p->tf->esp = p->sz;
1723
      np->tf = (struct trapframe*)(np->kstack + KSTACKSIZE) - 1;
                                                                                 1773
                                                                                 1774 // Make return address readable; needed for some gcc.
1724
1725
                                                                                 1775
      if(p){ // Copy process state from p.
                                                                                        p->tf->esp -= 4;
1726
        np->parent = p;
                                                                                 1776
                                                                                        *(uint*)(p->mem + p->tf->esp) = 0xefefefef;
1727
        memmove(np->tf, p->tf, sizeof(*np->tf));
                                                                                 1777
1728
                                                                                 1778 // On entry to user space, start executing at beginning of initcode.S.
1729
                                                                                 1779
                                                                                        p->tf->eip = 0;
        np->sz = p->sz;
1730
        if((np->mem = kalloc(np->sz)) == 0){
                                                                                 1780 memmove(p->mem, _binary_initcode_start, (int)_binary_initcode_size);
1731
          kfree(np->kstack, KSTACKSIZE);
                                                                                 1781 safestrcpy(p->name, "initcode", sizeof(p->name));
1732
          np->kstack = 0;
                                                                                 1782
                                                                                        p->state = RUNNABLE;
1733
          np->state = UNUSED;
                                                                                 1783
1734
          return 0;
                                                                                 1784 initproc = p;
1735
                                                                                 1785 }
1736
        memmove(np->mem, p->mem, np->sz);
                                                                                 1786
1737
                                                                                 1787 // Return currently running process.
1738
        for(i = 0; i < NOFILE; i++)
                                                                                 1788 struct proc*
1739
          if(p->ofile[i])
                                                                                 1789 curproc(void)
1740
            np->ofile[i] = filedup(p->ofile[i]);
                                                                                 1790 {
1741
        np -> cwd = idup(p -> cwd);
                                                                                 1791 struct proc *p;
1742
      }
                                                                                 1792
1743
                                                                                 1793
                                                                                        pushcli();
      // Set up new context to start executing at forkret (see below).
1744
                                                                                 1794 p = cpus[cpu()].curproc;
                                                                                        popcli();
1745
      memset(&np->context, 0, sizeof(np->context));
                                                                                 1795
1746
      np->context.eip = (uint)forkret;
                                                                                 1796 return p;
1747
      np->context.esp = (uint)np->tf;
                                                                                 1797 }
1748
                                                                                 1798
1749
                                                                                 1799
```

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Sheet 18 Sheet 18

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```
1900 // Atomically release lock and sleep on chan.
1901 // Reacquires lock when reawakened.
1902 void
1903 sleep(void *chan, struct spinlock *lk)
1904 {
1905 if(cp == 0)
1906
        panic("sleep");
1907
1908
      if(1k == 0)
        panic("sleep without lk");
1909
1910
1911 // Must acquire proc_table_lock in order to
1912 // change p->state and then call sched.
1913 // Once we hold proc_table_lock, we can be
1914 // guaranteed that we won't miss any wakeup
1915 // (wakeup runs with proc_table_lock locked),
1916 // so it's okay to release lk.
1917 if(lk != &proc table lock){
1918
        acquire(&proc_table_lock);
1919
        release(lk);
1920 }
1921
1922 // Go to sleep.
1923
      cp->chan = chan;
1924
      cp->state = SLEEPING;
1925
      sched();
1926
1927
      // Tidy up.
1928
      cp->chan = 0;
1929
1930 // Reacquire original lock.
1931
      if(lk != &proc_table_lock){
1932
        release(&proc_table_lock);
1933
        acquire(lk);
1934 }
1935 }
1936
1937
1938
1939
1940
1941
1942
1943
1944
1945
1946
1947
1948
1949
```

```
1950 // Wake up all processes sleeping on chan.
1951 // Proc_table_lock must be held.
1952 static void
1953 wakeup1(void *chan)
1954 {
1955 struct proc *p;
1956
       for(p = proc; p < &proc[NPROC]; p++)</pre>
1957
1958
        if(p->state == SLEEPING && p->chan == chan)
1959
           p->state = RUNNABLE;
1960 }
1961
1962 // Wake up all processes sleeping on chan.
1963 // Proc_table_lock is acquired and released.
1964 void
1965 wakeup(void *chan)
1966 {
1967
      acquire(&proc table lock):
1968
       wakeup1(chan):
1969
       release(&proc_table_lock);
1970 }
1971
1972 // Kill the process with the given pid.
1973 // Process won't actually exit until it returns
1974 // to user space (see trap in trap.c).
1975 int
1976 kill(int pid)
1977 {
1978 struct proc *p;
1979
1980
       acquire(&proc_table_lock);
1981
       for(p = proc; p < &proc[NPROC]; p++){</pre>
1982
        if(p->pid == pid){
1983
           p->killed = 1;
1984
           // Wake process from sleep if necessary.
1985
           if(p->state == SLEEPING)
1986
            p->state = RUNNABLE;
1987
           release(&proc_table_lock);
1988
           return 0:
1989
        }
1990 }
1991
       release(&proc_table_lock);
1992
       return -1;
1993 }
1994
1995
1996
1997
1998
1999
```

```
2000 // Exit the current process. Does not return.
2001 // Exited processes remain in the zombie state
2002 // until their parent calls wait() to find out they exited.
2003 void
2004 exit(void)
2005 {
2006 struct proc *p;
2007
      int fd;
2008
2009
      if(cp == initproc)
2010
        panic("init exiting");
2011
2012
      // Close all open files.
2013
      for(fd = 0; fd < NOFILE; fd++){</pre>
2014
        if(cp->ofile[fd]){
2015
          fileclose(cp->ofile[fd]);
2016
           cp->ofile[fd] = 0;
2017
        }
2018
      }
2019
2020
      iput(cp->cwd):
2021
      cp->cwd = 0;
2022
2023
      acquire(&proc_table_lock);
2024
2025
      // Parent might be sleeping in wait().
2026
      wakeup1(cp->parent);
2027
2028
      // Pass abandoned children to init.
2029
      for(p = proc; p < &proc[NPROC]; p++){</pre>
2030
        if(p->parent == cp){
2031
           p->parent = initproc;
2032
          if(p->state == ZOMBIE)
2033
            wakeup1(initproc);
2034
        }
2035 }
2036
2037
      // Jump into the scheduler, never to return.
2038
      cp->killed = 0:
2039 cp->state = ZOMBIE;
2040
      sched();
2041
      panic("zombie exit");
2042 }
2043
2044
2045
2046
2047
2048
2049
```

```
2050 // Wait for a child process to exit and return its pid.
2051 // Return -1 if this process has no children.
2052 int
2053 wait(void)
2054 {
2055 struct proc *p;
2056
      int i, havekids, pid;
2057
2058
      acquire(&proc_table_lock);
       for(;;){
2059
2060
        // Scan through table looking for zombie children.
2061
        havekids = 0:
2062
        for(i = 0; i < NPROC; i++){
2063
           p = &proc[i];
2064
           if(p->state == UNUSED)
2065
            continue:
2066
           if(p->parent == cp){
2067
            if(p->state == ZOMBIE){
2068
               // Found one.
2069
               kfree(p->mem, p->sz);
               kfree(p->kstack, KSTACKSIZE);
2070
2071
               pid = p->pid;
               p->state = UNUSED;
2072
2073
               p->pid = 0;
2074
               p->parent = 0;
2075
               p->name[0] = 0;
2076
               release(&proc_table_lock);
2077
               return pid;
2078
2079
            havekids = 1;
2080
          }
2081
        }
2082
2083
        // No point waiting if we don't have any children.
2084
        if(!havekids || cp->killed){
2085
           release(&proc_table_lock);
2086
           return -1;
2087
        }
2088
2089
        // Wait for children to exit. (See wakeup1 call in proc_exit.)
2090
        sleep(cp, &proc_table_lock);
2091 }
2092 }
2093
2094
2095
2096
2097
2098
2099
```

Sheet 20 Sheet 20

```
2100 // Print a process listing to console. For debugging.
                                                                                          void swtch(struct context *old, struct context *new);
2101 // Runs when user types ^P on console.
                                                                                 2151 #
2102 // No lock to avoid wedging a stuck machine further.
                                                                                 2152 # Save current register context in old
2103 void
                                                                                 2153 # and then load register context from new.
2104 procdump(void)
                                                                                 2154
2105 {
                                                                                 2155 .globl swtch
2106 static char *states[] = {
                                                                                 2156 swtch:
2107
       [UNUSED]
                   "unused",
                                                                                 2157 # Save old registers
2108
       [EMBRYO]
                   "embryo",
                                                                                 2158
                                                                                        movl 4(%esp), %eax
       [SLEEPING]
                  "sleep "
                                                                                 2159
2109
2110
      [RUNNABLE]
                  "runble",
                                                                                 2160
                                                                                        popl 0(%eax) # %eip
                  "run ".
                                                                                 2161 movl %esp, 4(%eax)
2111
       [RUNNING]
2112
      [ZOMBIE]
                   "zombie"
                                                                                 2162 movl %ebx, 8(%eax)
2113 };
                                                                                 2163
                                                                                        movl %ecx, 12(%eax)
2114 int i, j;
                                                                                 2164
                                                                                        movl %edx, 16(%eax)
2115
      struct proc *p;
                                                                                 2165
                                                                                        mov1 %esi, 20(%eax)
2116
      char *state;
                                                                                 2166
                                                                                        mov1 %edi, 24(%eax)
2117
      uint pc[10];
                                                                                 2167
                                                                                        mov1 %ebp, 28(%eax)
2118
                                                                                 2168
2119
       for(i = 0; i < NPROC; i++){
                                                                                 2169
                                                                                        # Load new registers
2120
        p = &proc[i]:
                                                                                 2170
                                                                                        movl 4(%esp), %eax # not 8(%esp) - popped return address above
2121
        if(p->state == UNUSED)
                                                                                 2171
2122
          continue:
                                                                                 2172
                                                                                        mov1 28(%eax), %ebp
2123
        if(p->state >= 0 && p->state < NELEM(states) && states[p->state])
                                                                                 2173
                                                                                        mov1 24(%eax), %edi
2124
                                                                                        mov1 20(%eax), %esi
          state = states[p->state];
                                                                                 2174
2125
                                                                                 2175
                                                                                        movl 16(%eax), %edx
        else
2126
           state = "???";
                                                                                 2176
                                                                                        movl 12(%eax), %ecx
2127
        cprintf("%d %s %s", p->pid, state, p->name);
                                                                                        mov1 8(%eax), %ebx
                                                                                 2177
2128
        if(p->state == SLEEPING){
                                                                                 2178
                                                                                        movl 4(%eax), %esp
2129
          getcallerpcs((uint*)p->context.ebp+2, pc);
                                                                                 2179
                                                                                        push1 0(%eax) # %eip
2130
          for(j=0; j<10 && pc[j] != 0; <math>j++)
                                                                                 2180
2131
            cprintf(" %p", pc[j]);
                                                                                 2181
                                                                                        ret
2132
        }
                                                                                 2182
2133
        cprintf("\n");
                                                                                 2183
2134 }
                                                                                 2184
2135 }
                                                                                 2185
2136
                                                                                 2186
2137
                                                                                 2187
2138
                                                                                 2188
2139
                                                                                 2189
2140
                                                                                 2190
2141
                                                                                 2191
2142
                                                                                 2192
2143
                                                                                 2193
2144
                                                                                 2194
                                                                                 2195
2145
2146
                                                                                 2196
2147
                                                                                 2197
2148
                                                                                 2198
2149
                                                                                 2199
```

Sheet 21 Sheet 21

```
2200 // Physical memory allocator, intended to allocate
2201 // memory for user processes. Allocates in 4096-byte "pages".
2202 // Free list is kept sorted and combines adjacent pages into
2203 // long runs, to make it easier to allocate big segments.
2204 // One reason the page size is 4k is that the x86 segment size
2205 // granularity is 4k.
2206
2207 #include "types.h"
2208 #include "defs.h"
2209 #include "param.h"
2210 #include "spinlock.h"
2212 struct spinlock kalloc_lock;
2213
2214 struct run {
2215 struct run *next:
2216 int len; // bytes
2217 }:
2218 struct run *freelist;
2219
2220 // Initialize free list of physical pages.
2221 // This code cheats by just considering one megabyte of
2222 // pages after _end. Real systems would determine the
2223 // amount of memory available in the system and use it all.
2224 void
2225 kinit(void)
2226 {
2227 extern int end;
2228 uint mem;
2229
      char *start;
2230
2231 initlock(&kalloc_lock, "kalloc");
2232 start = (char*) &end;
2233 start = (char*) (((uint)start + PAGE) & ~(PAGE-1));
2234
      mem = 256; // assume computer has 256 pages of RAM
      cprintf("mem = %d\n", mem * PAGE);
2235
2236
      kfree(start, mem * PAGE);
2237 }
2238
2239
2240
2241
2242
2243
2244
2245
2246
2247
2248
2249
```

```
2250 // Free the len bytes of memory pointed at by v,
2251 // which normally should have been returned by a
2252 // call to kalloc(len). (The exception is when
2253 // initializing the allocator; see kinit above.)
2254 void
2255 kfree(char *v, int len)
2256 {
2257
      struct run *r, *rend, **rp, *p, *pend;
2258
2259
      if(len <= 0 || len % PAGE)
2260
        panic("kfree");
2261
2262 // Fill with junk to catch dangling refs.
2263
      memset(v, 1, len);
2264
2265
      acquire(&kalloc_lock);
2266
      p = (struct run*)v;
2267
      pend = (struct run*)(v + len):
2268
      for(rp=&freelist; (r=*rp) != 0 \&\& r <= pend; rp=\&r->next){
2269
        rend = (struct run*)((char*)r + r->len);
2270
        if(r \le p \&\& p < rend)
2271
           panic("freeing free page");
2272
         if(pend == r){ // p next to r: replace r with p
2273
           p->len = len + r->len;
2274
           p->next = r->next;
2275
           *rp = p;
2276
           goto out;
2277
2278
        if(rend == p){ // r next to p: replace p with r
2279
           r->len += len:
2280
           if(r->next && r->next == pend){ // r now next to r->next?
2281
             r->len += r->next->len;
2282
             r->next = r->next->next;
2283
          }
2284
           goto out;
2285
2286 }
2287
      // Insert p before r in list.
2288 p - > 1en = 1en;
2289
      p->next = r;
2290
      *rp = p;
2291
2292 out:
2293
      release(&kalloc_lock);
2294 }
2295
2296
2297
2298
2299
```

Sheet 22

```
2300 // Allocate n bytes of physical memory.
2301 // Returns a kernel-segment pointer.
2302 // Returns 0 if the memory cannot be allocated.
2303 char*
2304 kalloc(int n)
2305 {
2306
       char *p;
2307
       struct run *r, **rp;
2308
2309
       if(n % PAGE || n <= 0)
2310
         panic("kalloc");
2311
2312
       acquire(&kalloc_lock);
2313
       for(rp=&freelist; (r=*rp) != 0; rp=&r->next){
2314
         if(r\rightarrow len == n)
2315
           *rp = r->next;
           release(&kalloc_lock);
2316
2317
           return (char*)r:
2318
2319
         if(r\rightarrow len > n){
2320
           r->1en -= n:
2321
           p = (char*)r + r -> len;
2322
           release(&kalloc_lock);
2323
           return p;
2324
         }
2325 }
2326
       release(&kalloc_lock);
2327
2328
       cprintf("kalloc: out of memory\n");
2329
       return 0;
2330 }
2331
2332
2333
2334
2335
2336
2337
2338
2339
2340
2341
2342
2343
2344
2345
2346
2347
2348
2349
```

```
2350 // x86 trap and interrupt constants.
2351
2352 // Processor-defined:
                                     // divide error
2353 #define T_DIVIDE
                              0
2354 #define T_DEBUG
                              1
                                     // debug exception
2355 #define T NMI
                              2
                                     // non-maskable interrupt
2356 #define T_BRKPT
                              3
                                     // breakpoint
                                     // overflow
2357 #define T_OFLOW
2358 #define T_BOUND
                                     // bounds check
2359 #define T_ILLOP
                                     // illegal opcode
2360 #define T_DEVICE
                              7
                                     // device not available
2361 #define T DBLFLT
                                     // double fault
2362 // #define T_COPROC
                              9
                                     // reserved (not used since 486)
2363 #define T_TSS
                             10
                                     // invalid task switch segment
2364 #define T SEGNP
                             11
                                     // segment not present
2365 #define T_STACK
                             12
                                     // stack exception
2366 #define T_GPFLT
                             13
                                     // general protection fault
2367 #define T PGFLT
                             14
                                     // page fault
2368 // #define T RES
                             15
                                     // reserved
2369 #define T_FPERR
                             16
                                     // floating point error
2370 #define T ALIGN
                             17
                                     // aligment check
2371 #define T MCHK
                             18
                                     // machine check
2372 #define T_SIMDERR
                             19
                                     // SIMD floating point error
2373
2374 // These are arbitrarily chosen, but with care not to overlap
2375 // processor defined exceptions or interrupt vectors.
2376 #define T_SYSCALL
                             48
                                     // system call
2377 #define T_DEFAULT
                            500
                                     // catchall
2378
2379 #define IRQ_OFFSET
                             32
                                     // IRQ 0 corresponds to int IRQ_OFFSET
2380
                              0
2381 #define IRQ_TIMER
2382 #define IRQ_KBD
                              1
2383 #define IRQ_IDE
                             14
2384 #define IRQ_ERROR
                             19
2385 #define IRQ_SPURIOUS
                             31
2386
2387
2388
2389
2390
2391
2392
2393
2394
2395
2396
2397
2398
2399
```

```
2400 #!/usr/bin/perl -w
2401
2402 # Generate vectors.S, the trap/interrupt entry points.
2403 # There has to be one entry point per interrupt number
2404 # since otherwise there's no way for trap() to discover
2405 # the interrupt number.
2406
2407 print "# generated by vectors.pl - do not edit\n";
2408 print "# handlers\n";
2409 print ".text\n";
2410 print ".globl alltraps\n";
2411 for(my i = 0; i < 256; i++)
        print ".globl vector$i\n";
2412
2413
        print "vector$i:\n";
2414
        if(($i < 8 || $i > 14) && $i != 17){
2415
            print " push1 \$0\n";
2416
        }
2417
        print " push1 \$$i\n";
2418
        print " jmp alltraps\n";
2419 }
2420
2421 print "\n# vector table\n";
2422 print ".data\n";
2423 print ".globl vectors\n";
2424 print "vectors:\n";
2425 for(my i = 0; i < 256; i++)
        print " .long vector$i\n";
2426
2427 }
2428
2429 # sample output:
2430 # # handlers
2431 #
        .text
2432 #
        .globl alltraps
2433 #
        .globl vector0
2434 # vector0:
2435 #
          push1 $0
2436 #
          push1 $0
2437 #
          jmp alltraps
2438 # ...
2439 #
2440 #
        # vector table
2441 #
        .data
2442 #
        .globl vectors
2443 # vectors:
2444 #
          .long vector0
2445 #
          .long vector1
2446 #
          .long vector2
2447 # ...
2448
2449
```

```
2450 .text
2451
2452 .set SEG_KDATA_SEL, 0x10 # selector for SEG_KDATA
2453
2454 # vectors.S sends all traps here.
2455 .globl alltraps
2456 alltraps:
2457 # Build trap frame.
2458 push1 %ds
      push1 %es
2459
2460
      pushal
2461
2462 # Set up data segments.
2463 movl $SEG_KDATA_SEL, %eax
2464 movw %ax.%ds
2465 movw %ax,%es
2466
2467 # Call trap(tf), where tf=%esp
2468 push1 %esp
2469
      call trap
2470
      addl $4. %esp
2471
2472 # Return falls through to trapret...
2473 .globl trapret
2474 trapret:
2475
      popal
2476
      popl %es
2477
      popl %ds
2478
      addl $0x8, %esp # trapno and errcode
2479 iret
2480
2481 # A forked process switches to user mode by calling
2482 # forkret1(tf), where tf is the trap frame to use.
2483 .globl forkret1
2484 forkret1:
2485 movl 4(%esp), %esp
2486
      jmp trapret
2487
2488
2489
2490
2491
2492
2493
2494
2495
2496
2497
2498
2499
```

```
2500 #include "types.h"
                                                                                  2550
                                                                                             ticks++;
2501 #include "defs.h"
                                                                                  2551
                                                                                             wakeup(&ticks);
2502 #include "param.h"
                                                                                  2552
                                                                                             release(&tickslock);
2503 #include "mmu.h"
                                                                                  2553
2504 #include "proc.h"
                                                                                  2554
                                                                                           lapic_eoi();
2505 #include "x86.h"
                                                                                  2555
                                                                                           break;
2506 #include "traps.h"
                                                                                  2556
                                                                                         case IRQ_OFFSET + IRQ_IDE:
2507 #include "spinlock.h"
                                                                                  2557
                                                                                           ide_intr();
2508
                                                                                  2558
                                                                                           lapic_eoi();
2509 // Interrupt descriptor table (shared by all CPUs).
                                                                                  2559
                                                                                           break:
2510 struct gatedesc idt[256];
                                                                                  2560
                                                                                         case IRQ_OFFSET + IRQ_KBD:
2511 extern uint vectors[]; // in vectors.S: array of 256 entry pointers
                                                                                  2561
                                                                                           kbd intr():
                                                                                  2562
                                                                                           lapic_eoi();
2512 struct spinlock tickslock;
2513 int ticks;
                                                                                  2563
                                                                                           break;
                                                                                  2564
                                                                                         case IRO OFFSET + IRO SPURIOUS:
2514
2515 void
                                                                                  2565
                                                                                           cprintf("cpu%d: spurious interrupt at %x:%x\n",
2516 tvinit(void)
                                                                                  2566
                                                                                                   cpu(), tf->cs, tf->eip);
2517 {
                                                                                  2567
                                                                                           lapic eoi():
2518 int i:
                                                                                  2568
                                                                                           break:
2519
                                                                                  2569
2520 for(i = 0: i < 256: i++)
                                                                                  2570
                                                                                         default:
2521
        SETGATE(idt[i], 0, SEG_KCODE<<3, vectors[i], 0);</pre>
                                                                                  2571
                                                                                           if(cp == 0 || (tf->cs&3) == 0){}
2522 SETGATE(idt[T_SYSCALL], 1, SEG_KCODE<<3, vectors[T_SYSCALL], DPL_USER);
                                                                                  2572
                                                                                             // In kernel, it must be our mistake.
2523
                                                                                  2573
                                                                                             cprintf("unexpected trap %d from cpu %d eip %x\n",
                                                                                  2574
2524 initlock(&tickslock, "time");
                                                                                                     tf->trapno, cpu(), tf->eip);
2525 }
                                                                                  2575
                                                                                             panic("trap");
2526
                                                                                  2576
2527 void
                                                                                  2577
                                                                                           // In user space, assume process misbehaved.
2528 idtinit(void)
                                                                                  2578
                                                                                           cprintf("pid %d %s: trap %d err %d on cpu %d eip %x -- kill proc\n",
                                                                                  2579
2529 {
                                                                                                   cp->pid, cp->name, tf->trapno, tf->err, cpu(), tf->eip);
2530 lidt(idt, sizeof(idt));
                                                                                  2580
                                                                                           cp->killed = 1;
2531 }
                                                                                  2581 }
                                                                                  2582
2532
2533 void
                                                                                  2583 // Force process exit if it has been killed and is in user space.
2534 trap(struct trapframe *tf)
                                                                                        // (If it is still executing in the kernel, let it keep running
2535 {
                                                                                  2585 // until it gets to the regular system call return.)
2536 if(tf->trapno == T_SYSCALL){
                                                                                  2586 if(cp && cp->killed && (tf->cs&3) == DPL_USER)
2537
        if(cp->killed)
                                                                                  2587
                                                                                           exit();
2538
          exit();
                                                                                  2588
2539
        cp->tf = tf;
                                                                                  2589
                                                                                        // Force process to give up CPU on clock tick.
2540
        syscall();
                                                                                        // If interrupts were on while locks held, would need to check nlock.
2541
        if(cp->killed)
                                                                                  2591 if(cp && cp->state == RUNNING && tf->trapno == IRQ_OFFSET+IRQ_TIMER)
2542
          exit();
                                                                                  2592
                                                                                           yield();
2543
        return;
                                                                                  2593 }
2544 }
                                                                                  2594
2545
                                                                                  2595
2546
      switch(tf->trapno){
                                                                                  2596
2547
       case IRQ_OFFSET + IRQ_TIMER:
                                                                                  2597
2548
        if(cpu() == 0){
                                                                                  2598
2549
           acquire(&tickslock);
                                                                                  2599
```

Sheet 25 Sheet 25

```
2600 // System call numbers
2601 #define SYS_fork
2602 #define SYS_exit
2603 #define SYS_wait
2604 #define SYS_pipe
2605 #define SYS_write
2606 #define SYS_read
2607 #define SYS_close 7
2608 #define SYS_kill
2609 #define SYS_exec
2610 #define SYS_open 10
2611 #define SYS mknod 11
2612 #define SYS unlink 12
2613 #define SYS_fstat 13
2614 #define SYS link 14
2615 #define SYS_mkdir 15
2616 #define SYS_chdir 16
2617 #define SYS dup
2618 #define SYS_getpid 18
2619 #define SYS_sbrk 19
2620 #define SYS sleep 20
2621
2622
2623
2624
2625
2626
2627
2628
2629
2630
2631
2632
2633
2634
2635
2636
2637
2638
2639
2640
2641
2642
2643
2644
2645
2646
2647
2648
2649
```

```
2650 #include "types.h"
2651 #include "defs.h"
2652 #include "param.h"
2653 #include "mmu.h"
2654 #include "proc.h"
2655 #include "x86.h"
2656 #include "syscall.h"
2657
2658 // User code makes a system call with INT T_SYSCALL.
2659 // System call number in %eax.
2660 // Arguments on the stack, from the user call to the C
2661 // library system call function. The saved user %esp points
2662 // to a saved program counter, and then the first argument.
2663
2664 // Fetch the int at addr from process p.
2665 int
2666 fetchint(struct proc *p, uint addr, int *ip)
2668 if(addr \Rightarrow p-\Rightarrowsz || addr+4 \Rightarrow p-\Rightarrowsz)
2669
         return -1;
     *ip = *(int*)(p->mem + addr);
2670
2671 return 0;
2672 }
2673
2674 // Fetch the nul-terminated string at addr from process p.
2675 // Doesn't actually copy the string - just sets *pp to point at it.
2676 // Returns length of string, not including nul.
2677 int
2678 fetchstr(struct proc *p, uint addr, char **pp)
2679 {
2680 char *s, *ep;
2681
2682 if(addr >= p->sz)
2683
         return -1;
2684
       *pp = p->mem + addr;
2685 ep = p->mem + p->sz;
2686
       for(s = *pp; s < ep; s++)
2687
        if(*s == 0)
2688
           return s - *pp;
2689
       return -1;
2690 }
2691
2692 // Fetch the nth 32-bit system call argument.
2693 int
2694 argint(int n, int *ip)
2695 {
2696 return fetchint(cp, cp->tf->esp + 4 + 4*n, ip);
2697 }
2698
2699
```

```
2700 // Fetch the nth word-sized system call argument as a pointer
                                                                                  2750 static int (*syscalls[])(void) = {
2701 // to a block of memory of size n bytes. Check that the pointer
                                                                                  2751 [SYS_chdir]
                                                                                                     sys_chdir.
2702 // lies within the process address space.
                                                                                  2752 [SYS_close]
                                                                                                     sys_close,
2703 int
                                                                                  2753 [SYS_dup]
                                                                                                      sys_dup,
2704 argptr(int n, char **pp, int size)
                                                                                  2754 [SYS_exec]
                                                                                                     sys_exec,
2705 {
                                                                                  2755 [SYS_exit]
                                                                                                     sys_exit,
2706 int i;
                                                                                  2756 [SYS_fork]
                                                                                                     sys_fork,
2707
                                                                                  2757 [SYS_fstat]
                                                                                                     sys_fstat,
2708
      if(argint(n, \&i) < 0)
                                                                                  2758 [SYS_getpid] sys_getpid,
                                                                                  2759 [SYS_kill]
2709
        return -1;
                                                                                                     sys_kill,
2710 if((uint)i >= cp->sz || (uint)i+size >= cp->sz)
                                                                                  2760 [SYS_link]
                                                                                                     sys_link,
2711
        return -1:
                                                                                  2761 [SYS_mkdir]
                                                                                                     sys_mkdir,
2712
      *pp = cp->mem + i;
                                                                                  2762 [SYS_mknod]
                                                                                                     sys_mknod,
2713
      return 0;
                                                                                  2763 [SYS_open]
                                                                                                     sys_open,
2714 }
                                                                                  2764 [SYS_pipe]
                                                                                                     sys_pipe,
2715
                                                                                  2765 [SYS_read]
                                                                                                     sys_read,
2716 // Fetch the nth word-sized system call argument as a string pointer.
                                                                                  2766 [SYS_sbrk]
                                                                                                     sys_sbrk,
2717 // Check that the pointer is valid and the string is nul-terminated.
                                                                                  2767 [SYS sleep]
                                                                                                     sys_sleep,
2718 // (There is no shared writable memory, so the string can't change
                                                                                  2768 [SYS_unlink] sys_unlink,
2719 // between this check and being used by the kernel.)
                                                                                  2769 [SYS_wait]
                                                                                                     sys_wait,
2720 int
                                                                                  2770 [SYS write]
                                                                                                     sys_write,
2721 argstr(int n, char **pp)
                                                                                  2771 };
2722 {
                                                                                  2772
2723 int addr;
                                                                                  2773 void
2724 if(argint(n, &addr) < 0)
                                                                                  2774 syscall(void)
2725
                                                                                  2775 {
        return -1;
2726
      return fetchstr(cp, addr, pp);
                                                                                  2776 int num;
2727 }
                                                                                  2777
2728
                                                                                  2778
                                                                                         num = cp->tf->eax;
2729 extern int sys_chdir(void);
                                                                                  2779
                                                                                         if(num >= 0 && num < NELEM(syscalls) && syscalls[num])</pre>
2730 extern int sys_close(void);
                                                                                  2780
                                                                                           cp->tf->eax = syscalls[num]();
2731 extern int sys_dup(void);
                                                                                  2781
                                                                                         else {
                                                                                  2782
                                                                                           cprintf("%d %s: unknown sys call %d\n",
2732 extern int sys_exec(void);
2733 extern int sys_exit(void);
                                                                                  2783
                                                                                                   cp->pid, cp->name, num);
                                                                                           cp->tf->eax = -1;
2734 extern int sys_fork(void);
                                                                                  2784
2735 extern int sys_fstat(void);
                                                                                  2785 }
2736 extern int sys_getpid(void);
                                                                                  2786 }
2737 extern int sys_kill(void);
                                                                                  2787
2738 extern int sys_link(void);
                                                                                  2788
2739 extern int sys_mkdir(void);
                                                                                  2789
2740 extern int sys_mknod(void);
                                                                                  2790
2741 extern int sys_open(void);
                                                                                  2791
2742 extern int sys_pipe(void);
                                                                                  2792
2743 extern int sys_read(void);
                                                                                  2793
2744 extern int sys_sbrk(void);
                                                                                  2794
2745 extern int sys_sleep(void);
                                                                                  2795
2746 extern int sys_unlink(void);
                                                                                  2796
2747 extern int sys_wait(void);
                                                                                  2797
2748 extern int sys_write(void);
                                                                                  2798
2749
                                                                                  2799
```

Sheet 27 Sheet 27

```
2800 #include "types.h"
2801 #include "defs.h"
2802 #include "param.h"
2803 #include "mmu.h"
2804 #include "proc.h"
2805
2806 int
2807 sys_fork(void)
2808 {
2809 int pid;
2810 struct proc *np;
2811
2812 if((np = copyproc(cp)) == 0)
2813
      return -1;
2814 pid = np->pid;
2815 np->state = RUNNABLE;
2816 return pid;
2817 }
2818
2819 int
2820 sys_exit(void)
2821 {
2822 exit();
2823 return 0; // not reached
2824 }
2825
2826 int
2827 sys_wait(void)
2828 {
2829 return wait();
2830 }
2831
2832 int
2833 sys_kill(void)
2834 {
2835 int pid;
2836
2837 if(argint(0, &pid) < 0)
2838
      return -1;
2839 return kill(pid);
2840 }
2841
2842 int
2843 sys_getpid(void)
2844 {
2845 return cp->pid;
2846 }
2847
2848
2849
```

```
2850 int
2851 sys_sbrk(void)
2852 {
2853 int addr;
2854 int n;
2855
2856 if(argint(0, &n) < 0)
       return -1;
2857
2858 if((addr = growproc(n)) < 0)
2859
       return -1;
2860 return addr;
2861 }
2862
2863 int
2864 sys_sleep(void)
2865 {
2866 int n, ticks0;
2867
2868 if(argint(0, &n) < 0)
2869
       return -1;
2870 acquire(&tickslock);
2871 ticks0 = ticks;
2872 while(ticks - ticks0 < n){
2873
       if(cp->killed){
2874
          release(&tickslock);
2875
          return -1;
2876
2877
        sleep(&ticks, &tickslock);
2878 }
2879
      release(&tickslock);
2880
      return 0;
2881 }
2882
2883
2884
2885
2886
2887
2888
2889
2890
2891
2892
2893
2894
2895
2896
2897
2898
2899
```

Sheet 29 Sheet 29

3000 3001	#define #define	O_RDONLY O_WRONLY	0x000 0x001
	#define	_	
3002		_	0x002
3003	#define	O_CREATE	0x200
3004			
3005			
3006			
3007			
3008			
3009			
3010			
3011			
3012			
3013			
3013			
3015			
3016			
3017			
3018			
3019			
3020			
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3046			
3047			
3048			
3049			

```
3050 struct stat {
3051 int dev;
                  // Device number
3052 uint ino; // Inode number on device
3053 short type; // Type of file
3054 short nlink; // Number of links to file
3055 uint size; // Size of file in bytes
3056 };
3057
3058
3059
3060
3061
3062
3063
3064
3065
3066
3067
3068
3069
3070
3071
3072
3073
3074
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3097
3098
3099
```

```
3100 struct file {
3101 enum { FD_CLOSED, FD_NONE, FD_PIPE, FD_INODE } type;
3102 int ref; // reference count
3103
      char readable;
3104 char writable;
3105 struct pipe *pipe;
3106 struct inode *ip;
3107 uint off;
3108 };
3109
3110
3111
3112
3113
3114
3115
3116
3117
3118
3119
3120
3121
3122
3123
3124
3125
3126
3127
3128
3129
3130
3131
3132
3133
3134
3135
3136
3137
3138
3139
3140
3141
3142
3143
3144
3145
3146
3147
3148
3149
```

```
3150 // On-disk file system format.
3151 // Both the kernel and user programs use this header file.
3152
3153 // Block O is unused.
3154 // Block 1 is super block.
3155 // Inodes start at block 2.
3156
3157 #define BSIZE 512 // block size
3158
3159 // File system super block
3160 struct superblock {
3161 uint size:
                         // Size of file system image (blocks)
3162 uint nblocks:
                         // Number of data blocks
3163 uint ninodes;
                         // Number of inodes.
3164 };
3165
3166 #define NADDRS (NDIRECT+1)
3167 #define NDIRECT 12
3168 #define INDIRECT 12
3169 #define NINDIRECT (BSIZE / sizeof(uint))
3170 #define MAXFILE (NDIRECT + NINDIRECT)
3171
3172 // On-disk inode structure
3173 struct dinode {
3174 short type:
                            // File type
3175 short major;
                            // Major device number (T_DEV only)
3176 short minor;
                            // Minor device number (T_DEV only)
3177 short nlink;
                            // Number of links to inode in file system
3178 uint size;
                            // Size of file (bytes)
3179 uint addrs[NADDRS]; // Data block addresses
3180 };
3181
3182 #define T_DIR 1 // Directory
3183 #define T_FILE 2 // File
3184 #define T_DEV 3 // Special device
3185
3186 // Inodes per block.
3187 #define IPB
                          (BSIZE / sizeof(struct dinode))
3188
3189 // Block containing inode i
3190 #define IBLOCK(i)
                          ((i) / IPB + 2)
3191
3192 // Bitmap bits per block
3193 #define BPB
                          (BSIZE*8)
3194
3195 // Block containing bit for block b
3196 #define BBLOCK(b, ninodes) (b/BPB + (ninodes)/IPB + 3)
3197
3198
3199
```

3200 // Directory is a file containing a sequence of dirent structures.	3250 // in-core file system types
3201 #define DIRSIZ 14	3251
3202	3252 struct inode {
3203 struct dirent {	3253 uint dev; // Device number
3204 ushort inum;	3254 uint inum; // Inode number
3205 char name[DIRSIZ];	3255 int ref; // Reference count
3206 };	3256 int flags; // I_BUSY, I_VALID
3207	3257
3208	3258 short type; // copy of disk inode
3209	3259 short major;
3210	3260 short minor;
3211	3261 short nlink;
3212	3262 uint size;
3213	3263 uint addrs[NADDRS];
3214	3264 };
3215	3265
3216	3266 #define I_BUSY 0x1
3217	3267 #define I_VALID 0x2
3218	3268
3219	3269
3220	3270
3221	3271
3222	3272
3223	3273
3224	3274
3225	3275
3226	3276
3227	3277
3228	3278
3229	3279
3230	3280
3231	3281
3232	3282
3233	3283
3234	3284
3235	3285
3236	3286
3237	3287
3238	3288
3239	3289
3240	3290
3241	3291
3242	3292
3243	3293
3244	3294
3245	3295
3246	3296
3247	3297
3248	3298
3249	3299
JETJ	<i>3233</i>

```
3350 void
3351 ide_init(void)
3352 {
3353 int i;
3354
3355 initlock(&ide_lock, "ide");
3356
      pic_enable(IRQ_IDE);
3357
      ioapic_enable(IRQ_IDE, ncpu - 1);
3358
     ide_wait_ready(0);
3359
3360
     // Check if disk 1 is present
3361
     outb(0x1f6, 0xe0 | (1 << 4));
3362
      for(i=0; i<1000; i++){
3363
        if(inb(0x1f7) != 0){
3364
          disk_1_present = 1;
3365
          break;
3366
        }
3367
     }
3368
3369
     // Switch back to disk 0.
     outb(0x1f6, 0xe0 | (0<<4));
3370
3371 }
3372
3373 // Start the request for b. Caller must hold ide_lock.
3374 static void
3375 ide_start_request(struct buf *b)
3376 {
3377 if(b == 0)
3378
        panic("ide_start_request");
3379
3380 ide_wait_ready(0);
3381
      outb(0x3f6, 0); // generate interrupt
      outb(0x1f2, 1); // number of sectors
3382
3383
      outb(0x1f3, b->sector & 0xff);
3384
      outb(0x1f4, (b->sector >> 8) & 0xff);
3385
      outb(0x1f5, (b->sector >> 16) & 0xff);
3386
      outb(0x1f6, 0xe0 | ((b->dev&1)<<4) | ((b->sector>>24)&0x0f));
3387
      if(b->flags & B_DIRTY){
3388
        outb(0x1f7, IDE_CMD_WRITE);
3389
        outs1(0x1f0, b->data, 512/4);
3390 } else {
3391
        outb(0x1f7, IDE_CMD_READ);
3392 }
3393 }
3394
3395
3396
3397
3398
3399
```

3348

3349

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3450 // Sync buf with disk.

```
3451 // If B_DIRTY is set, write buf to disk, clear B_DIRTY, set B_VALID.
3452 // Else if B_VALID is not set, read buf from disk, set B_VALID.
3453 void
3454 ide_rw(struct buf *b)
3455 {
3456 struct buf **pp;
3457
3458
     if(!(b->flags & B_BUSY))
3459
        panic("ide_rw: buf not busy");
3460
      if((b->flags & (B_VALID|B_DIRTY)) == B_VALID)
3461
        panic("ide_rw: nothing to do");
3462
      if(b->dev != 0 && !disk_1_present)
3463
        panic("ide disk 1 not present");
3464
3465
      acquire(&ide_lock);
3466
3467
      // Append b to ide_queue.
3468
      b->anext = 0:
3469
      for(pp=&ide_queue; *pp; pp=&(*pp)->qnext)
3470
3471
     *pp = b;
3472
3473
      // Start disk if necessary.
3474
      if(ide_queue == b)
3475
        ide_start_request(b);
3476
3477
      // Wait for request to finish.
3478
      // Assuming will not sleep too long: ignore cp->killed.
3479
      while((b->flags & (B_VALID|B_DIRTY)) != B_VALID)
3480
        sleep(b, &ide_lock);
3481
3482
      release(&ide_lock);
3483 }
3484
3485
3486
3487
3488
3489
3490
3491
3492
3493
3494
3495
3496
3497
3498
3499
```

```
3500 // Buffer cache.
                                                                                  3550
                                                                                         // Create linked list of buffers
3501 //
                                                                                         bufhead.prev = &bufhead;
                                                                                  3551
3502 // The buffer cache is a linked list of buf structures holding
                                                                                  3552
                                                                                         bufhead.next = &bufhead;
                                                                                         for(b = buf; b < buf+NBUF; b++){</pre>
3503 // cached copies of disk block contents. Caching disk blocks
                                                                                  3553
3504 // in memory reduces the number of disk reads and also provides
                                                                                  3554
                                                                                           b->next = bufhead.next;
3505 // a synchronization point for disk blocks used by multiple processes.
                                                                                  3555
                                                                                           b->prev = &bufhead;
3506 //
                                                                                  3556
                                                                                           bufhead.next->prev = b;
3507 // Interface:
                                                                                  3557
                                                                                           bufhead.next = b;
3508 // * To get a buffer for a particular disk block, call bread.
                                                                                  3558 }
                                                                                  3559 }
3509 // * After changing buffer data, call bwrite to flush it to disk.
3510 // * When done with the buffer, call brelse.
                                                                                  3560
3511 // * Do not use the buffer after calling brelse.
                                                                                  3561 // Look through buffer cache for sector on device dev.
3512 // * Only one process at a time can use a buffer,
                                                                                  3562 // If not found, allocate fresh block.
3513 //
            so do not keep them longer than necessary.
                                                                                  3563 // In either case, return locked buffer.
                                                                                  3564 static struct buf*
3514 //
                                                                                  3565 bget(uint dev, uint sector)
3515 // The implementation uses three state flags internally:
3516 // * B_BUSY: the block has been returned from bread
                                                                                  3566 {
           and has not been passed back to brelse.
                                                                                  3567 struct buf *b:
3518 // * B VALID: the buffer data has been initialized
                                                                                  3568
3519 // with the associated disk block contents.
                                                                                  3569
                                                                                         acquire(&buf_table_lock);
3520 // * B DIRTY: the buffer data has been modified
                                                                                  3570
3521 //
           and needs to be written to disk.
                                                                                  3571 loop:
3522
                                                                                  3572
                                                                                         // Try for cached block.
3523 #include "types.h"
                                                                                  3573
                                                                                         for(b = bufhead.next; b != &bufhead; b = b->next){
3524 #include "defs.h"
                                                                                  3574
                                                                                           if((b->flags & (B BUSY|B VALID)) &&
3525 #include "param.h"
                                                                                  3575
                                                                                              b->dev == dev && b->sector == sector){
3526 #include "spinlock.h"
                                                                                  3576
                                                                                              if(b->flags & B_BUSY){
3527 #include "buf.h"
                                                                                                sleep(buf, &buf_table_lock);
                                                                                  3577
3528
                                                                                  3578
                                                                                                goto loop;
3529 struct buf buf[NBUF];
                                                                                  3579
3530 struct spinlock buf_table_lock;
                                                                                  3580
                                                                                             b->flags |= B_BUSY;
                                                                                  3581
                                                                                              release(&buf_table_lock);
3532 // Linked list of all buffers, through prev/next.
                                                                                  3582
                                                                                              return b;
3533 // bufhead->next is most recently used.
                                                                                  3583
3534 // bufhead->tail is least recently used.
                                                                                  3584 }
3535 struct buf bufhead:
                                                                                  3585
3536
                                                                                  3586
                                                                                        // Allocate fresh block.
3537 void
                                                                                  3587
                                                                                         for(b = bufhead.prev; b != &bufhead; b = b->prev){
3538 binit(void)
                                                                                  3588
                                                                                           if((b\rightarrow flags \& B\_BUSY) == 0){
3539 {
                                                                                  3589
                                                                                             b->flags = B_BUSY;
3540 struct buf *b;
                                                                                  3590
                                                                                             b->dev = dev;
3541
                                                                                  3591
                                                                                             b->sector = sector:
3542
      initlock(&buf_table_lock, "buf_table");
                                                                                  3592
                                                                                              release(&buf_table_lock);
3543
                                                                                  3593
                                                                                              return b;
3544
                                                                                  3594
3545
                                                                                  3595 }
3546
                                                                                  3596
                                                                                         panic("bget: no buffers");
3547
                                                                                  3597 }
3548
                                                                                  3598
3549
                                                                                  3599
```

```
3600 // Return a B_BUSY buf with the contents of the indicated disk sector.
                                                                                   3650 // File system implementation. Four layers:
3601 struct buf*
                                                                                   3651 // + Blocks: allocator for raw disk blocks.
3602 bread(uint dev, uint sector)
                                                                                   3652 // + Files: inode allocator, reading, writing, metadata.
                                                                                   3653 // + Directories: inode with special contents (list of other inodes!)
3603 {
3604 struct buf *b;
                                                                                   3654 // + Names: paths like /usr/rtm/xv6/fs.c for convenient naming.
3605
                                                                                   3655 //
3606 b = bget(dev, sector);
                                                                                   3656 // Disk layout is: superblock, inodes, block in-use bitmap, data blocks.
3607 if(!(b->flags & B_VALID))
                                                                                   3657 //
3608
                                                                                   3658 // This file contains the low-level file system manipulation
       ide_rw(b);
3609 return b;
                                                                                   3659 // routines. The (higher-level) system call implementations
3610 }
                                                                                   3660 // are in sysfile.c.
3611
                                                                                   3661
                                                                                   3662 #include "types.h"
3612 // Write buf's contents to disk. Must be locked.
3613 void
                                                                                   3663 #include "defs.h"
3614 bwrite(struct buf *b)
                                                                                   3664 #include "param.h"
                                                                                   3665 #include "stat.h"
3615 {
3616 if((b\rightarrow flags \& B\_BUSY) == 0)
                                                                                   3666 #include "mmu.h"
3617
        panic("bwrite"):
                                                                                   3667 #include "proc.h"
3618 b->flags |= B_DIRTY;
                                                                                   3668 #include "spinlock.h"
3619 ide_rw(b);
                                                                                   3669 #include "buf.h"
3620 }
                                                                                   3670 #include "fs.h"
3621
                                                                                   3671 #include "fsvar.h"
3622 // Release the buffer buf.
                                                                                   3672 #include "dev.h"
3623 void
                                                                                   3673
3624 brelse(struct buf *b)
                                                                                   3674 \# define min(a, b) ((a) < (b) ? (a) : (b))
                                                                                   3675 static void itrunc(struct inode*);
3625 {
3626 if((b->flags & B_BUSY) == 0)
3627
        panic("brelse");
                                                                                   3677 // Read the super block.
3628
                                                                                   3678 static void
3629
      acquire(&buf_table_lock);
                                                                                   3679 readsb(int dev, struct superblock *sb)
                                                                                   3680 {
3630
3631 b \rightarrow next \rightarrow prev = b \rightarrow prev;
                                                                                   3681 struct buf *bp;
b \rightarrow prev \rightarrow next = b \rightarrow next;
                                                                                   3682
      b->next = bufhead.next;
3633
                                                                                   3683 bp = bread(dev, 1);
3634 b->prev = &bufhead;
                                                                                   3684 memmove(sb, bp->data, sizeof(*sb));
3635 bufhead.next->prev = b;
                                                                                   3685 brelse(bp);
3636
      bufhead.next = b;
                                                                                   3686 }
3637
                                                                                   3687
3638
      b->flags &= ~B_BUSY;
                                                                                   3688 // Zero a block.
3639
      wakeup(buf);
                                                                                   3689 static void
3640
                                                                                   3690 bzero(int dev, int bno)
3641
      release(&buf_table_lock);
                                                                                   3691 {
3642 }
                                                                                   3692 struct buf *bp;
3643
                                                                                   3693
3644
                                                                                   3694 bp = bread(dev, bno);
3645
                                                                                   3695 memset(bp->data, 0, BSIZE);
3646
                                                                                   3696
                                                                                         bwrite(bp);
3647
                                                                                   3697 brelse(bp);
3648
                                                                                   3698 }
3649
                                                                                   3699
```

```
3700 // Blocks.
3701
3702 // Allocate a disk block.
3703 static uint
3704 balloc(uint dev)
3705 {
3706 int b, bi, m;
3707
      struct buf *bp;
3708
      struct superblock sb;
3709
3710 bp = 0;
3711
      readsb(dev. &sb):
3712
      for(b = 0; b < sb.size; b += BPB){
3713
        bp = bread(dev, BBLOCK(b, sb.ninodes));
3714
        for(bi = 0: bi < BPB: bi++){
3715
          m = 1 \ll (bi \% 8);
3716
          if((bp->data[bi/8] \& m) == 0){ // Is block free?}
3717
            bp->data[bi/8] |= m; // Mark block in use on disk.
3718
            bwrite(bp):
3719
            brelse(bp);
3720
            return b + bi:
3721
3722
        }
3723
        brelse(bp);
3724 }
3725
      panic("balloc: out of blocks");
3726 }
3727
3728 // Free a disk block.
3729 static void
3730 bfree(int dev, uint b)
3731 {
3732 struct buf *bp;
3733 struct superblock sb;
3734 int bi, m;
3735
3736
      bzero(dev, b);
3737
3738
      readsb(dev, &sb);
3739
      bp = bread(dev, BBLOCK(b, sb.ninodes));
3740 bi = b % BPB:
3741 m = 1 \ll (bi \% 8):
3742 if((bp->data[bi/8] \& m) == 0)
3743
        panic("freeing free block");
3744 bp->data[bi/8] &= ~m; // Mark block free on disk.
3745 bwrite(bp);
3746 brelse(bp);
3747 }
3748
3749
```

```
3750 // Inodes.
3751 //
3752 // An inode is a single, unnamed file in the file system.
3753 // The inode disk structure holds metadata (the type, device numbers,
3754 // and data size) along with a list of blocks where the associated
3755 // data can be found.
3756 //
3757 // The inodes are laid out sequentially on disk immediately after
3758 // the superblock. The kernel keeps a cache of the in-use
3759 // on-disk structures to provide a place for synchronizing access
3760 // to inodes shared between multiple processes.
3762 // ip->ref counts the number of pointer references to this cached
3763 // inode; references are typically kept in struct file and in cp->cwd.
3764 // When ip->ref falls to zero, the inode is no longer cached.
3765 // It is an error to use an inode without holding a reference to it.
3766 //
3767 // Processes are only allowed to read and write inode
3768 // metadata and contents when holding the inode's lock.
3769 // represented by the I_BUSY flag in the in-memory copy.
3770 // Because inode locks are held during disk accesses.
3771 // they are implemented using a flag rather than with
3772 // spin locks. Callers are responsible for locking
3773 // inodes before passing them to routines in this file; leaving
3774 // this responsibility with the caller makes it possible for them
3775 // to create arbitrarily-sized atomic operations.
3777 // To give maximum control over locking to the callers,
3778 // the routines in this file that return inode pointers
3779 // return pointers to *unlocked* inodes. It is the callers'
3780 // responsibility to lock them before using them. A non-zero
3781 // ip->ref keeps these unlocked inodes in the cache.
3782
3783 struct {
3784 struct spinlock lock;
3785 struct inode inode[NINODE];
3786 } icache;
3787
3788 void
3789 iinit(void)
3790 {
3791 initlock(&icache.lock, "icache.lock");
3792 }
3793
3794
3795
3796
3797
3798
3799
```

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Sheet 38 Sheet 38

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```
3900 // Caller holds reference to unlocked ip. Drop reference.
3901 void
3902 iput(struct inode *ip)
3903 {
3904
       acquire(&icache.lock);
3905
       if(ip\rightarrow ref == 1 \&\& (ip\rightarrow flags \& I\_VALID) \&\& ip\rightarrow nlink == 0)
3906
         // inode is no longer used: truncate and free inode.
3907
         if(ip->flags & I_BUSY)
3908
           panic("iput busy");
3909
         ip->flags |= I_BUSY;
3910
         release(&icache.lock);
3911
         itrunc(ip);
3912
         ip->type = 0;
3913
         iupdate(ip);
3914
         acquire(&icache.lock);
3915
         ip->flags &= ~I_BUSY;
3916
         wakeup(ip);
3917 }
3918 ip->ref--:
3919
       release(&icache.lock);
3920 }
3921
3922 // Common idiom: unlock, then put.
3923 void
3924 iunlockput(struct inode *ip)
3925 {
3926 iunlock(ip);
3927 iput(ip);
3928 }
3929
3930
3931
3932
3933
3934
3935
3936
3937
3938
3939
3940
3941
3942
3943
3944
3945
3946
3947
3948
3949
```

```
3950 // Allocate a new inode with the given type on device dev.
3951 struct inode*
3952 ialloc(uint dev, short type)
3953 {
3954 int inum;
3955 struct buf *bp;
3956
      struct dinode *dip;
3957
      struct superblock sb;
3958
3959
      readsb(dev, &sb);
3960
      for(inum = 1; inum < sb.ninodes; inum++){ // loop over inode blocks</pre>
3961
        bp = bread(dev, IBLOCK(inum));
3962
        dip = (struct dinode*)bp->data + inum%IPB;
3963
        if(dip->type == 0){ // a free inode
3964
          memset(dip, 0, sizeof(*dip));
3965
          dip->type = type;
3966
          bwrite(bp); // mark it allocated on the disk
3967
          brelse(bp):
3968
          return iget(dev, inum);
3969
3970
        brelse(bp):
3971 }
3972 panic("ialloc: no inodes");
3973 }
3974
3975 // Copy inode, which has changed, from memory to disk.
3976 void
3977 iupdate(struct inode *ip)
3978 {
3979 struct buf *bp;
3980
      struct dinode *dip;
3981
3982 bp = bread(ip->dev, IBLOCK(ip->inum));
3983
      dip = (struct dinode*)bp->data + ip->inum%IPB;
3984
      dip->type = ip->type;
3985
      dip->major = ip->major;
3986
      dip->minor = ip->minor;
3987
      dip->nlink = ip->nlink;
3988 dip->size = ip->size;
3989
      memmove(dip->addrs, ip->addrs, sizeof(ip->addrs));
3990
      bwrite(bp);
3991 brelse(bp);
3992 }
3993
3994
3995
3996
3997
3998
3999
```

```
4000 // Inode contents
                                                                                  4050 // Truncate inode (discard contents).
4001 //
                                                                                  4051 static void
4002 // The contents (data) associated with each inode is stored
                                                                                  4052 itrunc(struct inode *ip)
4003 // in a sequence of blocks on the disk. The first NDIRECT blocks
                                                                                  4053 {
4004 // are listed in ip->addrs[]. The next NINDIRECT blocks are
                                                                                  4054 int i, j;
4005 // listed in the block ip->addrs[INDIRECT].
                                                                                  4055 struct buf *bp;
4006
                                                                                  4056
                                                                                         uint *a;
4007 // Return the disk block address of the nth block in inode ip.
                                                                                  4057
4008 // If there is no such block, alloc controls whether one is allocated.
                                                                                  4058
                                                                                         for(i = 0; i < NDIRECT; i++){
4009 static uint
                                                                                  4059
                                                                                           if(ip->addrs[i]){
4010 bmap(struct inode *ip, uint bn, int alloc)
                                                                                  4060
                                                                                             bfree(ip->dev, ip->addrs[i]);
4011 {
                                                                                  4061
                                                                                             ip->addrs[i] = 0;
4012 uint addr, *a;
                                                                                  4062
                                                                                           }
4013
      struct buf *bp;
                                                                                  4063
                                                                                       }
4014
                                                                                  4064
4015 if(bn < NDIRECT){
                                                                                  4065
                                                                                         if(ip->addrs[INDIRECT]){
4016
        if((addr = ip->addrs[bn]) == 0){
                                                                                  4066
                                                                                           bp = bread(ip->dev, ip->addrs[INDIRECT]);
4017
          if(!alloc)
                                                                                  4067
                                                                                           a = (uint*)bp->data:
4018
                                                                                  4068
                                                                                           for(j = 0; j < NINDIRECT; j++){
            return -1:
4019
          ip->addrs[bn] = addr = balloc(ip->dev);
                                                                                  4069
                                                                                             if(a[j])
4020
                                                                                  4070
                                                                                               bfree(ip->dev, a[j]);
4021
        return addr;
                                                                                  4071
4022
      }
                                                                                  4072
                                                                                           brelse(bp);
4023
      bn -= NDIRECT;
                                                                                  4073
                                                                                           ip->addrs[INDIRECT] = 0;
                                                                                  4074
4024
4025
      if(bn < NINDIRECT){</pre>
                                                                                  4075
4026
        // Load indirect block, allocating if necessary.
                                                                                  4076
                                                                                        ip->size = 0;
4027
        if((addr = ip->addrs[INDIRECT]) == 0){
                                                                                  4077
                                                                                         iupdate(ip);
4028
          if(!alloc)
                                                                                  4078 }
4029
            return -1;
                                                                                  4079
4030
          ip->addrs[INDIRECT] = addr = balloc(ip->dev);
                                                                                  4080 // Copy stat information from inode.
4031
4032
        bp = bread(ip->dev, addr);
                                                                                  4082 stati(struct inode *ip, struct stat *st)
4033
        a = (uint*)bp->data;
                                                                                  4083 {
4034
                                                                                  4084 st->dev = ip->dev;
4035
        if((addr = a[bn]) == 0){
                                                                                  4085 st->ino = ip->inum;
4036
          if(!alloc){
                                                                                  4086 st->type = ip->type;
4037
            brelse(bp);
                                                                                  4087 st->nlink = ip->nlink;
4038
            return -1;
                                                                                  4088 st->size = ip->size;
4039
                                                                                  4089 }
4040
          a[bn] = addr = balloc(ip->dev);
                                                                                  4090
4041
          bwrite(bp);
                                                                                  4091
4042
        }
                                                                                  4092
4043
        brelse(bp);
                                                                                  4093
4044
        return addr;
                                                                                  4094
4045 }
                                                                                  4095
4046
                                                                                  4096
4047
      panic("bmap: out of range");
                                                                                  4097
4048 }
                                                                                  4098
4049
                                                                                  4099
```

```
4100 // Read data from inode.
                                                                                  4150 // Write data to inode.
4101 int
                                                                                  4151 int
4102 readi(struct inode *ip, char *dst, uint off, uint n)
                                                                                  4152 writei(struct inode *ip, char *src, uint off, uint n)
4103 {
                                                                                  4153 {
4104 uint tot, m;
                                                                                  4154 uint tot, m;
4105
      struct buf *bp;
                                                                                  4155 struct buf *bp;
4106
                                                                                  4156
4107
      if(ip->type == T_DEV){
                                                                                  4157
                                                                                       if(ip->type == T_DEV){
4108
        if(ip->major < 0 || ip->major >= NDEV || !devsw[ip->major].read)
                                                                                  4158
                                                                                          if(ip->major < 0 || ip->major >= NDEV || !devsw[ip->major].write)
4109
                                                                                  4159
4110
        return devsw[ip->major].read(ip, dst, n);
                                                                                  4160
                                                                                          return devsw[ip->major].write(ip, src, n);
4111
                                                                                  4161
      }
                                                                                       }
4112
                                                                                  4162
4113
      if(off > ip->size || off + n < off)</pre>
                                                                                  4163
                                                                                       if(off + n < off)
4114
        return -1:
                                                                                  4164
                                                                                          return -1:
4115
      if(off + n > ip->size)
                                                                                  4165
                                                                                        if(off + n > MAXFILE*BSIZE)
4116
        n = ip->size - off;
                                                                                  4166
                                                                                          n = MAXFILE*BSIZE - off;
4117
                                                                                  4167
4118
      for(tot=0; tot<n; tot+=m, off+=m, dst+=m){</pre>
                                                                                  4168
                                                                                         for(tot=0; tot<n; tot+=m, off+=m, src+=m){</pre>
4119
        bp = bread(ip->dev, bmap(ip, off/BSIZE, 0));
                                                                                  4169
                                                                                          bp = bread(ip->dev, bmap(ip, off/BSIZE, 1));
        m = min(n - tot, BSIZE - off%BSIZE);
4120
                                                                                  4170
                                                                                          m = min(n - tot, BSIZE - off%BSIZE);
4121
        memmove(dst, bp->data + off%BSIZE, m);
                                                                                  4171
                                                                                          memmove(bp->data + off%BSIZE, src, m);
4122
        brelse(bp);
                                                                                  4172
                                                                                          bwrite(bp);
4123 }
                                                                                  4173
                                                                                          brelse(bp);
4124 return n;
                                                                                  4174 }
4125 }
                                                                                  4175
4126
                                                                                  4176
                                                                                       if(n > 0 \&\& off > ip->size){
4127
                                                                                  4177
                                                                                          ip->size = off;
                                                                                          iupdate(ip);
4128
                                                                                  4178
4129
                                                                                  4179 }
4130
                                                                                  4180
                                                                                       return n;
4131
                                                                                  4181 }
4132
                                                                                  4182
4133
                                                                                  4183
4134
                                                                                  4184
4135
                                                                                  4185
4136
                                                                                  4186
4137
                                                                                  4187
4138
                                                                                  4188
4139
                                                                                  4189
4140
                                                                                  4190
4141
                                                                                  4191
4142
                                                                                  4192
4143
                                                                                  4193
4144
                                                                                  4194
4145
                                                                                  4195
4146
                                                                                  4196
4147
                                                                                  4197
4148
                                                                                  4198
4149
                                                                                  4199
```

Sheet 41 Sheet 41

```
4200 // Directories
4201
4202 int
4203 namecmp(const char *s, const char *t)
4204 {
4205 return strncmp(s, t, DIRSIZ);
4206 }
4207
4208 // Look for a directory entry in a directory.
4209 // If found, set *poff to byte offset of entry.
4210 // Caller must have already locked dp.
4211 struct inode*
4212 dirlookup(struct inode *dp, char *name, uint *poff)
4213 {
4214 uint off, inum;
4215
      struct buf *bp;
4216
      struct dirent *de;
4217
4218 if(dp->type != T_DIR)
4219
        panic("dirlookup not DIR");
4220
4221
      for(off = 0; off < dp->size; off += BSIZE){
4222
        bp = bread(dp->dev, bmap(dp, off / BSIZE, 0));
4223
        for(de = (struct dirent*)bp->data;
4224
            de < (struct dirent*)(bp->data + BSIZE);
4225
            de++){
          if(de->inum == 0)
4226
4227
            continue;
4228
          if(namecmp(name, de->name) == 0){
4229
            // entry matches path element
4230
            if(poff)
4231
               *poff = off + (uchar*)de - bp->data;
4232
            inum = de->inum;
4233
            brelse(bp);
4234
             return iget(dp->dev, inum);
4235
          }
4236
        }
4237
        brelse(bp);
4238 }
4239 return 0;
4240 }
4241
4242
4243
4244
4245
4246
4247
4248
4249
```

```
4250 // Write a new directory entry (name, ino) into the directory dp.
4251 int
4252 dirlink(struct inode *dp, char *name, uint ino)
4253 {
4254 int off;
4255 struct dirent de:
4256
      struct inode *ip;
4257
4258 // Check that name is not present.
4259
      if((ip = dirlookup(dp, name, 0)) != 0){
4260
        iput(ip);
4261
        return -1:
4262 }
4263
4264 // Look for an empty dirent.
4265
      for(off = 0; off < dp->size; off += sizeof(de)){
4266
        if(readi(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
4267
           panic("dirlink read"):
4268
        if(de.inum == 0)
4269
          break;
4270 }
4271
      strncpy(de.name, name, DIRSIZ);
4272
4273
      de.inum = ino;
4274
      if(writei(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
4275
        panic("dirlink");
4276
4277
      return 0;
4278 }
4279
4280
4281
4282
4283
4284
4285
4286
4287
4288
4289
4290
4291
4292
4293
4294
4295
4296
4297
4298
4299
```

```
4300 // Paths
                                                                                 4350 // Look up and return the inode for a path name.
4301
                                                                                 4351 // If parent != 0, return the inode for the parent and copy the final
4302 // Copy the next path element from path into name.
                                                                                 4352 // path element into name, which must have room for DIRSIZ bytes.
4303 // Return a pointer to the element following the copied one.
                                                                                 4353 static struct inode*
4304 // The returned path has no leading slashes,
                                                                                 4354 _namei(char *path, int parent, char *name)
4305 // so the caller can check *path=='\0' to see if the name is the last one.
                                                                                 4355 {
4306 // If no name to remove, return 0.
                                                                                 4356
                                                                                       struct inode *ip, *next;
4307 //
                                                                                 4357
4308 // Examples:
                                                                                 4358
                                                                                        if(*path == '/')
4309 // skipelem("a/bb/c", name) = "bb/c", setting name = "a"
                                                                                 4359
                                                                                          ip = iget(ROOTDEV, 1);
4310 // skipelem("///a//bb", name) = "bb", setting name = "a"
                                                                                 4360
                                                                                        else
4311 // skipelem("", name) = skipelem("////", name) = 0
                                                                                 4361
                                                                                          ip = idup(cp->cwd);
4312 //
                                                                                 4362
4313 static char*
                                                                                 4363
                                                                                        while((path = skipelem(path, name)) != 0){
4314 skipelem(char *path, char *name)
                                                                                 4364
                                                                                          ilock(ip):
4315 {
                                                                                 4365
                                                                                          if(ip->type != T_DIR){
4316 char *s;
                                                                                 4366
                                                                                            iunlockput(ip);
4317 int len:
                                                                                 4367
                                                                                            return 0:
4318
                                                                                 4368
      while(*path == '/')
4319
                                                                                 4369
                                                                                          if(parent && *path == '\0'){
4320
        path++:
                                                                                 4370
                                                                                            // Stop one level early.
4321 if(*path == 0)
                                                                                 4371
                                                                                            iunlock(ip);
4322
        return 0;
                                                                                 4372
                                                                                            return ip;
4323 s = path;
                                                                                 4373
4324 while(*path != '/' && *path != 0)
                                                                                 4374
                                                                                          if((next = dirlookup(ip, name, 0)) == 0){
4325
                                                                                 4375
        path++;
                                                                                            iunlockput(ip);
4326 len = path - s;
                                                                                 4376
                                                                                            return 0;
      if(len >= DIRSIZ)
4327
                                                                                 4377
4328
        memmove(name, s, DIRSIZ);
                                                                                 4378
                                                                                          iunlockput(ip);
4329
                                                                                 4379
      else {
                                                                                          ip = next;
4330
        memmove(name, s, len);
                                                                                 4380 }
4331
                                                                                 4381 if(parent){
        name[len] = 0;
4332 }
                                                                                 4382
                                                                                          iput(ip);
4333 while(*path == '/')
                                                                                 4383
                                                                                          return 0;
4334
        path++;
                                                                                 4384 }
                                                                                 4385 return ip;
4335 return path;
4336 }
                                                                                 4386 }
4337
                                                                                 4387
4338
                                                                                 4388 struct inode*
4339
                                                                                 4389 namei(char *path)
4340
                                                                                 4390 {
                                                                                 4391 char name[DIRSIZ];
4341
4342
                                                                                 4392
                                                                                        return _namei(path, 0, name);
4343
                                                                                 4393 }
4344
                                                                                 4394
4345
                                                                                 4395 struct inode*
4346
                                                                                 4396 nameiparent(char *path, char *name)
4347
                                                                                 4397 {
4348
                                                                                 4398 return _namei(path, 1, name);
4349
                                                                                 4399 }
```

```
4400 #include "types.h"
4401 #include "defs.h"
4402 #include "param.h"
4403 #include "file.h"
4404 #include "spinlock.h"
4405 #include "dev.h"
4406
4407 struct devsw devsw[NDEV];
4408 struct spinlock file_table_lock;
4409 struct file file[NFILE];
4410
4411 void
4412 fileinit(void)
4413 {
4414 initlock(&file_table_lock, "file_table");
4415 }
4416
4417 // Allocate a file structure.
4418 struct file*
4419 filealloc(void)
4420 {
4421 int i;
4422
4423
      acquire(&file_table_lock);
4424
      for(i = 0; i < NFILE; i++){
4425
       if(file[i].type == FD_CLOSED){
4426
          file[i].type = FD_NONE;
4427
          file[i].ref = 1;
4428
          release(&file_table_lock);
4429
          return file + i;
4430
      }
4431 }
4432 release(&file_table_lock);
4433 return 0;
4434 }
4435
4436 // Increment ref count for file f.
4437 struct file*
4438 filedup(struct file *f)
4439 {
4440 acquire(&file_table_lock);
4441 if(f->ref < 1 \mid \mid f->type == FD_CLOSED)
4442
        panic("filedup");
4443 f->ref++;
4444 release(&file_table_lock);
4445
      return f;
4446 }
4447
4448
4449
```

```
4450 // Close file f. (Decrement ref count, close when reaches 0.)
4451 void
4452 fileclose(struct file *f)
4453 {
4454 struct file ff;
4455
4456 acquire(&file_table_lock);
4457 if(f->ref < 1 \mid | f->type == FD_CLOSED)
4458
        panic("fileclose");
4459 if(--f->ref > 0){
4460
        release(&file_table_lock);
4461
        return:
4462 }
4463 ff = *f;
4464 f \rightarrow ref = 0:
4465 f->type = FD_CLOSED;
4466 release(&file_table_lock);
4467
4468 if(ff.type == FD_PIPE)
4469
        pipeclose(ff.pipe, ff.writable);
4470 else if(ff.tvpe == FD INODE)
4471
        iput(ff.ip);
4472 else
4473
        panic("fileclose");
4474 }
4475
4476 // Get metadata about file f.
4477 int
4478 filestat(struct file *f, struct stat *st)
4480 if(f->type == FD_INODE){
4481
        ilock(f->ip);
4482
        stati(f->ip, st);
4483
        iunlock(f->ip);
4484
        return 0;
4485 }
4486 return -1;
4487 }
4488
4489
4490
4491
4492
4493
4494
4495
4496
4497
4498
4499
```

```
4500 // Read from file f. Addr is kernel address.
4501 int
4502 fileread(struct file *f, char *addr, int n)
4503 {
4504 int r;
4505
4506 if(f->readable == 0)
4507
        return -1;
4508 if(f->type == FD_PIPE)
        return piperead(f->pipe, addr, n);
4509
4510
      if(f->type == FD_INODE){
4511
        ilock(f->ip);
4512
        if((r = readi(f->ip, addr, f->off, n)) > 0)
4513
          f \rightarrow off += r;
4514
        iunlock(f->ip):
4515
        return r;
4516 }
4517 panic("fileread");
4518 }
4519
4520 // Write to file f. Addr is kernel address.
4521 int
4522 filewrite(struct file *f, char *addr, int n)
4523 {
4524 int r:
4525
4526 if(f->writable == 0)
4527
        return -1;
4528 if(f->type == FD_PIPE)
4529
       return pipewrite(f->pipe, addr, n);
4530 if(f->type == FD_INODE){
4531
        ilock(f->ip);
4532
        if((r = writei(f->ip, addr, f->off, n)) > 0)
4533
          f \rightarrow off += r;
4534
        iunlock(f->ip);
4535
        return r;
4536 }
4537
      panic("filewrite");
4538 }
4539
4540
4541
4542
4543
4544
4545
4546
4547
4548
4549
```

```
4550 #include "types.h"
4551 #include "defs.h"
4552 #include "param.h"
4553 #include "stat.h"
4554 #include "mmu.h"
4555 #include "proc.h"
4556 #include "fs.h"
4557 #include "fsvar.h"
4558 #include "file.h"
4559 #include "fcntl.h"
4560
4561 // Fetch the nth word-sized system call argument as a file descriptor
4562 // and return both the descriptor and the corresponding struct file.
4563 static int
4564 argfd(int n, int *pfd, struct file **pf)
4565 {
4566 int fd;
4567
    struct file *f:
4568
4569 if(argint(n, &fd) < 0)
4570
        return -1:
4571 if(fd < 0 || fd >= NOFILE || (f=cp->ofile[fd]) == 0)
4572
        return -1;
4573 if(pfd)
        *pfd = fd;
4574
4575 if(pf)
4576
        *pf = f;
4577 return 0;
4578 }
4579
4580 // Allocate a file descriptor for the given file.
4581 // Takes over file reference from caller on success.
4582 static int
4583 fdalloc(struct file *f)
4584 {
4585 int fd;
4586
4587 for(fd = 0; fd < NOFILE; fd++){
4588
        if(cp->ofile[fd] == 0){
4589
           cp->ofile[fd] = f;
4590
          return fd;
4591
        }
4592 }
4593
      return -1;
4594 }
4595
4596
4597
4598
4599
```

```
4600 int
                                                                                4650 int
4601 sys_read(void)
                                                                                4651 sys_fstat(void)
4602 {
                                                                                4652 {
4603 struct file *f;
                                                                                4653 struct file *f;
4604 int n;
                                                                                4654 struct stat *st;
4605 char *p;
                                                                                4655
4606
                                                                                4656 if(argfd(0, 0, &f) < 0 || argptr(1, (void*)&st, sizeof(*st)) < 0)
4607 if (argfd(0, 0, &f) < 0 \mid | argint(2, &n) < 0 \mid | argptr(1, &p, n) < 0)
                                                                                4657
                                                                                        return -1;
4608
        return -1;
                                                                                4658 return filestat(f, st);
4609 return fileread(f, p, n);
                                                                                4659 }
4610 }
                                                                                4660
4611
                                                                                4661 // Create the path new as a link to the same inode as old.
4612 int
4613 sys_write(void)
                                                                                4663 sys_link(void)
                                                                                4664 {
4614 {
4615 struct file *f;
                                                                                4665
                                                                                      char name[DIRSIZ], *new, *old;
4616 int n;
                                                                                4666
                                                                                     struct inode *dp, *ip;
4617 char *p:
                                                                                4667
4618
                                                                                4668 if(argstr(0, &old) < 0 || argstr(1, &new) < 0)
                                                                                        return -1;
4619 if(argfd(0, 0, &f) < 0 || argint(2, &n) < 0 || argptr(1, &p, n) < 0)
                                                                                4669
4620
        return -1:
                                                                                4670
                                                                                     if((ip = namei(old)) == 0)
4621 return filewrite(f, p, n);
                                                                                4671
                                                                                        return -1:
4622 }
                                                                                4672 ilock(ip);
4623
                                                                                4673 if(ip->type == T_DIR){
4624 int
                                                                                4674
                                                                                        iunlockput(ip);
4625 sys_dup(void)
                                                                                4675
                                                                                        return -1;
4626 {
                                                                                4676 }
4627 struct file *f;
                                                                                4677
                                                                                      ip->nlink++;
4628 int fd;
                                                                                4678
                                                                                      iupdate(ip);
4629
                                                                                4679
                                                                                     iunlock(ip);
4630 if (argfd(0, 0, &f) < 0)
                                                                                4680
4631
        return -1;
                                                                                4681 if((dp = nameiparent(new, name)) == 0)
4632 if((fd=fdalloc(f)) < 0)
                                                                                4682
                                                                                        goto bad;
4633
       return -1;
                                                                                4683 ilock(dp);
4634 filedup(f);
                                                                                4684
                                                                                     if(dp->dev != ip->dev || dirlink(dp, name, ip->inum) < 0)</pre>
4635 return fd;
                                                                                4685
                                                                                        goto bad;
4636 }
                                                                                4686 iunlockput(dp);
4637
                                                                                4687
                                                                                      iput(ip);
4638 int
                                                                                4688
                                                                                      return 0;
4639 sys_close(void)
                                                                                4689
4640 {
                                                                                4690 bad:
4641 int fd;
                                                                                4691 if(dp)
4642 struct file *f;
                                                                                4692
                                                                                        iunlockput(dp);
4643
                                                                                4693 ilock(ip);
4644 if (argfd(0, &fd, &f) < 0)
                                                                                4694 ip->nlink--;
4645
       return -1;
                                                                                4695 iupdate(ip);
4646 cp->ofile[fd] = 0;
                                                                                4696
                                                                                      iunlockput(ip);
4647 fileclose(f);
                                                                                4697
                                                                                      return -1;
4648 return 0;
                                                                                4698 }
4649 }
                                                                                4699
```

```
4700 // Is the directory dp empty except for "." and ".." ?
4701 static int
4702 isdirempty(struct inode *dp)
4703 {
4704 int off;
4705 struct dirent de;
4706
4707
      for(off=2*sizeof(de); off<dp->size; off+=sizeof(de)){
4708
        if(readi(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
4709
          panic("isdirempty: readi");
4710
        if(de.inum != 0)
4711
          return 0;
4712 }
4713 return 1;
4714 }
4715
4716
4717
4718
4719
4720
4721
4722
4723
4724
4725
4726
4727
4728
4729
4730
4731
4732
4733
4734
4735
4736
4737
4738
4739
4740
4741
4742
4743
4744
4745
4746
4747
4748
4749
```

```
4750 int
4751 sys_unlink(void)
4752 {
4753 struct inode *ip, *dp;
4754 struct dirent de;
4755 char name[DIRSIZ], *path;
4756
      uint off;
4757
4758
     if(argstr(0, \&path) < 0)
4759
        return -1;
4760
     if((dp = nameiparent(path, name)) == 0)
4761
        return -1;
4762 ilock(dp);
4763
4764
      // Cannot unlink "." or "..".
4765
      if(namecmp(name, ".") == 0 \mid \mid namecmp(name, "..") == 0){
4766
        iunlockput(dp);
4767
        return -1:
4768 }
4769
4770
     if((ip = dirlookup(dp, name, &off)) == 0){
4771
        iunlockput(dp);
        return -1;
4772
4773 }
4774
     ilock(ip);
4775
4776 if(ip->nlink < 1)
        panic("unlink: nlink < 1");</pre>
4777
4778 if(ip->type == T_DIR && !isdirempty(ip)){
4779
        iunlockput(ip);
4780
        iunlockput(dp);
4781
        return -1;
4782 }
4783
4784
      memset(&de, 0, sizeof(de));
4785 if(writei(dp, (char*)&de, off, sizeof(de)) != sizeof(de))
4786
        panic("unlink: writei");
4787
      iunlockput(dp);
4788
4789 ip->nlink--;
4790
      iupdate(ip);
4791 iunlockput(ip);
4792
      return 0;
4793 }
4794
4795
4796
4797
4798
4799
```

```
4800 static struct inode*
                                                                                  4850 int
4801 create(char *path, int canexist, short type, short major, short minor)
                                                                                  4851 sys_open(void)
4802 {
                                                                                  4852 {
4803
      uint off;
                                                                                  4853
                                                                                         char *path;
4804
      struct inode *ip, *dp;
                                                                                  4854
                                                                                        int fd, omode;
4805
      char name[DIRSIZ];
                                                                                  4855 struct file *f;
4806
                                                                                  4856
                                                                                         struct inode *ip;
4807
      if((dp = nameiparent(path, name)) == 0)
                                                                                  4857
4808
        return 0;
                                                                                  4858
                                                                                         if(argstr(0, \&path) < 0 \mid | argint(1, \&omode) < 0)
      ilock(dp);
4809
                                                                                  4859
                                                                                           return -1;
4810
                                                                                  4860
      if(canexist && (ip = dirlookup(dp, name, &off)) != 0){
4811
                                                                                  4861
                                                                                        if(omode & O CREATE){
4812
        iunlockput(dp);
                                                                                  4862
                                                                                           if((ip = create(path, 1, T_FILE, 0, 0)) == 0)
4813
        ilock(ip);
                                                                                  4863
                                                                                             return -1;
4814
        if(ip->type != type || ip->major != major || ip->minor != minor){
                                                                                  4864 } else {
4815
          iunlockput(ip);
                                                                                  4865
                                                                                           if((ip = namei(path)) == 0)
4816
          return 0;
                                                                                  4866
                                                                                             return -1;
4817
        }
                                                                                  4867
                                                                                           ilock(ip):
4818
                                                                                  4868
                                                                                           if(ip->type == T_DIR && (omode & (O_RDWR|O_WRONLY))){
        return ip;
4819 }
                                                                                  4869
                                                                                             iunlockput(ip);
4820
                                                                                  4870
                                                                                             return -1:
4821 if((ip = ialloc(dp->dev, type)) == 0){
                                                                                  4871
4822
        iunlockput(dp);
                                                                                  4872 }
4823
        return 0;
                                                                                  4873
4824 }
                                                                                         if((f = filealloc()) == 0 \mid | (fd = fdalloc(f)) < 0){
                                                                                  4874
4825 ilock(ip);
                                                                                  4875
                                                                                           if(f)
                                                                                             fileclose(f);
4826 ip->major = major;
                                                                                  4876
4827 ip->minor = minor;
                                                                                  4877
                                                                                           iunlockput(ip);
4828 ip->nlink = 1;
                                                                                  4878
                                                                                           return -1;
4829 iupdate(ip);
                                                                                  4879 }
4830
                                                                                  4880
                                                                                         iunlock(ip);
4831
      if(dirlink(dp, name, ip->inum) < 0){</pre>
                                                                                  4881
4832
        ip->nlink = 0;
                                                                                  4882 f \rightarrow type = FD_INODE;
4833
        iunlockput(ip);
                                                                                  4883 f \rightarrow ip = ip;
4834
        iunlockput(dp);
                                                                                  4884 	 f->off = 0;
4835
                                                                                  4885 f->readable = !(omode & O_WRONLY);
        return 0;
4836 }
                                                                                  4886
                                                                                        f->writable = (omode & O_WRONLY) || (omode & O_RDWR);
4837
                                                                                  4887
4838
      if(type == T_DIR){ // Create . and .. entries.
                                                                                  4888
                                                                                         return fd;
4839
        dp->nlink++; // for ".."
                                                                                  4889 }
4840
        iupdate(dp);
                                                                                  4890
        // No ip->nlink++ for ".": avoid cyclic ref count.
4841
                                                                                  4891
4842
        if(dirlink(ip, ".", ip->inum) < 0 || dirlink(ip, "..", dp->inum) < 0)</pre>
                                                                                  4892
4843
          panic("create dots");
                                                                                  4893
4844
                                                                                  4894
4845 iunlockput(dp);
                                                                                  4895
4846
      return ip;
                                                                                  4896
4847 }
                                                                                  4897
4848
                                                                                  4898
4849
                                                                                  4899
```

Sheet 48 Sheet 48

```
4900 int
                                                                                 4950 int
4901 sys_mknod(void)
                                                                                 4951 sys_exec(void)
4902 {
                                                                                 4952 {
4903 struct inode *ip;
                                                                                 4953 char *path, *argv[20];
4904 char *path;
                                                                                 4954
                                                                                      int i;
4905 int len;
                                                                                 4955 uint uargy, uarg;
4906 int major, minor;
                                                                                 4956
4907
                                                                                 4957
                                                                                      if(argstr(0, &path) < 0 || argint(1, (int*)&uargv) < 0)
4908 if((len=argstr(0, &path)) < 0 ||
                                                                                 4958
                                                                                         return -1;
                                                                                       memset(argv, 0, sizeof(argv));
4909
         argint(1, \&major) < 0 \mid \mid
                                                                                 4959
4910
         argint(2, &minor) < 0 ||
                                                                                 4960
                                                                                       for(i=0;; i++){
4911
         (ip = create(path, 0, T_DEV, major, minor)) == 0)
                                                                                 4961
                                                                                         if(i >= NELEM(argv))
4912
        return -1:
                                                                                 4962
                                                                                           return -1;
4913 iunlockput(ip);
                                                                                 4963
                                                                                         if(fetchint(cp, uargv+4*i, (int*)&uarg) < 0)</pre>
4914 return 0;
                                                                                 4964
                                                                                           return -1:
4915 }
                                                                                 4965
                                                                                         if(uarg == 0){
4916
                                                                                 4966
                                                                                            argv[i] = 0;
4917 int
                                                                                 4967
                                                                                            break:
4918 sys_mkdir(void)
                                                                                 4968
4919 {
                                                                                 4969
                                                                                         if(fetchstr(cp, uarg, &argv[i]) < 0)</pre>
4920 char *path:
                                                                                 4970
                                                                                            return -1:
4921 struct inode *ip;
                                                                                 4971 }
                                                                                 4972 return exec(path, argv);
4922
4923 if(argstr(0, &path) < 0 \mid | (ip = create(path, 0, T_DIR, 0, 0)) == 0)
                                                                                 4973 }
4924
      return -1:
                                                                                 4974
4925 iunlockput(ip);
                                                                                 4975 int
4926 return 0;
                                                                                 4976 sys_pipe(void)
4927 }
                                                                                 4977 {
4928
                                                                                 4978 int *fd;
4929 int
                                                                                 4979 struct file *rf, *wf;
4930 sys_chdir(void)
                                                                                 4980
                                                                                       int fd0, fd1;
4931 {
                                                                                 4981
4932 char *path;
                                                                                 4982 if(argptr(0, (void*)&fd, 2*sizeof(fd[0])) < 0)
4933 struct inode *ip;
                                                                                 4983
                                                                                         return -1;
4934
                                                                                 4984 if(pipealloc(&rf, &wf) < 0)
                                                                                        return -1;
4935 if(argstr(0, &path) < 0 || (ip = namei(path)) == 0)
                                                                                 4985
4936
      return -1;
                                                                                 4986 	ext{ fd0} = -1;
4937 ilock(ip);
                                                                                 4987
                                                                                       if((fd0 = fdalloc(rf)) < 0 \mid | (fd1 = fdalloc(wf)) < 0){
4938 if(ip->type != T_DIR){
                                                                                 4988
                                                                                         if(fd0 >= 0)
4939
        iunlockput(ip);
                                                                                 4989
                                                                                            cp->ofile[fd0] = 0;
4940
        return -1;
                                                                                 4990
                                                                                         fileclose(rf);
4941 }
                                                                                 4991
                                                                                         fileclose(wf);
4942 iunlock(ip);
                                                                                 4992
                                                                                         return -1;
4943 iput(cp->cwd);
                                                                                 4993 }
4944 cp -> cwd = ip;
                                                                                 4994 fd[0] = fd0;
4945
      return 0;
                                                                                 4995 fd[1] = fd1;
4946 }
                                                                                 4996
                                                                                       return 0;
4947
                                                                                 4997 }
4948
                                                                                 4998
4949
                                                                                 4999
```

```
5000 #include "types.h"
                                                                                  5050
                                                                                        // Allocate program memory.
5001 #include "param.h"
                                                                                  5051 sz = (sz+PAGE-1) \& \sim (PAGE-1);
5002 #include "mmu.h"
                                                                                  5052 \quad \text{mem} = \text{kalloc(sz)};
5003 #include "proc.h"
                                                                                  5053
                                                                                        if(mem == 0)
5004 #include "defs.h"
                                                                                  5054
                                                                                           goto bad;
5005 #include "x86.h"
                                                                                  5055
                                                                                         memset(mem, 0, sz);
5006 #include "elf.h"
                                                                                  5056
5007
                                                                                  5057
                                                                                         // Load program into memory.
5008 int
                                                                                         for(i=0, off=elf.phoff; i<elf.phnum; i++, off+=sizeof(ph)){</pre>
                                                                                  5058
                                                                                           if(readi(ip, (char*)&ph, off, sizeof(ph)) != sizeof(ph))
5009 exec(char *path, char **argv)
                                                                                  5059
5010 {
                                                                                  5060
                                                                                             goto bad;
5011 char *mem, *s, *last;
                                                                                  5061
                                                                                           if(ph.type != ELF_PROG_LOAD)
5012 int i, argc, arglen, len, off;
                                                                                  5062
                                                                                             continue:
5013 uint sz, sp, argp;
                                                                                  5063
                                                                                           if(ph.va + ph.memsz > sz)
5014 struct elfhdr elf:
                                                                                  5064
                                                                                             goto bad:
5015 struct inode *ip;
                                                                                  5065
                                                                                           if(readi(ip, mem + ph.va, ph.offset, ph.filesz) != ph.filesz)
5016 struct proghdr ph;
                                                                                  5066
5017
                                                                                  5067
                                                                                           memset(mem + ph.va + ph.filesz, 0, ph.memsz - ph.filesz);
5018 if((ip = namei(path)) == 0)
                                                                                  5068
5019
        return -1;
                                                                                  5069
                                                                                         iunlockput(ip);
5020 ilock(ip);
                                                                                  5070
5021
                                                                                  5071
                                                                                        // Initialize stack.
5022 // Compute memory size of new process.
                                                                                  5072
                                                                                         sp = sz;
5023
      mem = 0;
                                                                                  5073
                                                                                         argp = sz - arglen - 4*(argc+1);
5024
      sz = 0:
                                                                                  5074
5025
                                                                                  5075
                                                                                         // Copy argy strings and pointers to stack.
5026 // Program segments.
                                                                                  5076
                                                                                         *(uint*)(mem+argp + 4*argc) = 0; // argv[argc]
      if(readi(ip, (char*)&elf, 0, sizeof(elf)) < sizeof(elf))</pre>
5027
                                                                                  5077
                                                                                         for(i=argc-1; i>=0; i--){
5028
                                                                                  5078
                                                                                           len = strlen(argv[i]) + 1;
        goto bad;
5029 if(elf.magic != ELF_MAGIC)
                                                                                  5079
                                                                                           sp -= len;
5030
                                                                                  5080
        goto bad;
                                                                                           memmove(mem+sp, argv[i], len);
5031
      for(i=0, off=elf.phoff; i<elf.phnum; i++, off+=sizeof(ph)){</pre>
                                                                                  5081
                                                                                           *(uint*)(mem+argp + 4*i) = sp; // argv[i]
5032
        if(readi(ip, (char*)&ph, off, sizeof(ph)) != sizeof(ph))
                                                                                  5082 }
5033
                                                                                  5083
          goto bad;
5034
        if(ph.type != ELF_PROG_LOAD)
                                                                                  5084 // Stack frame for main(argc, argv), below arguments.
5035
          continue;
                                                                                  5085 sp = argp;
5036
        if(ph.memsz < ph.filesz)</pre>
                                                                                  5086 sp -= 4;
5037
                                                                                  5087
                                                                                         *(uint*)(mem+sp) = argp;
          goto bad;
5038
        sz += ph.memsz;
                                                                                  5088 sp -= 4:
5039 }
                                                                                  5089
                                                                                         *(uint*)(mem+sp) = argc;
5040
                                                                                  5090
5041 // Arguments.
                                                                                  5091
                                                                                         *(uint*)(mem+sp) = 0xffffffff; // fake return pc
5042
      arglen = 0;
                                                                                  5092
5043
      for(argc=0; argv[argc]; argc++)
                                                                                  5093
                                                                                         // Save program name for debugging.
5044
        arglen += strlen(argv[argc]) + 1;
                                                                                  5094
                                                                                         for(last=s=path; *s; s++)
                                                                                           if(*s == '/')
5045 arglen = (arglen+3) \& \sim 3;
                                                                                  5095
5046
      sz += arglen + 4*(argc+1);
                                                                                  5096
                                                                                              last = s+1;
5047
                                                                                  5097
                                                                                         safestrcpy(cp->name, last, sizeof(cp->name));
5048 // Stack.
                                                                                  5098
5049 sz += PAGE;
                                                                                  5099
```

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```
5150 #include "types.h"
5151 #include "defs.h"
5152 #include "param.h"
5153 #include "mmu.h"
5154 #include "proc.h"
5155 #include "file.h"
5156 #include "spinlock.h"
5157
5158 #define PIPESIZE 512
5159
5160 struct pipe {
5161 int readopen; // read fd is still open
5162 int writeopen; // write fd is still open
5163 int writep;
                     // next index to write
5164 int readp;
                     // next index to read
5165 struct spinlock lock;
5166 char data[PIPESIZE];
5167 };
5168
5169 int
5170 pipealloc(struct file **f0, struct file **f1)
5171 {
5172 struct pipe *p;
5173
5174 p = 0;
5175 *f0 = *f1 = 0;
5176 if((*f0 = filealloc()) == 0 \mid | (*f1 = filealloc()) == 0)
5177
        goto bad;
5178 if((p = (struct pipe*)kalloc(PAGE)) == 0)
        goto bad;
5179
5180 p->readopen = 1;
5181 p->writeopen = 1;
5182 p->writep = 0;
5183 p->readp = 0;
5184 initlock(&p->lock, "pipe");
5185 (*f0)->type = FD_PIPE;
5186 (*f0)->readable = 1;
5187 (*f0)->writable = 0;
5188 (*f0)->pipe = p;
5189 (*f1)->type = FD_PIPE;
5190 (*f1)->readable = 0;
5191 (*f1)->writable = 1;
5192 (*f1)->pipe = p;
5193
      return 0;
5194
5195
5196
5197
5198
5199
```

Sheet 51

5147

5148

5149

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```
5250 int
5251 pipewrite(struct pipe *p, char *addr, int n)
5252 {
5253 int i;
5254
5255
      acquire(&p->lock);
5256
      for(i = 0; i < n; i++){
5257
        while(((p->writep + 1) % PIPESIZE) == p->readp){
5258
           if(p->readopen == 0 || cp->killed){
5259
             release(&p->lock);
5260
             return -1;
5261
5262
          wakeup(&p->readp);
5263
           sleep(&p->writep, &p->lock);
5264
5265
        p->data[p->writep] = addr[i];
5266
        p->writep = (p->writep + 1) % PIPESIZE;
5267
5268
      wakeup(&p->readp);
5269
      release(&p->lock);
5270
      return i:
5271 }
5272
5273 int
5274 piperead(struct pipe *p, char *addr, int n)
5275 {
5276 int i;
5277
5278
      acquire(&p->lock);
5279
      while(p->readp == p->writep && p->writeopen){
5280
        if(cp->killed){
5281
           release(&p->lock);
5282
           return -1;
5283
        }
        sleep(&p->readp, &p->lock);
5284
5285 }
5286
      for(i = 0; i < n; i++){
5287
        if(p->readp == p->writep)
5288
          break:
5289
        addr[i] = p->data[p->readp];
5290
        p->readp = (p->readp + 1) % PIPESIZE;
5291 }
5292
      wakeup(&p->writep);
5293
      release(&p->lock);
5294
      return i;
5295 }
5296
5297
5298
5299
```

5248

5249

```
5300 #include "types.h"
5301
5302 void*
5303 memset(void *dst, int c, uint n)
5304 {
5305 char *d;
5306
5307 d = (char*)dst;
5308 while(n-- > 0)
      *d++ = c;
5309
5310
5311 return dst;
5312 }
5313
5314 int
5315 memcmp(const void *v1, const void *v2, uint n)
5316 {
5317 const uchar *s1, *s2;
5318
5319 	 s1 = v1;
5320 	 s2 = v2:
5321 while(n-- > 0){
5322
      if(*s1 != *s2)
5323
          return *s1 - *s2;
5324
        s1++, s2++;
5325 }
5326
5327 return 0;
5328 }
5329
5330 void*
5331 memmove(void *dst, const void *src, uint n)
5332 {
5333 const char *s;
5334 char *d;
5335
5336 s = src;
5337 d = dst;
5338 if(s < d \&\& s + n > d){
5339
       s += n;
5340
        d += n;
5341
        while(n-- > 0)
5342
          *--d = *--s;
5343 } else
5344
        while(n-- > 0)
5345
          *d++ = *s++;
5346
5347 return dst;
5348 }
5349
```

```
5350 int
5351 strncmp(const char *p, const char *q, uint n)
5353 while(n > 0 && *p && *p == *q)
5354
       n--, p++, q++;
5355 if(n == 0)
5356
       return 0;
5357 return (uchar)*p - (uchar)*q;
5358 }
5359
5360 char*
5361 strncpy(char *s, const char *t, int n)
5363 char *os;
5364
5365 os = s;
5366 while(n-- > 0 \&\& (*s++ = *t++) != 0)
5367
5368 while(n-- > 0)
5369
        *s++ = 0;
5370 return os;
5371 }
5372
5373 // Like strncpy but guaranteed to NUL-terminate.
5374 char*
5375 safestrcpy(char *s, const char *t, int n)
5376 {
5377 char *os;
5378
5379 os = s;
5380 if(n <= 0)
5381
       return os:
5382 while(--n > 0 \&\& (*s++ = *t++) != 0)
5383
5384 *s = 0;
5385 return os;
5386 }
5387
5388 int
5389 strlen(const char *s)
5390 {
5391 int n;
5392
5393 for(n = 0; s[n]; n++)
5394
5395 return n;
5396 }
5397
5398
5399
```

```
5400 // See MultiProcessor Specification Version 1.[14]
                                                                                  5450 // Table entry types
5401
                                                                                  5451 #define MPPROC
                                                                                                         0x00 // One per processor
                                                                                                          0x01 // One per bus
5402 struct mp {
                             // floating pointer
                                                                                  5452 #define MPBUS
                                     // "_MP_"
5403
      uchar signature[4];
                                                                                  5453 #define MPIOAPIC 0x02 // One per I/O APIC
                                     // phys addr of MP config table
5404
      void *physaddr;
                                                                                  5454 #define MPIOINTR 0x03 // One per bus interrupt source
5405
      uchar length;
                                                                                  5455 #define MPLINTR
                                                                                                         0x04 // One per system interrupt source
                                     // 1
5406
      uchar specrev;
                                     // [14]
                                                                                  5456
5407
      uchar checksum;
                                     // all bytes must add up to 0
                                                                                  5457
5408
      uchar type;
                                     // MP system config type
                                                                                  5458
      uchar imcrp;
5409
                                                                                  5459
5410
      uchar reserved[3];
                                                                                  5460
5411 }:
                                                                                  5461
5412
                                                                                  5462
5413 struct mpconf {
                             // configuration table header
                                                                                  5463
      uchar signature[4];
                                     // "PCMP"
5414
                                                                                  5464
5415
      ushort length;
                                     // total table length
                                                                                  5465
5416
      uchar version;
                                     // [14]
                                                                                  5466
                                     // all bytes must add up to 0
5417
      uchar checksum:
                                                                                  5467
5418
      uchar product[20];
                                     // product id
                                                                                  5468
                                     // OEM table pointer
5419
      uint *oemtable;
                                                                                  5469
5420
      ushort oemlenath:
                                     // OEM table length
                                                                                  5470
5421
      ushort entry;
                                     // entry count
                                                                                  5471
5422
      uint *lapicaddr;
                                     // address of local APIC
                                                                                  5472
5423
      ushort xlength;
                                     // extended table length
                                                                                  5473
5424
                                     // extended table checksum
                                                                                  5474
      uchar xchecksum:
5425
      uchar reserved;
                                                                                  5475
5426 };
                                                                                  5476
5427
                                                                                  5477
5428 struct mpproc {
                             // processor table entry
                                                                                  5478
5429
      uchar type;
                                     // entry type (0)
                                                                                  5479
5430
      uchar apicid;
                                     // local APIC id
                                                                                  5480
                                     // local APIC verison
5431
      uchar version;
                                                                                  5481
                                                                                  5482
5432
      uchar flags;
                                     // CPU flags
        #define MPBOOT 0x02
5433
                                       // This proc is the bootstrap processor.
                                                                                  5483
5434
      uchar signature[4];
                                     // CPU signature
                                                                                  5484
5435
      uint feature;
                                     // feature flags from CPUID instruction
                                                                                  5485
5436
      uchar reserved[8];
                                                                                  5486
5437 };
                                                                                  5487
5438
                                                                                  5488
5439 struct mpioapic {
                             // I/O APIC table entry
                                                                                  5489
5440
      uchar type;
                                     // entry type (2)
                                                                                  5490
5441
      uchar apicno;
                                     // I/O APIC id
                                                                                  5491
5442
      uchar version;
                                     // I/O APIC version
                                                                                  5492
5443
      uchar flags;
                                     // I/O APIC flags
                                                                                  5493
5444
      uint *addr:
                                    // I/O APIC address
                                                                                  5494
5445 };
                                                                                  5495
5446
                                                                                  5496
5447
                                                                                  5497
5448
                                                                                  5498
5449
                                                                                  5499
```

Sheet 54 Sheet 54

```
5500 // Multiprocessor bootstrap.
                                                                                  5550 // Search for the MP Floating Pointer Structure, which according to the
5501 // Search memory for MP description structures.
                                                                                  5551 // spec is in one of the following three locations:
5502 // http://developer.intel.com/design/pentium/datashts/24201606.pdf
                                                                                  5552 // 1) in the first KB of the EBDA;
5503
                                                                                  5553 // 2) in the last KB of system base memory;
5504 #include "types.h"
                                                                                  5554 // 3) in the BIOS ROM between 0xE0000 and 0xFFFFF.
5505 #include "defs.h"
                                                                                  5555 static struct mp*
5506 #include "param.h"
                                                                                  5556 mp_search(void)
5507 #include "mp.h"
                                                                                  5557 {
5508 #include "x86.h"
                                                                                  5558 uchar *bda;
5509 #include "mmu.h"
                                                                                  5559
                                                                                         uint p;
5510 #include "proc.h"
                                                                                  5560
                                                                                         struct mp *mp;
5511
                                                                                  5561
                                                                                  5562
5512 struct cpu cpus[NCPU];
                                                                                         bda = (uchar*)0x400;
5513 static struct cpu *bcpu;
                                                                                  5563
                                                                                        if((p = ((bda[0x0F] << 8) | bda[0x0E]) << 4)){
5514 int ismp:
                                                                                           if((mp = mp\_search1((uchar*)p, 1024)))
5515 int ncpu:
                                                                                  5565
                                                                                             return mp;
5516 uchar ioapic_id;
                                                                                  5566 } else {
5517
                                                                                  5567
                                                                                           p = ((bda[0x14] << 8)|bda[0x13])*1024:
5518 int
                                                                                  5568
                                                                                           if((mp = mp_search1((uchar*)p-1024, 1024)))
5519 mp_bcpu(void)
                                                                                  5569
                                                                                             return mp;
5520 {
                                                                                  5570 }
5521 return bcpu-cpus;
                                                                                  5571 return mp_search1((uchar*)0xF0000, 0x10000);
5522 }
                                                                                  5572 }
5523
                                                                                  5573
5524 static uchar
                                                                                  5574 // Search for an MP configuration table. For now,
5525 sum(uchar *addr, int len)
                                                                                  5575 // don't accept the default configurations (physaddr == 0).
5526 {
                                                                                  5576 // Check for correct signature, calculate the checksum and,
5527 int i, sum;
                                                                                  5577 // if correct, check the version.
5528
                                                                                  5578 // To do: check extended table checksum.
5529 \quad sum = 0;
                                                                                  5579 static struct mpconf*
5530 for(i=0; i<len; i++)
                                                                                  5580 mp_config(struct mp **pmp)
5531
        sum += addr[i];
                                                                                  5581 {
5532 return sum;
                                                                                  5582 struct mpconf *conf;
5533 }
                                                                                  5583 struct mp *mp;
5534
                                                                                  5584
5535 // Look for an MP structure in the len bytes at addr.
                                                                                  5585
                                                                                       if((mp = mp\_search()) == 0 \mid \mid mp->physaddr == 0)
5536 static struct mp*
                                                                                  5586
                                                                                           return 0:
5537 mp_search1(uchar *addr, int len)
                                                                                  5587
                                                                                         conf = (struct mpconf*)mp->physaddr;
5538 {
                                                                                  5588
                                                                                        if(memcmp(conf, "PCMP", 4) != 0)
5539 uchar *e, *p;
                                                                                  5589
                                                                                           return 0;
5540
                                                                                  5590
                                                                                        if(conf->version != 1 && conf->version != 4)
5541 e = addr+len:
                                                                                  5591
                                                                                           return 0:
5542
      for(p = addr; p < e; p += sizeof(struct mp))</pre>
                                                                                  5592 if(sum((uchar*)conf, conf->length) != 0)
5543
       if(memcmp(p, "_MP_", 4) == 0 \&\& sum(p, sizeof(struct mp)) == 0)
                                                                                  5593
                                                                                           return 0;
5544
           return (struct mp*)p;
                                                                                  5594
                                                                                         *qm = qmq*
5545 return 0;
                                                                                  5595
                                                                                         return conf;
5546 }
                                                                                  5596 }
5547
                                                                                  5597
5548
                                                                                  5598
5549
                                                                                  5599
```

```
5600 void
                                                                                 5650 // The local APIC manages internal (non-I/O) interrupts.
5601 mp_init(void)
                                                                                 5651 // See Chapter 8 & Appendix C of Intel processor manual volume 3.
5602 {
                                                                                 5652
5603
      uchar *p, *e;
                                                                                 5653 #include "types.h"
5604
      struct mp *mp;
                                                                                 5654 #include "defs.h"
                                                                                 5655 #include "traps.h"
5605
      struct mpconf *conf;
5606
      struct mpproc *proc;
                                                                                 5656 #include "mmu.h"
5607
      struct mpioapic *ioapic;
                                                                                 5657 #include "x86.h"
5608
                                                                                 5658
5609
      bcpu = &cpus[ncpu];
                                                                                 5659 // Local APIC registers, divided by 4 for use as uint[] indices.
5610
      if((conf = mp_config(&mp)) == 0)
                                                                                 5660 #define ID
                                                                                                      (0x0020/4) // ID
5611
        return:
                                                                                 5661 #define VER
                                                                                                      (0x0030/4) // Version
5612
                                                                                 5662 #define TPR
                                                                                                      (0x0080/4) // Task Priority
5613
                                                                                 5663 #define EOI
                                                                                                      (0x00B0/4)
                                                                                                                 // EOI
      ismp = 1;
      lapic = (uint*)conf->lapicaddr:
                                                                                 5664 #define SVR
5614
                                                                                                      (0x00F0/4) // Spurious Interrupt Vector
5615
                                                                                 5665 #define ENABLE
                                                                                                          0x00000100 // Unit Enable
5616
      for(p=(uchar*)(conf+1), e=(uchar*)conf+conf->length; p<e; ){</pre>
                                                                                 5666 #define ESR
                                                                                                      (0x0280/4) // Error Status
5617
        switch(*p){
                                                                                 5667 #define ICRLO
                                                                                                      (0x0300/4)
                                                                                                                // Interrupt Command
5618
        case MPPROC:
                                                                                 5668 #define INIT
                                                                                                          0x00000500 // INIT/RESET
5619
          proc = (struct mpproc*)p;
                                                                                 5669
                                                                                       #define STARTUP
                                                                                                          0x00000600 // Startup IPI
5620
          cpus[ncpu].apicid = proc->apicid:
                                                                                 5670
                                                                                       #define DELIVS
                                                                                                          0x00001000 // Delivery status
5621
          if(proc->flags & MPBOOT)
                                                                                 5671
                                                                                       #define ASSERT
                                                                                                          0x00004000 // Assert interrupt (vs deassert)
5622
            bcpu = &cpus[ncpu];
                                                                                 5672
                                                                                       #define LEVEL
                                                                                                          0x00008000 // Level triggered
5623
          ncpu++;
                                                                                 5673 #define BCAST
                                                                                                          0x00080000 // Send to all APICs, including self.
                                                                                 5674 #define ICRHI
5624
          p += sizeof(struct mpproc);
                                                                                                     (0x0310/4) // Interrupt Command [63:32]
5625
          continue;
                                                                                 5675 #define TIMER
                                                                                                      (0x0320/4) // Local Vector Table 0 (TIMER)
                                                                                 5676 #define X1
5626
        case MPIOAPIC:
                                                                                                          0x0000000B // divide counts by 1
                                                                                       #define PERIODIC 0x00020000 // Periodic
5627
          ioapic = (struct mpioapic*)p;
                                                                                 5677
5628
                                                                                 5678 #define PCINT
                                                                                                      (0x0340/4) // Performance Counter LVT
          ioapic_id = ioapic->apicno;
5629
          p += sizeof(struct mpioapic);
                                                                                 5679 #define LINTO
                                                                                                      (0x0350/4) // Local Vector Table 1 (LINTO)
5630
                                                                                 5680 #define LINT1
                                                                                                      (0x0360/4) // Local Vector Table 2 (LINT1)
          continue;
5631
        case MPBUS:
                                                                                 5681 #define ERROR
                                                                                                      (0x0370/4)
                                                                                                                  // Local Vector Table 3 (ERROR)
        case MPIOINTR:
                                                                                 5682 #define MASKED
                                                                                                          0x00010000 // Interrupt masked
5632
5633
        case MPLINTR:
                                                                                 5683 #define TICR
                                                                                                      (0x0380/4) // Timer Initial Count
5634
          p += 8;
                                                                                 5684 #define TCCR
                                                                                                      (0x0390/4)
                                                                                                                  // Timer Current Count
5635
                                                                                 5685 #define TDCR
          continue;
                                                                                                      (0x03E0/4) // Timer Divide Configuration
5636
        default:
                                                                                 5686
5637
          cprintf("mp_init: unknown config type %x\n", *p);
                                                                                 5687 volatile uint *lapic; // Initialized in mp.c
5638
                                                                                 5688
          panic("mp_init");
5639
        }
                                                                                 5689 static void
5640
     }
                                                                                 5690 lapicw(int index, int value)
5641
                                                                                 5691 {
5642
      if(mp->imcrp){
                                                                                 5692 lapic[index] = value;
5643
        // Bochs doesn't support IMCR, so this doesn't run on Bochs.
                                                                                 5693
                                                                                       lapic[ID]; // wait for write to finish, by reading
5644
        // But it would on real hardware.
                                                                                 5694 }
5645
                                                                                 5695
        outb(0x22, 0x70); // Select IMCR
5646
        outb(0x23, inb(0x23) \mid 1); // Mask external interrupts.
                                                                                 5696
5647
      }
                                                                                 5697
5648 }
                                                                                 5698
5649
                                                                                 5699
```

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Sheet 56 Sheet 56

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```
5700 void
                                                                                 5750 int
5701 lapic_init(int c)
                                                                                 5751 cpu(void)
5702 {
                                                                                 5752 {
5703 if(!lapic)
                                                                                 5753 // Cannot call cpu when interrupts are enabled:
5704
        return;
                                                                                 5754 // result not guaranteed to last long enough to be used!
5705
                                                                                 5755 // Would prefer to panic but even printing is chancy here:
5706
      // Enable local APIC; set spurious interrupt vector.
                                                                                 5756
                                                                                      // everything, including cprintf, calls cpu, at least indirectly
5707
      lapicw(SVR, ENABLE | (IRQ_OFFSET+IRQ_SPURIOUS));
                                                                                 5757
                                                                                      // through acquire and release.
5708
                                                                                 5758
                                                                                      if(read_eflags()&FL_IF){
5709 // The timer repeatedly counts down at bus frequency
                                                                                 5759
                                                                                         static int n;
5710 // from lapic[TICR] and then issues an interrupt.
                                                                                 5760
                                                                                         if(n++==0)
                                                                                           cprintf("cpu called from %x with interrupts enabled\n",
5711 // If xv6 cared more about precise timekeeping.
                                                                                 5761
5712 // TICR would be calibrated using an external time source.
                                                                                 5762
                                                                                             ((uint*)read_ebp())[1]);
5713 lapicw(TDCR, X1);
                                                                                 5763 }
      lapicw(TIMER. PERIODIC | (IRO OFFSET + IRO TIMER)):
                                                                                 5764
5715
      lapicw(TICR, 10000000);
                                                                                 5765
                                                                                      if(lapic)
5716
                                                                                 5766
                                                                                         return lapic[ID]>>24;
5717 // Disable logical interrupt lines.
                                                                                 5767
                                                                                       return 0:
5718
      lapicw(LINTO, MASKED):
                                                                                 5768 }
5719
      lapicw(LINT1, MASKED);
                                                                                 5769
5720
                                                                                 5770 // Acknowledge interrupt.
5721 // Disable performance counter overflow interrupts
                                                                                 5771 void
5722 // on machines that provide that interrupt entry.
                                                                                 5772 lapic_eoi(void)
5723 if(((lapic[VER]>>16) & 0xFF) >= 4)
                                                                                 5773 {
        lapicw(PCINT, MASKED):
5724
                                                                                 5774 if(lapic)
5725
                                                                                 5775
                                                                                         lapicw(EOI, 0);
5726
      // Map error interrupt to IRQ_ERROR.
                                                                                 5776 }
      lapicw(ERROR, IRQ_OFFSET+IRQ_ERROR);
5727
                                                                                 5777
5728
                                                                                 5778 // Spin for a given number of microseconds.
5729 // Clear error status register (requires back-to-back writes).
                                                                                 5779 // On real hardware would want to tune this dynamically.
5730
      lapicw(ESR, 0);
                                                                                 5780 static void
5731
      lapicw(ESR, 0);
                                                                                 5781 microdelay(int us)
5732
                                                                                 5782 {
5733 // Ack any outstanding interrupts.
                                                                                 5783 volatile int j = 0;
5734
      lapicw(EOI, 0);
                                                                                 5784
5735
                                                                                 5785
                                                                                       while(us-- > 0)
5736 // Send an Init Level De-Assert to synchronise arbitration ID's.
                                                                                 5786
                                                                                         for(j=0; j<10000; j++);
5737
      lapicw(ICRHI, 0);
                                                                                 5787 }
5738
      lapicw(ICRLO, BCAST | INIT | LEVEL);
                                                                                 5788
5739
      while(lapic[ICRLO] & DELIVS)
                                                                                 5789
5740
                                                                                 5790
5741
                                                                                 5791
5742
     // Enable interrupts on the APIC (but not on the processor).
                                                                                 5792
5743
      lapicw(TPR, 0);
                                                                                 5793
5744 }
                                                                                 5794
5745
                                                                                 5795
5746
                                                                                 5796
5747
                                                                                 5797
5748
                                                                                 5798
5749
                                                                                 5799
```

```
5800 #define IO_RTC 0x70
                                                                                 5850 // The I/O APIC manages hardware interrupts for an SMP system.
                                                                                 5851 // http://www.intel.com/design/chipsets/datashts/29056601.pdf
5801
5802 // Start additional processor running bootstrap code at addr.
                                                                                 5852 // See also picirg.c.
5803 // See Appendix B of MultiProcessor Specification.
                                                                                 5853
5804 void
                                                                                 5854 #include "types.h"
                                                                                 5855 #include "defs.h"
5805 lapic_startap(uchar apicid, uint addr)
5806 {
                                                                                 5856 #include "traps.h"
5807 int i;
                                                                                 5857
5808
      ushort *wrv;
                                                                                 5858 #define IOAPIC 0xFEC00000 // Default physical address of IO APIC
5809
                                                                                 5859
5810 // "The BSP must initialize CMOS shutdown code to OAH
                                                                                 5860 #define REG_ID
                                                                                                         0x00 // Register index: ID
5811 // and the warm reset vector (DWORD based at 40:67) to point at
                                                                                 5861 #define REG VER
                                                                                                        0x01 // Register index: version
5812 // the AP startup code prior to the [universal startup algorithm]."
                                                                                 5862 #define REG TABLE 0x10 // Redirection table base
5813
      outb(IO_RTC, 0xF); // offset 0xF is shutdown code
                                                                                 5863
5814 outb(IO RTC+1. 0x0A):
                                                                                 5864 // The redirection table starts at REG TABLE and uses
5815 wrv = (ushort*)(0x40<<4 \mid 0x67); // Warm reset vector
                                                                                 5865 // two registers to configure each interrupt.
5816
      wrv[0] = 0;
                                                                                 5866 // The first (low) register in a pair contains configuration bits.
5817
      wrv[1] = addr >> 4:
                                                                                 5867 // The second (high) register contains a bitmask telling which
5818
                                                                                 5868 // CPUs can serve that interrupt.
5819 // "Universal startup algorithm."
                                                                                 5869 #define INT_DISABLED 0x00010000 // Interrupt disabled
5820 // Send INIT (level-triggered) interrupt to reset other CPU.
                                                                                 5870 #define INT LEVEL
                                                                                                             0x00008000 // Level-triggered (vs edge-)
5821 lapicw(ICRHI, apicid<<24):
                                                                                 5871 #define INT ACTIVELOW 0x00002000 // Active low (vs high)
5822
      lapicw(ICRLO, INIT | LEVEL | ASSERT);
                                                                                 5872 #define INT_LOGICAL
                                                                                                            0x00000800 // Destination is CPU id (vs APIC ID)
5823
      microdelay(200);
                                                                                 5873
5824
      lapicw(ICRLO. INIT | LEVEL):
                                                                                 5874 volatile struct ioapic *ioapic:
5825
      microdelay(100); // should be 10ms, but too slow in Bochs!
                                                                                 5875
5826
                                                                                 5876 // IO APIC MMIO structure: write reg, then read or write data.
      // Send startup IPI (twice!) to enter bootstrap code.
5827
                                                                                 5877 struct ioapic {
      // Regular hardware is supposed to only accept a STARTUP
5828
                                                                                 5878 uint reg;
5829 // when it is in the halted state due to an INIT. So the second
                                                                                 5879
                                                                                       uint pad[3];
5830 // should be ignored, but it is part of the official Intel algorithm.
                                                                                 5880
                                                                                       uint data:
5831 // Bochs complains about the second one. Too bad for Bochs.
                                                                                 5881 };
5832 for(i = 0; i < 2; i++){
                                                                                 5882
5833
        lapicw(ICRHI, apicid<<24);</pre>
                                                                                 5883 static uint
5834
        lapicw(ICRLO, STARTUP | (addr>>12));
                                                                                 5884 ioapic_read(int reg)
5835
        microdelay(200);
                                                                                 5885 {
5836 }
                                                                                 5886 ioapic->reg = reg;
5837 }
                                                                                 5887
                                                                                       return ioapic->data;
5838
                                                                                 5888 }
5839
                                                                                 5889
5840
                                                                                 5890 static void
5841
                                                                                 5891 ioapic_write(int reg, uint data)
5842
                                                                                 5892 {
5843
                                                                                 5893 ioapic->reg = reg;
5844
                                                                                 5894
                                                                                       ioapic->data = data:
5845
                                                                                 5895 }
5846
                                                                                 5896
5847
                                                                                 5897
5848
                                                                                 5898
5849
                                                                                 5899
```

Sheet 58 Sheet 58

```
5900 void
                                                                                 5950 // Intel 8259A programmable interrupt controllers.
5901 ioapic_init(void)
                                                                                 5951
5902 {
                                                                                 5952 #include "types.h"
                                                                                 5953 #include "x86.h"
5903 int i, id, maxintr;
5904
                                                                                 5954 #include "traps.h"
5905 if(!ismp)
                                                                                 5955
5906
        return;
                                                                                 5956 // I/O Addresses of the two programmable interrupt controllers
5907
                                                                                 5957 #define IO_PIC1
                                                                                                              0x20
                                                                                                                    // Master (IRQs 0-7)
5908 ioapic = (volatile struct ioapic*)IOAPIC;
                                                                                 5958 #define IO_PIC2
                                                                                                              0xA0
                                                                                                                     // Slave (IRQs 8-15)
5909
      maxintr = (ioapic_read(REG_VER) >> 16) & 0xFF;
                                                                                 5959
5910 id = ioapic_read(REG_ID) >> 24;
                                                                                 5960 #define IRQ_SLAVE
                                                                                                              2
                                                                                                                     // IRQ at which slave connects to master
5911 if(id != ioapic id)
                                                                                 5961
5912
        cprintf("ioapic_init: id isn't equal to ioapic_id; not a MP\n");
                                                                                 5962 // Current IRQ mask.
5913
                                                                                 5963 // Initial IRQ mask has interrupt 2 enabled (for slave 8259A).
                                                                                 5964 static ushort irgmask = 0xFFFF & ~(1<<IRO SLAVE):
5914 // Mark all interrupts edge-triggered, active high, disabled,
5915 // and not routed to any CPUs.
                                                                                 5965
5916 for(i = 0; i \le maxintr; i++){
                                                                                 5966 static void
        ioapic_write(REG_TABLE+2*i, INT_DISABLED | (IRQ_OFFSET + i));
5917
                                                                                 5967 pic setmask(ushort mask)
5918
        ioapic_write(REG_TABLE+2*i+1, 0);
                                                                                 5968 {
5919 }
                                                                                 5969 irqmask = mask;
5920 }
                                                                                 5970 outb(IO PIC1+1. mask):
5921
                                                                                 5971 outb(IO_PIC2+1, mask >> 8);
5922 void
                                                                                 5972 }
5923 ioapic_enable(int irq, int cpunum)
                                                                                 5973
5924 {
                                                                                 5974 void
5925 if(!ismp)
                                                                                 5975 pic_enable(int irg)
5926
        return;
                                                                                 5976 {
5927
                                                                                 5977 pic_setmask(irgmask & ~(1<<irq));
5928 // Mark interrupt edge-triggered, active high,
                                                                                 5978 }
5929 // enabled, and routed to the given cpunum,
                                                                                 5979
5930 // which happens to be that cpu's APIC ID.
                                                                                 5980 // Initialize the 8259A interrupt controllers.
5931 ioapic_write(REG_TABLE+2*irq, IRQ_OFFSET + irq);
                                                                                 5981 void
5932 ioapic_write(REG_TABLE+2*irg+1, cpunum << 24);</pre>
                                                                                 5982 pic_init(void)
5933 }
                                                                                 5983 {
5934
                                                                                 5984 // mask all interrupts
5935
                                                                                 5985 outb(IO_PIC1+1, 0xFF);
5936
                                                                                 5986
                                                                                       outb(I0_PIC2+1, 0xFF);
5937
                                                                                 5987
5938
                                                                                 5988
                                                                                      // Set up master (8259A-1)
5939
                                                                                 5989
5940
                                                                                 5990
                                                                                       // ICW1: 0001q0hi
5941
                                                                                 5991 //
                                                                                             g: 0 = edge triggering, 1 = level triggering
5942
                                                                                 5992 // h: 0 = cascaded PICs, 1 = master only
5943
                                                                                 5993
                                                                                            i: 0 = \text{no ICW4}, 1 = \text{ICW4} required
5944
                                                                                 5994
                                                                                       outb(IO_PIC1, 0x11);
5945
                                                                                 5995
5946
                                                                                 5996
                                                                                       // ICW2: Vector offset
5947
                                                                                 5997
                                                                                       outb(I0_PIC1+1, IRQ_OFFSET);
5948
                                                                                 5998
5949
                                                                                 5999
```

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```
6000
      // ICW3: (master PIC) bit mask of IR lines connected to slaves
                                                                                   6050 // PC keyboard interface constants
6001 //
                 (slave PIC) 3-bit # of slave's connection to master
                                                                                   6051
                                                                                                                        // kbd controller status port(I)
6002
      outb(IO_PIC1+1, 1<<IRO_SLAVE);</pre>
                                                                                   6052 #define KBSTATP
                                                                                                                0x64
6003
                                                                                   6053 #define KBS_DIB
                                                                                                                0x01
                                                                                                                        // kbd data in buffer
6004
      // ICW4: 000nbmap
                                                                                   6054 #define KBDATAP
                                                                                                                0x60
                                                                                                                        // kbd data port(I)
6005
      //
            n: 1 = special fully nested mode
                                                                                   6055
6006
      //
            b: 1 = buffered mode
                                                                                   6056 #define NO
                                                                                                                0
6007
     //
            m: 0 = slave PIC, 1 = master PIC
                                                                                   6057
6008 //
               (ignored when b is 0, as the master/slave role
                                                                                   6058 #define SHIFT
                                                                                                                 (1 << 0)
      //
                                                                                   6059 #define CTL
6009
               can be hardwired).
                                                                                                                 (1 << 1)
6010
      //
            a: 1 = Automatic EOI mode
                                                                                   6060 #define ALT
                                                                                                                 (1 << 2)
            p: 0 = MCS - 80/85 \text{ mode}. 1 = intel x86 \text{ mode}
6011 //
                                                                                   6061
6012
                                                                                   6062 #define CAPSLOCK
      outb(I0_PIC1+1, 0x3);
                                                                                                                 (1 << 3)
6013
                                                                                   6063 #define NUMLOCK
                                                                                                                 (1 << 4)
                                                                                   6064 #define SCROLLLOCK
6014 // Set up slave (8259A-2)
                                                                                                                 (1 < < 5)
      outb(I0_PIC2, 0x11);
                                                                                   6065
6015
                                             // ICW1
      outb(IO_PIC2+1, IRQ_OFFSET + 8);
6016
                                             // ICW2
                                                                                   6066 #define E0ESC
                                                                                                                 (1 << 6)
6017
      outb(IO PIC2+1. IRO SLAVE):
                                             // ICW3
                                                                                   6067
6018
      // NB Automatic EOI mode doesn't tend to work on the slave.
                                                                                   6068 // Special keycodes
                                                                                   6069 #define KEY_HOME
6019
      // Linux source code says it's "to be investigated".
                                                                                                                0xE0
6020
      outb(I0 PIC2+1. 0x3):
                                             // ICW4
                                                                                   6070 #define KEY END
                                                                                                                0xE1
6021
                                                                                   6071 #define KEY UP
                                                                                                                0xE2
                                                                                   6072 #define KEY_DN
6022 // OCW3: 0ef01prs
                                                                                                                0xE3
6023 // ef: 0x = NOP, 10 = clear specific mask, 11 = set specific mask
                                                                                   6073 #define KEY_LF
                                                                                                                0xE4
6024 // p: 0 = \text{no polling}, 1 = \text{polling mode}
                                                                                   6074 #define KEY RT
                                                                                                                0xE5
      // rs: 0x = NOP, 10 = read IRR, 11 = read ISR
                                                                                   6075 #define KEY_PGUP
6025
                                                                                                                0xE6
6026
      outb(IO_PIC1, 0x68);
                                        // clear specific mask
                                                                                   6076 #define KEY_PGDN
                                                                                                                0xE7
      outb(I0_PIC1, 0x0a);
                                        // read IRR by default
                                                                                   6077 #define KEY_INS
6027
                                                                                                                0xE8
6028
                                                                                   6078 #define KEY_DEL
                                                                                                                0xE9
6029
      outb(IO_PIC2, 0x68);
                                        // OCW3
                                                                                   6079
6030
      outb(I0_PIC2, 0x0a);
                                        // OCW3
                                                                                   6080 // C('A') == Control-A
6031
                                                                                   6081 #define C(x) (x - '@')
6032
      if(irqmask != 0xFFFF)
                                                                                   6082
6033
        pic_setmask(irqmask);
                                                                                   6083 static uchar shiftcode[256] =
6034 }
                                                                                   6084 {
6035
                                                                                   6085 [0x1D] CTL,
6036
                                                                                   6086
                                                                                         [0x2A] SHIFT,
6037
                                                                                   6087
                                                                                          [0x36] SHIFT,
6038
                                                                                   6088
                                                                                          [0x38] ALT,
6039
                                                                                   6089
                                                                                          [0x9D] CTL,
6040
                                                                                   6090
                                                                                         [0xB8] ALT
                                                                                   6091 };
6041
6042
                                                                                   6092
6043
                                                                                   6093 static uchar togglecode[256] =
6044
                                                                                   6094 {
6045
                                                                                   6095
                                                                                          [0x3A] CAPSLOCK,
6046
                                                                                   6096
                                                                                          [0x45] NUMLOCK.
                                                                                          [0x46] SCROLLLOCK
6047
                                                                                   6097
6048
                                                                                   6098 };
6049
                                                                                   6099
```

Sheet 60 Sheet 60

```
6100 static uchar normalmap[256] =
6101 {
                                                                                      6151 {
6102
       NO,
             0x1B, '1',
                          '2',
                                '3'.
                                       '4',
                                             '5', '6', // 0x00
                                                                                      6152
       77',
                   '9'.
                                '_'
                                       '='
                                             '\b',
                                                   '\t',
6103
             '8',
                          '0',
                                                                                      6153
                                             'u', 'i', // 0x10
6104
       'q',
             'w',
                   'e',
                          'n,
                                't',
                                      'у'
                                                                                      6154
       'o',
6105
             'n,
                                '\n', NO,
                                             'a', 's',
                                                                                      6155
                   'g'
                                'j',
6106
       'd',
                          'h',
                                       'k'
                                             '1',
                                                                                      6156
                                                         // 0x20
             ,,,
       '\'',
6107
                   NO,
                          '\\',
                                'z',
                                      'х'
                                             'c',
                                                   'v',
                                                                                      6157
6108
       'b',
             'n,
                   'n,
                                                   '*', // 0x30
                                                                                      6158
                                            NO,
                   NO,
6109
       NO,
                                NO,
                                      NO,
                                             NO,
                                                                                      6159
                          NO,
                                                   77',
6110
       NO.
             NO.
                   NO,
                          NO,
                                NO,
                                      NO,
                                             NO,
                                                        // 0x40
                                                                                      6160
                   '-'.
       '8',
             '9'.
                          4',
                                '5',
                                      '6'.
                                                   '1'.
6111
                                             '+'.
                                                                                      6161
6112
       '2', '3',
                   '0'.
                                NO,
                                      NO,
                                            NO,
                                                   NO,
                                                                                      6162
                                                         // 0x50
6113
       [0x9C] '\n',
                          // KP_Enter
                                                                                      6163
                          // KP_Div
6114
       [0xB5]'/',
                                                                                      6164
       [0xC8] KEY_UP,
                          [0xD0] KEY_DN,
                                                                                      6165
6115
       [0xC9] KEY_PGUP,
6116
                         [0xD1] KEY_PGDN,
                                                                                      6166 };
6117
       [0xCB] KEY_LF,
                          [0xCD] KEY_RT,
                                                                                      6167
6118
       [0x97] KEY_HOME,
                          [0xCF] KEY_END
                                                                                      6168
6119
       [0xD2] KEY_INS,
                          [0xD3] KEY_DEL
                                                                                      6169
6120 }:
                                                                                      6170
6121
                                                                                      6171
6122 static uchar shiftmap[256] =
                                                                                      6172
6123 {
                                                                                      6173
6124
       NO.
                                '#'
                                      '$',
                                             '%',
                                                   '^', // 0x00
                                                                                      6174
             033.
                   '!'.
                          '@'.
6125
       '&',
             , , ,
                   '(',
                          ')'.
                                       ,<sub>+</sub>,
                                             '\b', '\t',
                                                                                      6175
       'Q',
             'W',
                   'Ε',
                                      Ύ,
                                             'U', 'I', // 0x10
6126
                          'R',
                                'Τ',
                                                                                      6176
       '0',
             'Ρ',
                                 '\n'
                                                   'S'
6127
                                      NO,
                                             'Α',
                                                                                      6177
       'D'.
                    'G'.
                                'J'.
                                                   ':'.
6128
             'F'
                          'H',
                                       'K'
                                             'L',
                                                                                      6178
                                                        // 0x20
             '~'.
6129
       , ,,
                          'l',
                                'Z',
                                       'X'
                                                   'Υ'.
                   NO,
                                             'C',
                                                                                      6179
                                                   '<sub>*</sub>',
                    'M'
                                       '?',
6130
       'B',
             'N',
                          '<',
                                '>',
                                            NO,
                                                        // 0x30
                                                                                      6180
6131
       NO,
                   NO,
                          NO,
                                NO,
                                      NO,
                                            NO.
                                                                                      6181
                                                   NO,
       NO,
                   NO,
                          NO,
                                NO,
                                      NO.
                                             NO,
                                                   '7'.
6132
             NO,
                                                        // 0x40
                                                                                      6182
                                                   '1',
6133
       '8'.
             '9',
                          '4',
                                '5',
                                      '6',
                                             '+',
                                                                                      6183
                          '.', NO,
                   '0',
6134
       '2', '3',
                                      NO.
                                            NO,
                                                   NO,
                                                         // 0x50
                                                                                      6184
6135
       [0x9C] '\n',
                                                                                      6185
                          // KP_Enter
                          // KP_Div
6136
       [0xB5] '/',
                                                                                      6186
6137
       [0xC8] KEY_UP,
                          [0xD0] KEY_DN,
                                                                                      6187
6138
       [0xC9] KEY_PGUP,
                         [0xD1] KEY_PGDN,
                                                                                      6188
6139
       [0xCB] KEY_LF,
                          [0xCD] KEY_RT,
                                                                                      6189
6140
       [0x97] KEY_HOME,
                         [0xCF] KEY_END
                                                                                      6190
6141
       [0xD2] KEY_INS,
                          [0xD3] KEY_DEL
                                                                                      6191
6142 };
                                                                                      6192
6143
                                                                                      6193
6144
                                                                                      6194
                                                                                      6195
6145
6146
                                                                                      6196
6147
                                                                                      6197
6148
                                                                                      6198
6149
                                                                                      6199
```

```
6150 static uchar ctlmap[256] =
      NO,
               NO,
                        NO,
                                 NO,
                                          NO.
                                                   NO,
                                                            NO.
                                                                     NO,
      NO,
               NO,
                        NO,
                                 NO,
                                          NO,
                                                   NO,
                                                            NO,
                                                                     NO,
      C('Q'), C('W'), C('E'), C('R'),
                                         C('T'), C('Y'), C('U'), C('I'),
      C('0'), C('P'), NO,
                                 NO,
                                          '\r',
                                                   NO.
                                                            C('A'), C('S'),
      C('D'), C('F'), C('G'), C('H'), C('J'), C('K'), C('L'), NO,
      NO.
               NO,
                        NO,
                                 C(''), C('Z'), C('X'), C('C'), C('V'),
      C('B'), C('N'), C('M'), NO,
                                          NO,
                                                   C('/'), NO,
                                                                     NO,
      [0x9C] '\r',
                        // KP_Enter
      [0xB5] C('/'),
                        // KP_Div
       [0xC8] KEY_UP,
                        [0xD0] KEY_DN,
      [0xC9] KEY_PGUP,
                        [0xD1] KEY_PGDN
      [0xCB] KEY_LF,
                         [0xCD] KEY_RT,
      [0x97] KEY_HOME,
                        [0xCF] KEY_END,
      [0xD2] KEY_INS,
                        [0xD3] KEY_DEL
```

Sheet 61 Sheet 61

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6250 // Console input and output.

```
6251 // Input is from the keyboard only.
6252 // Output is written to the screen and the printer port.
6253
6254 #include "types.h"
6255 #include "defs.h"
6256 #include "param.h"
6257 #include "traps.h"
6258 #include "spinlock.h"
6259 #include "dev.h"
6260 #include "mmu.h"
6261 #include "proc.h"
6262 #include "x86.h"
6263
6264 #define CRTPORT 0x3d4
6265 #define LPTPORT 0x378
6266 #define BACKSPACE 0x100
6267
6268 static ushort *crt = (ushort*)0xb8000; // CGA memory
6270 static struct spinlock console lock:
6271 int panicked = 0:
6272 int use_console_lock = 0;
6274 // Copy console output to parallel port, which you can tell
6275 // .bochsrc to copy to the stdout:
6276 // parport1: enabled=1, file="/dev/stdout"
6277 static void
6278 lpt_putc(int c)
6279 {
6280 int i;
6281
6282 for (i = 0; !(inb(LPTPORT+1) \& 0x80) \&\& i < 12800; i++)
6283
if(c == BACKSPACE)
       c = ' b';
6285
6286
      outb(LPTPORT+0, c);
6287
      outb(LPTPORT+2, 0x08|0x04|0x01);
6288
      outb(LPTPORT+2, 0x08);
6289 }
6290
6291
6292
6293
6294
6295
6296
6297
6298
6299
```

```
6350 void
6351 printint(int xx, int base, int sqn)
6352 {
6353 static char digits[] = "0123456789ABCDEF";
6354 char buf[16];
6355 int i = 0, neg = 0;
6356
      uint x;
6357
6358 if(sgn && xx < 0){
        neg = 1;
6359
6360
       x = 0 - xx;
6361 } else {
6362
        x = xx;
6363 }
6364
6365
      do{
6366
        buf[i++] = digits[x % base];
6367
     while((x /= base) != 0);
6368
     if(nea)
6369
        buf[i++] = '-';
6370
6371 while(--i >= 0)
6372
        cons_putc(buf[i]);
6373 }
6374
6375 // Print to the console. only understands %d, %x, %p, %s.
6376 void
6377 cprintf(char *fmt, ...)
6378 {
6379 int i, c, state, locking;
6380
      uint *argp;
6381 char *s;
6382
6383
      locking = use_console_lock;
6384 if(locking)
6385
        acquire(&console_lock);
6386
6387
      argp = (uint*)(void*)&fmt + 1;
6388
     state = 0;
6389
      for(i = 0; fmt[i]; i++){
6390
        c = fmt[i] & 0xff;
6391
        switch(state){
6392
        case 0:
6393
          if(c == '%')
6394
            state = '%';
6395
          else
6396
            cons_putc(c);
6397
          break;
6398
6399
```

6340

6342

6343 }

6344

6345

6346

6347

6348

6349

6341 lpt_putc(c);

cga_putc(c);

```
6400
        case '%':
6401
           switch(c){
6402
          case 'd':
6403
            printint(*argp++, 10, 1);
6404
            break;
6405
           case 'x':
6406
           case 'p':
6407
            printint(*argp++, 16, 0);
6408
            break;
           case 's':
6409
6410
            s = (char*)*argp++;
6411
            if(s == 0)
6412
              s = "(null)";
6413
            for(; *s; s++)
6414
               cons_putc(*s);
6415
            break;
6416
           case '%':
6417
            cons_putc('%');
6418
            break;
6419
           default:
6420
            // Print unknown % sequence to draw attention.
6421
            cons_putc('%');
6422
             cons_putc(c);
6423
            break;
6424
          }
6425
           state = 0;
6426
           break;
6427
6428 }
6429
6430
      if(locking)
6431
         release(&console_lock);
6432 }
6433
6434 int
6435 console_write(struct inode *ip, char *buf, int n)
6436 {
6437 int i;
6438
6439
      iunlock(ip);
6440
      acquire(&console_lock);
6441
      for(i = 0; i < n; i++)
6442
        cons_putc(buf[i] & 0xff);
6443
      release(&console_lock);
6444
      ilock(ip);
6445
6446
      return n;
6447 }
6448
6449
```

```
6450 #define INPUT_BUF 128
6451 struct {
6452 struct spinlock lock;
6453 char buf[INPUT_BUF];
6454 int r; // Read index
6455 int w; // Write index
6456 int e; // Edit index
6457 } input;
6458
6459 #define C(x) ((x)-'@') // Control-x
6460
6462 console_intr(int (*getc)(void))
6463 {
6464 int c;
6465
6466
      acquire(&input.lock);
6467
      while((c = getc()) >= 0){
6468
        switch(c){
6469
        case C('P'): // Process listing.
6470
          procdump():
6471
          break:
6472
        case C('U'): // Kill line.
6473
          while(input.e > input.w &&
6474
                input.buf[(input.e-1) % INPUT_BUF] != '\n'){
6475
             input.e--:
6476
             cons_putc(BACKSPACE);
6477
6478
          break;
        case C('H'): // Backspace
6479
6480
          if(input.e > input.w){
6481
            input.e--;
6482
             cons_putc(BACKSPACE);
6483
6484
          break;
6485
        default:
6486
          if(c != 0 && input.e < input.r+INPUT_BUF){</pre>
6487
            input.buf[input.e++ % INPUT_BUF] = c;
6488
             cons_putc(c);
6489
            if(c == '\n' || c == C('D') || input.e == input.r+INPUT_BUF){
6490
              input.w = input.e;
6491
              wakeup(&input.r);
6492
            }
6493
6494
          break;
6495
6496
6497
      release(&input.lock);
6498 }
6499
```

```
6500 int
6501 console_read(struct inode *ip, char *dst, int n)
6502 {
6503 uint target;
6504 int c;
6505
6506 iunlock(ip);
6507
      target = n;
6508
      acquire(&input.lock);
      while(n > 0){
6509
6510
        while(input.r == input.w){
6511
          if(cp->killed){
6512
            release(&input.lock);
6513
            ilock(ip);
6514
            return -1;
6515
          }
6516
          sleep(&input.r, &input.lock);
6517
6518
        c = input.buf[input.r++ % INPUT_BUF];
6519
        if(c == C('D')){ // EOF
6520
          if(n < target){
6521
            // Save ^D for next time, to make sure
6522
            // caller gets a 0-byte result.
6523
            input.r--;
6524
6525
          break;
6526
6527
        *dst++ = c;
6528
        --n;
6529
        if(c == '\n')
6530
          break;
6531 }
6532 release(&input.lock);
6533 ilock(ip);
6534
6535 return target - n;
6536 }
6537
6538
6539
6540
6541
6542
6543
6544
6545
6546
6547
6548
6549
```

```
6550 void
6551 console_init(void)
6552 {
6553 initlock(&console_lock, "console");
6554
      initlock(&input.lock, "console input");
6555
6556
      devsw[CONSOLE].write = console_write;
6557
      devsw[CONSOLE].read = console_read;
6558
      use_console_lock = 1;
6559
6560
      pic_enable(IRQ_KBD);
6561 ioapic_enable(IRQ_KBD, 0);
6562 }
6563
6564 void
6565 panic(char *s)
6566 {
6567 int i;
6568
      uint pcs[10];
6569
6570
      __asm __volatile("cli");
6571 use_console_lock = 0;
6572 cprintf("cpu%d: panic: ", cpu());
6573 cprintf(s, 0);
6574 cprintf("\n", 0);
6575
      getcallerpcs(&s, pcs);
6576
     for(i=0; i<10; i++)
6577
        cprintf(" %p", pcs[i]);
6578
      panicked = 1; // freeze other CPU
6579
      for(;;)
6580
6581 }
6582
6583
6584
6585
6586
6587
6588
6589
6590
6591
6592
6593
6594
6595
6596
6597
6598
6599
```

```
6600 // Intel 8253/8254/82C54 Programmable Interval Timer (PIT).
                                                                                  6650 // Blank page
6601 // Only used on uniprocessors;
                                                                                  6651
6602 // SMP machines use the local APIC timer.
                                                                                  6652
6603
                                                                                  6653
6604 #include "types.h"
                                                                                  6654
6605 #include "defs.h"
                                                                                  6655
6606 #include "traps.h"
                                                                                  6656
6607 #include "x86.h"
                                                                                  6657
6608
                                                                                  6658
6609 #define IO_TIMER1
                             0x040
                                             // 8253 Timer #1
                                                                                  6659
6610
                                                                                  6660
6611 // Frequency of all three count-down timers;
                                                                                  6661
6612 // (TIMER_FREQ/freq) is the appropriate count
                                                                                  6662
6613 // to generate a frequency of freq Hz.
                                                                                  6663
6614
                                                                                  6664
6615 #define TIMER_FREQ
                                                                                  6665
                             1193182
6616 #define TIMER_DIV(x)
                             ((TIMER_FREQ+(x)/2)/(x))
                                                                                  6666
6617
                                                                                  6667
6618 #define TIMER_MODE
                             (IO_TIMER1 + 3) // timer mode port
                                                                                  6668
6619 #define TIMER_SELO
                             0x00
                                     // select counter 0
                                                                                  6669
6620 #define TIMER RATEGEN
                             0x04
                                     // mode 2, rate generator
                                                                                  6670
6621 #define TIMER_16BIT
                             0x30
                                     // r/w counter 16 bits, LSB first
                                                                                  6671
6622
                                                                                  6672
6623 void
                                                                                  6673
6624 timer_init(void)
                                                                                  6674
6625 {
                                                                                  6675
6626 // Interrupt 100 times/sec.
                                                                                  6676
       outb(TIMER_MODE, TIMER_SELO | TIMER_RATEGEN | TIMER_16BIT);
6627
                                                                                  6677
6628
       outb(IO_TIMER1, TIMER_DIV(100) % 256);
                                                                                  6678
6629
       outb(IO_TIMER1, TIMER_DIV(100) / 256);
                                                                                  6679
6630
       pic_enable(IRQ_TIMER);
                                                                                  6680
6631 }
                                                                                  6681
6632
                                                                                  6682
6633
                                                                                  6683
6634
                                                                                  6684
6635
                                                                                  6685
6636
                                                                                  6686
6637
                                                                                  6687
6638
                                                                                  6688
6639
                                                                                  6689
6640
                                                                                  6690
6641
                                                                                  6691
6642
                                                                                  6692
6643
                                                                                  6693
6644
                                                                                  6694
6645
                                                                                  6695
6646
                                                                                  6696
6647
                                                                                  6697
6648
                                                                                  6698
6649
                                                                                  6699
```

Sheet 66 Sheet 66

6700	# Initial process execs /init.
6701	
6702	<pre>#include "syscall.h"</pre>
6703	#include "traps.h"
6704	
6705	<pre># exec(init, argv)</pre>
	.globl start
	start:
6708	pushl \$argv
6709	pushl \$init
6710	push1 \$0
6711	·
6712	
6713	· —
6714	# for(;;) exit();
	exit:
6716	movl \$SYS exit %eax
6717	int \$T_SYSCALL
6718	jmp exit
6719	· ·
	<pre># char init[] = "/init\0";</pre>
	init:
6722	
6723	
	# char *argv[] = { init, 0 };
	.p2align 2
6726	argy:
6727	.long init
6728	.long 0
6729	. 3
6730	
6731	
6732	
6733	
6734	
6735	
6736	
6737	
6738	
6739	
6740	
6741	
6742	
6743	
6744	
6745	
6746	
6747	
6748	
6749	

```
6750 // init: The initial user-level program
6751
6752 #include "types.h"
6753 #include "stat.h"
6754 #include "user.h"
6755 #include "fcntl.h"
6756
6757 char *sh_args[] = { "sh", 0 };
6758
6759 int
6760 main(void)
6761 {
6762 int pid, wpid;
6763
6764 if(open("console", 0_RDWR) < 0){</pre>
6765
        mknod("console", 1, 1);
6766
        open("console", O_RDWR);
6767 }
      dup(0); // stdout
6768
6769
      dup(0); // stderr
6770
6771
      for(;;){
        printf(1, "init: starting sh\n");
6772
6773
        pid = fork();
6774
        if(pid < 0){
6775
          printf(1, "init: fork failed\n");
6776
          exit();
6777
6778
        if(pid == 0){
6779
          exec("sh", sh_args);
6780
          printf(1, "init: exec sh failed\n");
6781
          exit();
6782
6783
        while((wpid=wait()) >= 0 && wpid != pid)
6784
          printf(1, "zombie!\n");
6785 }
6786 }
6787
6788
6789
6790
6791
6792
6793
6794
6795
6796
6797
6798
6799
```

6800	#include "syscall.h"
	#include "traps.h"
6802	
6803	#define STUB(name) \
6804	.globl name; ∖
6805	=
6806	movl \$SYS_ ## name, %eax; \
6807	int \$T_SYSCALL; \
6808	ret
6809	
6810	STUB(fork)
6811	
6812	STUB(wait)
6813	STUB(pipe)
6814	STUB(read)
6815	STUB(write)
6816	STUB(close)
6817	STUB(kill)
6818	STUB(exec)
6819	STUB(open)
6820	STUB(mknod)
6821	STUB(unlink)
6822	STUB(fstat)
6823	STUB(link)
6824	STUB(mkdir)
6825	
6826	STUB(dup)
6827	13 1
6828	
6829	STUB(sleep)
6830	
6831	
6832	
6833	
6834	
6835	
6836	
6837	
6838	
6839	
6840	
6841 6842	
6843 6844	
6845	
6846	
6847	
6848	
6849	
3073	

```
6850 // Shell.
6851
6852 #include "types.h"
6853 #include "user.h"
6854 #include "fcntl.h"
6855
6856 // Parsed command representation
6857 #define EXEC 1
6858 #define REDIR 2
6859 #define PIPE 3
6860 #define LIST 4
6861 #define BACK 5
6862
6863 #define MAXARGS 10
6864
6865 struct cmd {
6866 int type;
6867 };
6868
6869 struct execomd {
6870 int type;
6871 char *argv[MAXARGS];
6872 char *eargv[MAXARGS];
6873 };
6874
6875 struct redircmd {
6876 int type;
6877 struct cmd *cmd;
6878 char *file;
6879 char *efile;
6880 int mode;
6881 int fd;
6882 };
6883
6884 struct pipecmd {
6885 int type;
6886 struct cmd *left;
6887 struct cmd *right;
6888 };
6889
6890 struct listcmd {
6891 int type;
6892 struct cmd *left;
6893 struct cmd *right;
6894 };
6895
6896 struct backcmd {
6897 int type;
6898 struct cmd *cmd;
6899 };
```

```
6900 int fork1(void); // Fork but panics on failure.
                                                                                  6950
                                                                                        case PIPE:
6901 void panic(char*);
                                                                                  6951
                                                                                           pcmd = (struct pipecmd*)cmd;
6902 struct cmd *parsecmd(char*);
                                                                                  6952
                                                                                           if(pipe(p) < 0)
6903
                                                                                  6953
                                                                                             panic("pipe");
6904 // Execute cmd. Never returns.
                                                                                  6954
                                                                                           if(fork1() == 0){
6905 void
                                                                                  6955
                                                                                             close(1);
6906 runcmd(struct cmd *cmd)
                                                                                  6956
                                                                                             dup(p[1]);
6907 {
                                                                                  6957
                                                                                             close(p[0]);
6908 int p[2];
                                                                                  6958
                                                                                             close(p[1]);
6909 struct backcmd *bcmd;
                                                                                             runcmd(pcmd->left);
                                                                                  6959
6910 struct execcmd *ecmd;
                                                                                  6960
                                                                                           if(fork1() == 0){
6911
      struct listcmd *lcmd:
                                                                                  6961
6912 struct pipecmd *pcmd;
                                                                                  6962
                                                                                             close(0);
      struct redircmd *rcmd;
6913
                                                                                  6963
                                                                                             dup(p[0]);
6914
                                                                                  6964
                                                                                             close(p[0]);
6915 if(cmd == 0)
                                                                                  6965
                                                                                             close(p[1]);
6916
        exit();
                                                                                  6966
                                                                                             runcmd(pcmd->right);
6917
                                                                                  6967
6918
      switch(cmd->type){
                                                                                  6968
                                                                                           close(p[0]);
6919
      default:
                                                                                  6969
                                                                                           close(p[1]);
6920
        panic("runcmd");
                                                                                  6970
                                                                                           wait():
6921
                                                                                  6971
                                                                                           wait();
6922
      case EXEC:
                                                                                  6972
                                                                                           break;
6923
        ecmd = (struct execcmd*)cmd;
                                                                                  6973
6924
        if(ecmd->argv[0] == 0)
                                                                                  6974 case BACK:
6925
                                                                                  6975
                                                                                           bcmd = (struct backcmd*)cmd;
          exit();
6926
        exec(ecmd->argv[0], ecmd->argv);
                                                                                  6976
                                                                                           if(fork1() == 0)
6927
        printf(2, "exec %s failed\n", ecmd->argv[0]);
                                                                                  6977
                                                                                             runcmd(bcmd->cmd);
6928
        break;
                                                                                  6978
                                                                                           break;
6929
                                                                                  6979 }
6930
       case REDIR:
                                                                                  6980 exit();
6931
        rcmd = (struct redircmd*)cmd;
                                                                                  6981 }
6932
                                                                                  6982
        close(rcmd->fd);
6933
        if(open(rcmd->file, rcmd->mode) < 0){</pre>
                                                                                  6983 int
6934
          printf(2, "open %s failed\n", rcmd->file);
                                                                                  6984 getcmd(char *buf, int nbuf)
6935
                                                                                  6985 {
          exit();
6936
        }
                                                                                  6986 printf(2, "$ ");
6937
        runcmd(rcmd->cmd);
                                                                                  6987
                                                                                         memset(buf, 0, nbuf);
6938
        break:
                                                                                  6988 gets(buf, nbuf);
6939
                                                                                  6989
                                                                                        if(buf[0] == 0) // EOF
6940
       case LIST:
                                                                                  6990
                                                                                           return -1;
6941
        lcmd = (struct listcmd*)cmd;
                                                                                  6991 return 0;
6942
        if(fork1() == 0)
                                                                                  6992 }
6943
          runcmd(lcmd->left);
                                                                                  6993
6944
        wait():
                                                                                  6994
6945
        runcmd(lcmd->right);
                                                                                  6995
6946
        break;
                                                                                  6996
6947
                                                                                  6997
6948
                                                                                  6998
6949
                                                                                  6999
```

```
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7000 int
                                                                                 7050 // Constructors
7001 main(void)
                                                                                 7051
7002 {
                                                                                 7052 struct cmd*
7003 static char buf[100];
                                                                                 7053 execcmd(void)
7004 int fd;
                                                                                 7054 {
7005
                                                                                 7055 struct execcmd *cmd;
7006
      // Assumes three file descriptors open.
                                                                                 7056
7007
      while((fd = open("console", O_RDWR)) >= 0){
                                                                                 7057 cmd = malloc(sizeof(*cmd));
7008
       if(fd >= 3){
                                                                                 7058 memset(cmd, 0, sizeof(*cmd));
          close(fd);
                                                                                 7059 cmd->type = EXEC;
7009
7010
          break;
                                                                                 7060 return (struct cmd*)cmd;
7011
        }
                                                                                 7061 }
7012
      }
                                                                                 7062
7013
                                                                                 7063 struct cmd*
7014 // Read and run input commands.
                                                                                 7064 redircmd(struct cmd *subcmd. char *file. char *efile. int mode. int fd)
7015
      while(getcmd(buf, sizeof(buf)) >= 0){
                                                                                 7065 {
7016
        if(buf[0] == 'c' && buf[1] == 'd' && buf[2] == ''){
                                                                                 7066 struct redircmd *cmd;
7017
          // Clumsv but will have to do for now.
                                                                                 7067
7018
          // Chdir has no effect on the parent if run in the child.
                                                                                 7068 cmd = malloc(sizeof(*cmd));
7019
          buf[strlen(buf)-1] = 0; // chop \n
                                                                                 7069
                                                                                        memset(cmd, 0, sizeof(*cmd));
7020
          if(chdir(buf+3) < 0)
                                                                                 7070 cmd \rightarrow tvpe = REDIR:
7021
            printf(2, "cannot cd %s\n", buf+3);
                                                                                 7071 cmd \rightarrow cmd = subcmd:
7022
          continue;
                                                                                 7072 cmd->file = file;
7023
                                                                                 7073 cmd->efile = efile;
7024
                                                                                 7074 cmd->mode = mode:
        if(fork1() == 0)
7025
                                                                                 7075 cmd \rightarrow fd = fd;
          runcmd(parsecmd(buf));
                                                                                 7076 return (struct cmd*)cmd;
7026
        wait();
7027 }
                                                                                 7077 }
7028 exit();
                                                                                 7078
7029 }
                                                                                 7079 struct cmd*
7030
                                                                                 7080 pipecmd(struct cmd *left, struct cmd *right)
7031 void
                                                                                 7081 {
7032 panic(char *s)
                                                                                 7082 struct pipecmd *cmd;
7033 {
                                                                                 7083
7034 printf(2, "%s\n", s);
                                                                                 7084 cmd = malloc(sizeof(*cmd));
7035 exit();
                                                                                 7085 memset(cmd, 0, sizeof(*cmd));
                                                                                 7086 cmd->type = PIPE;
7036 }
7037
                                                                                 7087 cmd->left = left;
7038 int
                                                                                 7088 cmd->right = right;
7039 fork1(void)
                                                                                 7089
                                                                                        return (struct cmd*)cmd;
7040 {
                                                                                 7090 }
7041 int pid;
                                                                                 7091
7042
                                                                                 7092
7043
      pid = fork();
                                                                                 7093
7044 	ext{ if(pid == -1)}
                                                                                 7094
        panic("fork");
7045
                                                                                 7095
7046
      return pid;
                                                                                 7096
7047 }
                                                                                 7097
7048
                                                                                 7098
7049
                                                                                 7099
```

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```
7100 struct cmd*
7101 listcmd(struct cmd *left, struct cmd *right)
7102 {
7103 struct listcmd *cmd;
7104
7105 cmd = malloc(sizeof(*cmd));
7106 memset(cmd, 0, sizeof(*cmd));
      cmd->type = LIST;
7107
7108 cmd->left = left;
7109 cmd->right = right;
7110 return (struct cmd*)cmd;
7111 }
7112
7113 struct cmd*
7114 backcmd(struct cmd *subcmd)
7115 {
7116 struct backcmd *cmd;
7117
7118 cmd = malloc(sizeof(*cmd));
7119 memset(cmd, 0, sizeof(*cmd));
7120 cmd->type = BACK;
7121 cmd->cmd = subcmd;
7122 return (struct cmd*)cmd;
7123 }
7124
7125
7126
7127
7128
7129
7130
7131
7132
7133
7134
7135
7136
7137
7138
7139
7140
7141
7142
7143
7144
7145
7146
7147
7148
7149
```

```
7150 // Parsing
7151
7152 char whitespace[] = " t\r\n\v";
7153 char symbols[] = "<|>&;()";
7154
7155 int
7156 gettoken(char **ps, char *es, char **q, char **eq)
7157 {
7158 char *s;
7159 int ret;
7160
7161 s = *ps;
7162 while(s < es && strchr(whitespace, *s))
7163
        S++;
7164 if(a)
7165
        *q = s;
7166 ret = *s;
7167 switch(*s){
7168 case 0:
7169
        break;
7170 case '|':
7171 case '(':
7172
      case ')':
     case ';':
7173
7174 case '&':
7175 case '<':
7176
        S++;
7177
        break;
7178 case '>':
7179
        S++;
7180
        if(*s == '>'){
7181
          ret = '+';
7182
          S++;
7183
        }
7184
        break;
7185
      default:
7186
        ret = 'a';
7187
        while(s < es && !strchr(whitespace, *s) && !strchr(symbols, *s))</pre>
7188
          S++:
7189
        break;
7190 }
7191 if(eq)
7192
        eq = s;
7193
7194 while(s < es && strchr(whitespace, *s))
7195
        S++;
7196 *ps = s;
7197 return ret;
7198 }
7199
```

```
7200 int
7201 peek(char **ps, char *es, char *toks)
7202 {
7203 char *s;
7204
7205 s = *ps;
7206 while(s < es && strchr(whitespace, *s))
7207
       S++;
7208 *ps = s;
7209 return *s && strchr(toks, *s);
7210 }
7211
7212 struct cmd *parseline(char**, char*);
7213 struct cmd *parsepipe(char**, char*);
7214 struct cmd *parseexec(char**, char*);
7215 struct cmd *nulterminate(struct cmd*);
7216
7217 struct cmd*
7218 parsecmd(char *s)
7219 {
7220 char *es:
7221 struct cmd *cmd;
7222
7223 es = s + strlen(s);
7224 cmd = parseline(&s, es);
7225
      peek(&s, es, "");
7226 if(s != es){
7227
        printf(2, "leftovers: %s\n", s);
7228
        panic("syntax");
7229 }
7230 nulterminate(cmd);
7231 return cmd;
7232 }
7233
7234 struct cmd*
7235 parseline(char **ps, char *es)
7236 {
7237 struct cmd *cmd;
7238
7239 cmd = parsepipe(ps, es);
7240 while(peek(ps, es, "&")){
7241
        gettoken(ps, es, 0, 0);
7242
        cmd = backcmd(cmd);
7243 }
7244 if(peek(ps, es, ";")){
7245
        gettoken(ps, es, 0, 0);
7246
        cmd = listcmd(cmd, parseline(ps, es));
7247 }
7248 return cmd;
7249 }
```

```
7250 struct cmd*
7251 parsepipe(char **ps, char *es)
7252 {
7253 struct cmd *cmd;
7254
7255 cmd = parseexec(ps, es);
7256 if(peek(ps, es, "|")){
7257
        gettoken(ps, es, 0, 0);
7258
        cmd = pipecmd(cmd, parsepipe(ps, es));
7259 }
7260 return cmd;
7261 }
7262
7263 struct cmd*
7264 parseredirs(struct cmd *cmd, char **ps, char *es)
7265 {
7266 int tok;
7267 char *q, *eq;
7268
7269 while(peek(ps, es, "<>")){
7270
        tok = gettoken(ps, es, 0, 0);
7271
        if(gettoken(ps, es, &q, &eq) != 'a')
7272
          panic("missing file for redirection");
7273
        switch(tok){
7274
        case '<':
7275
          cmd = redircmd(cmd, q, eq, O_RDONLY, 0);
7276
          break:
7277
        case '>':
7278
           cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
7279
7280
        case '+': // >>
7281
          cmd = redircmd(cmd, q, eq, O_WRONLY|O_CREATE, 1);
7282
           break:
7283
        }
7284 }
7285 return cmd;
7286 }
7287
7288
7289
7290
7291
7292
7293
7294
7295
7296
7297
7298
7299
```

```
7300 struct cmd*
7301 parseblock(char **ps, char *es)
7302 {
7303 struct cmd *cmd;
7304
7305 if(!peek(ps, es, "("))
7306
       panic("parseblock");
7307 gettoken(ps, es, 0, 0);
7308 cmd = parseline(ps, es);
7309 if(!peek(ps, es, ")"))
7310
       panic("syntax - missing )");
7311 gettoken(ps, es, 0, 0);
7312 cmd = parseredirs(cmd, ps, es);
7313 return cmd;
7314 }
7315
7316 struct cmd*
7317 parseexec(char **ps, char *es)
7318 {
7319 char *q, *eq;
7320 int tok. argc:
7321 struct execcmd *cmd;
7322 struct cmd *ret;
7323
7324 if(peek(ps, es, "("))
7325
        return parseblock(ps, es);
7326
7327
      ret = execcmd();
7328
      cmd = (struct execcmd*)ret;
7329
7330 argc = 0;
7331
      ret = parseredirs(ret, ps, es);
7332
      while(!peek(ps, es, "|)&;")){
7333
        if((tok=gettoken(ps, es, &q, &eq)) == 0)
7334
          break;
7335
        if(tok != 'a')
7336
          panic("syntax");
7337
        cmd->argv[argc] = q;
7338
        cmd->eargv[argc] = eq;
7339
        argc++;
7340
        if(argc >= MAXARGS)
7341
          panic("too many args");
7342
        ret = parseredirs(ret, ps, es);
7343 }
7344 cmd \rightarrow argv[argc] = 0;
7345 cmd \rightarrow eargv[argc] = 0;
7346 return ret;
7347 }
7348
7349
```

```
7350 // NUL-terminate all the counted strings.
7351 struct cmd*
7352 nulterminate(struct cmd *cmd)
7353 {
7354 int i;
7355 struct backcmd *bcmd;
7356 struct execcmd *ecmd;
7357 struct listcmd *lcmd;
7358 struct pipecmd *pcmd;
     struct redircmd *rcmd;
7359
7360
7361 if(cmd == 0)
7362
        return 0;
7363
7364 switch(cmd->type){
7365
      case EXEC:
7366
        ecmd = (struct execcmd*)cmd;
7367
        for(i=0; ecmd->argv[i]; i++)
7368
          *ecmd->eargv[i] = 0;
7369
        break;
7370
7371
      case REDIR:
7372
        rcmd = (struct redircmd*)cmd;
7373
        nulterminate(rcmd->cmd);
7374
        *rcmd->efile = 0:
7375
        break;
7376
7377
      case PIPE:
7378
        pcmd = (struct pipecmd*)cmd;
7379
        nulterminate(pcmd->left);
7380
        nulterminate(pcmd->right);
7381
        break;
7382
7383
      case LIST:
7384
        lcmd = (struct listcmd*)cmd;
7385
        nulterminate(lcmd->left);
7386
        nulterminate(lcmd->right);
7387
        break;
7388
7389
      case BACK:
7390
        bcmd = (struct backcmd*)cmd;
7391
        nulterminate(bcmd->cmd);
7392
        break;
7393 }
7394 return cmd;
7395 }
7396
7397
7398
7399
```