

The diagram shows the AD7998 ADC connected to a 3.3V supply. The power supply is connected to the VDD pin through a 10kΩ resistor (R1). The input is connected to the VIN_1 pin. The output is connected to the SCL pin, which is also connected to a 10kΩ pull-up resistor (R2) to the 3.3V supply. The SDA pin is connected to the SCL pin. The device is labeled U4 and AD7998.

The schematic diagram shows the HMC5843 magnetometer chip (U1) with the following connections:

- Power and Ground:**
 - VDD, VREN, and SCL are connected to a 3.3V supply.
 - AVDD, SDAP, SCL, and SDA are connected to the 3.3V supply.
 - AGND and DGND are connected to GND.
 - NC pins are connected to GND.
- I2C Communication:**
 - SCL and SDA are connected to the 3.3V supply.
- Capacitors:**
 - C4 (0.22uF) is connected between SETP and SETC.
 - C5 (4.7uF) is connected between C1 and GND.
- Test Point:**
 - TP1 is connected to MAG_DBDY.

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