

# REAL TIME CLOCK MODULE (I<sup>2</sup>C-Bus)

Built-in 32.768 kHz DTCXO, High Stability, Power switching



**Product Number** 1,000 pcs / Reel

RX8900SA UA: X1B000292000100 RX8900SA UB: X1B000292000200 RX8900SA UC: X1B000292000300

2.000 pcs / Reel

RX8900CE UA: X1B000301000100 RX8900CE UB: X1B000301000200 RX8900CE UC: X1B000301000300





**RX8900SA** 

RX8900CE

 $(10.1 \times 7.4 \text{ mm}, t = 3.3 \text{ mm Max.})$   $(3.2 \times 2.5 \text{ mm}, t = 1.0 \text{ mm Max.})$ 

# RX8900SA / RX8900CE

• Built-in frequency adjusted 32.768 kHz crystal unit and DTCXO

 Interface Type : I2C-Bus Interface voltage range : 2.5 V to 5.5 V • Temp. compensated voltage range : 2.0 V to 5.5 V Timekeeping voltage range : 1.6 V to 5.5 V

 Auto power switching func ion : Automatically switches to backup power

supply by monitoring the VDD voltage

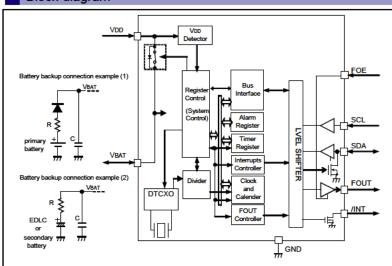
 Interrupt output : Wake up every minute or every second

 Alarm interruption : Day, date, hour, minute

Auto repeat wakeup timer interruption

The I<sup>2</sup>C-Bus is a trademark of NXP Semiconductors

# Block diagram



### Overview

 Interface type I<sup>2</sup>C-Bus interface Fast-Mode 400 kHz

High stability

UA: ± 3.4 x 10 <sup>6</sup> / -40 °C to +85 °C (equiv. to ±9 s of mo. deviation) UB: ± 5.0 x 10<sup>-6</sup> / -40 °C to +85 °C (equiv. to ±13 s of mo. deviation) UC: ± 5.0 x 10<sup>-6</sup> / -30 °C to +70 °C (equiv. to ±13 s of mo. deviation)

Auto power switch function

The  $\dot{V_{DD}}$  voltage is monitored and it switches to the backup power supply by the automatic operation

Backup power supply switching voltage 1.9 V Min.

Clock output function

Output frequency is selectable from 32.768 kHz, 1024 Hz, 1 Hz

Wakeup timer function

Selectable from 244 µs to 2.8 days (12 bit x 1 ch.) Timer source clock selectable from 1/60 Hz, 1 Hz, 64 Hz, 4096 Hz Auto release after interrupt output from /INT pin at timer completes This operation is auto repeat with a selected cycle, it can be used like a watchdog timer

Alarm function

It is possible program from day to minute

· Temp. sensor function

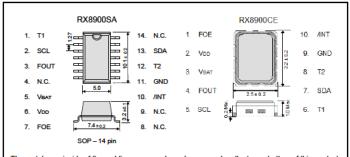
Available readout temperature data from embedded temp sensor

# Pin Function

| Signal Name | 1/0            | Function   |
|-------------|----------------|--|
| T1          | -              | Test pin in the factory (Do not connect externally)  |
| SCL         | Input          | Serial clock input pin   |
| FOUT        | Output         | Frequency output pin (CMOS)<br>(frequency selection: 32.768 kHz, 1024 Hz, 1 Hz)  |
| VBAT        | -              | This is a power supply pin for backup battery<br>Connect an EDLC, a secondary battery, a primary<br>battery. In the backup voltage range, supplied<br>to IC, from this pin |
| VDD         | -              | Power-supply pin   |
| FOE         | Input          | The FOUT output control pin  |
| /INT        | Output         | Interrupt output (N-ch. open drain).   |
| GND         | -              | Ground pin   |
| T2          | -              | Test pin in the factory (Do not connect externally)  |
| SDA         | Input / Output | Serial data input and output pin   |

### Terminal connection / External dimensions

(Unit: mm)



The metal case inside of the molding compound may be exposed on the top or bottom of this product. This purely cosmetic and does not have any effect on quality, reliability or electrical specs

## Specifications (characteristics)

| ■ Electrical Characteristics       |                   |   |  |                       |      |      |      |                    |  |  |  |
|------------------------------------|-------------------|---|--|-----------------------|------|------|------|--------------------|--|--|--|
| tem                                | Symbol            | Conditions  |  |                       | Min. | Тур. | Max. | Unit               |  |  |  |
| Operating voltage                  | VDD               | -   |  |                       | 25   | 3.0  | 5.5  | ٧                  |  |  |  |
| Temp. compensated Voltage          | Vтем              | -   |  |                       | 20   | 3.0  | 5.5  | V                  |  |  |  |
| Clock supply voltage               | Vclk              | -   |  |                       | 16   | 3.0  | 5.5  | ٧                  |  |  |  |
| V <sub>DD</sub> detect voltage (3) | V <sub>DET3</sub> | -   |  |                       | 23   | 2.4  | 2.5  | ٧                  |  |  |  |
| Operating temperature Ta           |                   | -   |  | -40                   | +25  | +85  | °C   |                    |  |  |  |
|                                    | Δf/f              | UA  | Ta = -40 °C to +85 °C                          |                       | ±3.4 |      |      | x 10 <sup>-6</sup> |  |  |  |
| Stability                          |                   | UB  | Ta = -40 °C to +85 °C<br>Ta = -30 °C to +70 °C |                       | ±5.0 |      |      |                    |  |  |  |
|                                    |                   | UC  |  |                       |      |      |      |                    |  |  |  |
| Current consumption (1)            | IDD1              | FOE = GND, VDD = VBAT, FOUT: OFF, Temp Compensation |  | V <sub>DD</sub> = 5 V | -    | 0.72 | 1.5  | μΑ                 |  |  |  |
| Current consumption (2)            | IDD2              |   |  | V <sub>DD</sub> = 3 V | -    | 0.70 | 1.4  |                    |  |  |  |

#### \* Refer to application manual for details

