## CMS

# **ETL** Tamale Module Design

#### **Revision History**

]	Revision	Date	Author(s)	Description
1	1	June, 2022	C. Fangmeier	Initial description based on Tamale module design presented at TIP in March 2022

Disclaimer: This document comprises a snapshot in time of the evolution of the ETL module design. We anticipate no major changes from what is described here, but small adjustments here and there are expected.

#### Overview of Design

The ETL Module is built from a stack-up of a components as illustrated in Fig. 1. Starting from the bottom, the layers are:

- A ceramic baseplate. Currently, Alumina or Aluminum Nitride are the leading candidate materials for their relatively high thermal conductivity and low cost.
- An adhesive layer. The leading candidate is an 80um thick silicone-based phase-change thin-film material. It serves as both a strong mechanical and relatively low-resistance thermal interface between the silicon components and the baseplate.
- Four ETROC+LGAD subassemblies. These will be bump-bonded by an external vendor.
- A 2nd adhesive layer. It will be the same material as the sensor-mount adhesive.
- A PCB. This serves as the power and I/O interface between the readout board and the module via two boardto-board connectors. It also serves as a location to place any SMT passive components that must be placed very near the ETROCs.

The overall module dimensions are 56.5mm long by 43.1mm wide with an estimated stackup height of 2.97mm. For prototype modules to be constructed using ETROC2, the length is increased by 1mm to 57.5mm to ease the wire bonding process.

This document is structured to follow the gantry-based assembly procedure while providing relevant details on the mechanical structure of the module and components along the way. A mechanical jig-based assembly is also being developed to be deployed as module factories that do not have gantries. The modules produced with either method will be identical.

#### Module PCB

Assembly begins with the Module PCB as the base layer of the stackup. It is important to note that the modules are built "upside down", i.e. PCB-side down, while they will be mounted baseplate-side down.

The Module PCB's dimensions match that of the module overall at 56.5x43.1mm. The 56.5mm length is a limiting constraint on the length of the wire bonds connecting each ETROC with the module PCB. Thus, for modules assembled with the prototype ETROC2 will increase the overall length by 1mm to 57.5mm. The module dimensions will be finalized following studies with the ETROC2 prototype and considerations in the power board design.

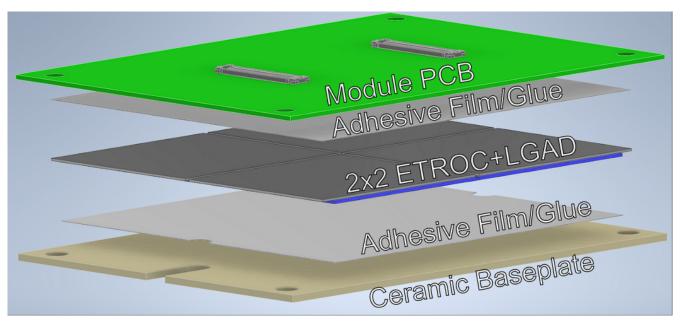


Figure 1: Stackup of ETL Module design

The module PCB design currently has four 2.2mm diameter mounting holes, one in each corner. These are through holes for M2 screws that will serve to hold the assembly of the readout board and its several modules to the disc. The module PCB geometry is shown in Fig. 2 The PCB will be a 4-layer board with 0.5mm nominal thickness. The board-to-board connectors are JAE Electronics WP7B-S050VA1-R8000. These are surface-mount connectors with 50 connections each and an overall stacking height of 0.7mm. They are placed with a separation of 23.53mm centered on the top of the board. The top of the board will also potentially have a small number of surface-mount passive components as required by the ETROC.

The opposite side of the board has a pattern of wire-bonding pads to connect with the ETROCs. This is described in more detail in a later section.

## Sensor-mount and Baseplate films

As mentioned in the introduction, adhesive layers are used to mechanically attach the module layers together as well as provide a thermal path for removing heat from the ETROC and LGAD. The leading candidate material is the Laird TPCM (Thermal Phase Change Material) 580 thin-film adhesive. The film will be cut to shape by an outside vendor and will arrive to the module factories with plastic liners on each side with tabs to allow easy removal.

The sensor-mount film matches the envelope of the four ETROCs on the module (should it be slightly larger?). The dimensions are 42.6x46.6mm and 80um thick. The film will be applied to the Module PCB using a mechanical jig for alignment and to minimize the presence of trapped air between the film and the PCB.

A second film is applied to the baseplate and is used to fix the baseplate to the rest of the assembly. This film, the "Baseplate Film", is 43x43.4mm and has two 3mm diameter semicircular cutouts centered on each ETROC-LGAD subassembly along the shorter edge to provide access to the sensor for bias wire bonds. This film will be applied to the baseplate using a similar jig to that used for applying the sensor-mount film.

Immediately after placement, the subassemblies will be placed in a vacuum oven for a minimum of 20 minutes at 60°C to cure the films. Curing the films causes them to conform to and better adhere to the surfaces they are in contact with. The first cure, or pre-assembly cure, is important to ensure that the films are well-adhered to the

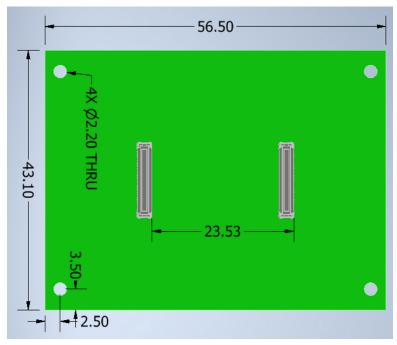


Figure 2: Geometry of Module PCB

module PCBs and baseplates. Lacking this, there is a risk of the film shifting at any point up to the post-assembly cure.

Prior experience in the field with silicone-based adhesives shows good performance after irradiation (see 2001 CERN yellow report on adhesives), but samples should be prepared and evaluated after irradiation to HL-LHC fluences (neutrons? gammas?). Other adhesive options include Araldite two-component epoxy, which has already been evaluated to fluences greater than those expected in MTD throughout HL-LHC lifetime.

## **ETROC & LGAD Subassembly**

The active components of the module, the ETL Read-out Chip, or ETROC, and the Low-gain Avalanche Diode (Detector?) sensor, will arrive at the module factories already bump-bonded together. The ETROC is 21x23mm and is 0.25mm thick. The LGAD consists of a 16x16 grid of pixels has overall dimensions of 21.4x21.6mm and is 0.3mm thick. The ETROC and LGAD are aligned such that the LGAD overhangs the edge of the ETROC on three sides by 0.2mm, and on the 4th side the ETROC extends past the sensor by 1.6mm with the wirebond pads along the edge. This is illustrated in Fig. 3.

Each module has four ETROG+LGAD subassemblies. They are placed in a 2x2 array with a spacing of 0.2mm between adjacent sensors (is this sufficient to prevent potential sparking?). The array is centered on the module PCB with each subassembly's wire bond pads facing out (Fig. 4).

The ETROC+LGAD subassemblies are placed onto a module PCB using a high-precision robotic gantry. Prior to pick-and-place the top plastic liner on the sensor-mount film on the module PCB is removed. The gantry is equipped with a vision system that measures the positions of the subassemblies with respect to the module PCB after placement. These measurements are used to check that the assembly precision meets requirements.

After placing the ETROC+LGAD subassemblies, the module is again placed in a vacuum oven for the post-assembly cure. This is needed to ensure that the subassemblies do not shift during wirebonding or encapsulation.

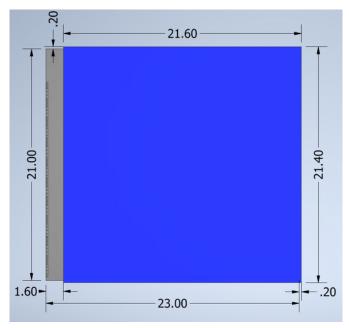


Figure 3: Geometry of ETROC+LGAD subassembly

#### Wirebonding

Each ETROC requires 142 wire bonds to connect it to the module PCB. The pattern for these wire bonds is shown in Fig. 5. In addition, bias voltage is supplied to the sensor via wire bonds that attach to the metallized backplane of the sensor. The ETROC and LGAD wire bonds both will be completed during the same stage of assembly. The bonding wire will be 25 um diameter aluminum-silicon alloy. The bond pads should have ENEPIG surface finish to achieve optimal wire bonding performance. In addition, to facilitate wire bonding, fiducial markers (TBD) should be included to simplify navigation of the bond pads on both sides.

## Wirebond Encapsulation

It is important to encapsulate wire bonds with a suitable material to reduce the risk of physical damage, increase electrical isolation between the bonds, and damp any potential oscillations caused the Lorentz force on the wires in the magnetic field of CMS.

A suitable material that has been used extensively in CMS is Sylgard 186. It is a 2-part silicone elastomer that is dispensed over the wire bonds using a dedicated dispensing robot. At minimum, the encapsulant must cover the wire bond pads and feet, and full wire coverage is also being considered. Studies have shown that Sylgard lacks sufficient radiation tolerance to be used in HL-LHC tracking detectors. It is expected to be ok for use in ETL, but needs to be confirmed with studies of irradiated samples.

### **Baseplate**

The final step in assembly is attaching the module baseplate. Alumina and Aluminum Nitride ceramics are leading candidates for the baseplate material. It has overall dimensions matching the module PCB, but with a thickness of 1mm. The baseplate has a 2.5mm diameter hole in each corner. The baseplate provides mechanical rigidity and a thermal path between the ETROC-LGAD subassemblies and Al dees containing embedded cooling loops. The

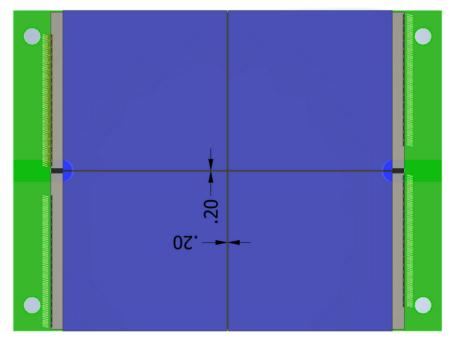


Figure 4: Positioning of ETROC+LGAD subassemblies on the module PCB

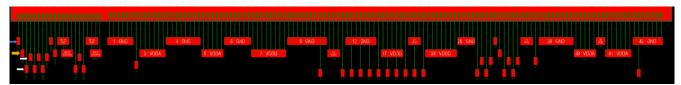


Figure 5: Proposed wirebonding pattern for the ETROC

mounting scheme is still under consideration. It is worth noting that the number of mounting holes is a driving factor in the fabrication cost of the Al dees.

The baseplate also has a 3mm wide cutout centered along each of the short ends that provides access for the bias wirebonds to the top surface of the sensor. This is shown in Fig. 7.

At this point in assembly a film has already been placed onto the baseplate and been pre-cured. What remains is to use the robotic gantry to place the baseplate on top of the module, and then perform the final film cure. At this point the module assembly is finished. After this, extensive testing and calibration will be performed on each module. A description of these tests is outside the scope of this document, but can be found in XXX.



Figure 6: Geometry of module baseplate

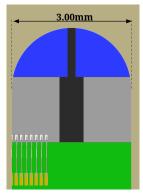


Figure 7: Detail of the baseplate cutout providing access to the sensor