

Low 64K [0000 – FFFF]  
(24x/240x equivalent data space)

Block Start Address
0x00 0000
0x00 0400
0x00 0800
0x00 0D00
0x00 0E00
0x00 6000
0x00 7000
0x00 8000
0x00 9000
0x00 A000
0x00 C000

Data Space	Prog Space
M0 SARAM (1 K × 16)	
M1 SARAM (1 K × 16)	
Peripheral Frame 0	
PIE Vector – RAM (256 × 16) (Enabled if ENPIE = 1)	
Peripheral Frame 1 (protected)	
Peripheral Frame 2 (protected)	
L0 SARAM (0-wait) (4 k × 16, Secure Zone, Dual Mapped)	
L1 SARAM (0-wait) (4 k × 16, Secure Zone, Dual Mapped)	
H0 SARAM (0-wait) (8 k × 16, Dual Mapped)	
OTP (1 k × 16, Secure Zone)	
FLASH (64 k × 16, Secure Zone)	
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128-bit Password	
L0 SARAM (0-wait) (4 k × 16, Secure Zone, Dual Mapped)	
L1 SARAM (0-wait) (4 k × 16, Secure Zone, Dual Mapped)	
H0 SARAM (0-wait) (8 k × 16, Dual Mapped)	
Boot ROM (4 k × 16)	
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Vectors (32 × 32) (enabled if VMAP = 1, ENPIE = 0)	

High 64K [3F0000 – 3FFFFFF]  
(24x/240x equivalent program space)

0x3D 7800
0x3D 7C00
0x3E 8000
0x3F 7FF8
0x3F 8000
0x3F 9000
0x3F A000
0x3F C000
0x3F F000
0x3F FFC0

Reserved