|  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| op | 000000 | 000000 | 001101 | 100011 | 101011 | 000100 | 001111 | 000011 | 000000 |
| func | 100001 | 100011 | N/A | | | | | | 001000 |
|  | addu | subu | ori | lw | sw | beq | lui | jal | jr |
| RegDst | 01 | 01 | 00 | 00 | xx | xx | 00 | 10 | xx |
| ALUSrc | 0 | 0 | 1 | 1 | 1 | 0 | 1 | x | 0 |
| MemtoReg | 00 | 00 | 00 | 01 | 00 | 00 | 00 | 10 | 00 |
| RegWrite | 1 | 1 | 1 | 1 | 0 | 0 | 1 | 1 | 0 |
| MemWrite | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 |
| Branch | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 |
| Jump | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 01 | 10 |
| ExtOp | xx | xx | 00 | 01 | 01 | 01 | 10 | xx | xx |
| ALUctr | 00 | 01 | 10 | 00 | 00 | 00 | 10 | xx | 00 |