

ASAP 2018

IEEE International Conference on
Application-specific Systems, Architecture and Processors
<http://asap18.necst.it>

Politecnico di Milano
Aula Rogers
July 10, 2018

Marco D. Santambrogio
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Politecnico di Milano

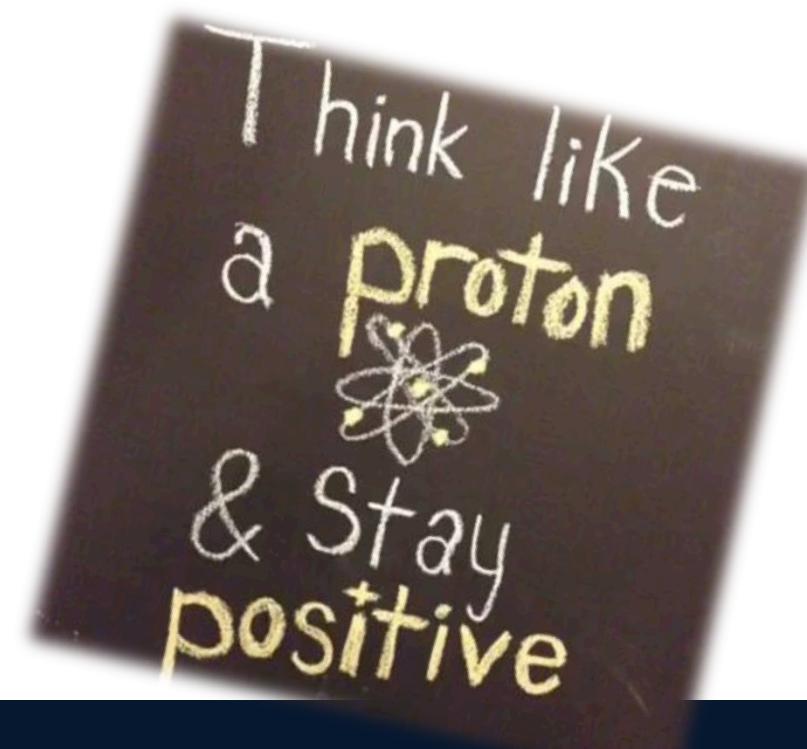
Dates and Venue

- Dates
 - ASAP (main conference)
 - 10-12 July, 2018
 - Co-located workshops
 - Day before and after ASAP
- Venue
 - Milano
 - Politecnico di Milano



Highlights

- Technical sessions and presentations
- 4 Keynotes
- Posters available through the entire conference
- 2 special sessions
- Social event



Organizing Committee

- General Chair
 - Marco D. Santambrogio, Politecnico di Milano, Italy
 - Jose L. Ayala, Complutense University of Madrid, Spain
- Program Chairs
 - Zhiru Zhang, Cornell University, New York, USA
 - Diana Goehringer, TU Dresden, Germany
- Finance and Sponsorship Chair
 - Sara Notargiacomo, Politecnico di Milano, Italy
- Workshop/Special Session Chairs:
 - Lorenzo Di Tucci, Politecnico di Milano, Italy
- Local Chair
 - Marco Rabozzi, Politecnico di Milano, Italy
- Publicity and Media Chairs
 - Jari Nurmi, Tampere University of Technology, Finland
 - Brian F. Veale, IBM, USA



General Statistics

- Submissions 98 (last year 72)
- Accepted 28 (+13 short)
- Acceptance rate 0.30 full
- Reviews
 - Overall #Reviews 3 per paper

Technical Program

- Monday: Co-located Workshop
 - Registrations opening
- Tuesday: ASAP – Day 1
 - Opening
 - 2 Keynotes and technical sessions
 - Cheese and wine welcome drink (Social Event 1)
- Wednesday: ASAP – Day 2
 - 2 Keynote and technical sessions
 - Social Dinner (Social Event 2)
- Thursday: ASAP – Day 3
 - 1 Keynote
 - Technical sessions
 - Closing remarks
- Friday: Co-located Workshop

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Keynote

How to Make Sparse Fast



Saman P. Amarasinghe,
Professor and Associate
Department Head, MIT

Keynote

Deep reinforcement learning and
algorithmic risks in finance



Marcello Paris,
Head of Math & IT research,
R&D Dept. UniCredit

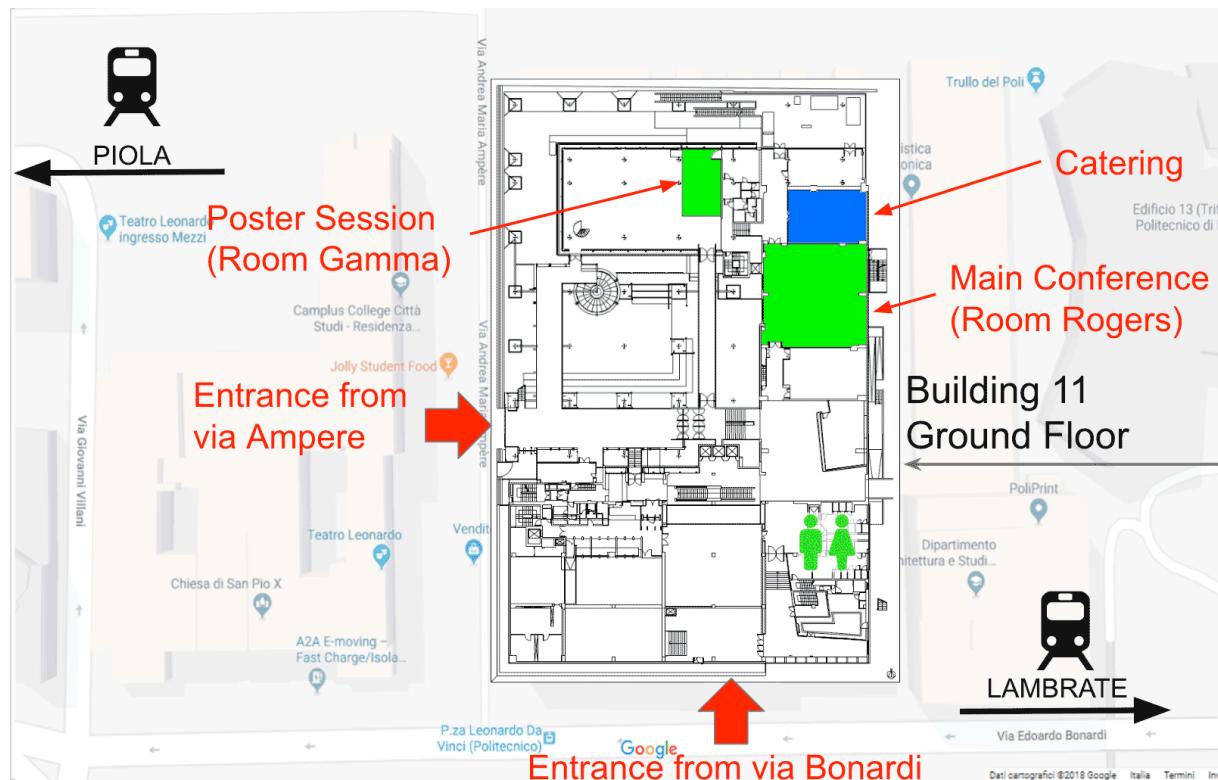
Cheese and Wine

- Welcome Party @ ASAP 2018
 - Tuesday, 7.30pm @ Catering room



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Keynote

How open source designs will drive the next generation of HPC Systems



David Donofrio,
Group Lead & Computer
Systems Engineer at Lawrence
Berkeley National Lab

Keynote

Google Pixel Visual Core:
A Portable Domain-Specific Processor for
Computational Photography and Machine Learning



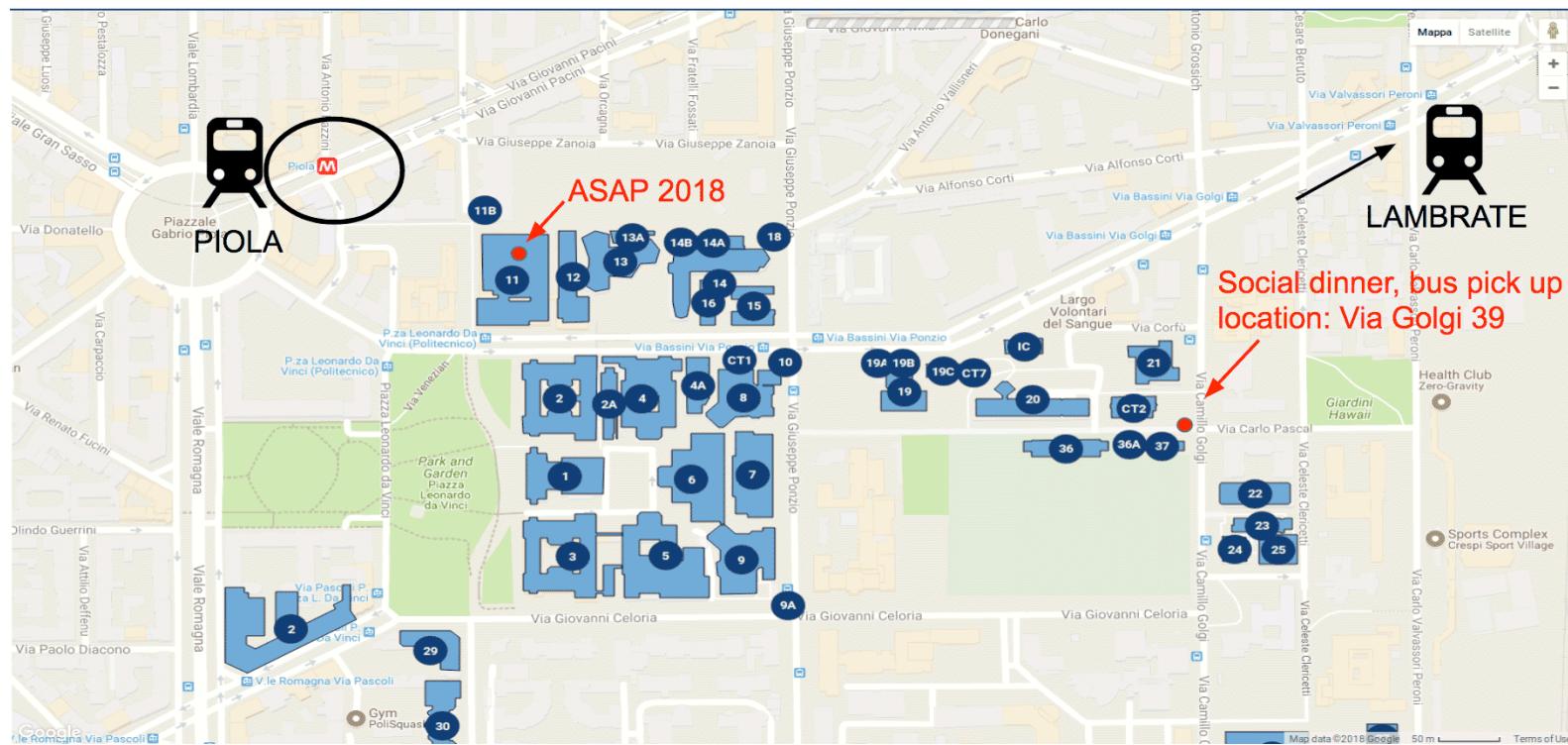
Andrea Di Blas
Software Engineer at Google

Social Event

- Wine testing, and “not only”, in Franciacorta
 - Wednesday, 6.15pm @ Via Golgi 39
 - Departure by private bus from Politecnico di Milano to Polastri Maceler (Via Quattro Vie 1, Torbiato di Adro, Brescia)
 - Estimated time back in Milano 11/11.30pm

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Keynote

Design Trade-offs for Machine Learning Solutions
on Reconfigurable Devices



Michaela Blott,
Principal Engineer at Xilinx

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Workshop on FPGA-based Accelerated Cloud Computing



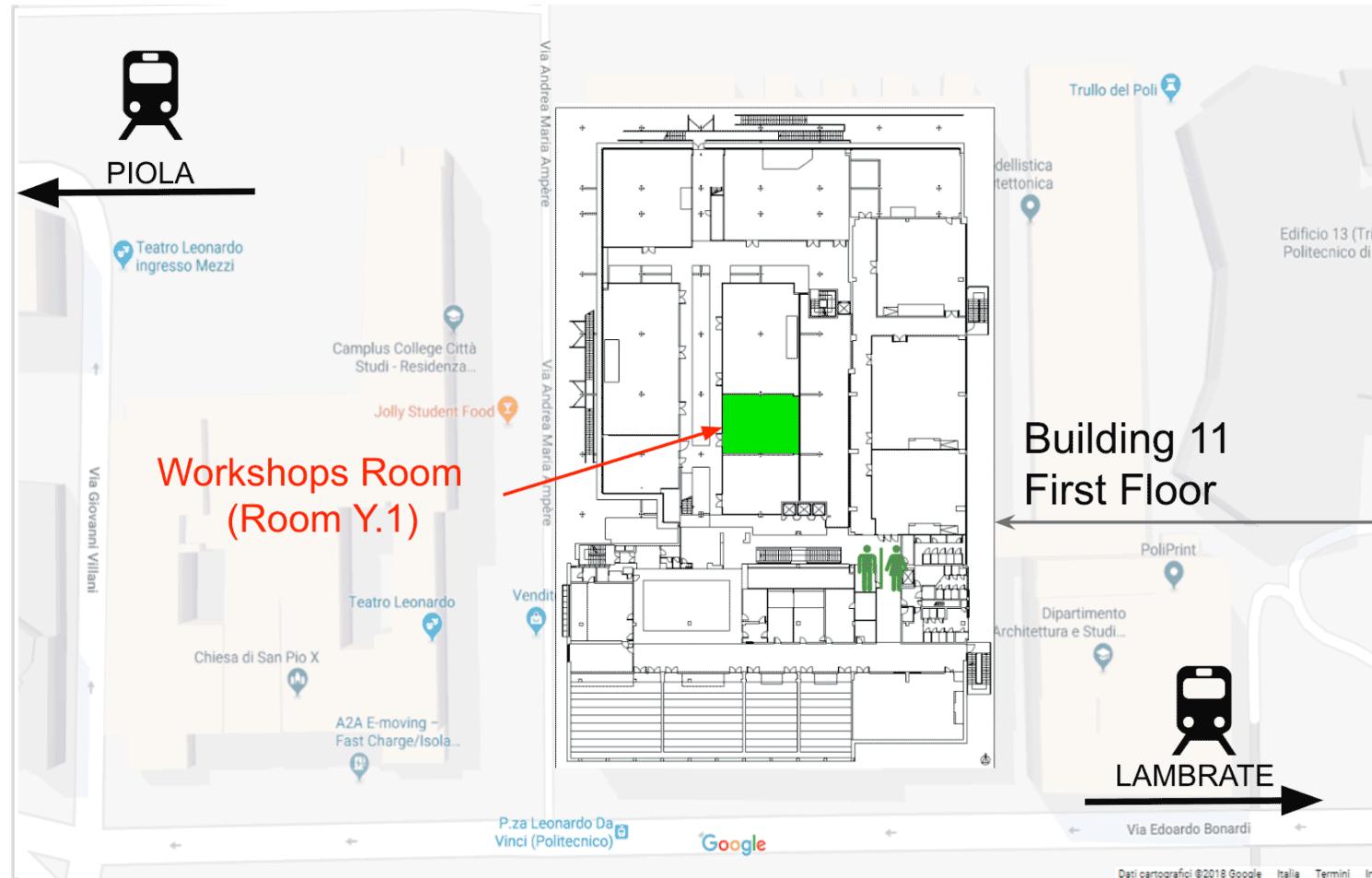
Cathal McCabe,
Xilinx University Program



ALL PROGRAMMABLE™

- From 9am to 6pm
- Classroom Y.1 (this same building, 1st floor)
- To register: <https://tinyurl.com/xilinx-asap18>

Workshop on FPGA-based Accelerated Cloud Computing



Follow us on our website...



The screenshot shows the homepage of the ASAP 2018 conference website. At the top, there is a navigation bar with the text "ASAP 18" in bold, followed by "Home", "Committee", "Call for paper", "Partecipate", and "WiFi". Below the navigation bar is a large banner featuring the text "ASAP 2018" in large, bold, white letters, with a subtitle "The 27th Annual IEEE International Conference on Application-specific Systems, Architectures and Processors" and a "Welcome to Milan!" message overlaid on a background image of the Milan Cathedral. Below the banner, the text "Welcome to ASAP-2018!" is displayed. At the bottom left, there is a note about the 29th conference taking place in Milan, Italy, and at the bottom right, the text "Conference Sponsors:".

<http://asap18.necst.it>

Special Thanks

- All the members of the Steering Committee and Program Committee
- IEEE CS, TCVLSI, CAS
- Unicredit
- Reply
- intel
- Politecnico di Milano
- EXTRA EU, Fondazione Cariplo, Regione Lombardia



Regione
Lombardia



POLITECNICO
MILANO 1863

For the speakers...

- Please use your own computer
 - Check in advance that your computer works ok with the projector
 - Contact your session chair in advance to verify that everything is ok
- Poster sessions
 - Brief oral introduction (9-10 slides, 10 mins)
 - A space/board will be available for each poster
 - Place your poster on Monday morning, they can be available through the entire conference

Enjoy your time in Milano!



KEEP
CALM
AND
ENJOY
YOUR TIME

Keynote

How to Make Sparse Fast



Saman P. Amarasinghe,
Professor and Associate
Department Head, MIT