A Formally Verified NAT Stack



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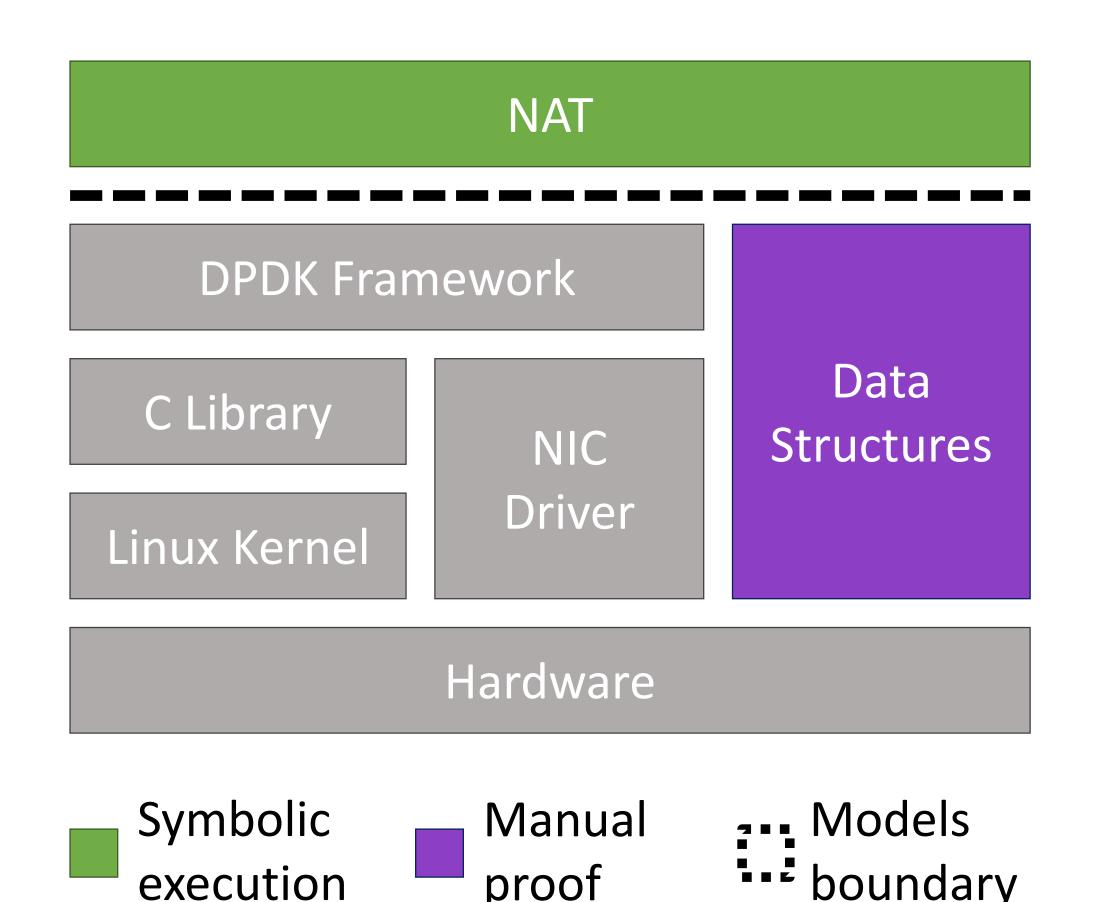


George Candea

Verified NAT

Symbolically execute NAT
Manually prove data structures
Model DPDK + driver

- × Unverified components
- × DPDK & driver are immature



This Work

Insight

DPDK & driver are *large*, but not *complex*

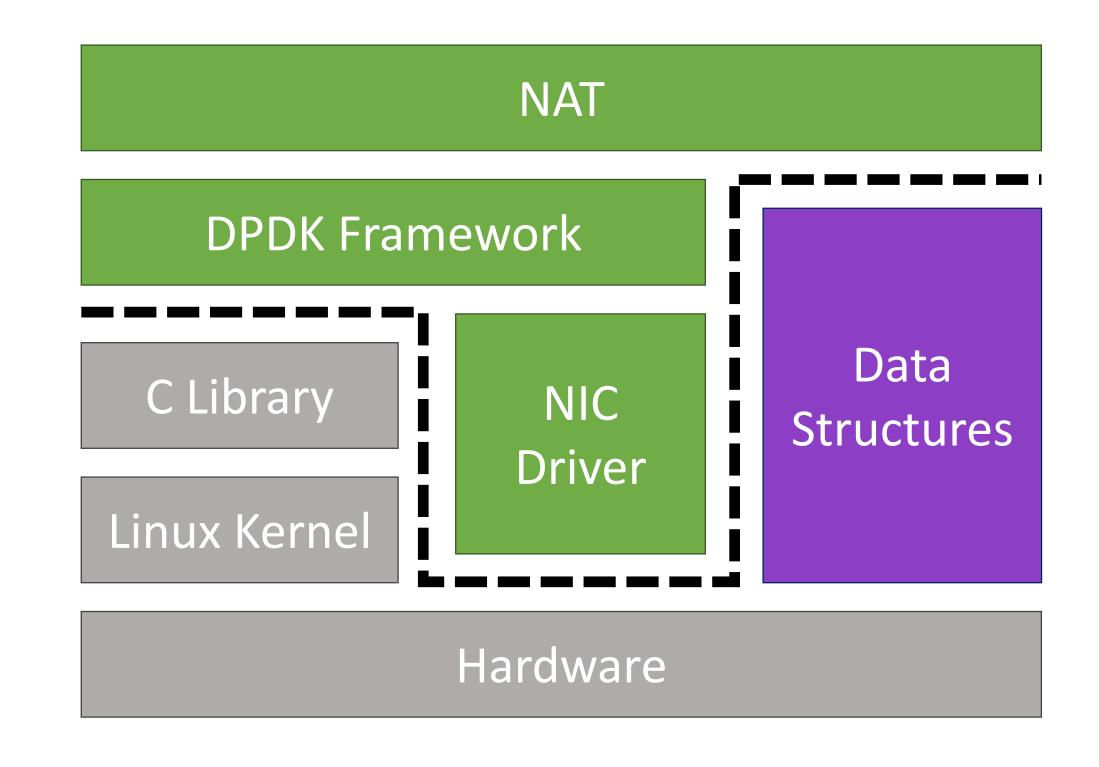
Lower-level models

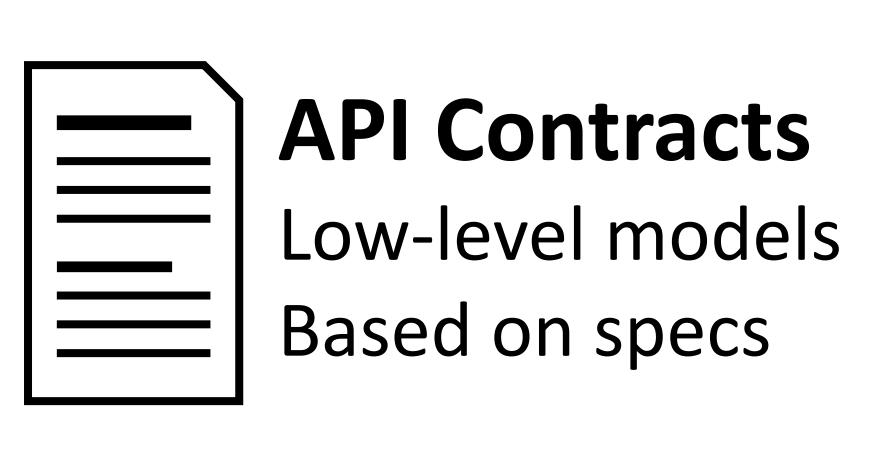
C library
Hardware
Registers
DMA
I²C, SFP, ...

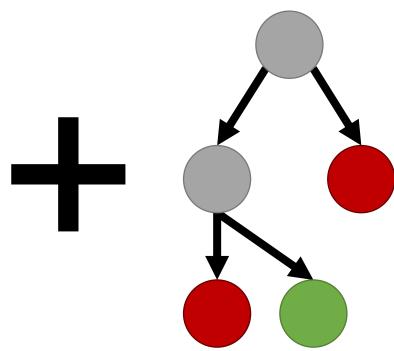
Verified Stack

Symb. exec. NAT, DPDK, driver

- ✓ More verified components
- ✓ Unverified code is mature







Symbolic Execution

Explore all paths

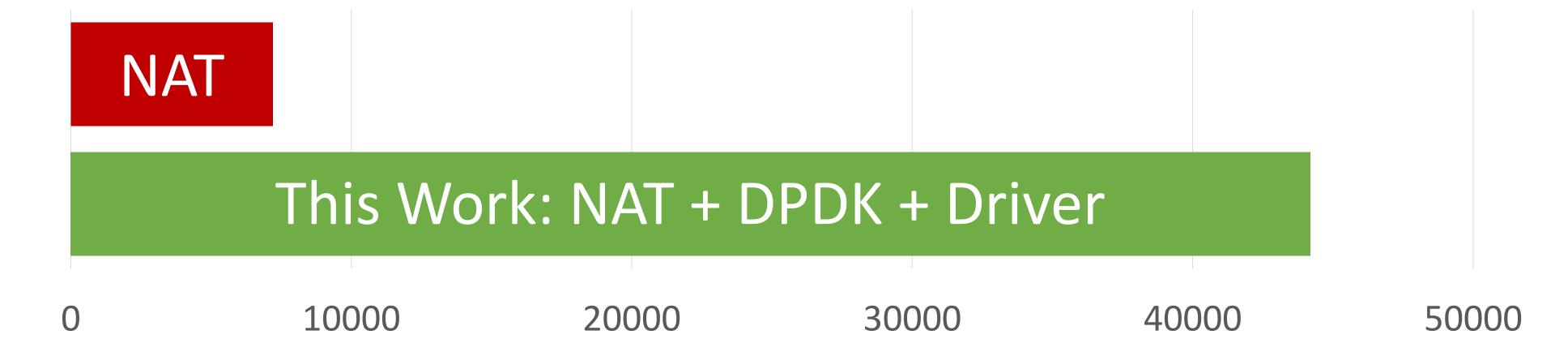
Enforce contracts

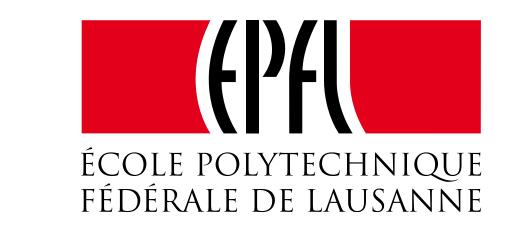


Findings

API misuses
Incorrect semantics
Synchronization errors
Non-existent registers

Covered Instructions







Network
Architecture
Laboratory

